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OF
ELECTRONIC
CIRCUITS**

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HANDBOOK OF ELECTRONIC CONTROL CIRCUITS

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TELEVISION AND RADIO REPAIRING

SOURCEBOOK OF ELECTRONIC CIRCUITS

*Over 3,000 modern electronic circuits
complete with values of all parts,
organized in 100 logical chapters
for quick reference
and convenient browsing*

JOHN MARKUS

Manager, Information Research, McGraw-Hill, Inc.

Senior Member, Institute of Electrical and Electronics Engineers

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SOURCEBOOK OF ELECTRONIC CIRCUITS

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Preface

More than 3,000 electronic circuits, published largely within the past five years, are presented here in 100 logically organized chapters for convenient reference and browsing by practical electronic engineers and technicians. Circuits in a given category are arranged side by side for easy comparison and selection, much as at a smorgasbord table. Each circuit has the values of all significant components, an identifying title, a concise description of circuit function, performance data, and application ideas. This information will generally be sufficient for use as a starting point in redesigning a circuit to meet other requirements. For those who need further information, each circuit also has a citation giving the title of the original article, its author, and its exact location in the original publication.

Engineering libraries, particularly in foreign countries, will find this volume a highly acceptable substitute for the original sources when facing limitations on budgets or shelf space. Most users will find that sufficient information is given with each circuit to meet their needs, thereby making a file of original sources unnecessary.

For the average circuit search, start with the alphabetically arranged table of contents at the front of the book. This will show almost at a glance the chapters that are most likely to contain the desired type of circuit. The random arrangement of circuits within a chapter encourages the browsing that so often turns up an unexpectedly valuable idea.

If a chapter search fails to give the exact circuit needed, use the back-of-book index. Here the circuits are indexed in depth under a variety of type and application names, combined with hundreds of *see* and *See also* cross references that will speed comprehensive searching for a particular circuit even when it is combined with other circuits. The result is a desktop information-retrieval system for the most significant transistor and tube circuits developed in recent years. With it, you can retrieve a desired circuit within a matter of minutes, as compared to the hours or days usually required to get results from costly computer-based information systems.

One goal of this book is to provide a maximum of circuit information in minimum space. Accordingly, there is an absolute minimum of repetition in each circuit description. To get maximum information, the chapter title, the bold-face circuit heading, and the original title in the citation should be read along with the description and the circuit itself.

On those few occasions when additional information is desired, most users will go to a library for the original source of a circuit. The citations therefore give vol-

ume and issue number for publications, in accordance with the preferences of librarians. For those who have their own files, the equivalent years and issue dates for the two most frequently cited publications—*Electronics* and *EEE*—can be quickly determined from handy tables following the table of contents. Here also are listed the abbreviations most frequently used on the diagrams and in the text, with meanings.

The values of the important components are given for every circuit, since these help an engineer to read the circuit and redesign it for his own needs. The development of a working circuit for a new application is speeded tremendously when the design work can be started with a working circuit, instead of starting from scratch. Research and experimentation are thereby cut to a minimum, so that even a single use of this pioneering circuit-retrieval search book could pay for its initial cost many times over.

Although the majority of the circuits are recent semiconductor designs, important new electron-tube circuits are adequately represented because there are still many applications where only tubes can do the required job.

Never before have so many circuits, complete with values, been collected in a single volume for such convenient reference, to provide the desired circuit within minutes and at the same time tell where further information on it can be obtained. Results are obtained in only a fraction of the time that would be required to scan the hundreds of magazines and books from which this volume was compiled.

To the original publications cited in this volume and to their authors and editors should go the real credit for making possible this contribution to the advancement of electronic circuit design. Particular credit goes to publisher Jim Randolph and editor Lewis Young of *Electronics* for recognizing the importance of easy retrieval of the many valuable circuit design articles they have published. Specific credit must be given also to George Rostky, editor of *EEE*, for approving the inclusion of diagrams from his famous "Circuit Design Award" section. Other sources, equally appreciated but too numerous to mention here, are credited in the individual citations.

To artist and orchid-hybridizer Jack Quint, more active than ever in Florida retirement, goes full credit for arranging the thousands of circuits on these pages so well, each unmistakably associated with its own text.

John Markus

Contents

Preface	v	51. Modulator circuits	393
1. Alarm circuits	1	52. Motor control circuits	404
2. Amplifier circuits	8	53. Multiplier circuits	413
3. Analog circuits	30	54. Multivibrator circuits	417
4. Audio circuits	38	55. Noise circuits	439
5. Automatic frequency control circuits	54	56. Oscillator circuits	447
6. Automatic gain control circuits	57	57. Phase control circuits	465
7. Automotive circuits	65	58. Photoelectric circuits	475
8. Battery charging circuits	72	59. Photography circuits	485
9. Beacon circuits	76	60. Power supply circuits	491
10. Bridge circuits	81	61. Preamplifier circuits	497
11. Capacitance control circuits	85	62. Protection circuits	501
12. Cathode ray circuits	89	63. Pulse amplifier circuits	511
13. Character generator circuits	100	64. Pulse generator circuits	516
14. Chopper circuits	103	65. Pulse height analyzer circuits	533
15. Clock circuits	109	66. Pulse processing circuits	537
16. Comparator circuits	114	67. Pulse shaping circuits	541
17. Computer circuits	120	68. Radar circuits	545
18. Control circuits	127	69. Radiation circuits	558
19. Converter circuits	138	70. Receiver circuits	568
20. Counter circuits—objects	148	71. Recorder circuits	581
21. Counter circuits—pulses	151	72. Regulated power supply circuits	588
22. Current control circuits	167	73. Remote control circuits	615
23. D-c amplifier circuits	172	74. Sampling circuits	623
24. Delay circuits	178	75. Sawtooth generator circuits	629
25. Demodulator circuits	190	76. Scanner circuits	639
26. Differential amplifier circuits	193	77. Servo circuits	643
27. Discriminator circuits	200	78. Shift register circuits	664
28. Display circuits	204	79. Signal generator circuits	669
29. Electronic heating circuits	209	80. Simulator circuits	677
30. Electronic music circuits	213	81. Staircase generator circuits	685
31. Filter circuits	216	82. Stereo circuits	689
32. Flash circuits	223	83. Sweep circuits	696
33. Flip-flop circuits	231	84. Switching circuits	703
34. Frequency divider circuits	240	85. Tape recorder circuits	714
35. Frequency measuring circuits	246	86. Telemetry circuits	723
36. Frequency modulation circuits	250	87. Television camera circuits	734
37. Frequency multiplier circuits	256	88. Television circuits—black-and-white	739
38. Function generator circuits	265	89. Television circuits—color	747
39. Gate circuits	269	90. Temperature control circuits	755
40. I-f amplifier circuits	281	91. Test circuits	764
41. Infrared circuits	289	92. Timer circuits	783
42. Integrated circuits	295	93. Transceiver circuits	792
43. Integrator circuits	306	94. Transmitter circuits	796
44. Inverter circuits	313	95. Trigger circuits	807
45. Latching circuits	327	96. Tuner circuits	821
46. Limiter circuits	330	97. Ultrasonic circuits	825
47. Logic circuits	335	98. Video circuits	838
48. Magnetic amplifier circuits	350	99. Voltage measuring circuits	850
49. Measuring circuits	356	100. Welding circuits	859
50. Medical circuits	377	Index	865

ABBREVIATIONS USED

a-c	alternating current	gto	gate turnoff	na	nanoampere	rfl	radio frequency
adf	automatic direction finder	hr	hour	nipo	negative input positive output		interference
a-f	audio frequency	Hz	hertz	npn	negative-positive- negative	rms	root mean square
afc	automatic frequency control	IC	integrated circuit	nrz	nonreturn-to-zero	rpm	revolutions per minute
agc	automatic gain control	i-f	intermediate frequency	nsec	nanosecond	RTL	resistor-transistor logic
a-m	amplitude modulation	iff	identification friend or foe	nw	nanowatt	rz	return-to-zero
amp	ampere	K	kilohm	pam	pulse-amplitude modulation	slope	oscilloscope
bcd	binary coded decimal	kc	kilocycle	pcm	pulse-code modulation	scr	silicon controlled rectifier
C	Centigrade	kv	kilovolt	pf	picoFarad	scs	silicon controlled switch
CB	Citizens Band	kw	kilowatt	pino	positive input negative output	sec	second
CCTV	closed-circuit television	ma	milliampere	pnp	positive-negative- positive	s/n	signal-to-noise
cm	centimeter	Mc	megacycle	pnpn	positive-negative- positive-negative	sq cm	square centimeter
cps	cycle per second	meg	megohm	p-m	phase modulation	ssb	single sideband
cr	cathode-ray	mfd	microfarad	ppm	pulse per minute	sync	synchronizing
cro	cathode-ray oscilloscope	mh	millihenry	pps	pulse per second	td	tunnel diode
crt	cathode-ray tube	MHz	megahertz	preamp	preamplifier	t-r	transmit-receive
c-w	continuous-wave	microamp	microampere	prf	pulse repetition frequency	tv	television
db	decibel	microsec	microsecond	prp	peak-to-peak	twf	traveling-wave tube
dbm	decibels above 1 milliwatt	mil	0.001 inch	ppm	pulse per minute	uhf	ultrahigh frequency
d-c	direct current	millimicrosec	milli- microsecond	ppr	pulse repetition rate	ujt	unijunction transistor
DCTL	diode-capacitor- transistor logic	millisec	millisecond	pwm	pulse width modulation	v	volt
ecg	electrocardiograph	mm	millimeter	RC	resistor-capacitor	v-a	volt-ampere
eeg	electroencephalo- graph	mmfd	micromicrofarad	RCTL	resistor-capacitor- transistor logic	vco	voltage-controlled oscillator
fet	field effect transistor	mono	monostable multivibrator	RDTL	resistor-diode- transistor logic	vfo	variable-frequency oscillator
f-m	frequency modulation	mos fet	metal-oxide semiconductor field-effect transistor	r-f	radio frequency	vhr	very high frequency
ft	feet	mti	moving target indicator			vlf	very low frequency
gc	gigacycle	mv	millivolt			vswr	voltage standing wave ratio
G-M	Geiger-Muller	mvbr	multivibrator			vu	volume unit
		mw	milliwatt			w	watt

GUIDE TO VOLUME AND ISSUE NUMBER

ELECTRONICS

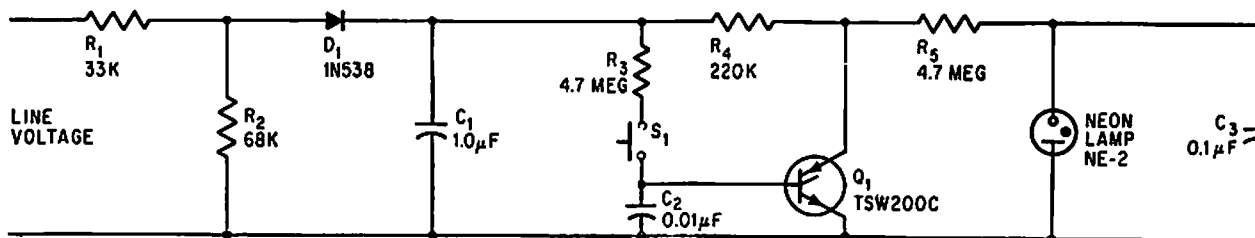
Vol.	Year	Date of Issue No. 1	Frequency
31	1958	Jan. 3	Every 7 days
32	1959	Jan. 2	Every 7 days
33	1960	Jan. 1	Every 7 days
34	1961	Jan. 6	Every 7 days
35	1962	Jan. 5	Every 7 days
36	1963	Jan. 4	Every 7 days
37	1964	Jan. 3	Every 7 days
37	1964	Mar. 23 (No. 12)	Every 14 days
38	1965	Jan. 11	Every 14 days
39	1966	Jan. 10	Every 14 days
40	1967	Jan. 9	Every 14 days

EEE

Vol.	Year	Issue No.	Frequency
10	1962	Jan.	Monthly
11	1963	Jan.	Monthly
12	1964	Jan.	Monthly
13	1965	Jan.	Monthly
14	1966	Jan.	Monthly
15	1967	Jan.	Monthly

CHAPTER 1

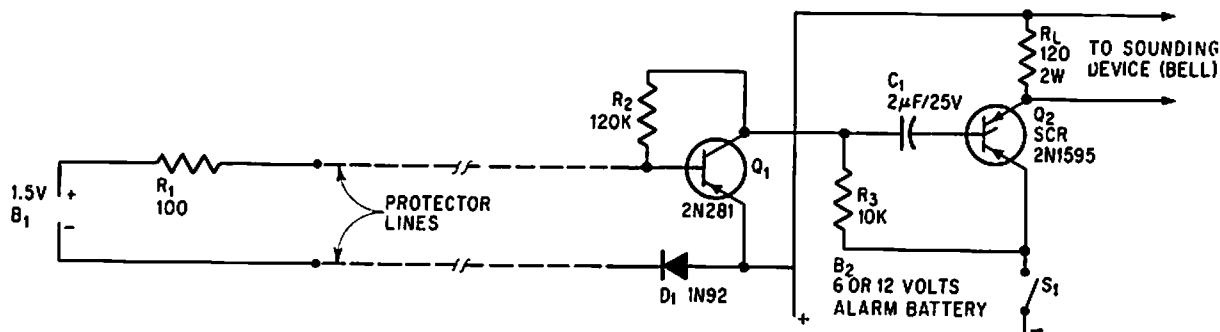
Alarm Circuits



POWER FAILURE—Current of 0.5 ma through R4 holds scr Q1 on normally. After power failure, scr will not fire again because it has

no gate current. Line voltage across scr then makes neon relaxation oscillator flash warning signal. Depressing S1 resets circuit.—R.

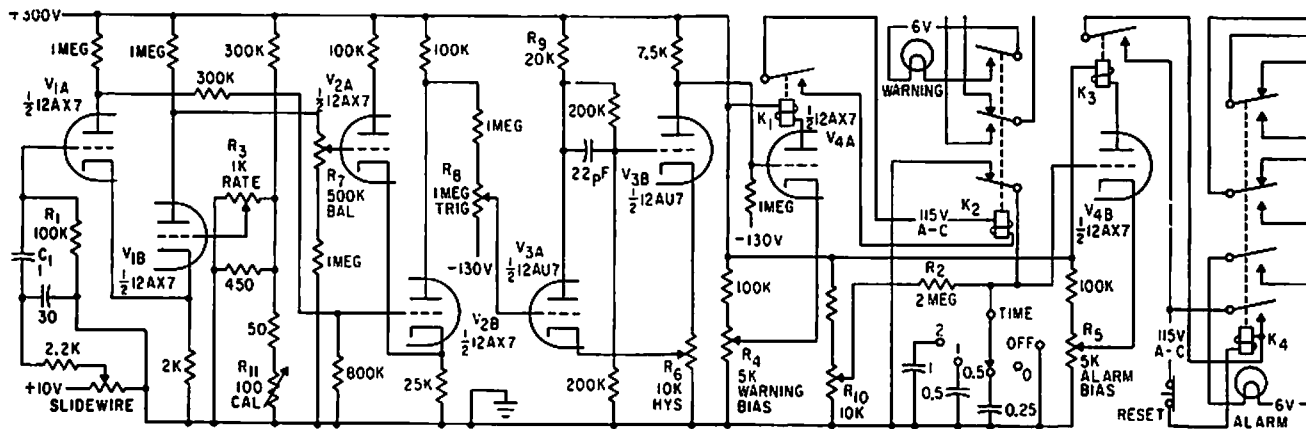
K. Honeycutt, SCR Monitors Power Failure, *Electronics*, 38:15, p 63.



INTRUDER ALARM—Circuit responds to break or short in loop of foil or wire encircling area to be protected, by reacting to change in

normal current drain of 500 microamp from 1.5-v battery in protector loop. Circuit is reset after an alarm by opening S1 momentarily.

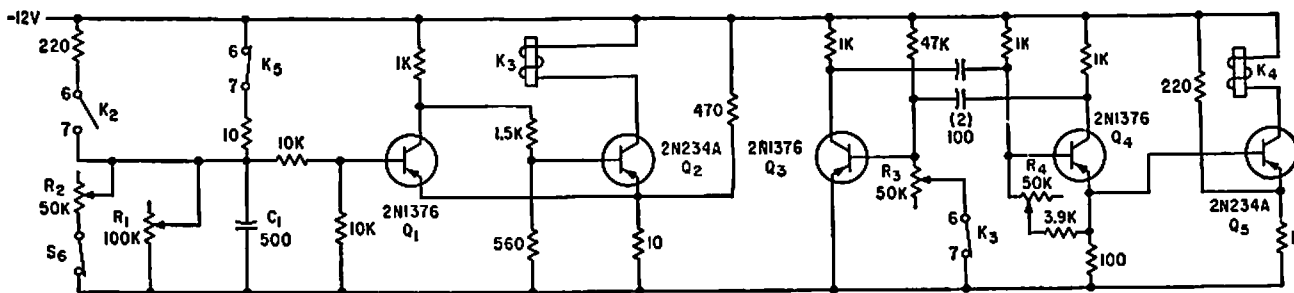
ily.—W. Vollenweider, Low-Current Alarm, *Electronics*, 39:5, p 105-106.



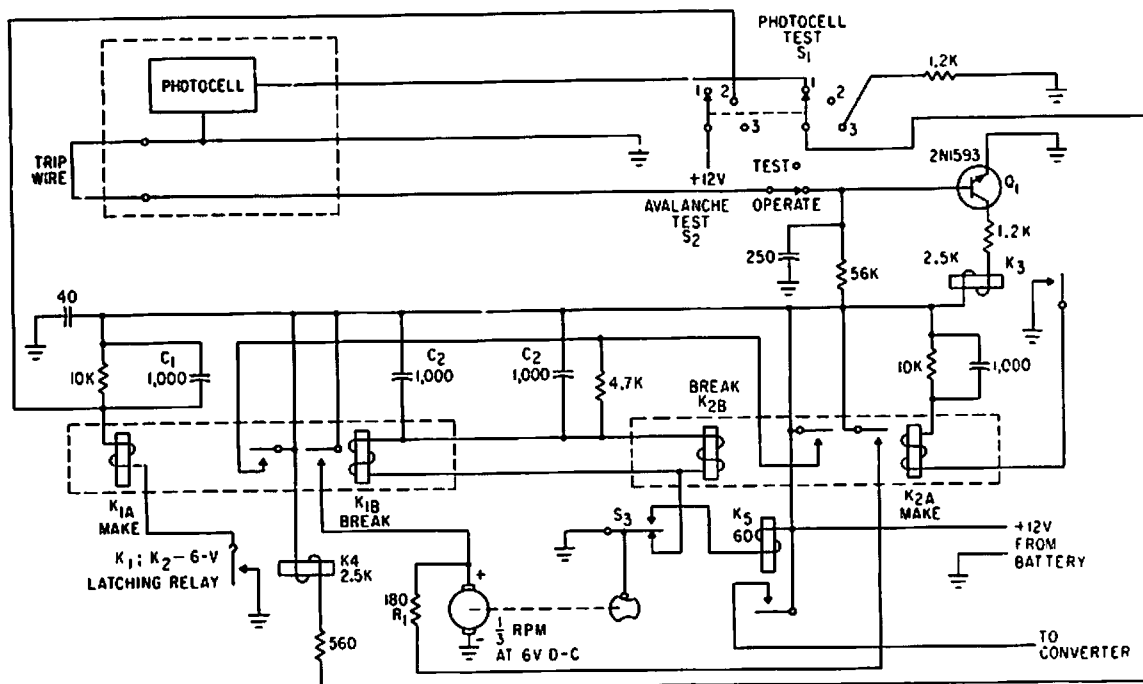
RATE-OF-RISE HEAT ALARM WITH DELAY—Thermocouple senses rise in temperature of machinery or heat of fire, and feeds servo null-balance recorder having repeater slidewire. Output of slidewire is differentiated by

C1-R1 and compared with reference rise rate voltage at grid of V1B. When amplified difference at output of V2 switches Schmitt trigger V3, V4A conducts and energizes K1. Additional triode and relays provide time

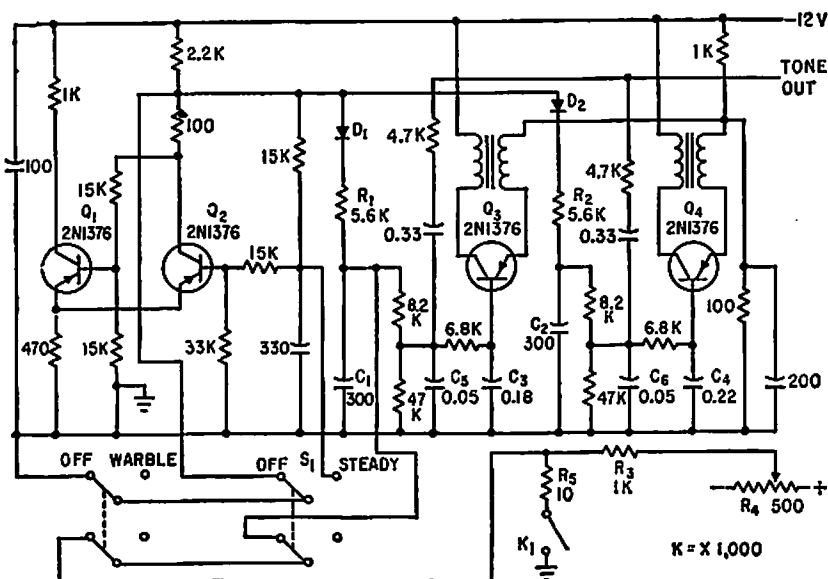
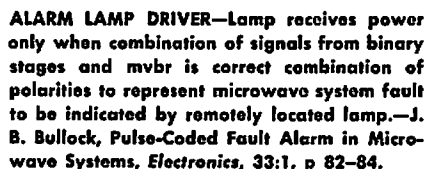
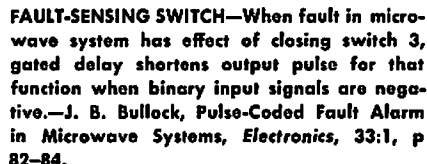
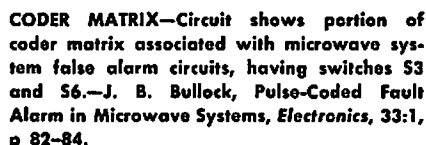
delay for alarm lamp.—T. L. Greenwood, Indicator Warns of Excessive Rise Rates, *Electronics*, 35:7, p 54-56.



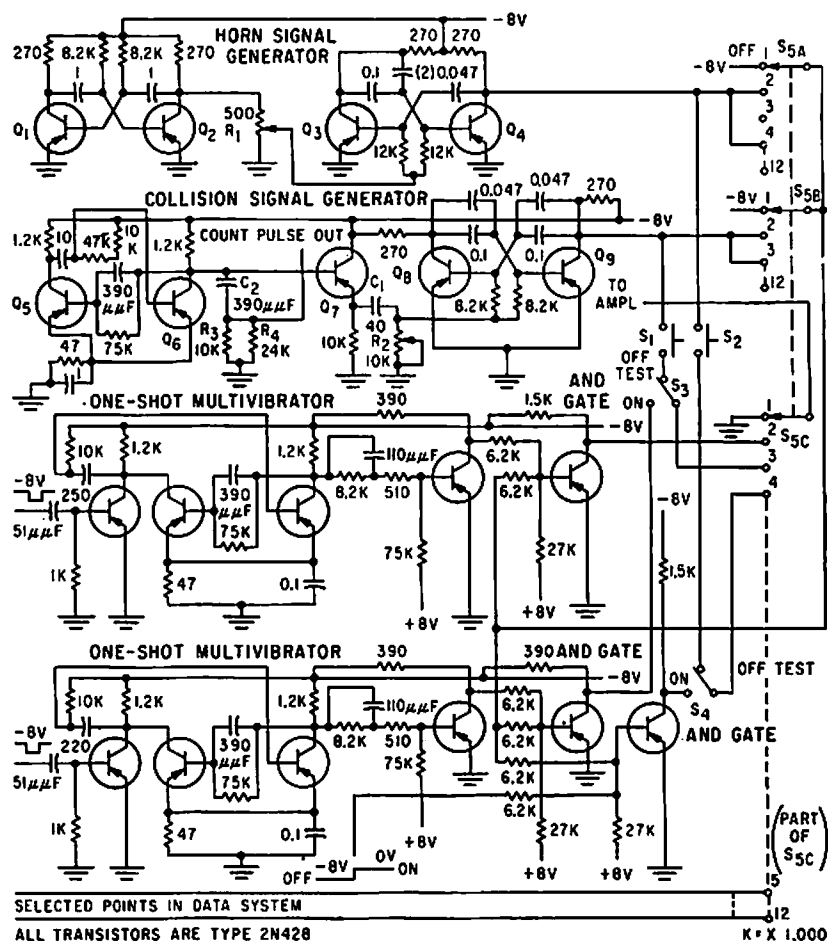
terminated zone coding (zone 1213 for code group shown).—W. F. Ferguson, High-Powered Audio Alarm Systems, *Electronics*, 33:16, p 70-72.



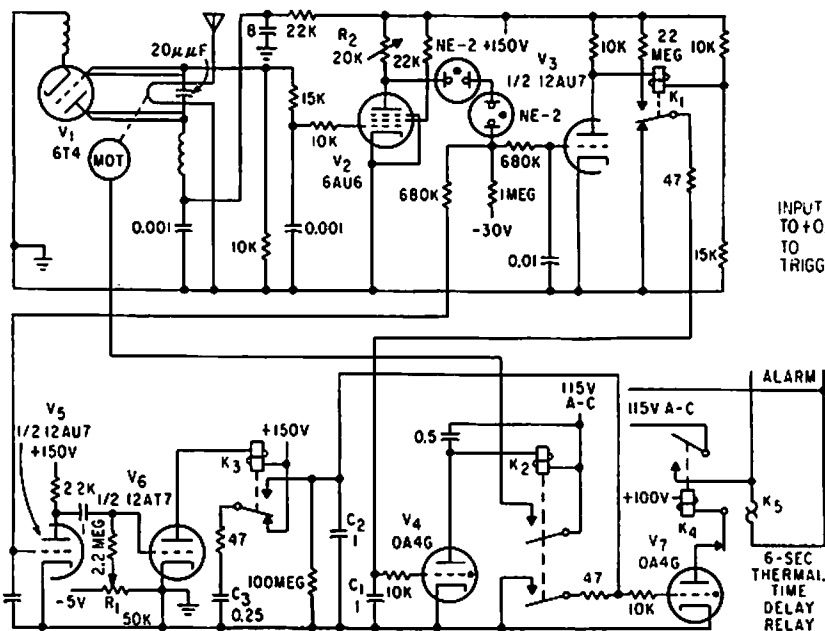
transmission daily.—G. Neal and S. A. Stone, Hybrid Telemeter Detects Avalanches, *Electronics*, 34:50, p 72-73.



wave output of low-frequency mvbr Q1-Q2, to produce siren-type wail.—W. F. Ferguson, High-Powered Audio Alarm Systems, *Electronics*, 33:16, p 70-72.

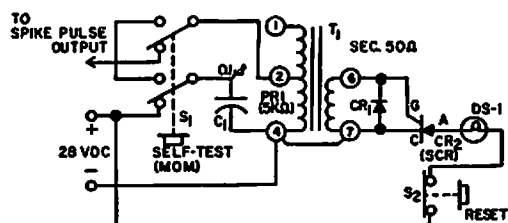


shown, for monitoring two circuits. Mixing these two signals produces battle stations sound for monitoring third circuit.—S. Fierston, *Alarm Circuit Warns of Faults in Digital Systems*, *Electronics*, 32:27, p 48-49.



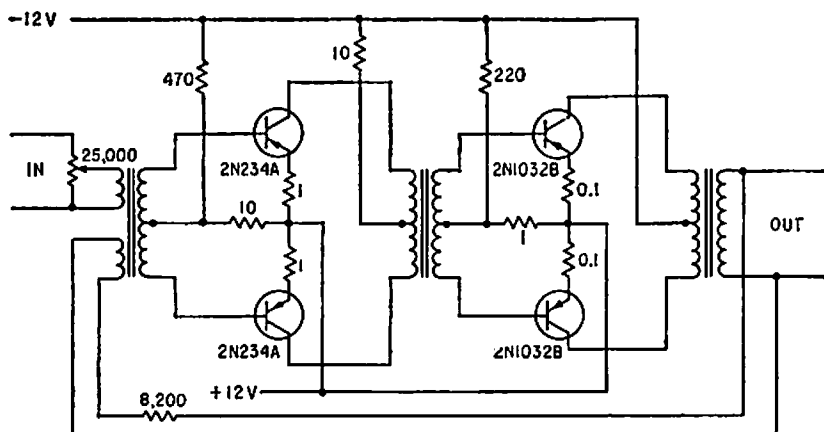
VHF INTRUSION ALARM—Based on fact that object moving toward or away from antenna causes phase relationship of radiated and reflected waves to shift through 2 pi radians at antenna for each half-wavelength of movement. Varying phase changes amplitude of oscillation, detected by circuit and used to

turn on alarm. Drift in oscillator grid voltage activates timing motor which adjusts degree of coupling between oscillator tank and antenna, to make alarm self-adjusting.—G. A. Whitlow, VHF Intrusion Alarm is Self-Adjusting, *Electronics*, 32:35, p 62-66.



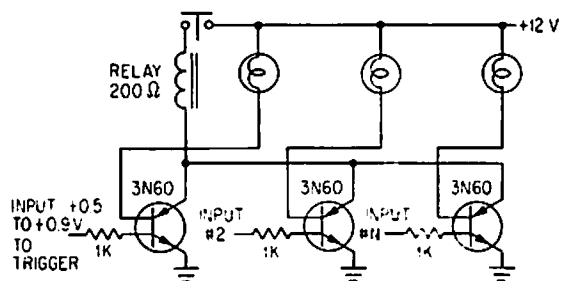
TRANSIENT PULSE DETECTOR—Determines occurrence of single spike pulse having maximum amplitude of 50 v at 2 ma and duration of 1 millise. Spike pulse is stepped down by transformer to 5 v at 20 ma, which is sufficient to fire GE C10 scr, causing 28-v lamp

to come on. When reset button is pressed, scr cuts off, lamp goes out, and circuit is ready for another spike. C1 is 0.1 mfd and CR1 is 1N270.—Transient Spike Pulse Detector, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., p 204.

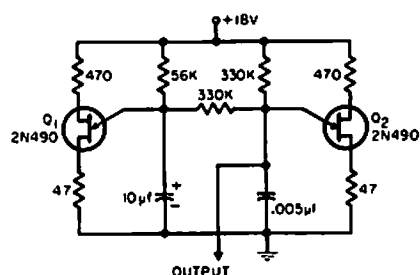


SIREN POWER AMPLIFIER—Four-transistor class AB audio amplifier delivers 200 w to four loudspeakers. Standby power drain is 12 w.

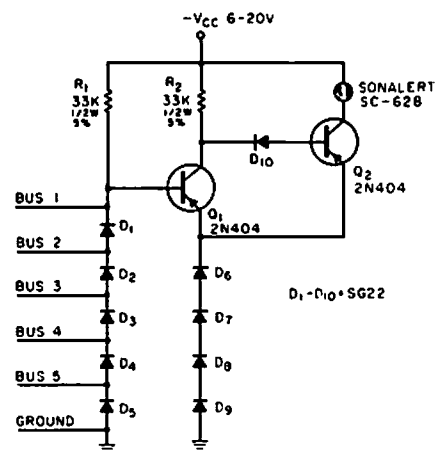
Input is obtained from warble generator.—W. F. Ferguson, High-Powered Audio Alarm Systems, *Electronics*, 33:16, p 70-72.



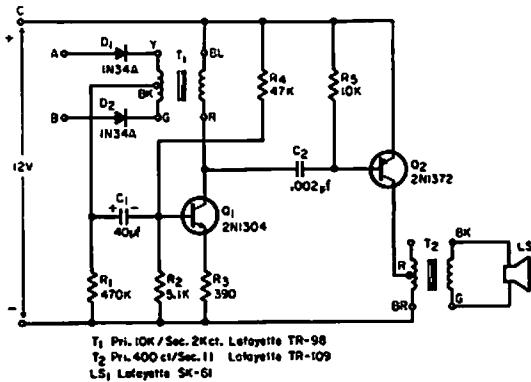
MULTIPLE-INPUT OVERVOLTAGE ALARM—Lamp load of each silicon-controlled switch lights when its input exceeds threshold voltage, to identify input that is responsible for pulling in relay that sounds alarm or shuts down equipment. Lamps also serve to suppress rate effect.—R. A. Stasior, How to Suppress Rate Effect in PNP Devices, *Electronics*, 37:2, p 30-33.



VARYING-FREQUENCY OSCILLATOR—Output frequency of alarm tone generator changes continuously. With suitable amplifier and loudspeaker, can easily be heard in noisy environments where single tone or amplitude-modulated tone would go unnoticed.—A. Mall, Varying-Frequency Warning Alarm, *EEE*, 12:7, p 25.

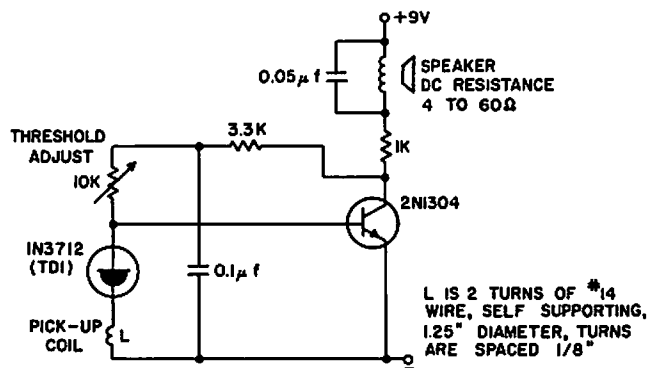


SHORT-CIRCUIT ALARM—Sounds an alarm if a short occurs between any two of five different voltage buses or between any bus and ground. Used in checking complicated point-to-point backplane wiring for computers, to detect wiring errors or solder splashes.—J. J. Russo, Short-Circuit Alarm, *EEE*, 13:6, p 66-68.

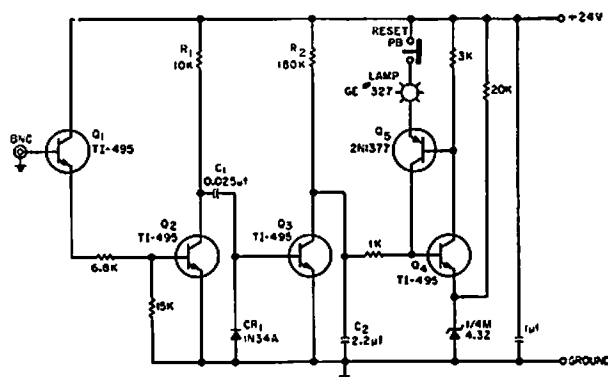


DIFFERENTIAL-VOLTAGE ALARM—Detector circuit with high sensitivity and stability, followed by audio amplifier, serves as differential voltage or current alarm. Input may be d-c or low-frequency a-c. Output is dis-

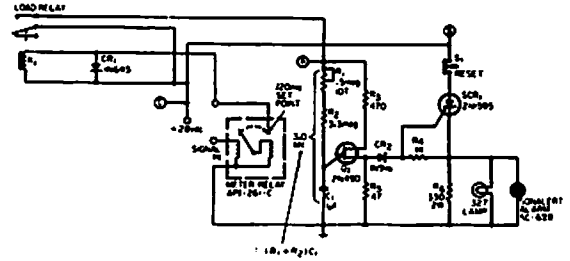
tinctive series of audio beeps or continuous tone, occurring only when preselected polarity unbalance is present at input.—C. E. Miller, Differential-Voltage or Current Alarm Circuit, *EEE*, 12:7, p 25.



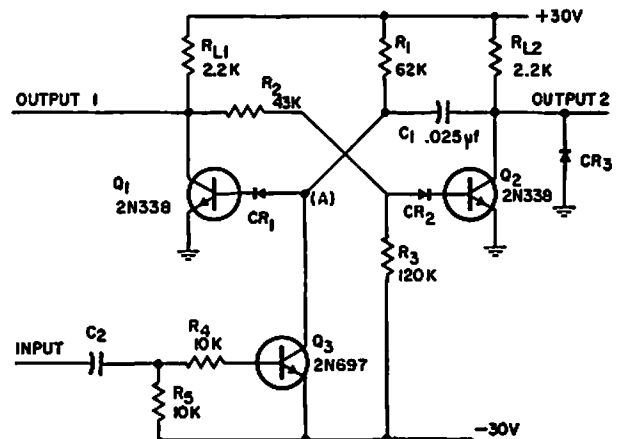
200-MC R-F RADIATION DETECTOR—Gives 1,800-cps alarm tone when signal is picked up by coil or by small slot antenna serving as sensor.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 363.



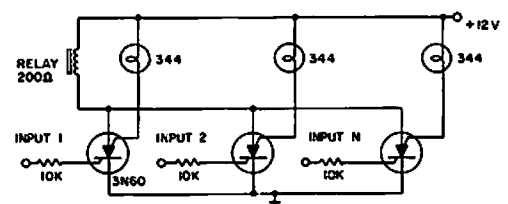
MISSING-PULSE DETECTOR—Warning light comes on to indicate occasional skipping of timing pulse or gate trigger having 20-pps rate, with durations ranging from 2 microsec to 30 millisecc. Detector is triggered if interval between any two pulses exceeds 75 millisecc, and must then be reset by pushbutton.—H. S. Reichard, Missing Pulse Detector, *EEE*, 10:6, p 35.



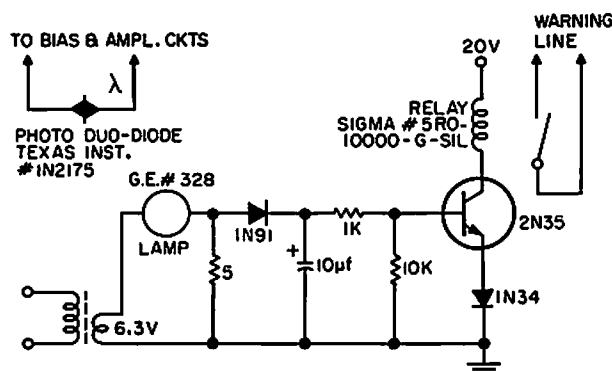
GYRO FAULT ALARM—Circuit sounds alarm if gyro wheel is locked up, as indicated by input signal remaining at high current or voltage level for longer than preset interval. Circuit can also be used as pulse-level discriminator.—R. L. Szapansky, Pulse-Level Discriminator and Fault Indicator, *EEE*, 13:8, p 68.



PULSE AND D-C MONITOR—Indicates presence of continuous train of pulses, absence of one or more pulses in train, and dropout of d-c level beyond predetermined time interval. Uses controlled monostable mvbr. With d-c inputs, C2 is shorted. With values shown for R1 and C1, output occurs 1.07 millisecc after last pulse.—Pulse and DC Monitor Circuit, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., p 201.

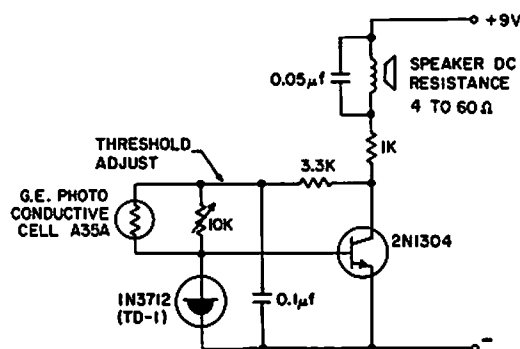


MULTIPLE-INPUT ALARM—Any of several inputs will pull in common alarm relay, with corresponding lamp giving visual indication of triggered circuit. For higher-current lamps, use 3N81 silicon controlled switches.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 425.

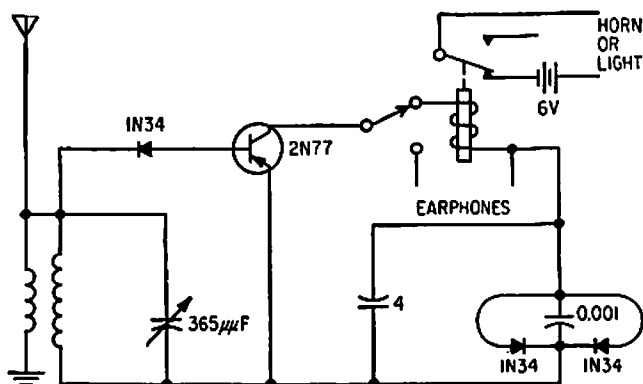


LAMP BURNOUT ALARM—Used when photo-cells and lamps are employed to detect end of magnetic tape, load point, or bad spot. Failure of lamp can cause serious trouble in magnetic tape handler. With circuit shown, when lamp burns out, transistor can no

longer energize relay, and relay contact closes to actuate alarm at computer console.—J. E. Kienle and R. W. Wooldridge, Photocell Lamp Burnout Warning Circuit, *EEE*, 10:8, p 27-28.

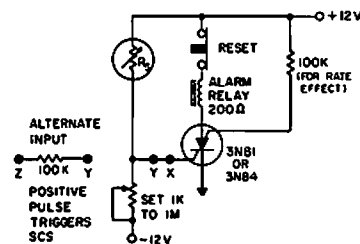


LIGHT DETECTOR—Gives 1,800-cps alarm tone when illumination on photocell exceeds predetermined level, which can be below 0.1 foot-candle near 5,500 angstroms.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 363.

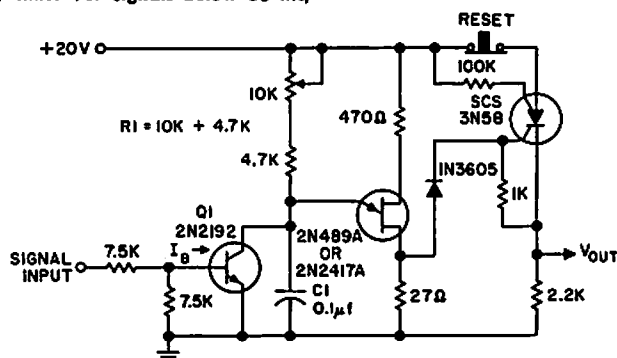


SIGNAL-POWERED ALARM RECEIVER—On arrival of carrier signal at frequency to which antenna and input are tuned, transistor pulls in relay, applying battery power to horn or light. No power is drawn from battery during standby time. For signals below 50 Mc.

best pickup is obtained with single-wire antenna 150 feet long, at right angles to line-of-sight path and as high as possible.—L. R. Crump, *Radio Waves Power Transistor Circuits*, *Electronics*, 31:19, p 63-65.

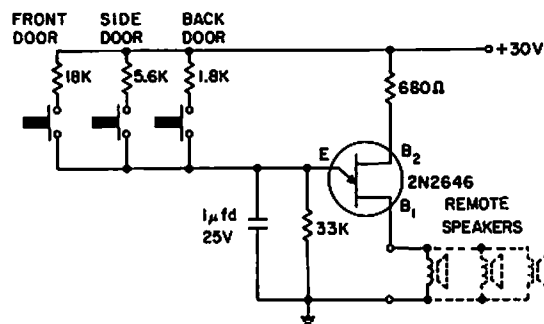


RESISTANCE-TRIGGERED ALARM—Silicon controlled switch is triggered when temperature-, light-, or radiation-sensitive resistor R_s up to 1 meg drops below value of preset potentiometer. Interchanging R_s and potentiometer will trigger alarm on increase in sensing resistor.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 425.



SIGNAL DROPOUT DETECTOR—Used to provide indication of momentary dropout of d-c, a-c, or pulse input signal. Time between disappearance of signal and indication of fault is

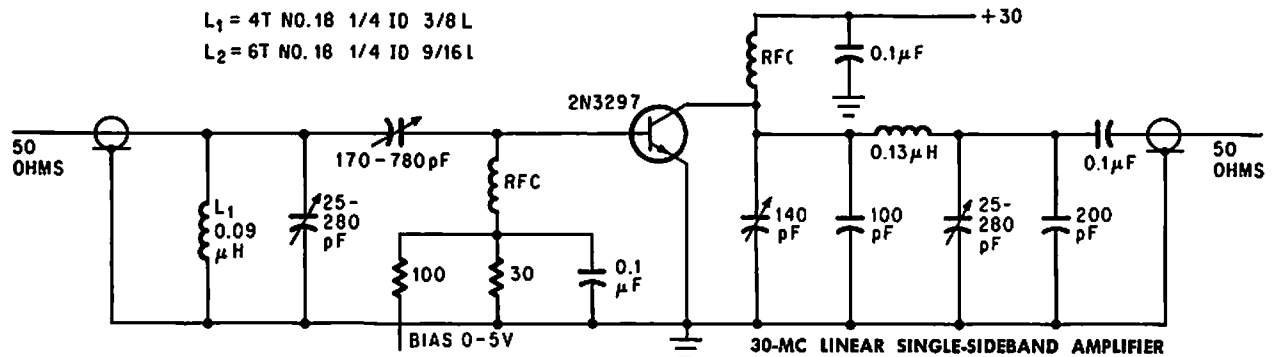
adjustable. Output signal remains until scs is turned off by momentarily opening reset switch.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 336.



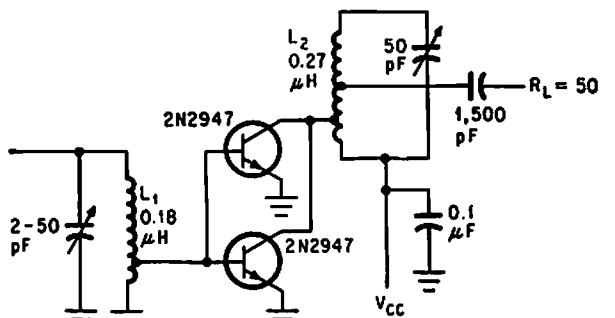
ELECTRONIC DOORBELL—Single unijunction transistor oscillates at different tone for each door.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 380.

CHAPTER 2

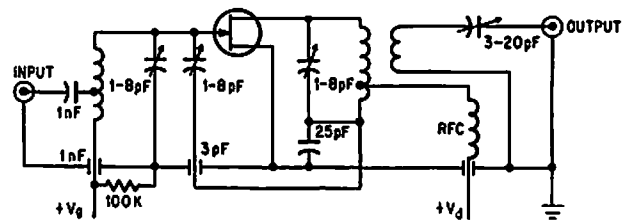
Amplifier Circuits



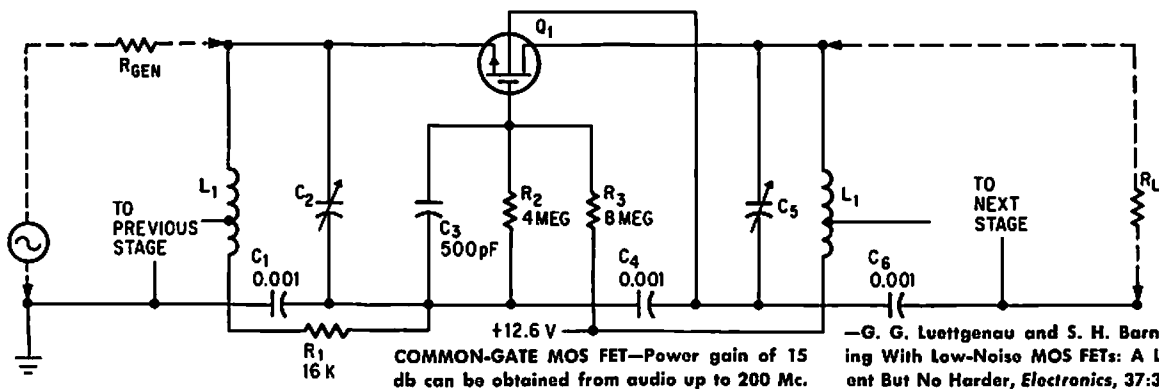
30-MC LINEAR SINGLE-SIDEBAND AMPLIFIER—Single silicon transistor with impedance-matching network and pi output network delivers 20 w of r-f peak envelope power, with all odd-order distortion products at least 30 db down.—R. C. Hejhall, *Getting Transistors Into Single-Sideband Amplifiers*, *Electronics*, 37:17, p 72-75.



$L_1 = 4\text{ T NO. 14, } 1/2 \text{ ID } 5/8 \text{ L}$, $L_2 = 4\text{ T } 1/8 \text{ COP. TUBE } 3/4 \text{ ID } 1" \text{ L}$,
 TAPPED 1/2 T FROM GND TAPPED 1/2 T FROM GND
50-MC 40-W POWER AMPLIFIER—Paralleled silicon transistors operating as class C are biased on by incoming r-f signal, hence stop conducting when there is no signal.—R. C. Hejhall, *Getting Transistors Into Single-Sideband Amplifiers*, *Electronics*, 37:17, p 72-75.

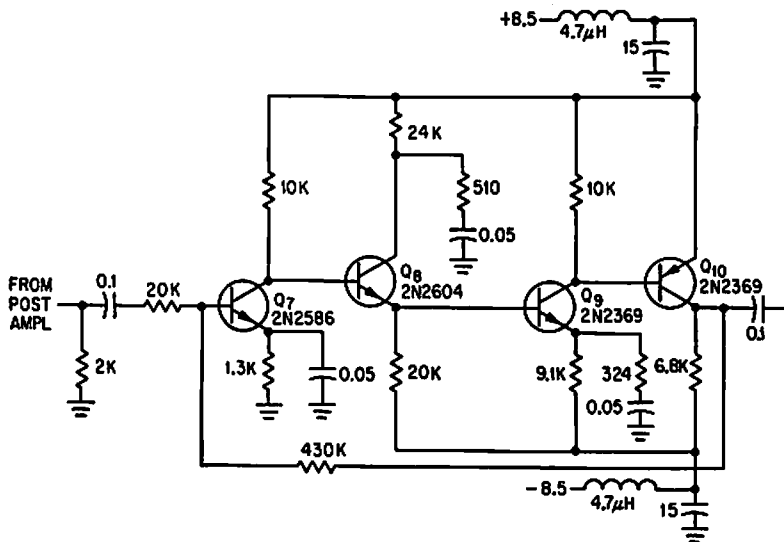


100-MC FET AMPLIFIER—Low-cost insulated-gate fet circuit can handle signals up to several volts in uhf range, with inherently low cross-modulation distortion.—Low-Cost Power Booster, *Electronics*, 37:14, p 29-30.



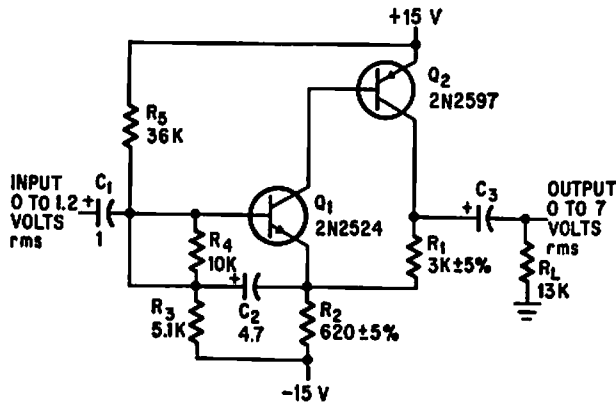
COMMON-GATE MOS FET—Power gain of 15 db can be obtained from audio up to 200 Mc.

—G. G. Luettgenau and S. H. Barnes, *Designing With Low-Noise MOS FETs: A Little Different But No Harder*, *Electronics*, 37:31, p 53-58.



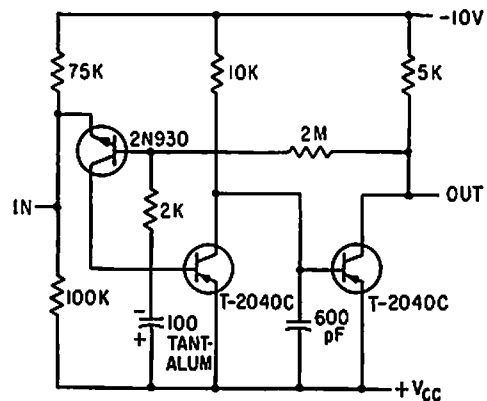
DISCRIMINATOR AMPLIFIER—Direct-coupled voltage amplifier with a-c coupling at input and output has loop gain of 36 for bandwidth of 1 Mc, including low-impedance

driver Q10.—R. Cuikay and T. Callahan, Orbiting Observatory to Measure Stars' Dim Light, *Electronics*, 37:9, p 28-31.

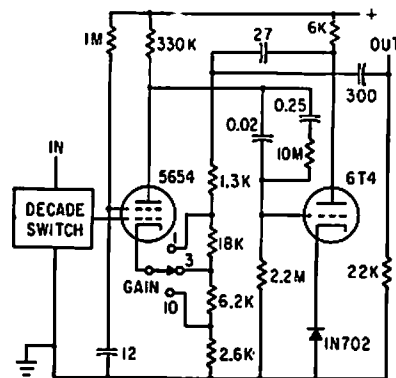


COMPLEMENTARY-TRANSISTOR AMPLIFIER—Bootstrapping and negative feedback provide 220,000-ohm input impedance and 60,000-ohm output impedance, with stabilized gain

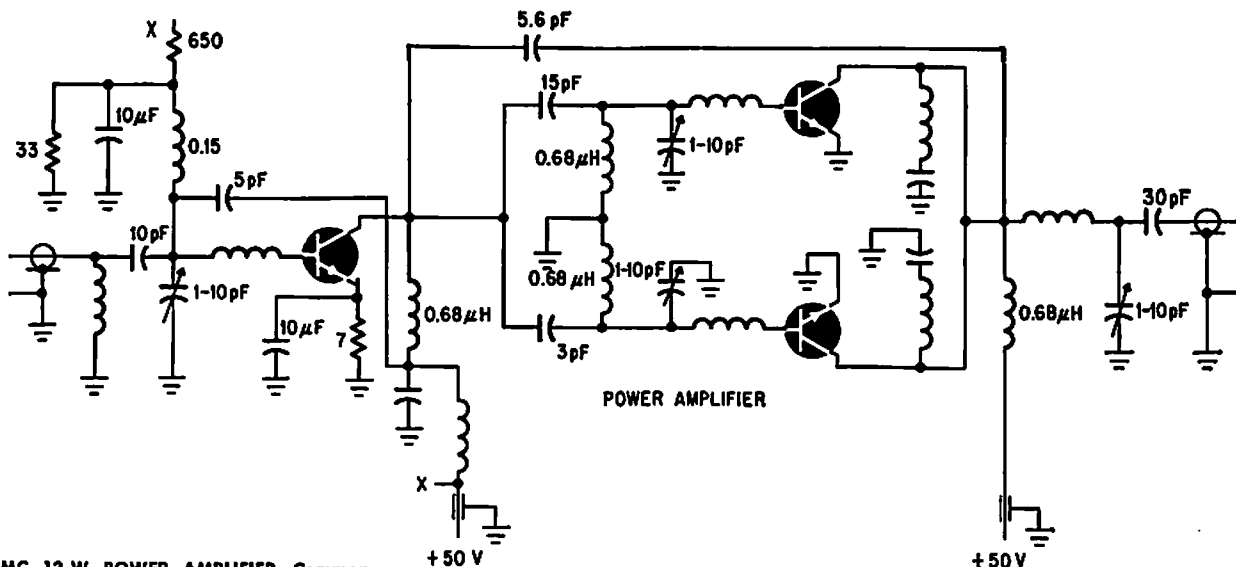
over wide temperature range.—L. J. Ernst, Complementary Amplifier Offers High Input Impedance, *Electronics*, 37:16, p 92-93.



60 DB GAIN AT 1 CPS TO 1 MC—Direct coupled a-c amplifier with feedback and current-derived stabilization uses only two capacitors.—P. Laakmann, Direct Coupling Shrinks Amplifier Size and Cost, *Electronics*, 36:12, p 66-68.



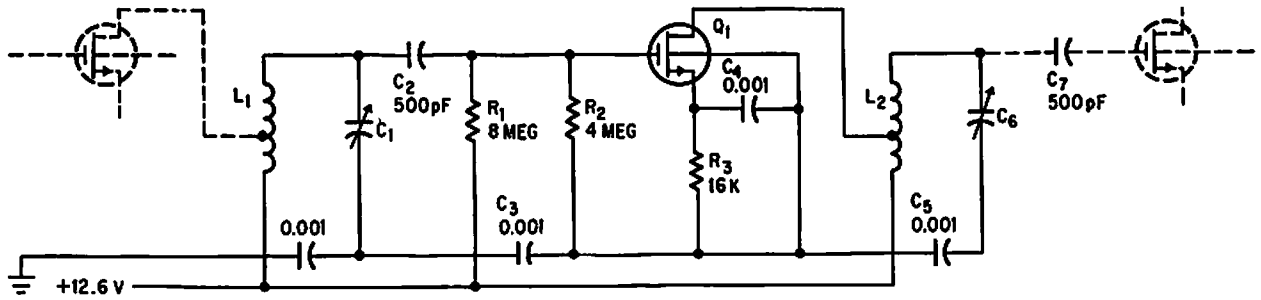
RESPONSE BELOW 2 CPS—With input resistance of 1,000 meg, piezoelectric gage amplifier gives gain choices of 1, 3, or 10 for loads above 2,500 ohms. Decade input switch can provide choice of shunt capacitances for trimming sensitivity of gages.—Extending Piezoelectric Gage L-F Response, *Electronics*, 36:4, p 100-103.



250-MC 12-W POWER AMPLIFIER—Common-emitter circuits operating class C serve for driver and parallel-connected power amplifier

transistors. Total gain is 12 db.—N. Downs and B. van Sutphin, Solid-State Transmitter

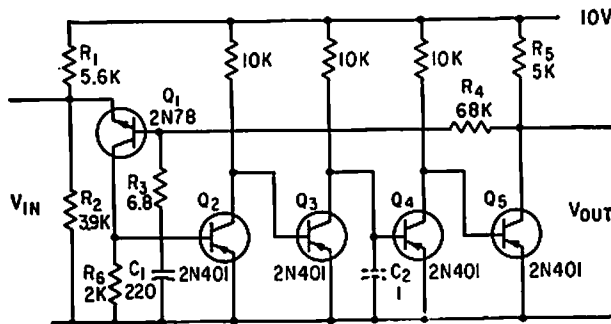
Ready for UHF Telemetry, *Electronics*, 37:17, p 76-80.



UNNEUTRALIZED MOS FET—Low drain-gate capacitance is needed for high power gains

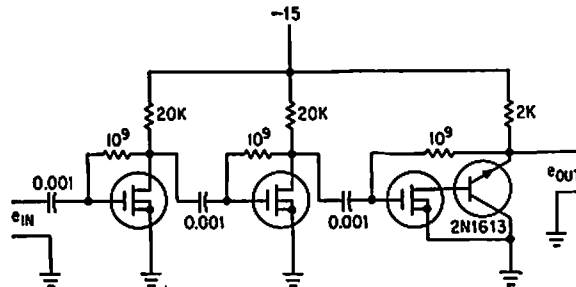
above 2 Mc from common-source mos fet.—G. G. Luettgenau and S. H. Barnes, *Designing*

With Low-Noise MOS FETs: A Little Different But No Harder, *Electronics*, 37:31, p 53-58.

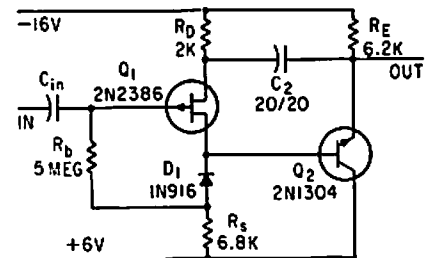


CURRENT-DERIVED STABILIZATION—Bridge-derived stabilization in direct-coupled a-c amplifier provides current sensitivity of 0.1

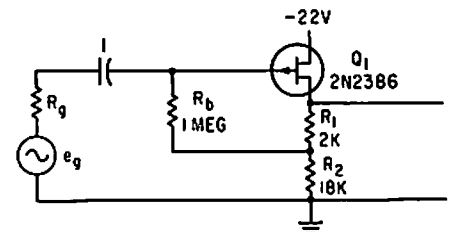
microamp per ma of output current.—P. Laakmann, *Direct Coupling Shrinks Amplifier Size and Cost*, *Electronics*, 36:12, p 66-68.



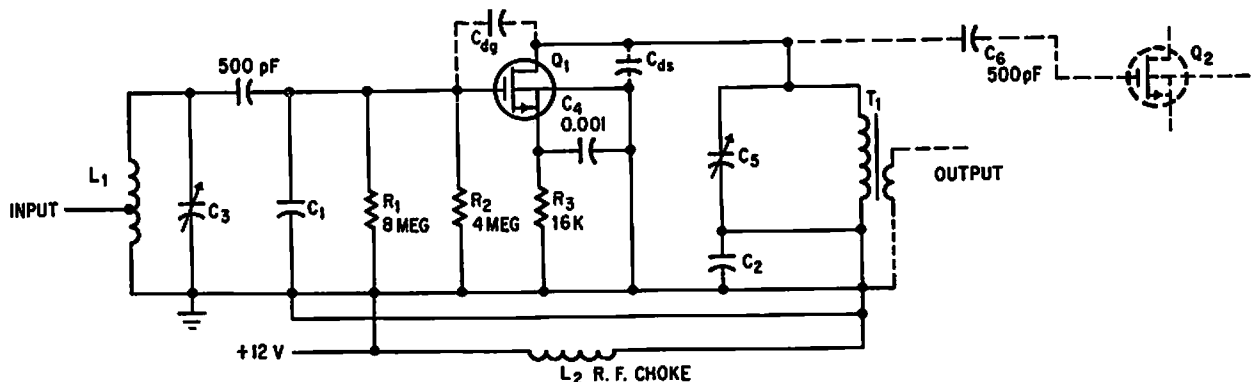
BROADBAND WITH GAIN OF 1,350—Metal-oxide semiconductor transistors (p-mos) in Darlington configuration give high input impedance and low output impedance from 5 cps to 72 kc.—F. M. Wanlass, *Noval Field-Effect Device Provides Broadband Gain*, *Electronics*, 36:44, p 30-33.



SOURCE FOLLOWER WITH REDUCED GATE-TO-DRAIN CAPACITANCE—Diode D1 gives maximum a-c bootstrapping of gate bias resistance while providing required d-c bias. Emitter-follower Q2 couples to drain of fet Q1 a signal in phase with input, to give extremely low input capacitance.—T. R. Signall, *How to Get Maximum Input Impedance with Field-Effect Transistors*, *Electronics*, 36:10, p 44-46.



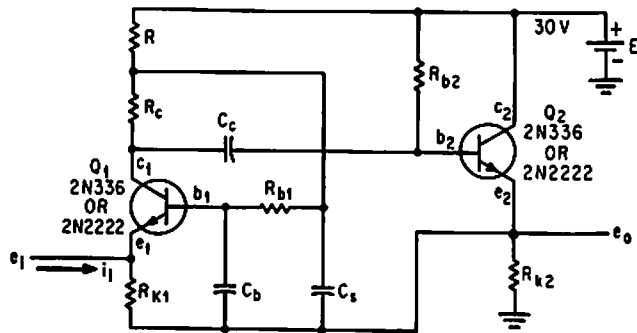
SOURCE FOLLOWER USES BOOTSTRAPPING—Bandwidth is 10 cps to 10 kc, d-c input impedance is 10 meg, and input impedance at 10 kc is above 1 meg for input signals from 1 mv to 10 mv.—T. R. Signell, *How to Get Maximum Input Impedance with Field-Effect Transistors*, *Electronics*, 36:10, p 44-46.



NEUTRALIZED MOS FET—Delivers power gain of 20 db at 100 Mc, with common-source

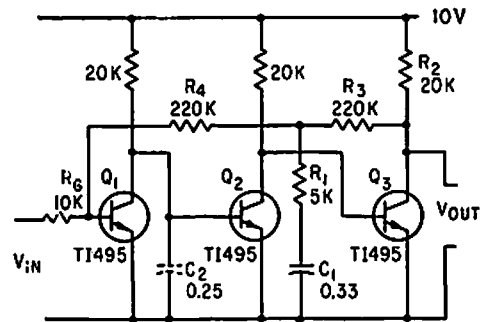
connection.—G. G. Luettgenau and S. H. Barnes, *Designing With Low-Noise MOS FETs: A Little*

Different But No Harder, *Electronics*, 37:31, p 53-58.



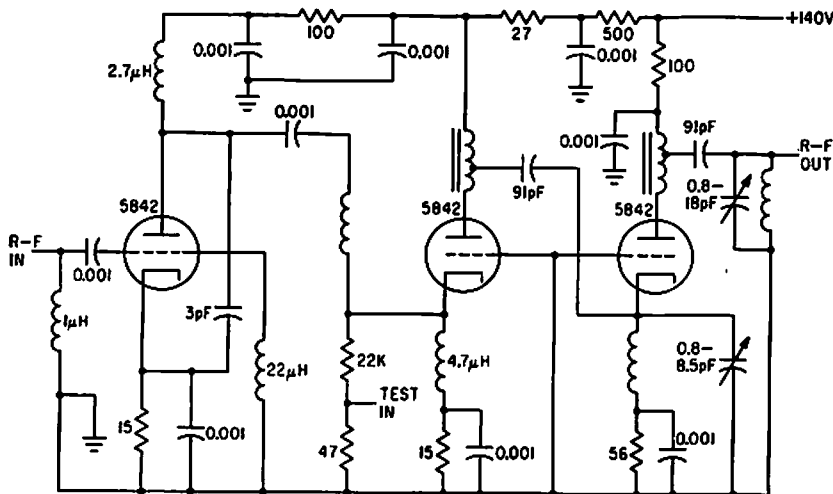
UNITY-GAIN BUFFER—Positive gain of unity is obtained with high input impedance, low output impedance, negligible phase shift, and without phase reversal. Can be used for iso-

lating resolvers from loads.—D. K. Phillips, Unity-Gain Buffer Acquires Precision by Feedback, *Electronics*, 36:51, p 36-37.



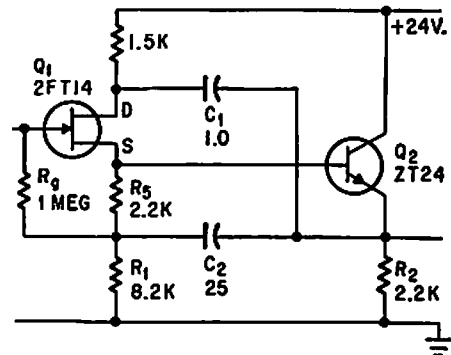
STABILIZED DIRECT-COUPLED A-C AMPLIFIER

—Current-derived stabilization gives simple amplifier using only one capacitor. Adding C2 improves stability but lowers cutoff frequency.—P. Laakmann, *Direct Coupling Shrinks Amplifier Size and Cost*, *Electronics*, 36:12, p 66-68.

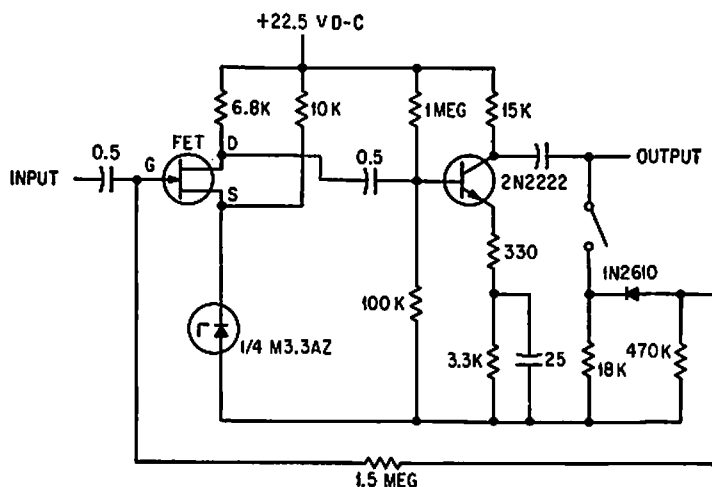


GROUNDING-GRID BROADBAND—Grounded-cathode preamplifier has plate load that provides increasing gain with increasing frequency to drive following two tubes in cas-

cade. Both source and load impedances are 50 ohms.—Broadband VHF Amplifier Covers 30 to 260 Mc Range, *Electronics*, 35:4, p 102.

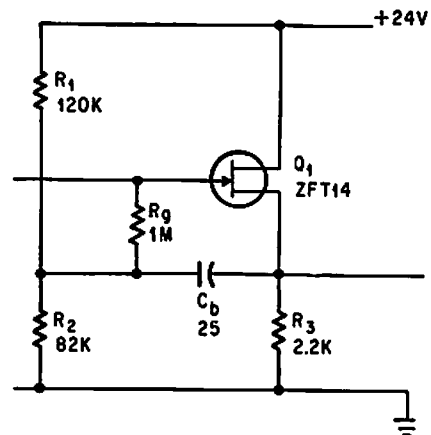


WIDEBAND FET—Feedback and bootstrapping techniques give overall input capacitance of 0.4 pf for 30-pf gate capacitance of fet. Transistor serves as source follower.—B. Down, Using Feedback in FET Circuit to Reduce Input Capacitance, *Electronics*, 37:31, p 63-65.

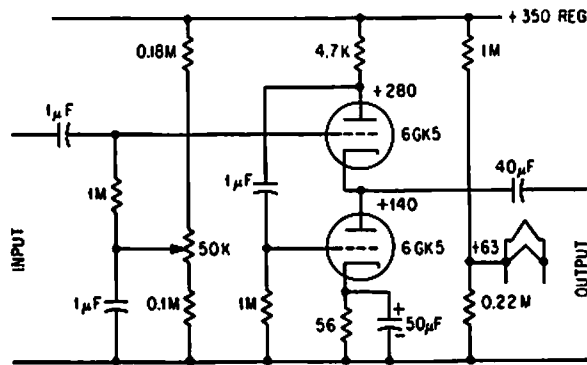


LOW-NOISE FET AMPLIFIER—Agc feedback extends input level to 150 mv.—L. E. Clark, E. B. Mack, and R. C. Hejhall, Highlights of

Small-Signal Circuit Design, *Electronics*, 36:49, p 46-50.

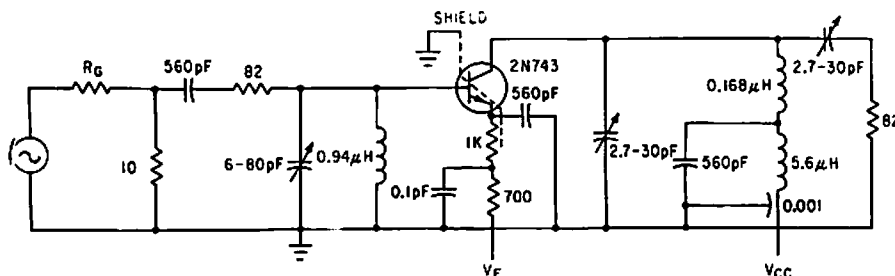


FET SOURCE-FOLLOWER—Voltage-divider biasing increases input impedance. R3 provides negative feedback.—B. Down, Using Feedback in FET Circuit to Reduce Input Capacitance, *Electronics*, 37:31, p 63-65.



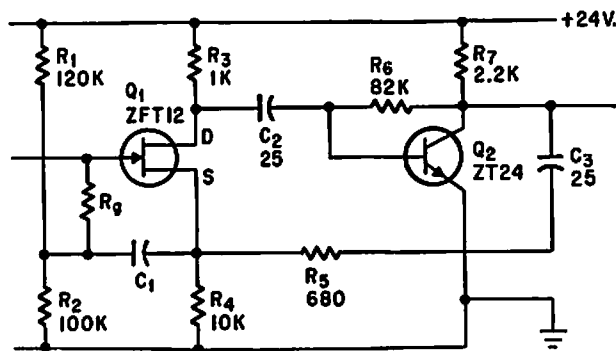
CASCODE FOLLOWER—Output is 20 v peak-to-peak into 1,000 ohms, down to 5 cps, with high stability.—R. W. Johnson, Circuit with a

Twist: The Cascode Follower, *Electronics*, 36:49, p 69-70.



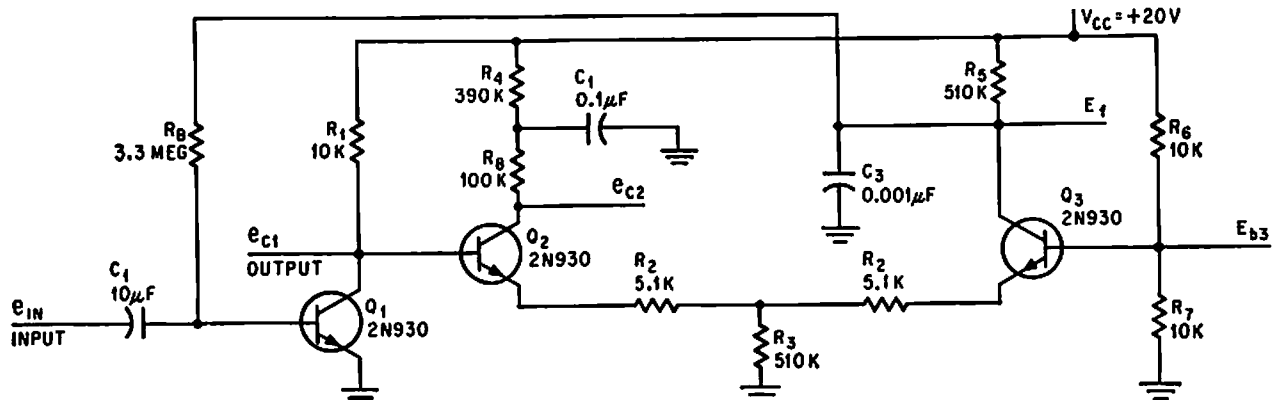
SMALL-SIGNAL 60-MC—Epitaxial 2N743 is operated common-emitter at signal frequency and common-base for biasing. Unneutralized

gain per stage is 17 db.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.



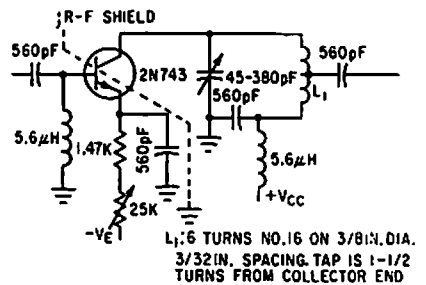
50-MEG INPUT IMPEDANCE—Feedback from Q2 to fet Q1 boosts input impedance.—B. Down, Using Feedback in FET Circuit to Re-

duce Input Capacitance, *Electronics*, 37:31, p 63-65.

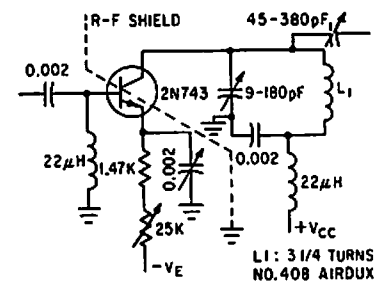


CONSTANT GAIN—Differential amplifier Q2-Q3 regulates bias of Q1 to keep gain con-

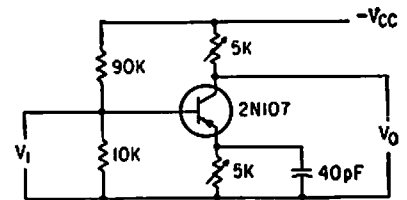
stant despite variations in load or in circuit components.—R. C. Lavigne and L. L. Klein-



60-MC LOW-NOISE—Noise figure is only 6 db for generator resistance of 150 ohms.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.

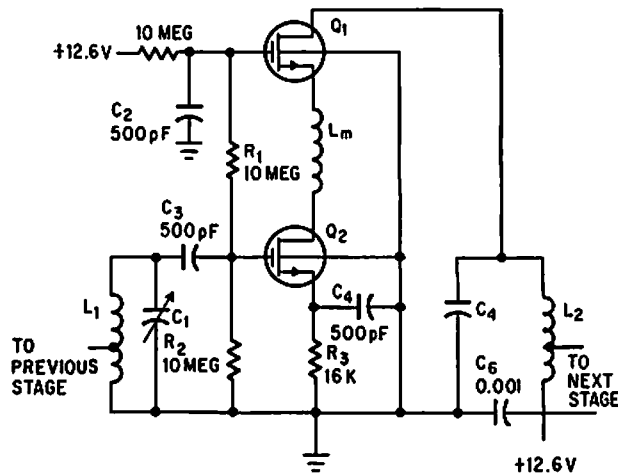


30-MC LOW-NOISE—Noise figure is only 4 db for generator resistance of 200 ohms.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.



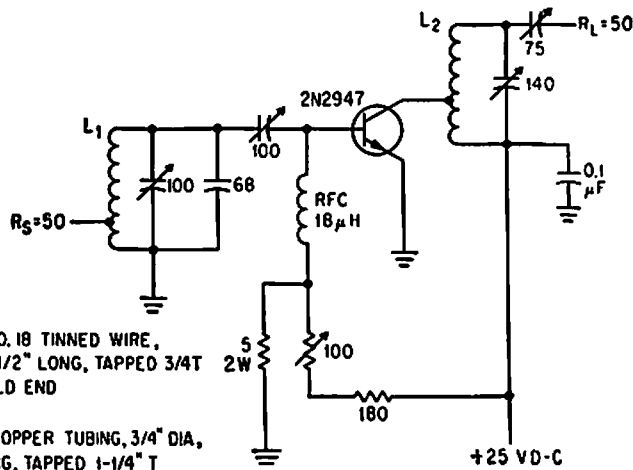
MAGNETICALLY CONTROLLED TRANSISTOR—Uncapped pnp germanium alloy junction transistor placed in strong magnetic field shows gain variation with flux density, with direction and amount of gain depending on direction of magnetic field.—R. W. Lade et al., Magnetic Fields Vary Transistor Gain, *Electronics*, 34:5, p 68-70.

berg, Amplifier Gain is Constant Despite Changes in Load, *Electronics*, 38:13, p 75-77.



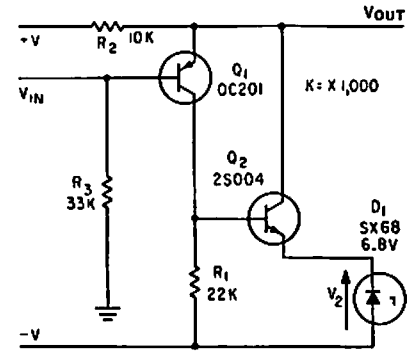
CASCODE MOS FET—Power gain is slightly higher than for neutralized common-source stage.—G. G. Luetgenau and S. H. Barnes,

Designing With Low-Noise MOS FETs: A Little Different But No Harder, Electronics, 37:31, p 53-58.

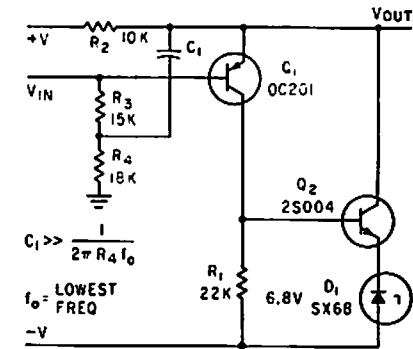


30-MC LINEAR SSB—Power gain is 13 db and output is 8 w peak envelope power.—L. E. Clark, E. B. Mack, and R. C. Hejhall, High-

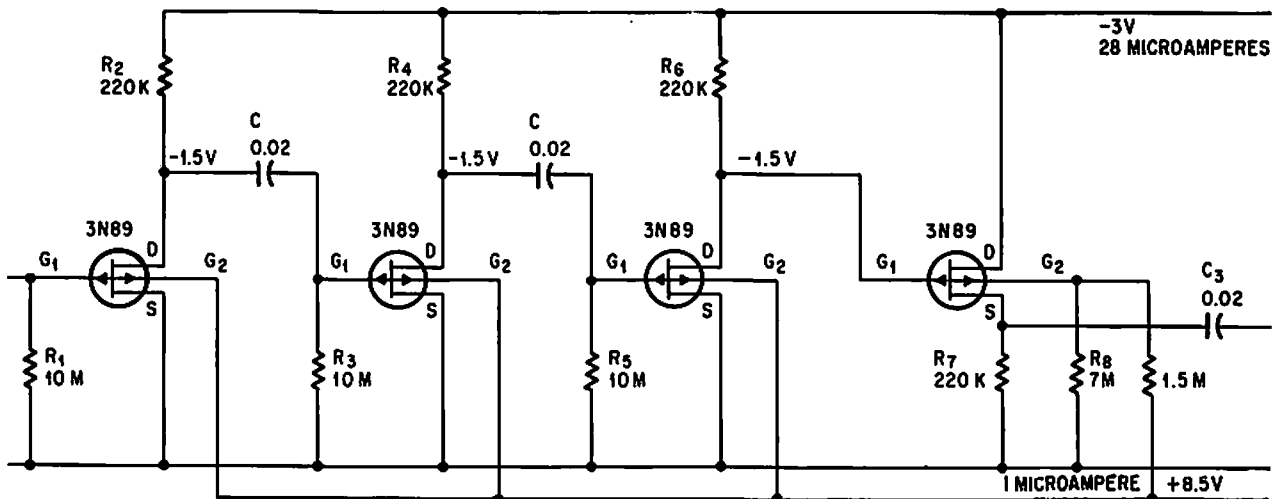
lights of Small-Signal Circuit Design, Electronics, 36:49, p 46-50.



COMPOUNDED EMITTER-FOLLOWER—Compounded emitter-follower with feedback, operated as complementary pair, gives higher input impedance, higher gain, and lower output impedance than conventional emitter-follower.—T. K. Hemingway and J. Willis, *Transistor Pairs Improve Emitter-Follower Performance, Electronics, 35:21, p 48-49.*



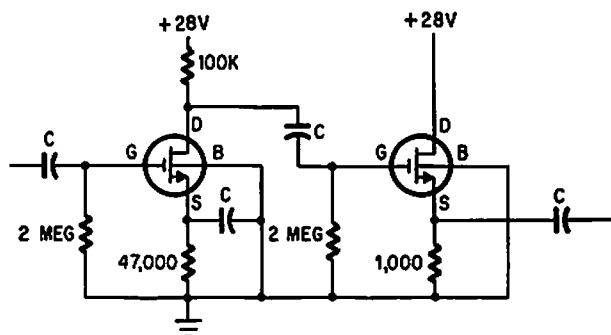
MODIFIED EMITTER-SQUARED FOLLOWER—Complementary transistor arrangement of feedback amplifier is modified to reduce shunting effect of R_3 by applying positive feedback voltage that artificially increases value of R_3 . —T. K. Hemingway and J. Willis, *Transistor Pairs Improve Emitter-Follower Performance, Electronics, 35:21, p 48-49.*



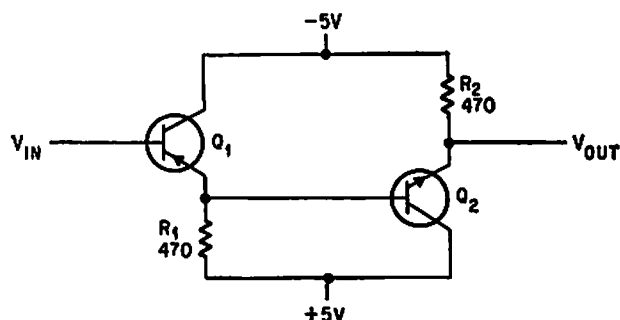
FET MICROPOWER—Voltage gain is 60 db from 1 cps to 30 kc, with 0.5 v rms maximum

output voltage, for power drain under 100 microwatts. First three transistors should be

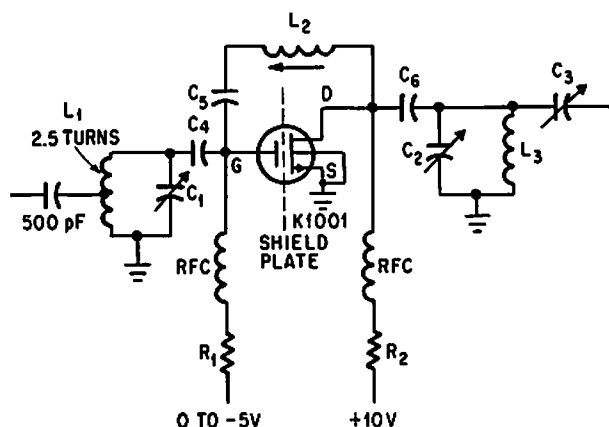
matched.—J. S. Sherwin, *An FET Micropower Amplifier, Electronics, 37:31, p 74-75.*



TWO-STAGE MOS FET—Pair of 3N98 transistors give voltage gain of 10 with low output impedance.—D. M. Griswold, *Understanding and Using the MOS FET*, *Electronics*, 37:31, p 66-70.



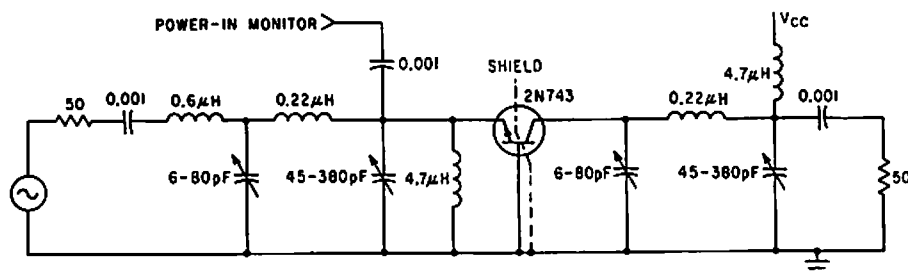
BROADBAND IMPEDANCE TRANSFORMER—Darlington circuit gives unity gain from d-c up to several Mc, using any complementary pair of transistors having sufficiently high gain and cutoff frequency.—I. Ingemarsson, *Darlington Maintains Constant Unity Gain*, *Electronics*, 38:22, p 69.



COMPONENT VALUES					
C ₁	0.9-7 pF	L ₁	5 TURNS, $\frac{5}{8} \times \frac{1}{2}$ " NO.20 WIRE, 0.29 μ H		
C ₂	0.9-7 pF	L ₂	MILLER TYPE 20A107RBI OR EQUIV. 0.125-0.079 μ H		
C ₃	0.9-7 pF	L ₃	5 TURNS, $\frac{5}{8} \times \frac{1}{2}$ " NO.20 WIRE		
C ₄	500 pF				
C ₅	500 pF	R ₁	6,800 OHMS		
C ₆	500 pF	R ₂	2,200 OHMS		

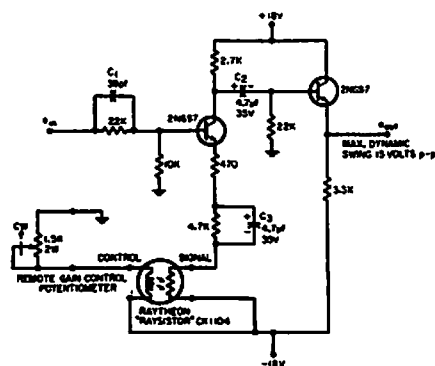
200-MC FET NEUTRALIZED AMPLIFIER—Capacitor C5 between drain and gate provides neutralization by nullifying feedback. Neutralized stable gain at 250 Mc is 8 db. Bandwidth is

12 Mc.—P. E. Kolk and I. A. Maloff, *The Field-Effect Transistor as High-Frequency Amplifier*, *Electronics*, 37:31, p 71-74.

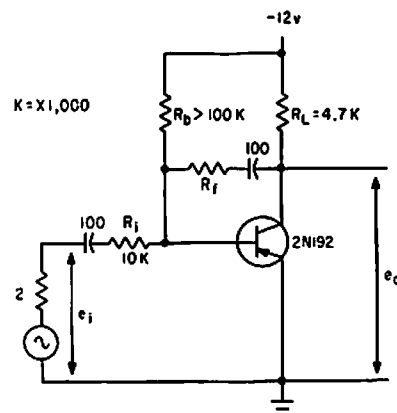


LOW-SUPPLY-VOLTAGE VHF—Good high-frequency parameters of epitaxial mesa transistor give high gain and efficiency at supply voltage of only 12 v. Output is 0.5 w at 70

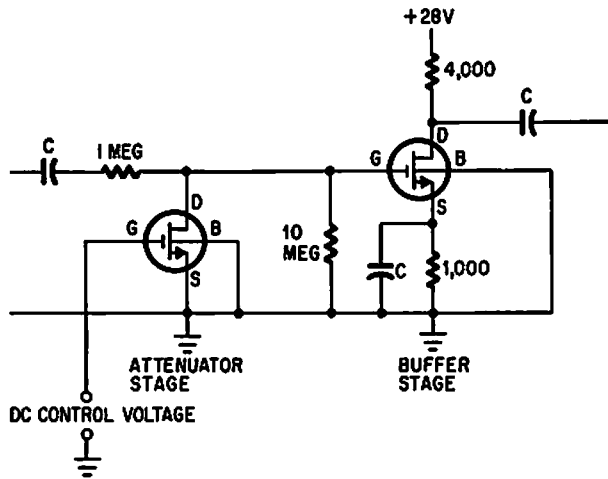
Mc.—D. Hall, *Using Epitaxial Transistors in Switching and R-F Circuits*, *Electronics*, 34:13, p 52-53.



REMOTE GAIN CONTROL—Permits adjusting gain of wideband amplifier over full range from maximum to zero with two-wire low-voltage line up to 1,000 feet long. Control and signal circuits are completely isolated. Components shown give maximum gain of 1.—R. S. Young, *Amplifier with Remote Gain Control*, *EEE*, 12:8, p 71.

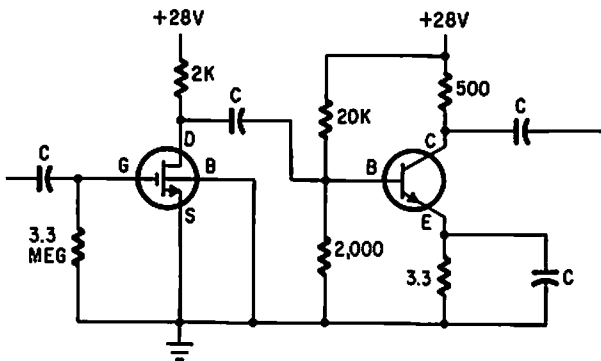


NONLINEAR FEEDBACK LOOP—Type WE41A copper-oxide varistor for R_f in feedback loop gives radically different voltage-gain characteristic than silicon diode for R_f, but both give exponential response and increase dynamic range.—J. C. Looney, *Designing Amplifiers with Nonlinear Feedback*, *Electronics*, 34:13, p 46-49.



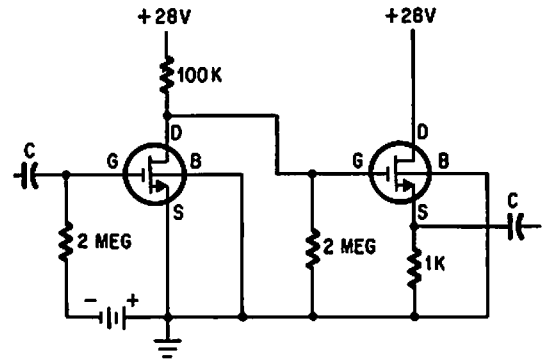
VOLTAGE-CONTROLLED ATTENUATOR—Can attenuate input signals 70 db when mos fet is followed by high-impedance load such as

common-source mos fet amplifier.—D. M. Griswold, *Understanding and Using the MOS FET*, Electronics, 37:31, p 66-70.

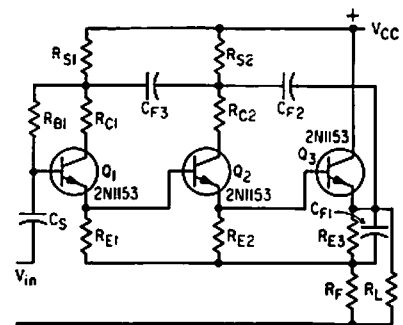


MOS FET WITH NPN—FET input stage serves as high-to-low impedance transformer for power transistor and gives very high power

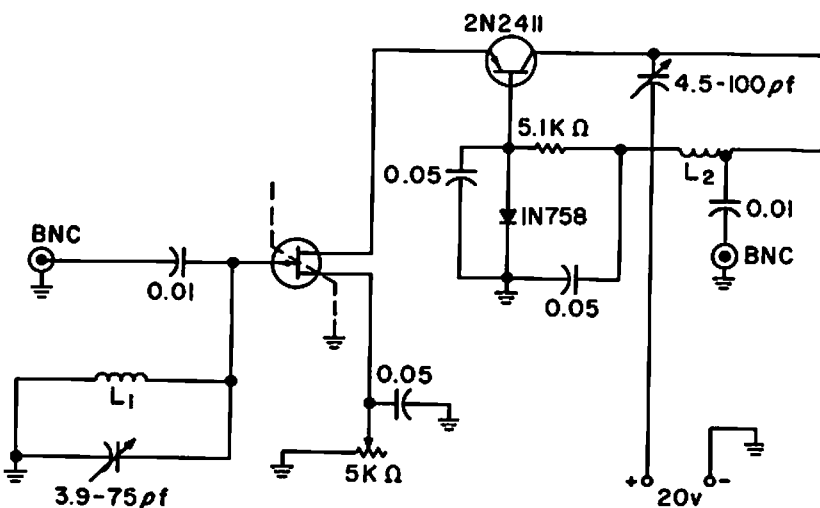
gain.—D. M. Griswold, *Understanding and Using the MOS FET*, Electronics, 37:31, p 66-70.



DIRECTLY COUPLED MOS FET—Eliminates coupling capacitors but requires additional bias supply.—D. M. Griswold, *Understanding and Using the MOS FET*, Electronics, 37:31, p 66-70.

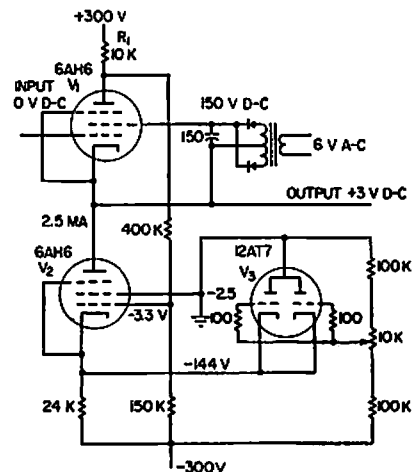


100-MEG INPUT IMPEDANCE—Variation of Darlington connection gives low-noise amplifier with high input impedance. Thermal and shot noise are much lower than flicker, leakage, and surface noise.—I. Levine, *High Input Impedance Transistor Circuits*, Electronics, 33:36, p 50-52.

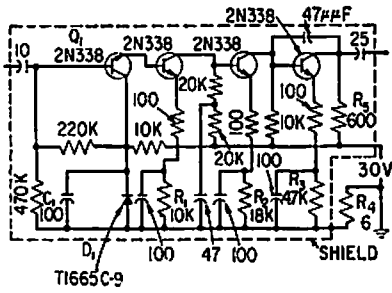


TUNED 10-MC AMPLIFIER WITHOUT NEUTRALIZATION—Low reverse transfer of cascode connection makes possible stable operation of common-source fet. Transducer gains are 20.6

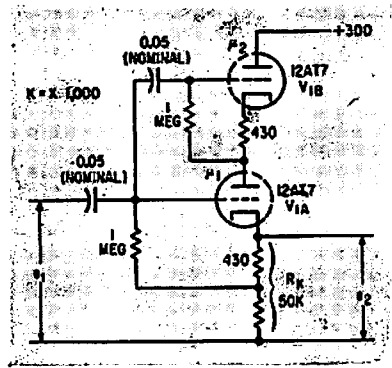
db and 25.3 db for 2N2497 and 2N2499 respectively.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 142.



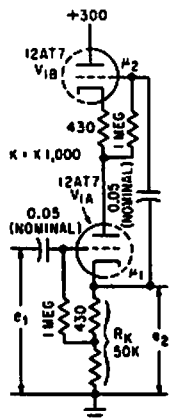
CATHODE FOLLOWER WITH 0.99963 GAIN—Used to couple high-impedance source to low-impedance output without attenuating or loading source signal. Output impedance is 50 ohms and response is flat within 3 db from d-c to 250 kc. Circuit delivers outputs from -140 to +210 v at -0.8 to +2 ma. Feedback through pentode helps maintain unity gain.—Cathode-Follower Gain Approaches Unity, Electronics, 31:1, p 94-96.



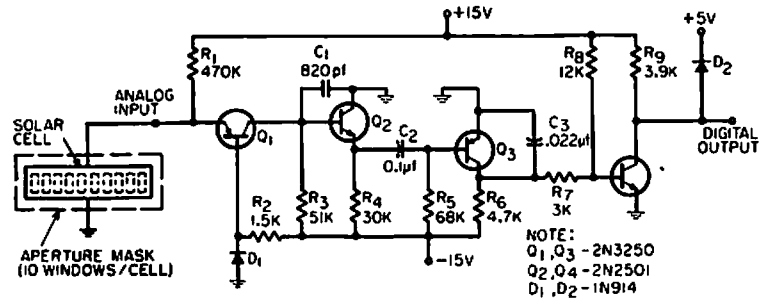
STABILIZING GAIN—Use of negative feedback in four-stage direct-coupled amplifier keeps gain constant within 1 db of 40 db from 6 cps to 300 kc. Input impedance is 8 meg and output impedance is 600 ohms.—Feedback Increases Input Impedance, *Electronics*, 32:11, p 150-153.



ISOLATION AMPLIFIER—Gain stability and input impedance are much better than conventional cathode follower. Feedback capacitor goes between triode grids, but may also go between grid of V1B and cathode of V1A.—G. M. Davidson and R. F. Brady, Unity-Gain Amplifier Offers High Stability, *Electronics*, 33:9, p 66-67.

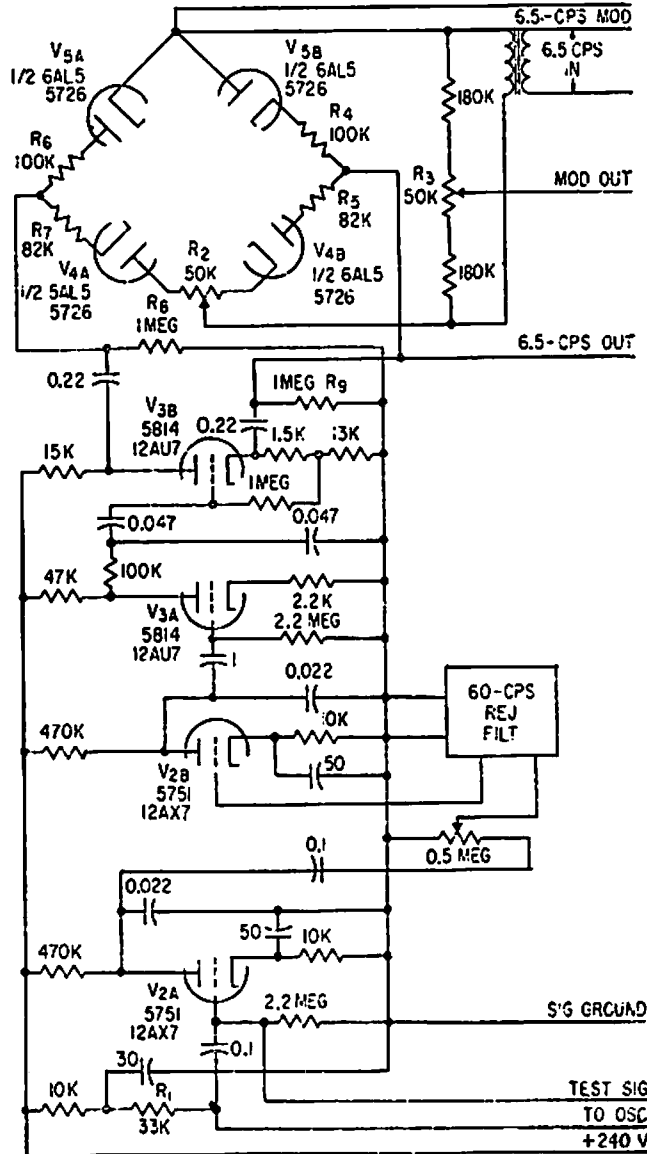


ISOLATION AMPLIFIER—Effective gain stability factor is approximately equal to reciprocal of product of amplification factors of two halves of tube. Gives high transmission accuracy, with high input impedance.—G. M. Davidson and R. F. Brady, Unity-Gain Amplifier Offers High Stability, *Electronics*, 33:9, p 66-67.



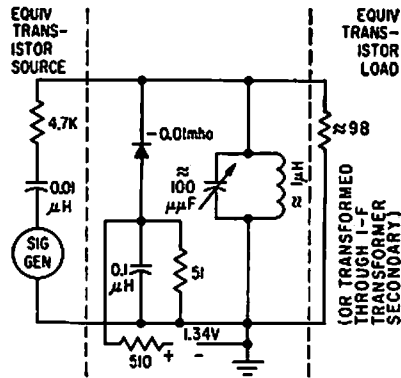
SOLAR-CELL AMPLIFIER—Used with multi-aperture solar cells to generate 10 strobe pulses. Eight circuits with cells are needed to generate 80 strobe pulses for reading

conventional punched card.—G. R. Hoarn, Multi-Aperture Solar Cell Amplifier, *EEE*, 14:4, p 43-44.

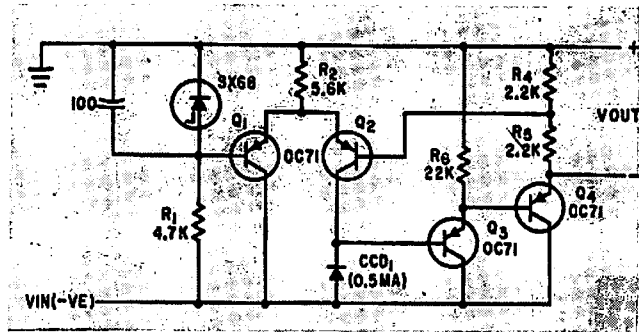


6.5-CPS AMPLIFIER—Consists of three triode stages (V2A, V2B, and V3A). Peaks at about 6.5 cps, with 18 db attenuation at each octave. Double-T rejection filter between V2A and V2B attenuates any 60-cps pickup. Overall gain is 80 db. Phase inverter V3B provides 180° out-of-phase signal for full-wave

phase-sensitive bridge rectifier that uses reference signal. Output is rectified error signal for infrared analyzer used to detect leaks in automobile air-suspension systems.—P. G. Balke, Infrared Finds Auto Suspension Leaks, *Electronics*, 31:49, p 82-85.

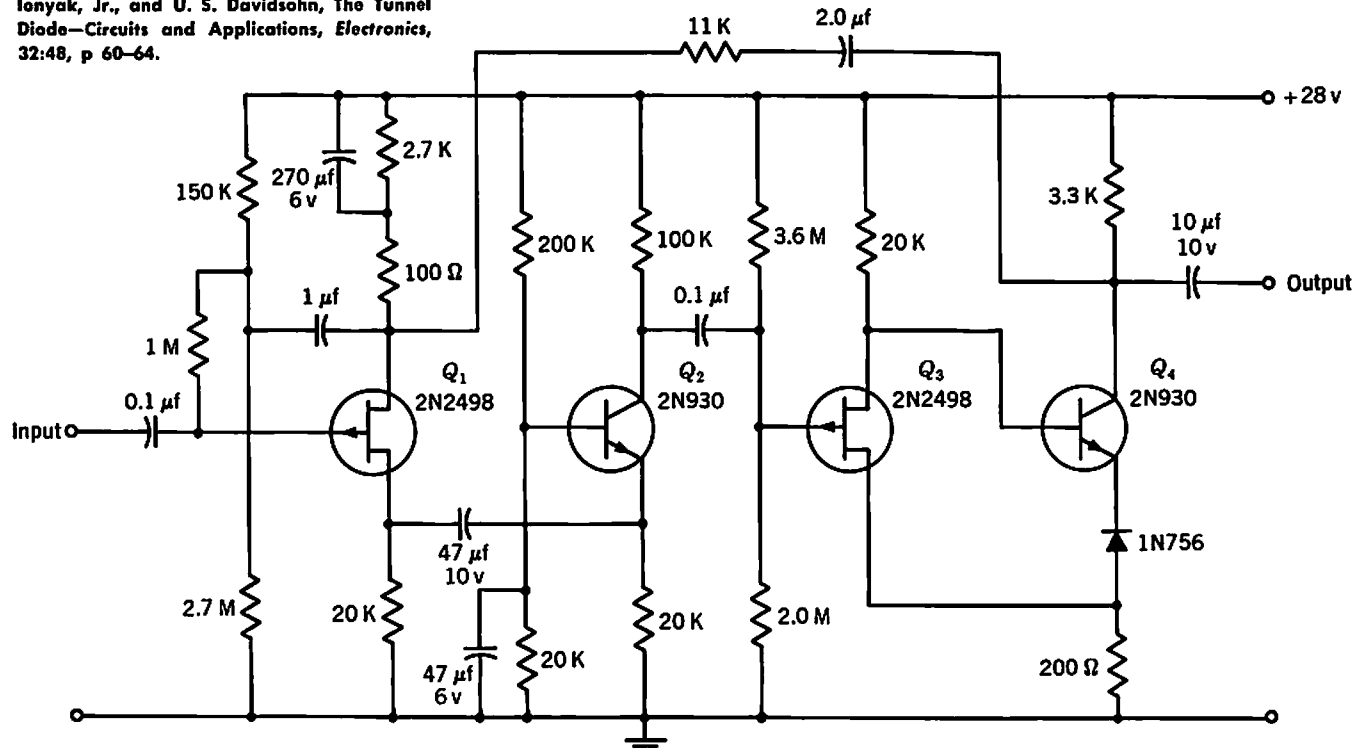


445-KC TUNNEL-DIODE AMPLIFIER—Has approximately 20 db gain.—I. A. Lesk, N. Holonyak, Jr., and U. S. Davidsohn, *The Tunnel Diode—Circuits and Applications*, *Electronics*, 32:48, p 60-64.



HIGH-GAIN VOLTAGE STABILIZER—Use of constant-current diode as collector load increases overall gain of amplifier from 500 to

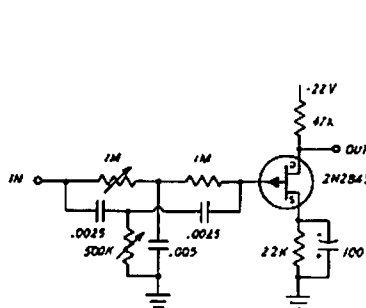
about 700.—T. K. Hemingway, *Applications of the Constant-Current Diode*, *Electronics*, 34:42, p 60-63.



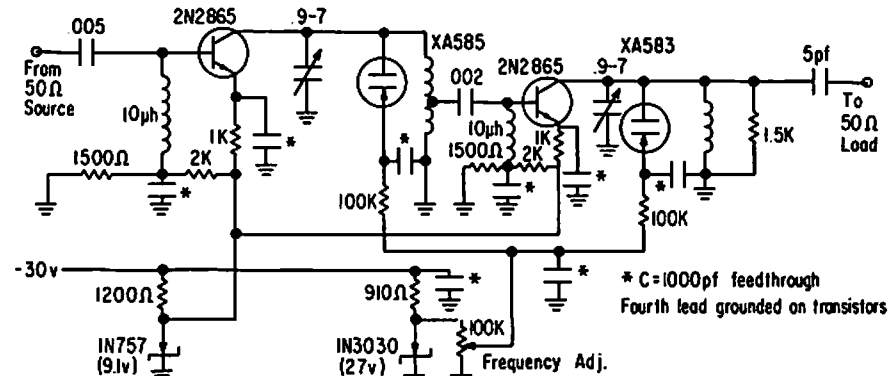
HIGH-INPUT-IMPEDANCE A-C AMPLIFIER—Gives input impedance of 30 meg without sacrificing bandwidth or noise performance. Voltage gain is 40 db. Technique involves

bootstrapping for Q1 and using fixed bias for its gate. Q2 is operated grounded-base to reduce Miller capacitance of field effect at high frequencies.—Texas Instruments Inc.,

"*Transistor Circuit Design*," McGraw-Hill, N.Y., 1963, p 520.

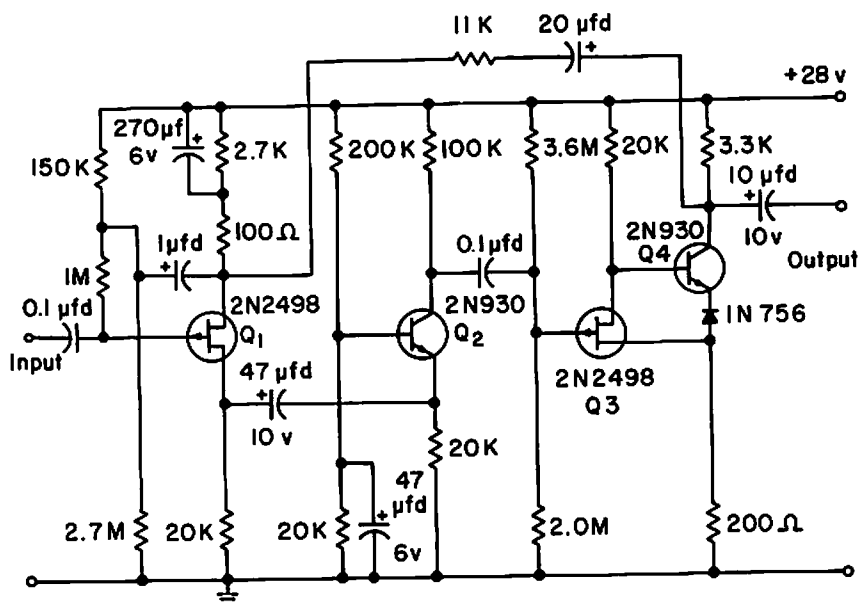


60-CPS BANDSTOP AMPLIFIER—With fet as output buffer to give near-infinite load impedance, twin-T notch filter gives up to 50 db attenuation at notch frequency.—FET's and RC Networks (Siliconix ad), *Electronics*, 39:4, p 71.



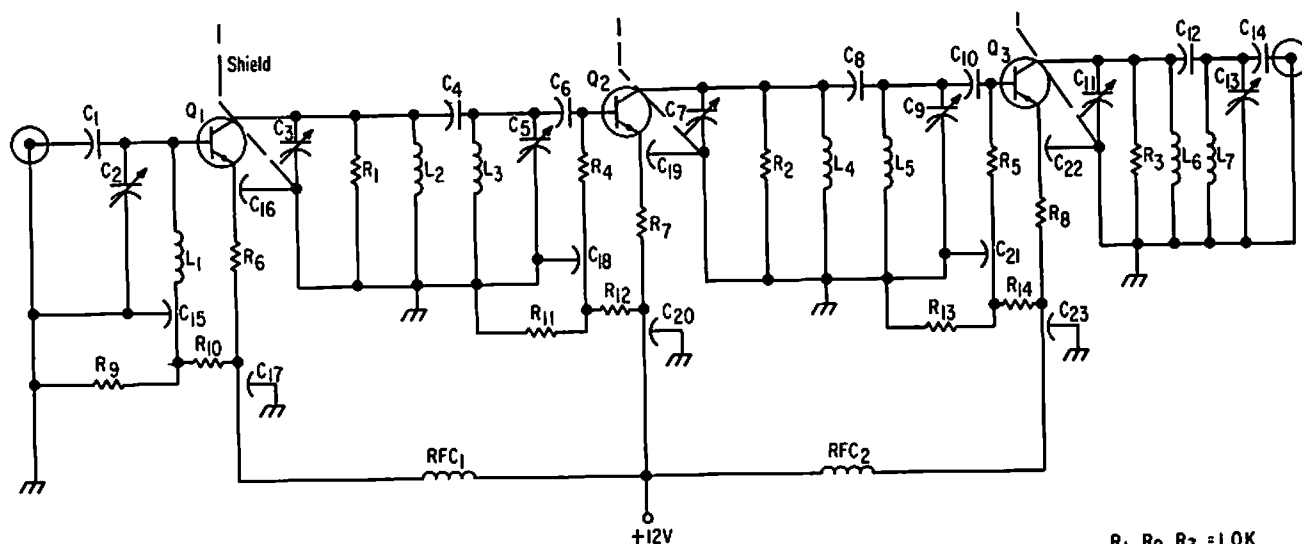
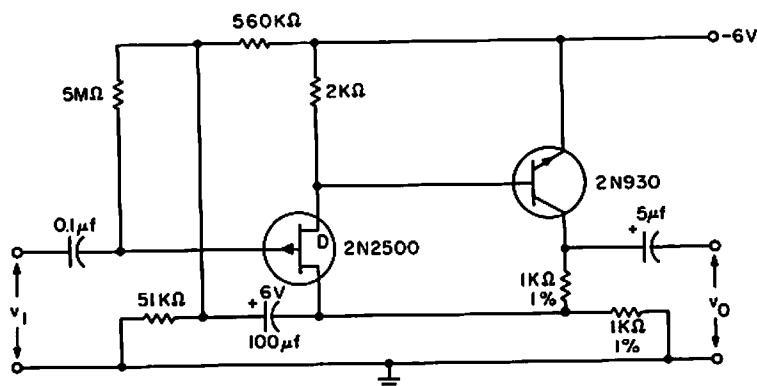
VOLTAGE-TUNED 60 TO 90 MC—Gives over 40 db gain with 50-ohm source and load. Untuned input allows constant source impedance over tunable frequency range of silicon

XA585 voltage-variable capacitance diodes, with excellent stability and tracking.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 297.



A-C COUPLED CASCODE—Circuit is uniquely suited to increasing bandwidth of low-noise amplifier by reducing Miller effect and permitting independent adjustment of operating conditions for optimum noise performance.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 143.

BOOTSTRAPPED FET SOURCE-FOLLOWER—Gives 6 db gain for high-impedance transducer output, when 5-meg gate bias resistor is bootstrapped to the source through 100-mfd electrolytic.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 67.



$Q_1 - Q_3 = 2N2996$
 $C_1 = 50\text{ pf}$
 $C_2 = 9-180\text{ pf}$
 $C_3, C_5, C_7, C_9, C_{11}, C_{13} = 9-180\text{ pf}$
 $C_4, C_8, C_{12} = 12\text{ pf}$

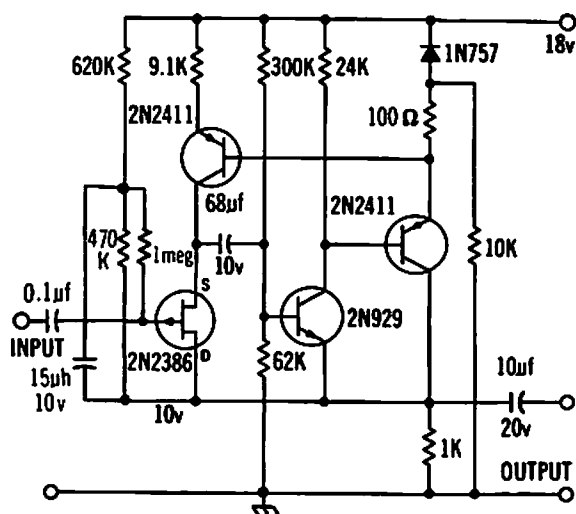
$C_6, C_{10}, C_{14} = 25\text{ pf}$
 $C_{15} \text{ to } C_{23} = 0.0015\text{ µf}$
 $\text{RFC}_1 - \text{RFC}_2 = 15\text{ µh}$
 $L_1 \text{ to } L_7 = 0.27\text{ µh}$

$R_1, R_2, R_3 = 1.0\text{ K}$
 $R_4, R_5 = 100\text{ OHMS}$
 $R_6, R_7, R_8 = 1.5\text{ K}$
 $R_9 \text{ to } R_{14} = 3.0\text{ K}$

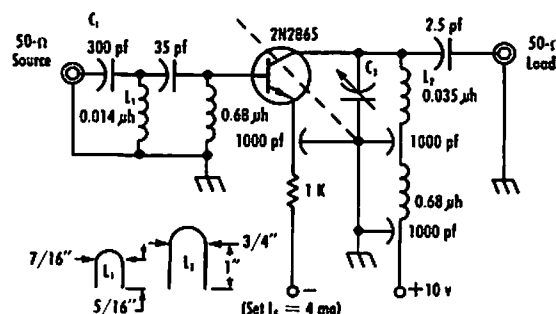
30-MC DOUBLE-TUNED—Proper loading provides good stability along with gain of 21 db per stage, despite inherent instability of

2N2996 at this frequency. Total power gain is thus 63 db, for noise figure of 2.3 db and bandwidth of 3 Mc.—Texas Instruments Inc.,

"Solid-State Communications," McGraw-Hill, N.Y., 1966, p 310.

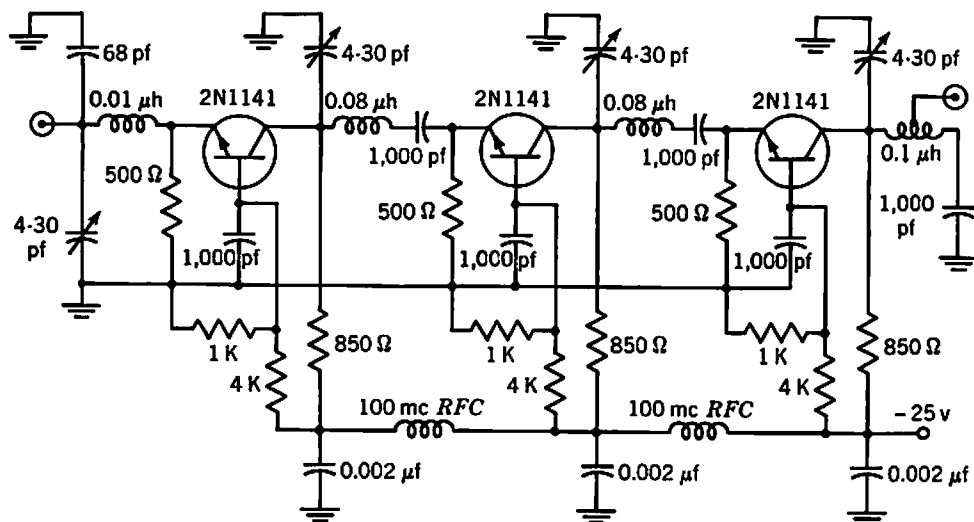


WIDEBAND UNITY-GAIN FET—Input impedance is about 100 meg, and response is within 3 db from below 10 cps to 1 Mc for 100K generator resistance.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 296.



C_1 = 300-pf undipped ceramic capacitor
Bypass capacitors = Aerovox Hi-D EF4, 1000 pf, 1000 v
 C_2 = 1.8-13 pf, set at 7 pf
 L_1 = 0.014 μ h: copper strip, 1/32" x 5/16", bent to 7/16" diam. Q_1 = 200
 L_2 = 0.035 μ h: copper strip, 1/32" x 3/8", bent to 3/4" diam. Q_2 ~ 300

250-MC R-F FOR MILITARY VHF BAND—Gives 12.5 db gain and noise figure of only 5 db, with excellent stability.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 297.

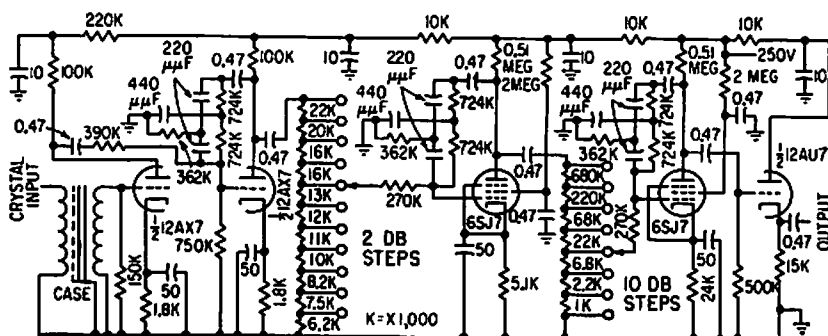


0.08 and 0.1 μ h coils are 3 and 4 turns respectively of No. 18 tinned bus wire, $\frac{1}{4}$ " diameter
0.01 μ h coil is 1 turn of NO. 00 enameled wire, $\frac{3}{8}$ " diameter.

100-MC CASCADED—Uses mismatch design technique to obtain desired overall gain of

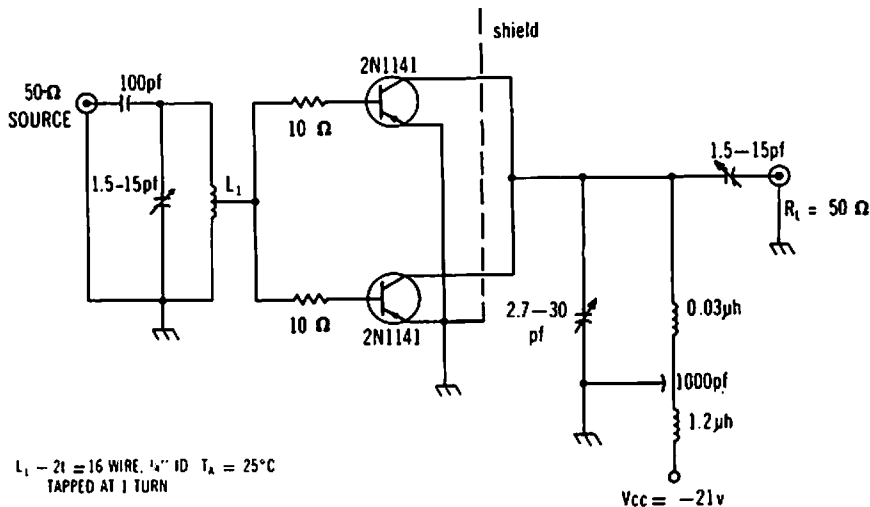
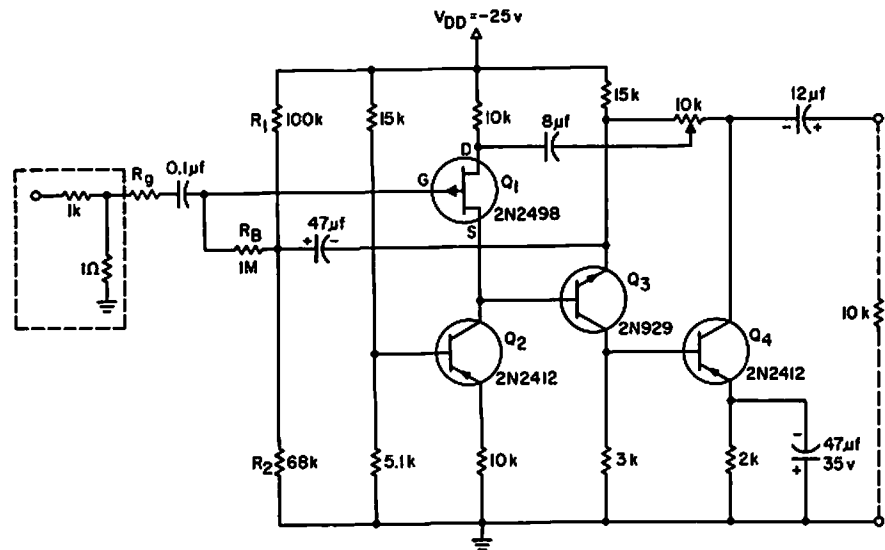
37.5 db at bandwidth of 9 Mc for three cascaded stages.—Texas Instruments Inc.

"Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 290.



WIDEBAND AMPLIFIER—Twin-T amplifier is used between crystal detector and cre of microwave spectrometer for studying electron resonance phenomenon in paramagnetic materials.—R. R. Unterberger, Microwave Spectrometer Tests Electron Resonance, *Electronics*, 32:11, p 142-144.

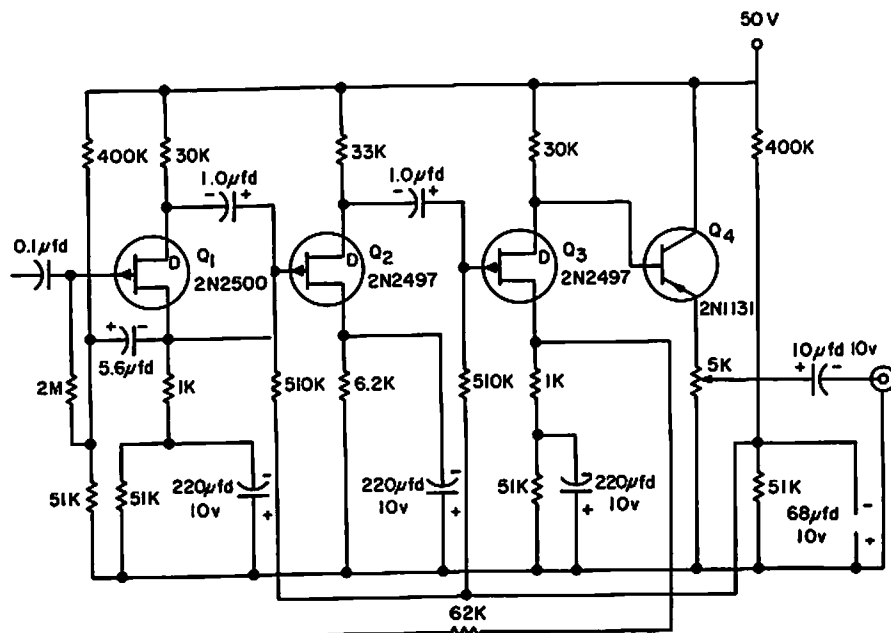
DOUBLE-BOOTSTRAPPED FET—Both drain and source are bootstrapped in 6-db high-input-impedance amplifier, to reduce effect of fat capacitances so they are insignificant compared to stray circuit capacitances at input terminal. 10-K pot provides gain compensation adjustment.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 69.

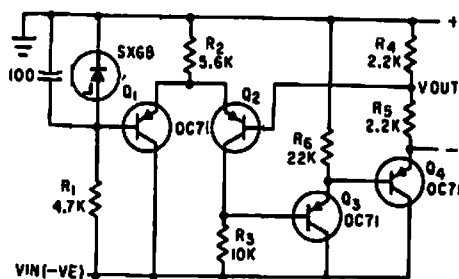


173-MC POWER AMPLIFIER—Uses two 2N-1141's in parallel to deliver average of 400 mw, with power gain of 11.5 db and collector efficiency of 42%. Has excellent large-signal performance.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 320.

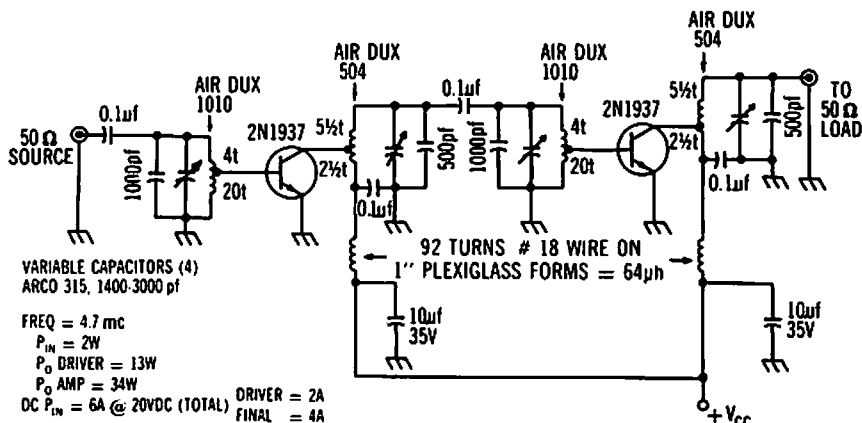
$L_1 - 21 \pm 16$ WIRE, $\frac{1}{4}$ " ID $T_A = 25^\circ\text{C}$
TAPPED AT 1 TURN

THREE-FET A-C AMPLIFIER—Can be used in applications requiring amplification of micro-volt signals, as in ultrasensitive preamps for null detectors, medical research equipment, recorders, oscilloscopes, and low-level transducers. With 100K generator resistance, amplifier 3-db bandwidth is 1 cps to 40 kc.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 107.



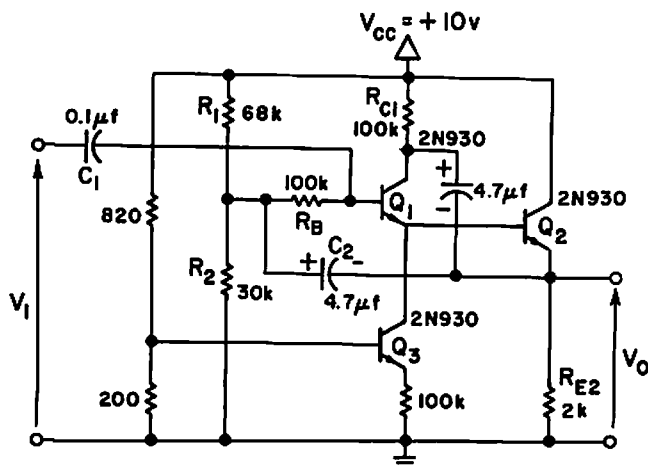


SIGNAL VOLTAGE STABILIZER—Main amplifier is followed by emitter-followers to reduce fraction of d-c load current flowing in collector load of amplifier. In this conventional arrangement, overall gain is only about 500. —T. K. Hemingway, *Applications of the Constant-Current Diode*, *Electronics*, 34:42, p 60-63.

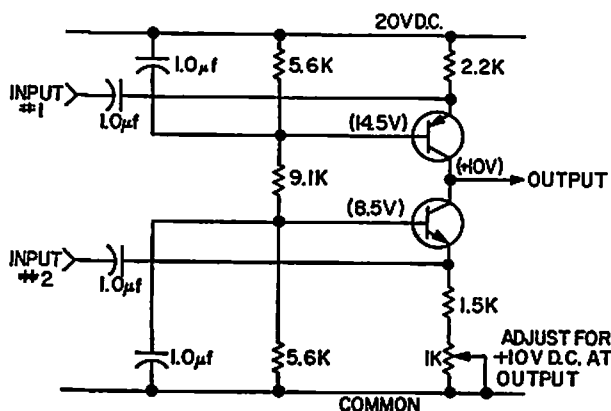


4.7-MC DRIVER AND AMPLIFIER—Common-emitter connection gives good power gain with collector-emitter voltage of 20 v, though

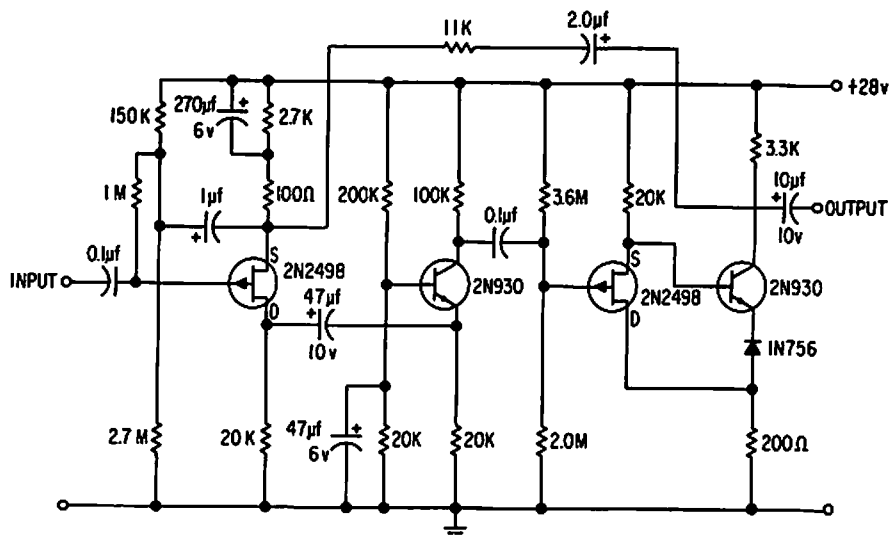
gain varies with frequency.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 316.



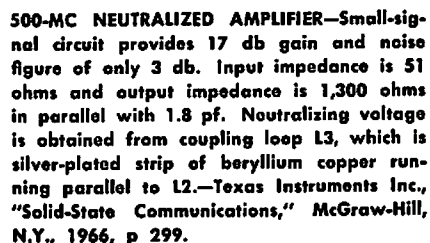
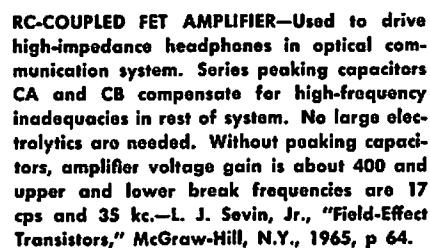
CASCADED EMITTER-FOLLOWER WITH CURRENT BIAS—Improved frequency response is obtained by biasing Q1 with current generator in emitter leg. Input impedance is 6 meg. Frequency response is within 3 db from 10 cps to 1 Mc.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 181.



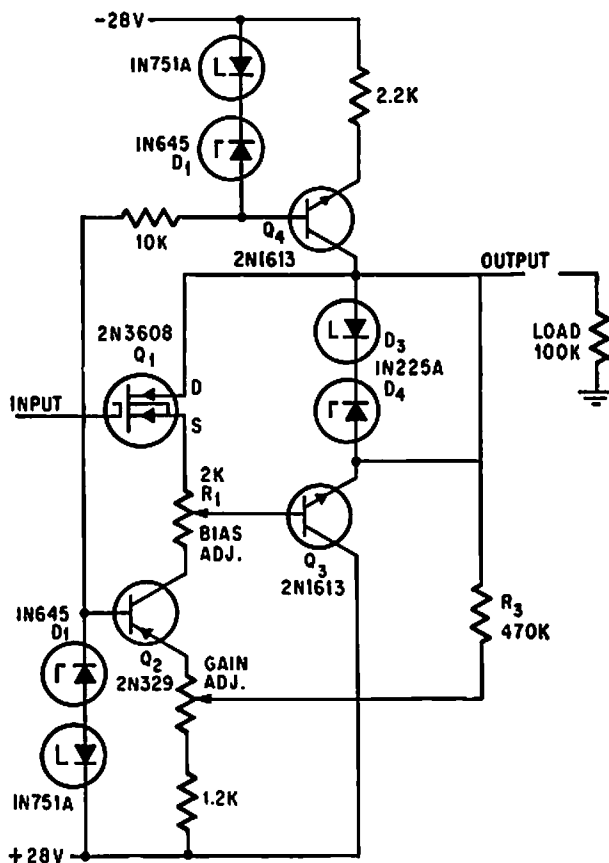
HIGH VOLTAGE GAIN—Provides gain of 3,500 into 10-meg load, by operating transistor in common-base configuration and using constant-current source in collector circuit. Circuit is balanced, so either transistor can operate as gain stage while other serves as current source. Feedback overcomes problem of critical d-c bias.—A. J. Adler, *High-Gain Amplifier*, *EEE*, 11:8, p 31.



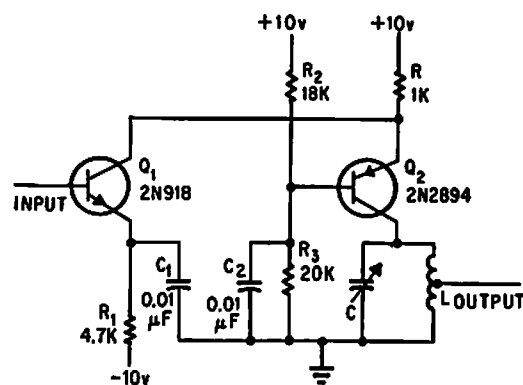
LOW-LEVEL 1 CPS TO 500 KC—Features include input impedance above 30 meg and noise figure below 3 db over wide range of generator resistances. Bootstrapping of input stage enhances high input impedance of fet. Will operate down to 1 cps without need for large capacitors. Upper frequency limit is 500 kc for generator resistance of 100K. Voltage gain is stable within 0.5 db of 40, from -55 to +125°C.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 290.



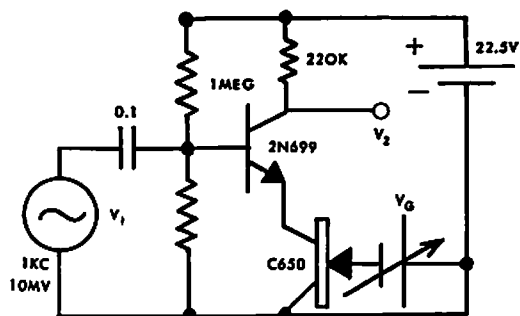
L₁=SILVER-PLATED BRASS ROD-1 9/16" LENGTH, 1/4" DIA.
L₂=SILVER-PLATED BRASS ROD-2 1/8" LENGTH, 1/4" DIA.



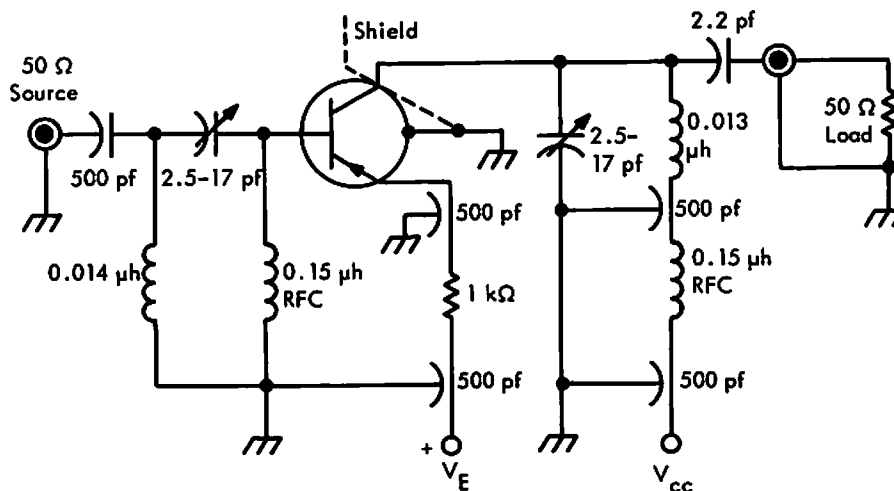
INFINITE INPUT RESISTANCE—Uses metal oxide semiconductor field effect transistor as buffer to give input impedance of 10^{15} ohms. Amplifier gain is unity within 0.1% for 100K load. When adjusted, output equals input: within 10 mv over allowable range of 10 v above or below ground.—A. D. Delagrangé. Amplifier Provides 10^{15} -Ohm Input Resistance, *Electronics*, 39:17, p 99.



STABLE 600-MC CASCODE—Gives high gain without external capacitor to neutralize negative feedback of collector-base junction.—M. D. Wood, Cascode Amplifier Stabilized by Reducing Internal Feedback, *Electronics*, 38:11, p 70.



GAIN-CONTROLLED LOG AMPLIFIER—Based on fact that gain of common-emitter fet stage is almost inversely proportional to emitter resistance, and resistance of fet operating below cutoff is linear function of grid voltage. Can be used as age amplifier and as multiplier.—Y. J. Lubkin, Gain Controlled Log Amplifier, *EEE*, 10:9, p 91.



450-MC R-F AMPLIFIER—Gives average power gain of 8.6 db, bandwidth of 48 Mc, and noise figure of 6 db. Uses linear active net-

work, designed with Linvill chart. Lead inductance was minimized by removing most of the Teflon from TO-18 socket so only thin

disk, approximately chassis thickness, remains.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 97.

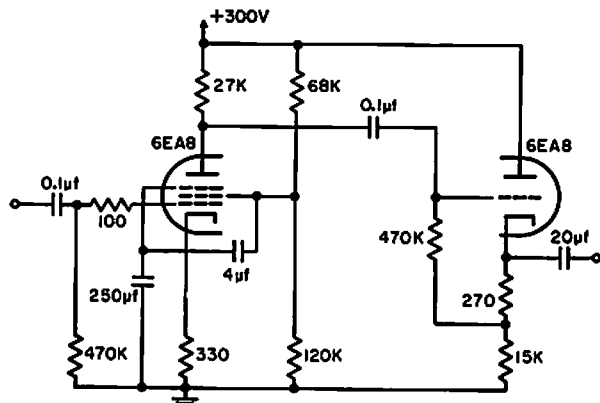
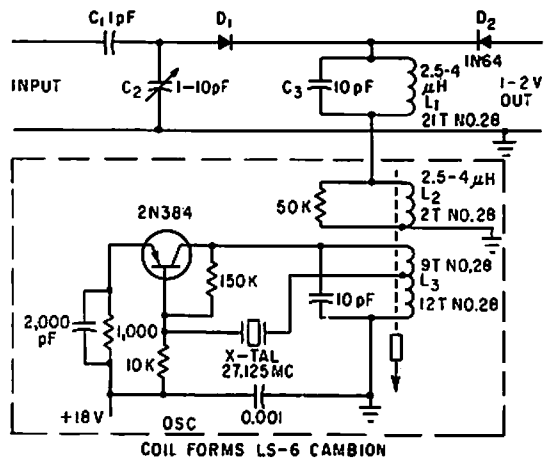
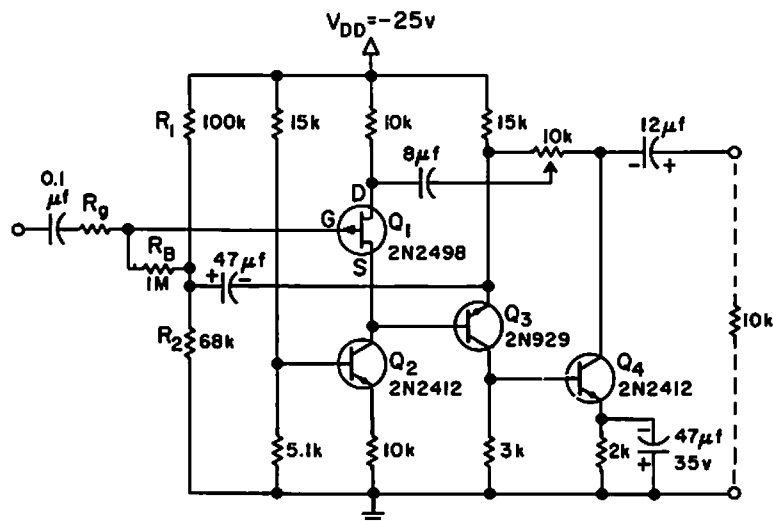


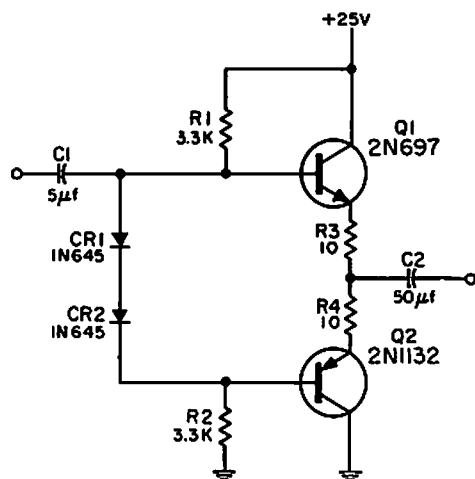
PLATE-CATHODE FOLLOWER—Has low output impedance, good gain stability, wide response, and low distortion, making it ideal as bridge driver for Wien-bridge oscillator. —K. H. Liu, Plate-Cathode Follower Wien-Bridge Oscillator, *EEE*, 11:2, p 27.



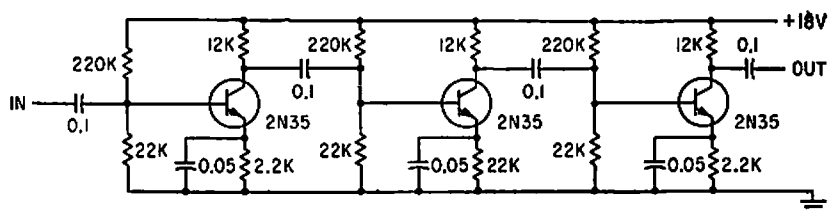
HIGH-IMPEDANCE PARAMETRIC AMPLIFIER—Wide-band amplifier uses diode biased with r-f to give 40-db gain. Input impedance is above 10 gigohms, and frequency response is within 3 db from 3 cps to 200 kc. Diode D1 is energy storage element. Crystal-controlled transistor oscillator is pump frequency source.—D. Revati, Diode Amplifier Has Ten-Gigohm Input Impedance, *Electronics*, 34:51, p 38-40.



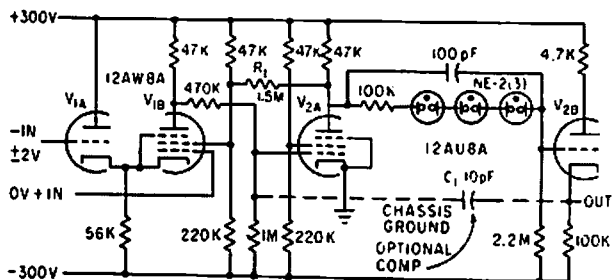
HIGH-INPUT-Z FET AMPLIFIER—Bias current is obtained from common-base current generator Q2. Q3 and Q4 function as complementary current multiplier. Bootstrapping for RB is obtained directly from emitter of Q3. Voltage gain is 2 and input impedance is 200 meg. Response is flat within 3 db from 1 to 500 kc with generator resistance of 1 meg.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 187.



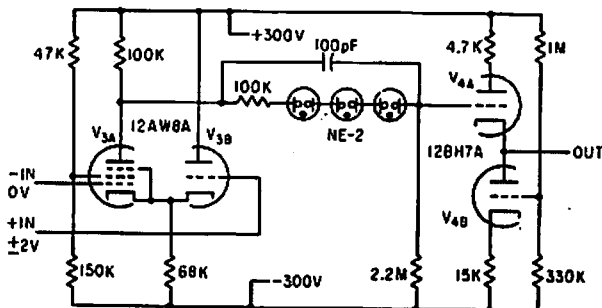
PREFERRED EMITTER-FOLLOWER—Used to match high-impedance circuit to low-impedance load. Will accept positive and negative pulses or sinusoidal input. Low output impedance for pulses results in high operating speed into capacitive loads. Bandwidth for 600-ohm source impedance is 50 cps to 3.5 Mc. Voltage amplification is 0.8 and power gain is 12 db.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, 1962, PSC 22 (originally PC 222), p 22-2.



100-DB AMPLIFIER—Used in distortion monitor to drive indicating vtvm.—G. H. Smith, *Distortion Monitor Checks Linear Amplifier Characteristics*, *Electronics*, 34:27, p 57-59.

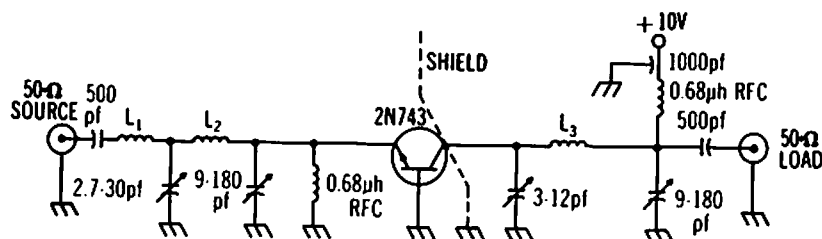
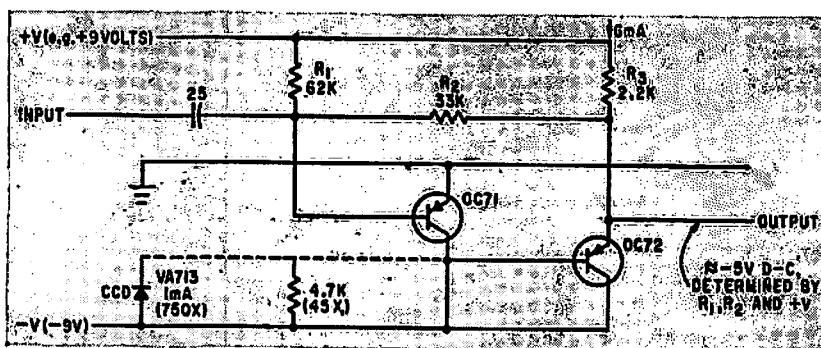


16-KC COMPENSATED OPERATIONAL AMPLIFIER—Uses pentode V2A as voltage amplifier to provide up to 140 v peak signals at grid of output cathode follower V2A. Will go up to 50 kc without compensation.—H. Koerner, How to Extend Operational Amplifier Response, *Electronics*, 33:46, p 90-91.



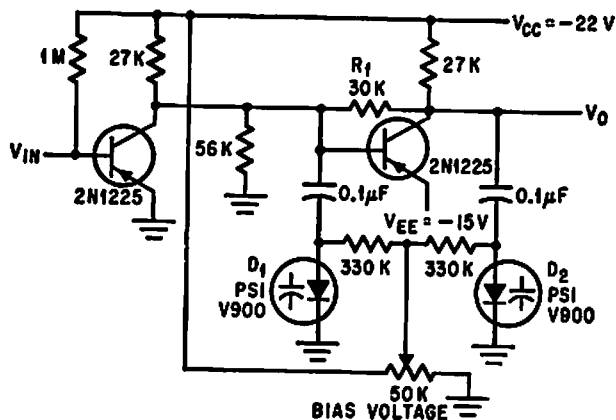
50-KC OPERATIONAL AMPLIFIER—Develops full rated output of 100 v into 10,000-ohm load up to 50 kc. Open-loop gain is 36 db.—H. Koerner, How to Extend Operational Amplifier Response, *Electronics*, 33:46, p 90-91.

CONSTANT-CURRENT DIODE AS COLLECTOR LOAD—Gain is 45 with 4.7K resistive load, and increases to 750 with CCD as load. Gain-reducing effect of external load paralleling CCD can be eliminated by using emitter-follower to isolate load from collector circuit.—T. K. Hemingway, Applications of the Constant-Current Diode, *Electronics*, 34:42, p 60-63.



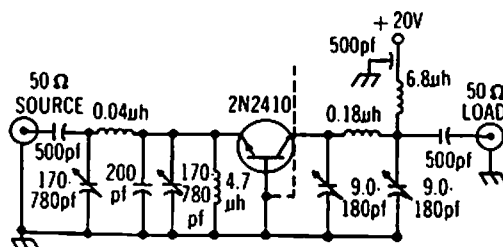
INDUCTANCE DATA		
COIL NO	AIR DUX NO	NO TURNS
L ₁	404	5
L ₂	408	2
L ₃	404	3

250-MC POWER AMPLIFIER—Gives good performance for both small and large signals.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 320.



ADJUSTABLE-BANDWIDTH AMPLIFIER—Bandwidth can be varied from 190 to 280 kc by varying bias voltage on varicap diodes between 0 and 10 v. Used in frequency-response equalization and other system appli-

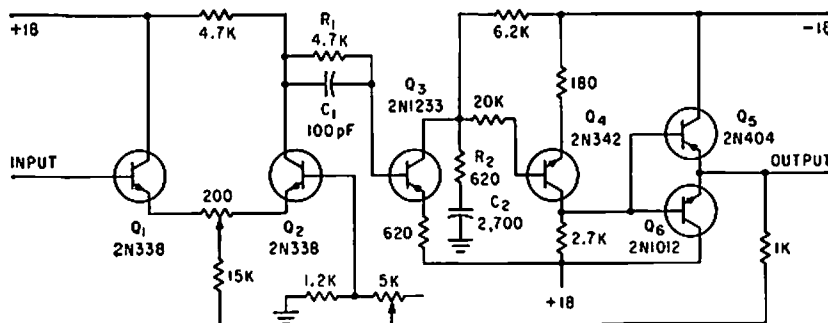
cations requiring automatically adjustable bandwidth in low-pass circuit.—M. G. Wilson, Low-pass Amplifier with Adjustable Bandwidth, *Electronics*, 39:11, p 90-91.



50-MC POWER AMPLIFIER—Power output is up to 1 w and collector efficiency above 50% for common-base operation.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 317.

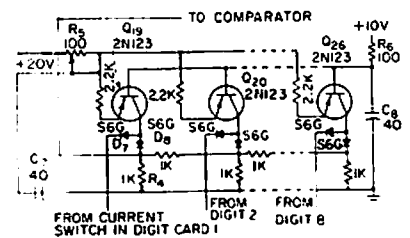
CHAPTER 3

Analog Circuits

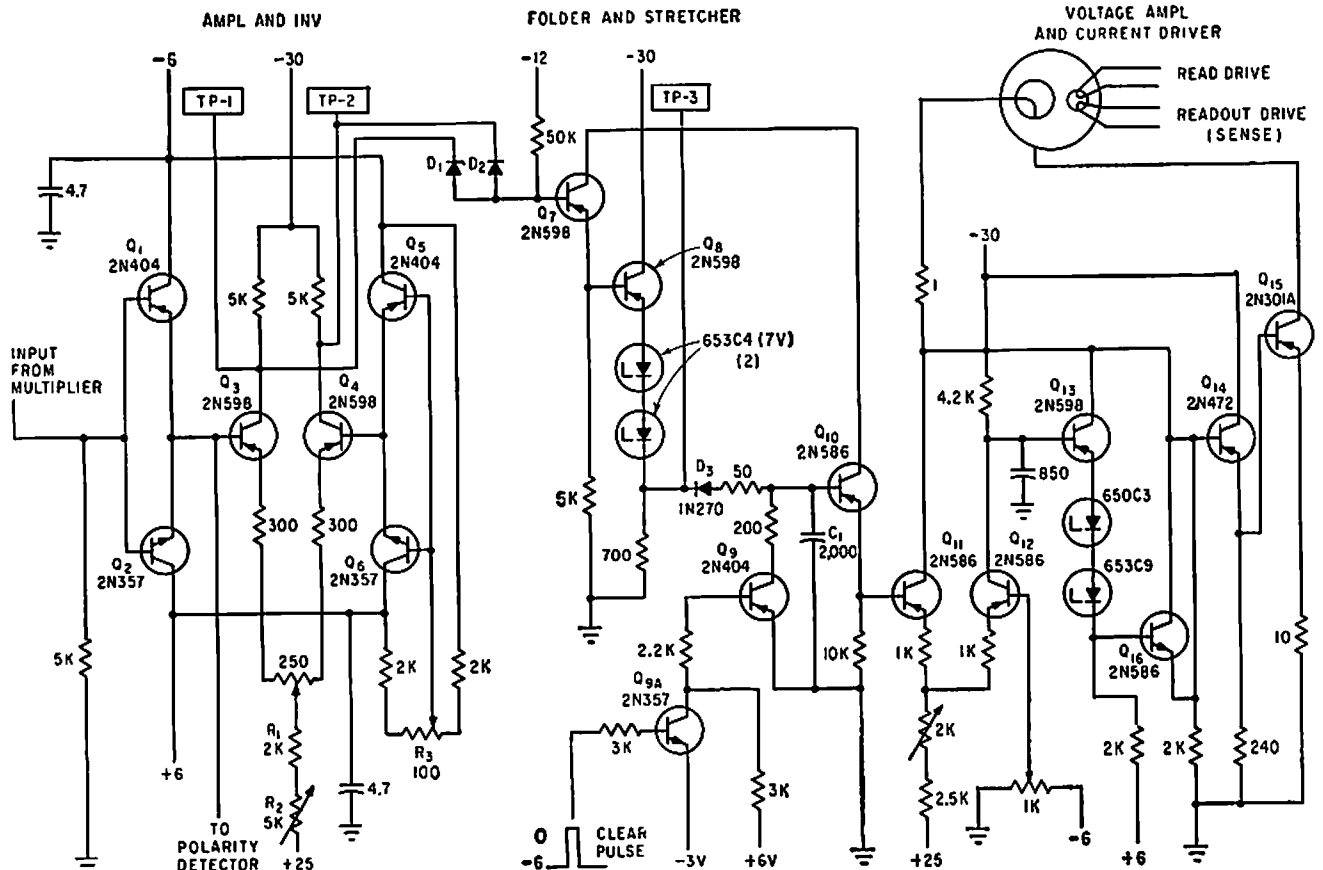


ANALOG DIFFERENTIAL AMPLIFIER—Input impedance is above 300,000 ohms and output impedance 1 ohm, in d-c amplifier for analog input channel. Q3 and Q4 provide gain and phase inversion for feedback through

complementary emitter-follower Q5-Q6 to differential amplifier Q1-Q2.—N. Aron and C. Granger, *Analog-To-Digital Converter Uses Transfluxors*, *Electronics*, 35:20, p 62-66.



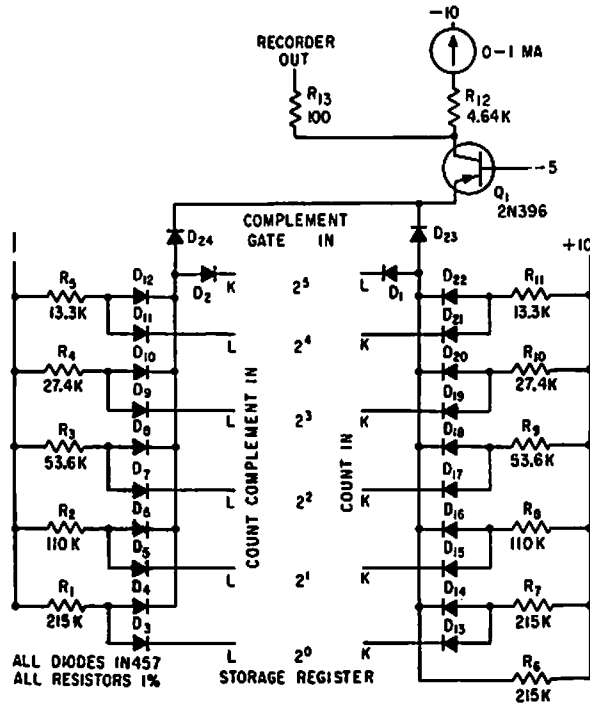
SUMMING AND WEIGHTING NETWORK—Consists of eight identical circuits. Precise value of direct current from constant-current source, fed into first node of resistive ladder network, produces 2.5-v step at summing and weighting network output, or half of maximum analog input of 5 v.—W. B. Towles, *Transistorized Analog-Digital Converter*, *Electronics*, 31:31, p 90-93.



INVERTER AND PULSE STRETCHER—Circuit takes sampled output of multiplexer and provides current required for driving trans-

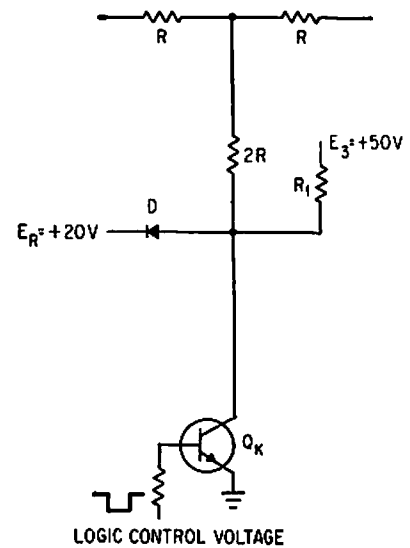
fluxor in analog-digital converter that produces six-bit binary Gray code.—N. Aron and C. Granger, *Analog-To-Digital Converter*

Uses Transfluxors, *Electronics*, 35:20, p 62-66.

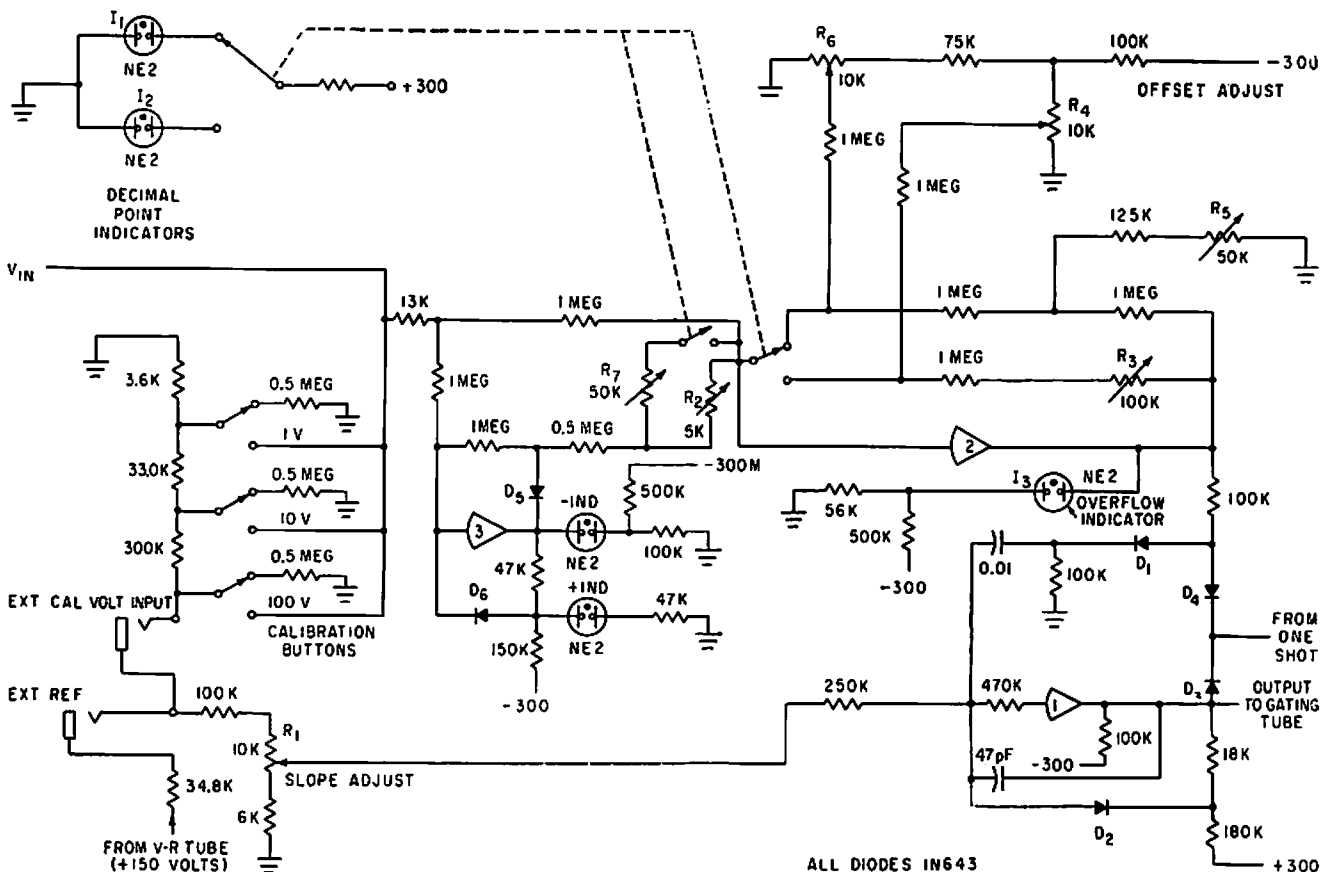


DIGITAL-ANALOG CONVERTER—Converts binary digits to analog form.—K. H. Brackney and D. R. Gosch, Pulse Comparator Circuit

Measures Frequency Jitter, *Electronics*, 34:27, p 54-56.



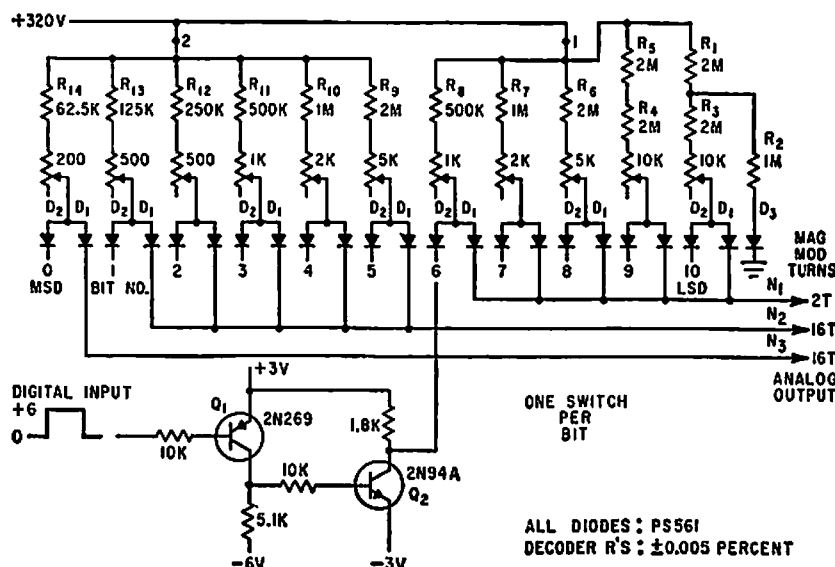
LADDER-TYPE NETWORK DECODER—Transistor replaces spdt switch for binary conversion of analog signal. Transistor's own saturation voltage (shown as ground for simplicity) serves as lower reference, while diode provides upper reference. Chief drawback is poor temperature stability.—C. R. Pearman and A. E. Popodi, How to Design High-Speed D-A Converters, *Electronics*, 37:8, p 28-32.



DIGITAL VOLTMETER CALIBRATOR—Calibration voltages of 100, 10, and 1 v are derived from reference voltage, for use in calibrating digital voltmeter in which analog

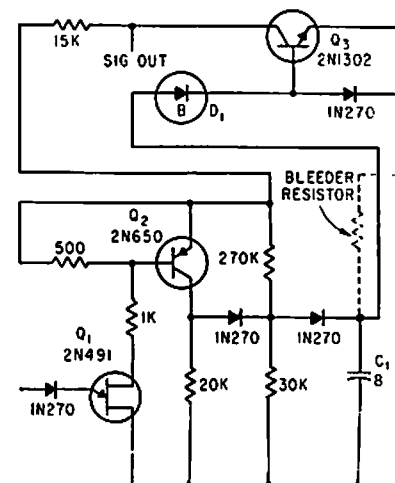
voltage is converted to pulse whose width is proportional to input amplitude. Used for gating clock pulses into digital counters for voltage readout.—B. Barker and M. McMa-

han, Digital Voltmeter Employs Voltage-To-Time Converter, *Electronics*, 34:18, p 67-69.

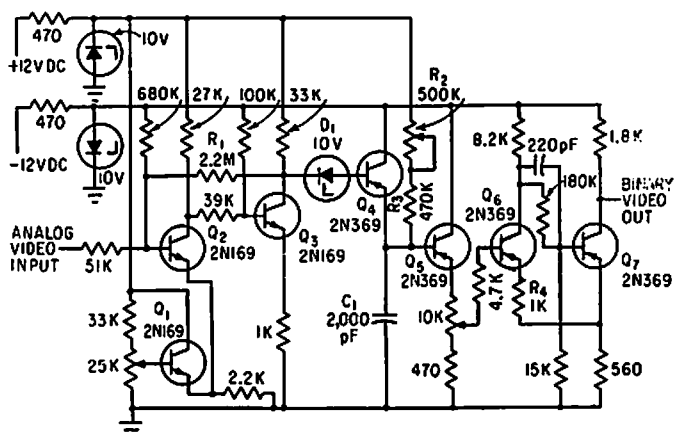


ELEVEN-BIT DECODER—Well-regulated transistor power supply and binary-weighted network of precision wire-wound resistors give high-precision conversion of 11-bit digital value to current analog. Output goes to magnetic modulator. Regulated 320-v supply

(not shown) uses silicon junction diodes in full-wave bridge, with silicon zener diode as reference.—N. Aron, Precise Converter Takes Current Analog of Digital Voltage Pulses, *Electronics*, 35:32, p 68-71.

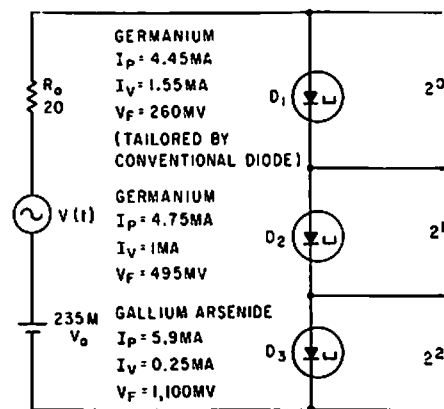


UJT CONVERTER FOR ACCELEROMETER—Converts digital output to voltage analog that indicates rate of acceleration or, when bleeder resistor is removed, actual distance traveled.—F. W. Kear, Unijunction Transistor Pulse-Circuit Design, *Electronics*, 35:21, p 58-60.

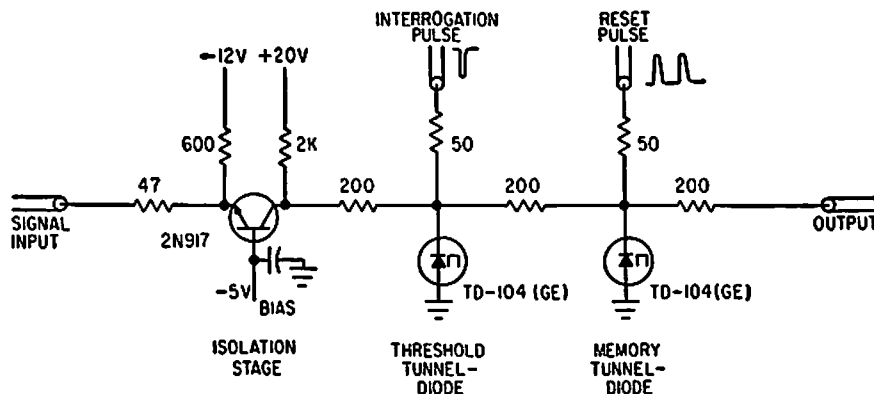


VIDEO PROCESSOR—Analog video input from closed-circuit tv microscope is converted to binary video output by difference amplifier Q1-Q2, squaring amplifier Q3, pulse shaper Q4, and emitter-follower Q5 which buffers

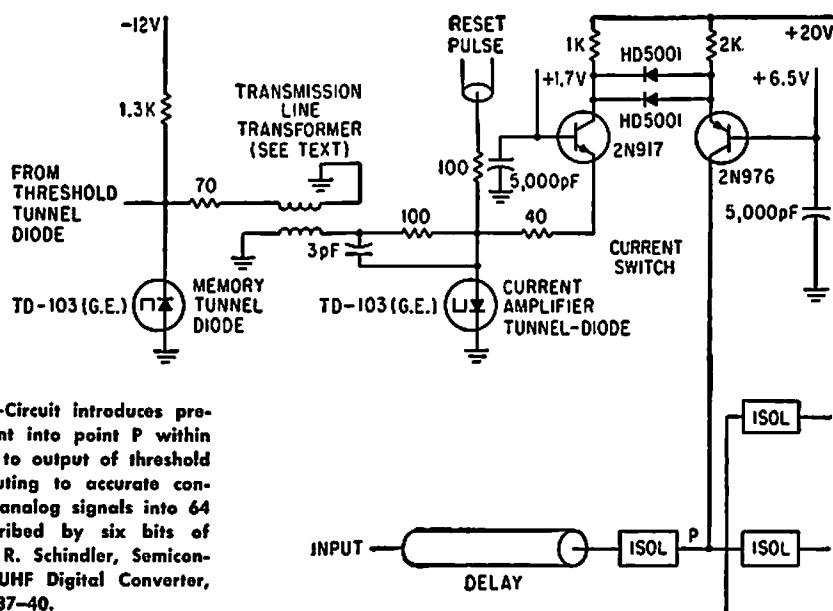
R-C network from Schmitt trigger Q6-Q7.—N. F. Izzo and W. Coles, Blood-Cell Scanner Identifies Rare Cells, *Electronics*, 35:17, p 52-57.



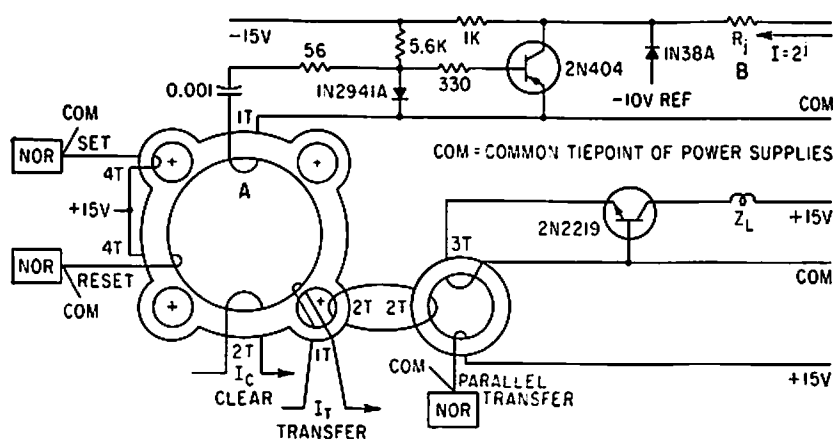
THREE DIODES PROVIDE 8 STATES—Three different tunnel diodes, each switching at a different input voltage level, act together to convert analog input voltage to digital form.—B. Rabinovici and J. Klapper, Designing Tunnel-Diode Circuits Using Composite Characteristics, *Electronics*, 35:7, p 46-48.



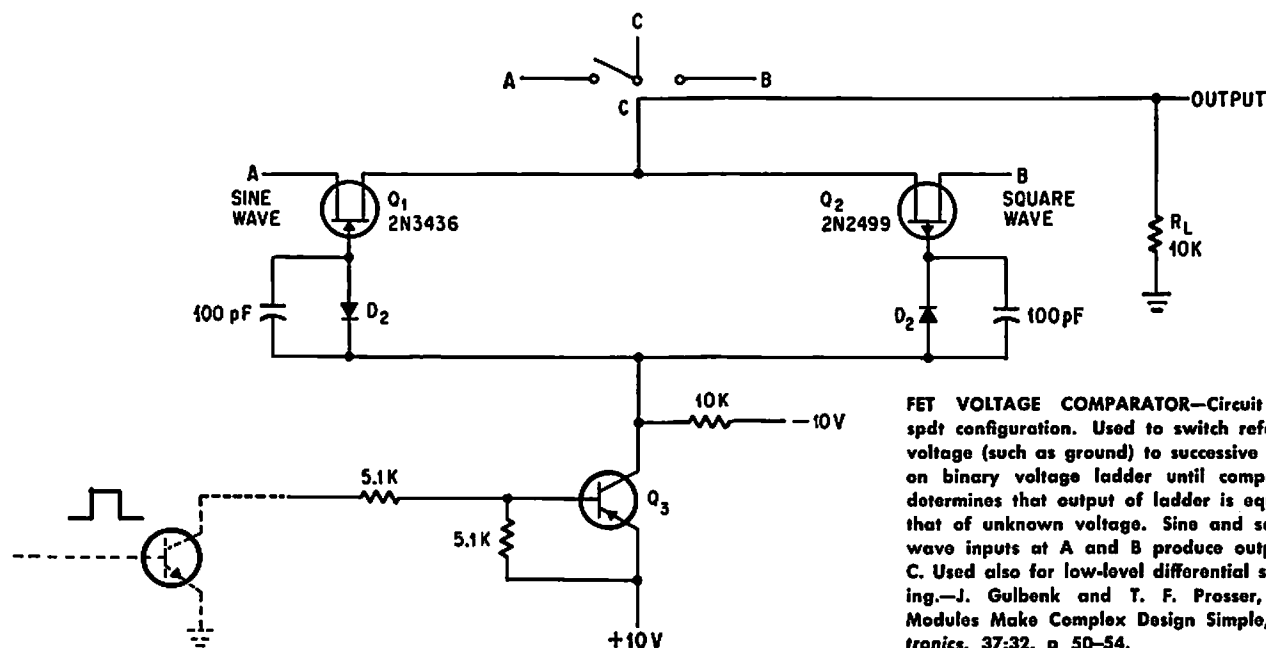
50-MC SAMPLING AND THRESHOLD CIRCUIT—Threshold tunnel diode receives signal current to be sampled and interrogation pulses repeating at 50 Mc. Diode fires when signal current is below threshold level, making memory tunnel diode switch into its high-voltage state. Current level differences of less than 50 microamp can be resolved in 0.3 nsec, sufficient for converting input into six bits corresponding to 64 levels.—H. R. Schindler, Semiconductor Circuits in a UHF Digital Converter, *Electronics*, 36:35, p 37-40.



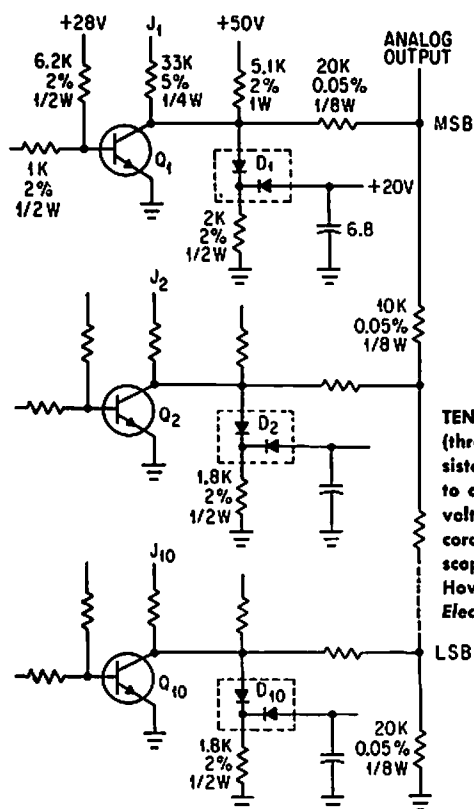
WEIGHTING CIRCUIT—Circuit introduces precise amount of current into point P within few nsec in response to output of threshold tunnel diode, contributing to accurate conversion of wideband analog signals into 64 levels that are described by six bits of binary language.—H. R. Schindler, Semiconductor Circuits in a UHF Digital Converter, *Electronics*, 36:35, p 37-40.



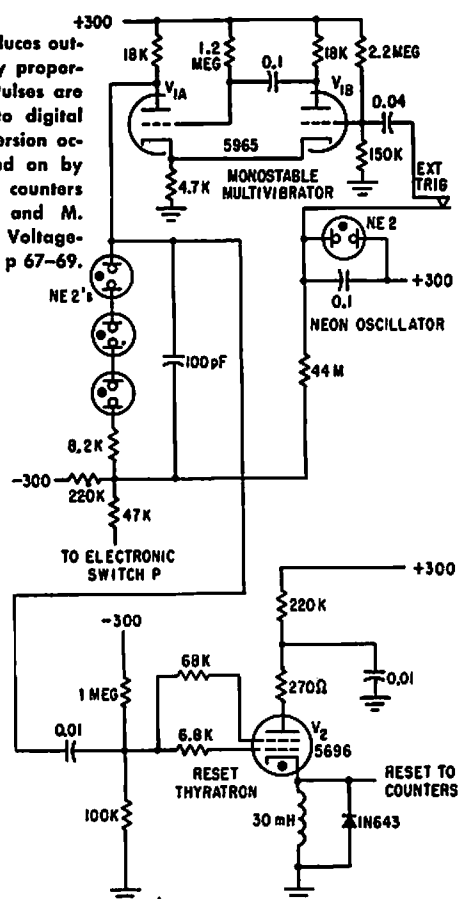
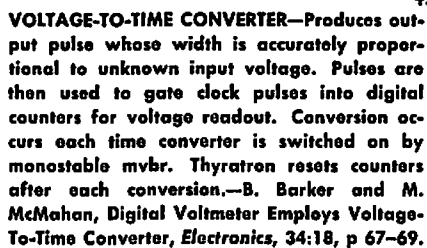
TUNNEL DIODES READ 5-APERTURE CORE— Circuit also controls switching of binary weighted current generators used in analog-digital converter.—W. G. Trabold, Tunnel Diodes Save Parts—Continuous Readout of Magnetic Cores, *Electronics*, 36:36, p 38-39.



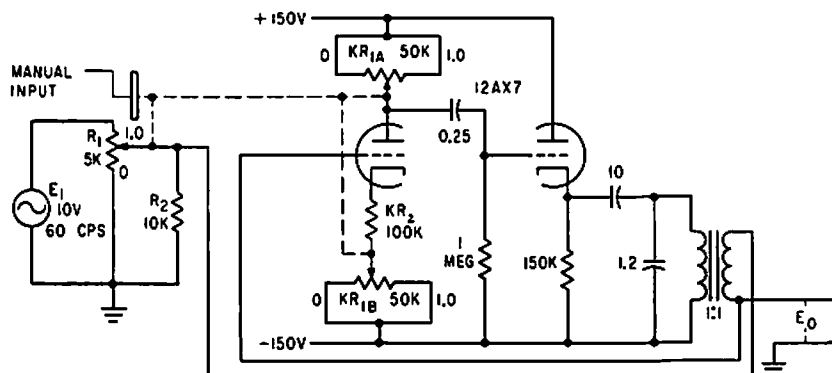
FET VOLTAGE COMPARATOR—Circuit uses spdt configuration. Used to switch reference voltage (such as ground) to successive points on binary voltage ladder until comparator determines that output of ladder is equal to that of unknown voltage. Sine and square-wave inputs at A and B produce output at C. Used also for low-level differential switching.—J. Gulbenk and T. F. Prosser, *How Modules Make Complex Design Simple, Electronics*, 37:32, p 50-54.



TEN-BIT D/A CONVERTER—Ten identical stages (three are shown) use selected 2N2501 transistors and matched FA2054 clamping diodes to convert digital signals to equivalent analog voltages for driving servomotors, pen recorders, and deflection circuits of oscilloscopes.—C. R. Pearman and A. D. Popodi, How to Design High-Speed D-A Converters, *Electronics*, 37:8, p 28-32.



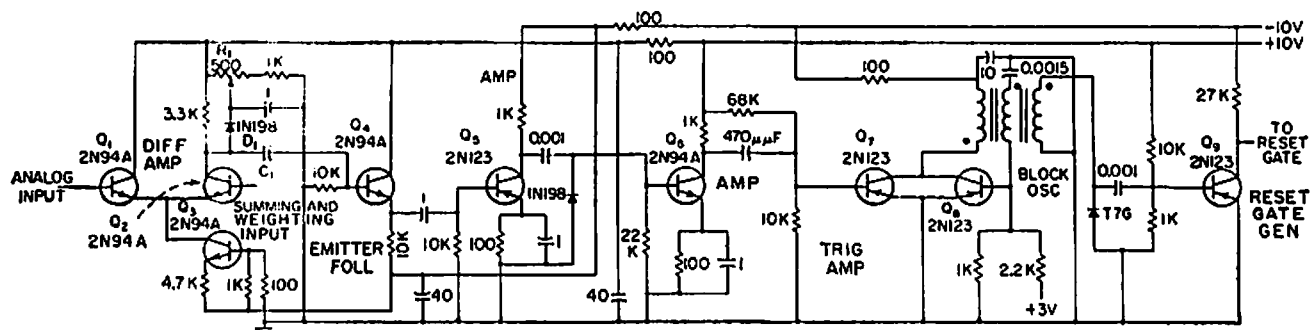
POTENTIOMETER ERROR COMPUTER—Compensation technique eliminates need for precise high-gain isolation amplifiers when linear potentiometers are used as precision voltage dividers in analog computing circuits. Error is reduced by factor of 100.—M. Kanner, How to Reduce Errors in Loaded Potentiometers, *Electronics*, 32:34, p 34-35.



RANDOM-PULSE CONVERTER—Transforms random information, as from radiation counter and micrometeorite detector, into analog form

suitable for multiplexing, and provides memory between events.—O. B. King, *Multiplexing Techniques for Satellite Applications*, Elec-

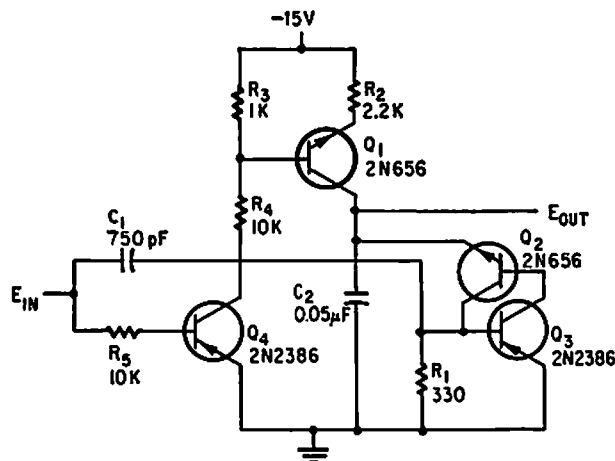
tronics, 32:44, p 58–62.



COMPARATOR—Comparator action begins when summing and weighting output exceeds analog input and negative pulse is coupled

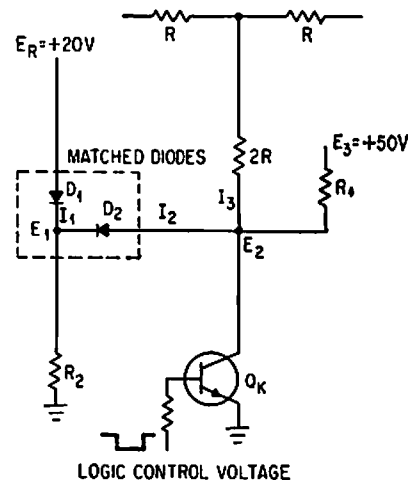
through C1. Trailing edge of blocking oscillator pulse activates reset-rate generator.—W. B. Towles, Transistorized Analog-Digital

Converter, *Electronics*, 31:31, p 90-93.



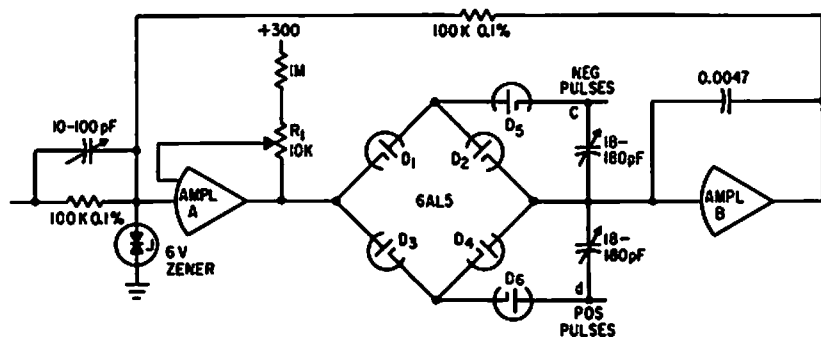
PULSE WIDTH TO ANALOG DEMODULATOR—Circuit integrates incoming pulse and holds final value until next pulse arrives. Output then returns to zero for next integration.

Output range is 0 to 10 v for input pulse width range of 0 to 1 microsec.—D. Knowlton, Modulated Pulse Width Converted to Analog Voltage, *Electronics*, 38:20, p 99-100.

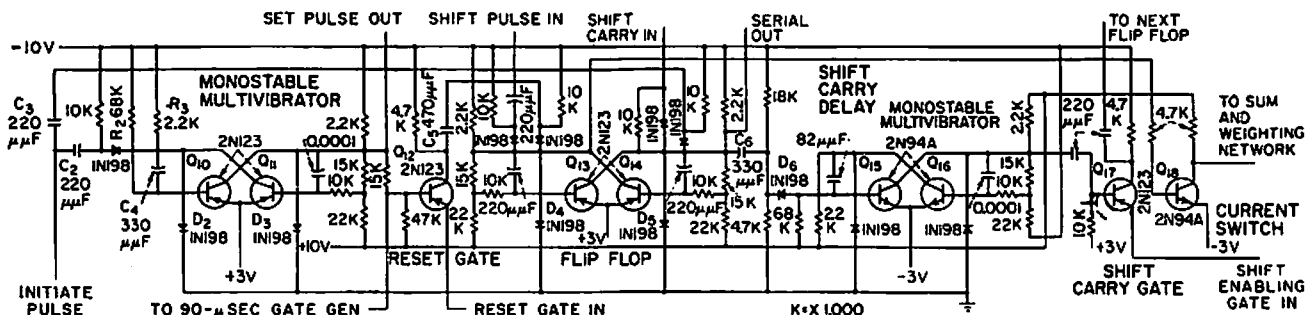


LOGIC CONTROL VOLTAGE

TEMPERATURE-COMPENSATED DECODER—Matched diodes in ladder-type network decoder change one reference voltage of transistor switch to compensate for temperature effects.—C. R. Pearman and A. E. Popodi, How to Design High-Speed D-A Converters, *Electronics*, 37:8, p 28-32.



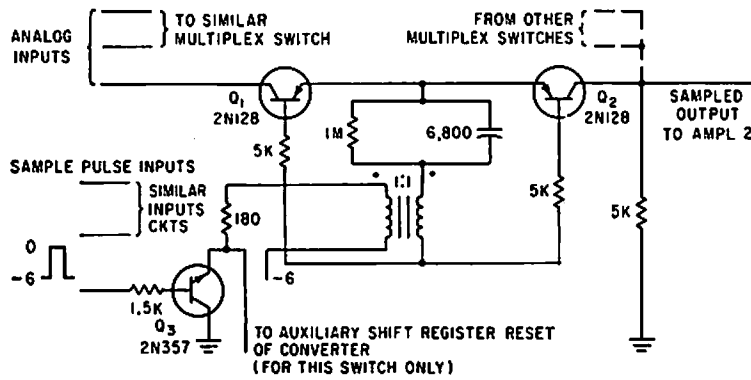
ANALOG SAMPLE-HOLD CIRCUIT—Uses diode bridge as switching circuit. Operational amplifier A delivers maximum current of 10 ma. Chopper-stabilized operational amplifier B delivers 100 v at 10 ma.—T. A. Brubaker, Precision Analog Memory Has Extended Frequency Response, *Electronics*, 34:39, p 141-143.



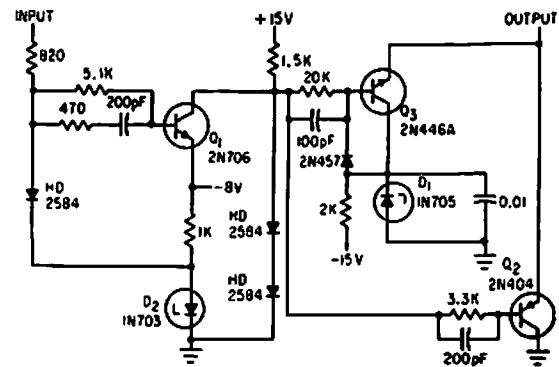
CODING DIGIT CARD—Initiate pulse starts coding in digit card of analog-digital converter and ultimately provides positive shift-carry pulse for next card. Codes inputs up to 5 v

at maximum sampling rate of 5,000 inputs per second with 0.5% accuracy. Eight binary-digit result is shifted out serially at 100,000 digits per second.—W. B. Towles, Transistor-

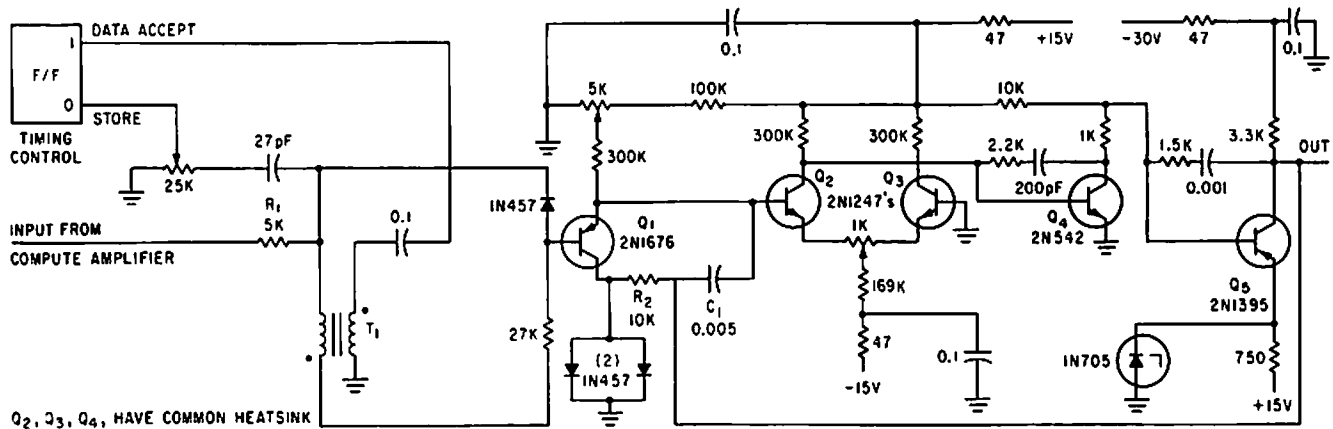
ized Analog-Digital Converter, *Electronics*, 31:31, p 90-93.



SAMPLER AND MULTIPLEXER—Sample of signal input voltage is fed to output when sampling pulse from external digital timing matrix is applied to primary of pulse transformer through Q3.—N. Aron and C. Granger, *Analog-To-Digital Converter Uses Transfluxors*, *Electronics*, 35:20, p 62-66.



REFERENCE SWITCH—Provides low-zero-offset 5-microsec pulses with stabilized amplitude, obtained from synchronous flip-flop. Output pulses switch from zero to -5 v, for driving compute and hold amplifiers of serial decoder.—R. M. Centner and J. R. Wilkinson, *New Approach to Serial Decoding Eliminates Static Storage*, *Electronics*, 35:34, p 32-35.



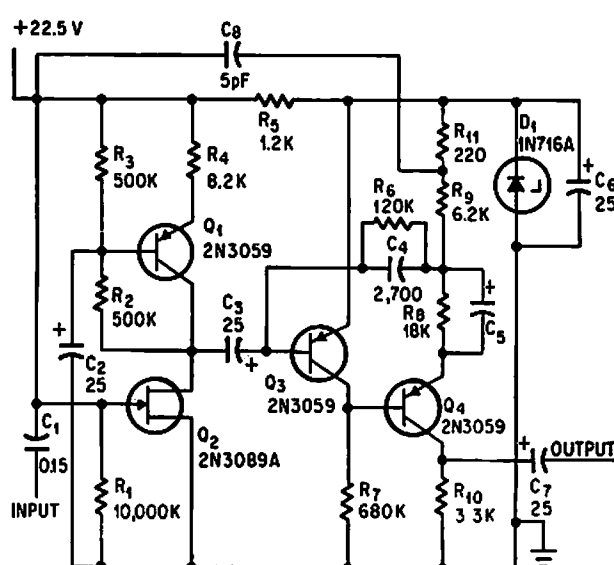
HOLD AMPLIFIER—Samples output of compute amplifier at end of each word, to provide d-c output for serial decoder and permit time-

sharing of computer amplifier. Full-scale output is -10 v d-c.—R. M. Centner and J. R. Wilkinson, *New Approach to Serial Decoding*

Eliminates Static Storage, *Electronics*, 35:34, p 32-35.

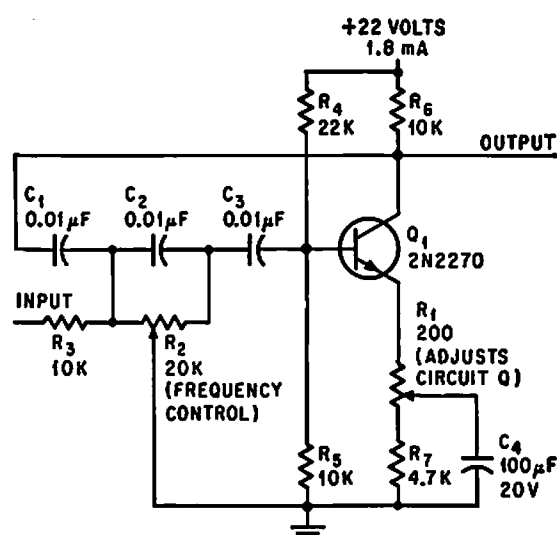
CHAPTER 4

Audio Circuits

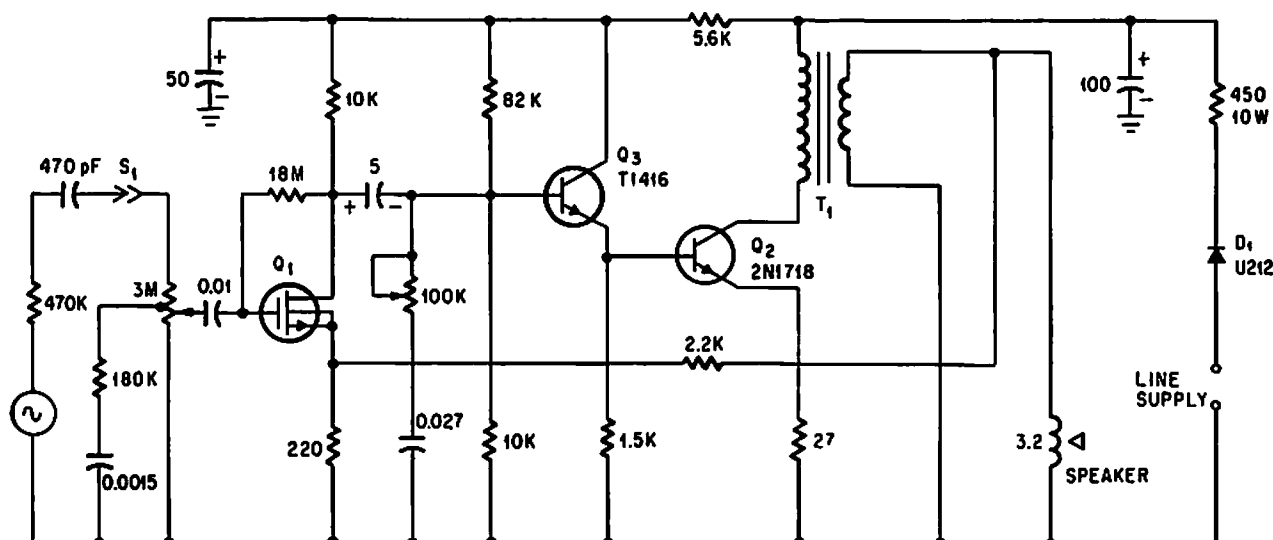


SINGLE-FET CASCODE—Costs less than two-fet version, but has somewhat poorer stability. Voltage gain is 500 for 33,000-ohm output

impedance.—B. Smith, *Low-Noise FETs Sound Good To Circuit Designers*, *Electronics*, 37:31, p 58-62.



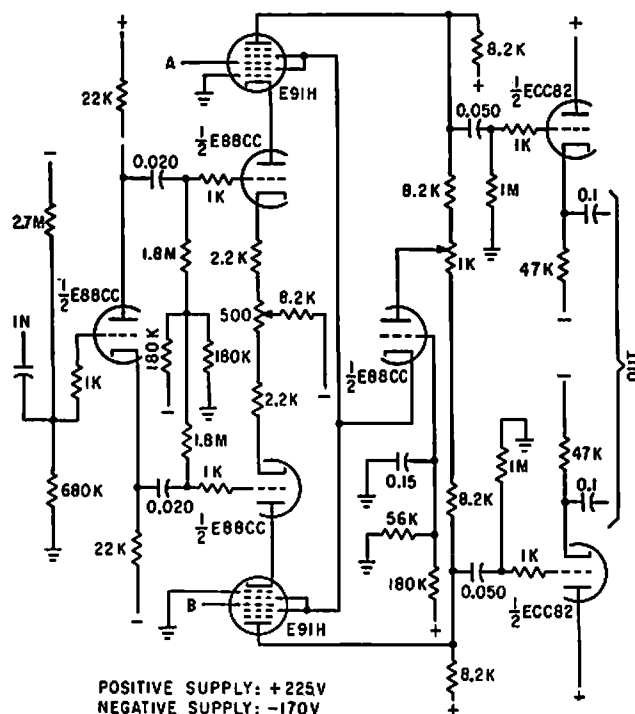
TUNED A-F—R2 tunes three-step ladder network of feedback loop from 800 to 1,000 cps. Circuit is stable. Used in a-c bridge.—J. F. Delpech, *Simple Circuit Tunes Audio Amplifier*, *Electronics*, 38:6, p 84-85.



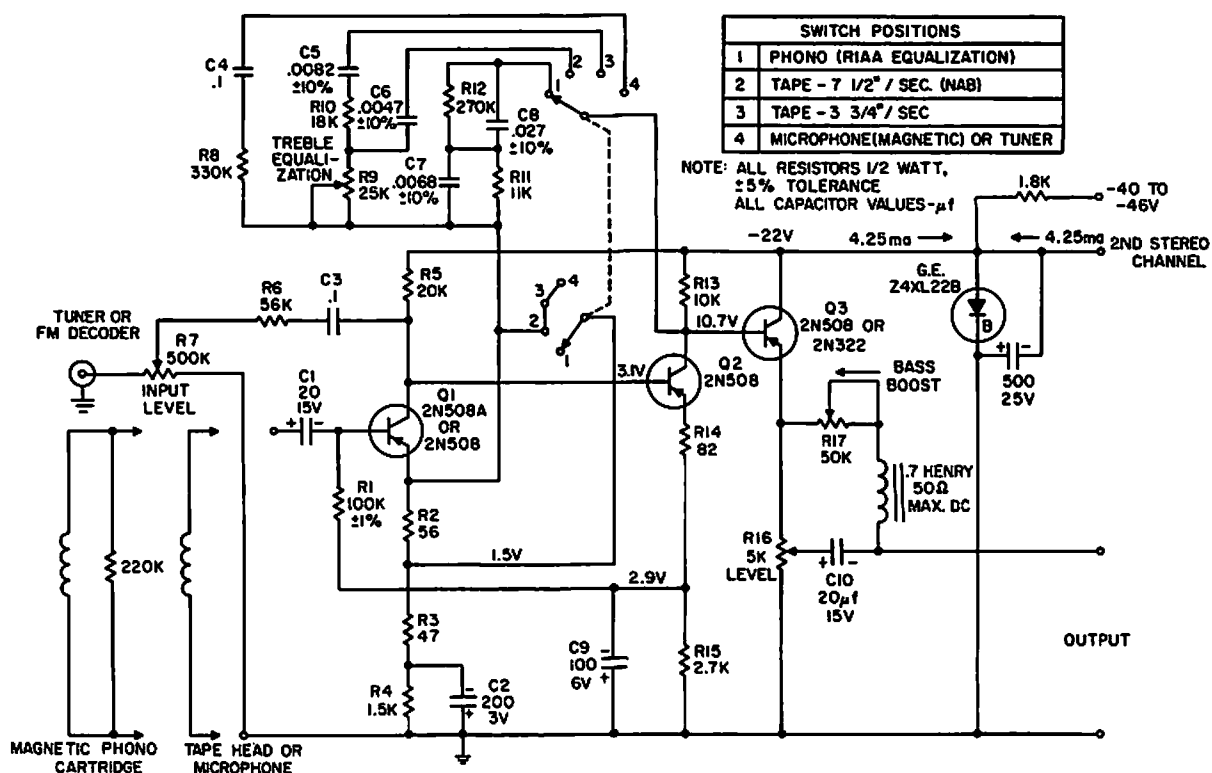
PHONO PREAMP—High input impedance of mos fet will not load ceramic cartridge represented by 470K and 470 pf. Q2 as emit-

ter-follower driver transforms low input impedance of Q3 to sufficiently high value for required voltage gain at power output

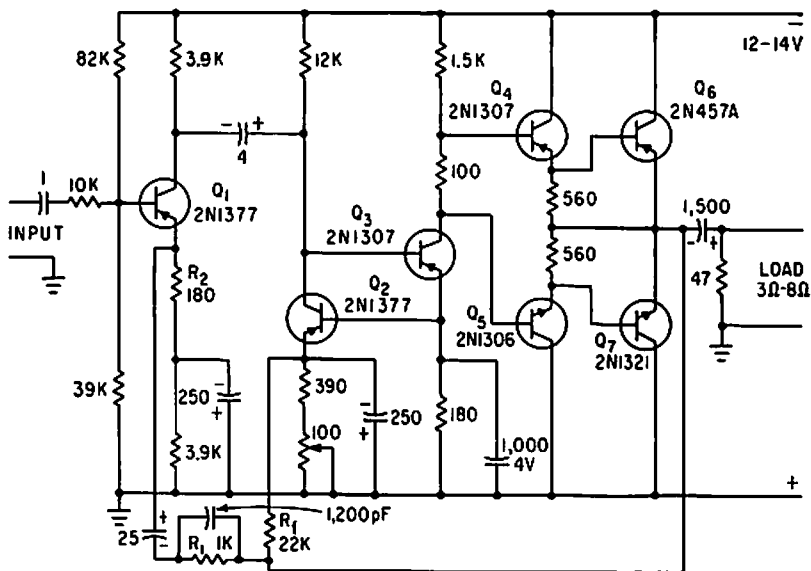
of 1 w.—V. Harrap et al., *Researchers Turn to Germanium For a MOS Field-Effect Transistor*, *Electronics*, 37:30, p 64-68.



ELECTRONIC FADER—Used to fade audio signals on and off without producing audible switching transients. Signals from matching network of driver are applied to points A and B.—E. de Boer, *Electronic Fader for Auditory Research, Electronics*, 33:50, p 85-87.

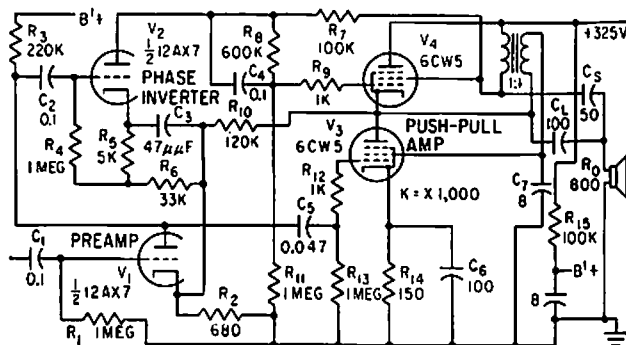


Manual," Seventh Edition, General Electric Co., 1964, p 252.



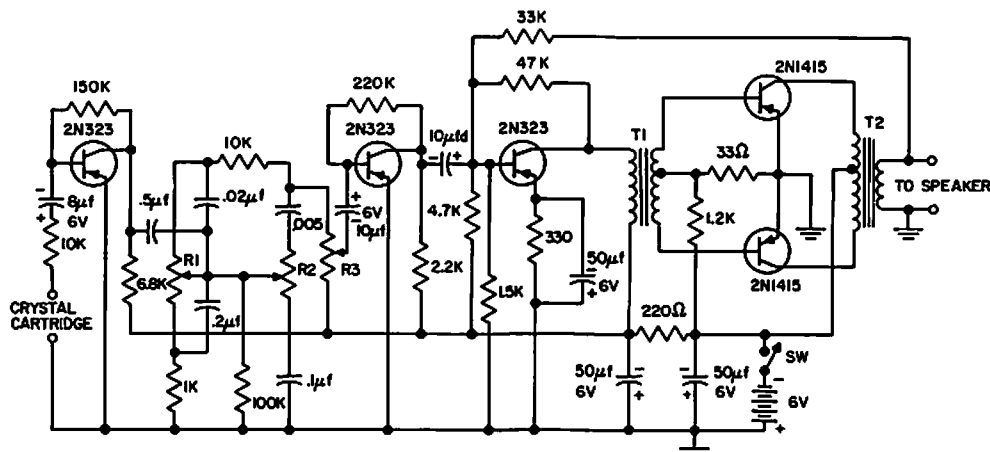
COMPLEMENTARY-SYMMETRY AUDIO AMPLIFIER—Provides nearly maximum power theoretically available from single d-c supply. Distortion is low. Large feedbacks, both a-c and d-c, make amplifier insensitive to unbal-

ance of output transistors.—R. S. Richards, How to Design Transformerless Audio-Frequency Power Amplifiers, *Electronics*, 35:46, p 50-52.



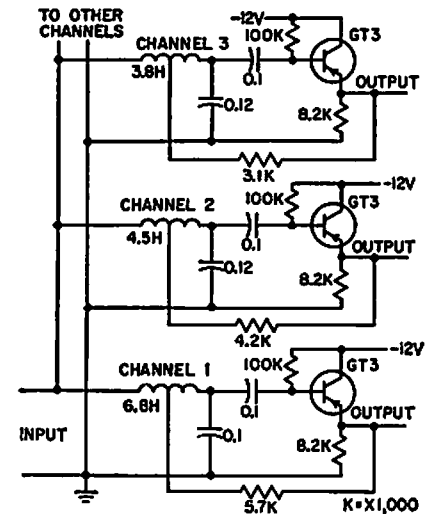
10-W SINGLE-ENDED PUSH-PULL OUTPUT—Feeds voice coil directly, making output transformer unnecessary. First preamplifying stage has positive feedback to point of oscillation, while amplifier and output stages have nega-

tive feedback. Circuit has low distortion, flat response, and only a few degrees of phase shift over audio range.—J. Rodrigues De Miranda, Push-Pull Amplifiers Drive Speaker Directly, *Electronics*, 31:29, p 76-79.

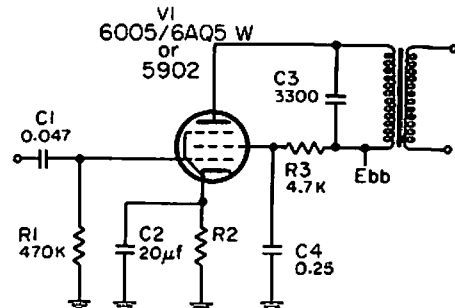


6-V PHONO AMPLIFIER—Provides 300 mw at 10% distortion. Bass control R1 and treble

control R2 are 50K linear taper. Volume control R3 is 10K audio taper.—"Transistor Man-

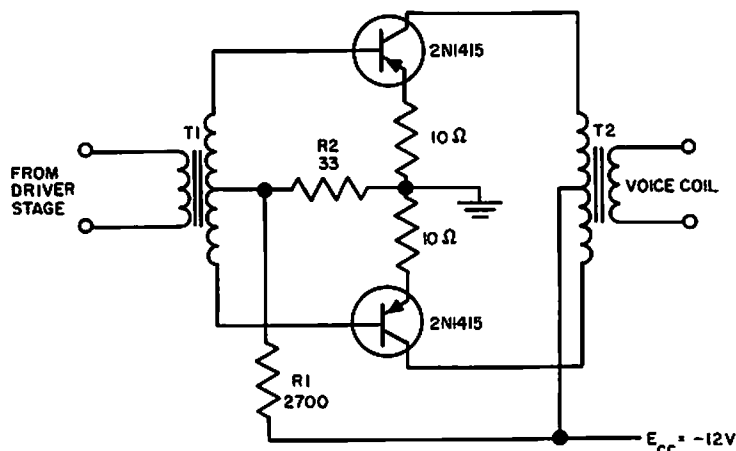


Q MULTIPLIER—Circuit shows three channels of multi-channel selective a-f amplifier (190, 216.5, and 235 cps) using various coil-capacitor combinations with transistor Q multiplier to provide staggered resonant frequencies. Used in frequency-selective calling systems.—G. B. Millor, Transistor Q Multiplier for Audio Frequencies, *Electronics*, 31:19, p 79-81.



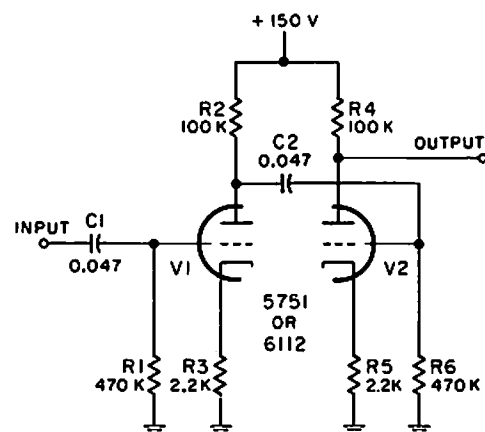
PREFERRED POWER AMPLIFIER—For 6AQ5W, with plate supply of 250 v, output is 115 v to transformer at 2.21 w for 6 v rms input. For 5902, with plate supply of 150 v, output to transformer is 75 v at 0.8 w for 5 v rms input.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 61, p 61-2.

ual," Seventh Edition, General Electric Co., 1964, p 376.

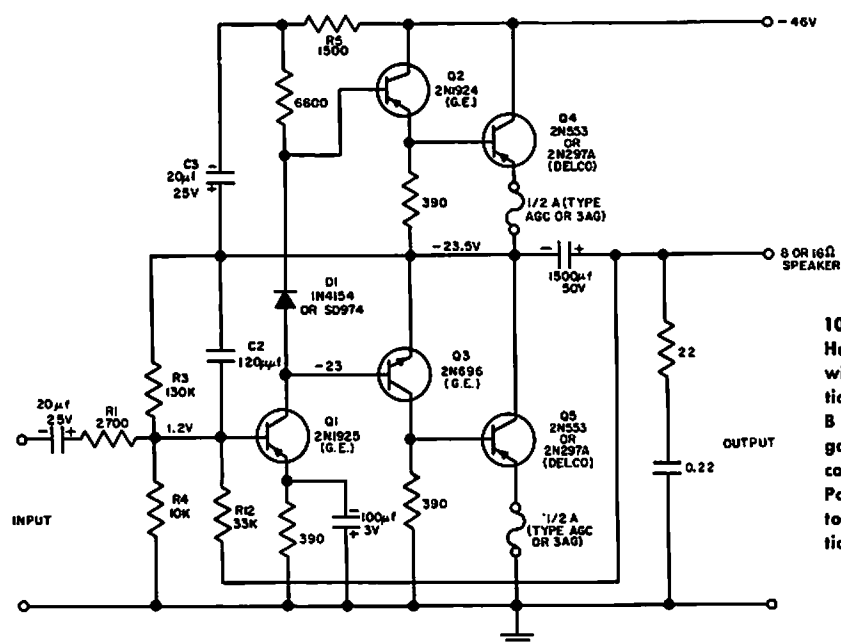


BASIC CLASS-B PUSH-PULL OUTPUT—Design procedures are given. Resistor in emitter leads prevent thermal runaway when ambi-

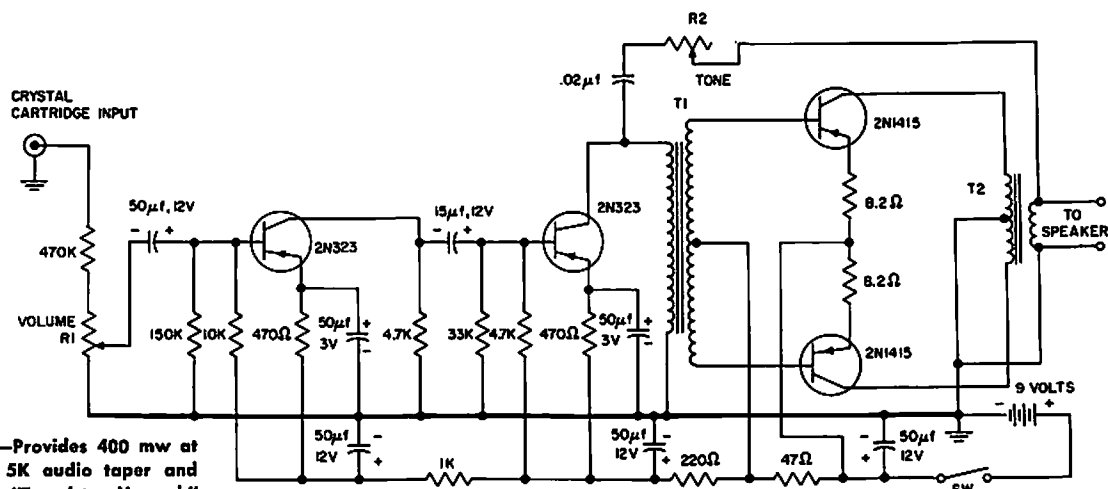
ent temperature is below 55°C.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 242.



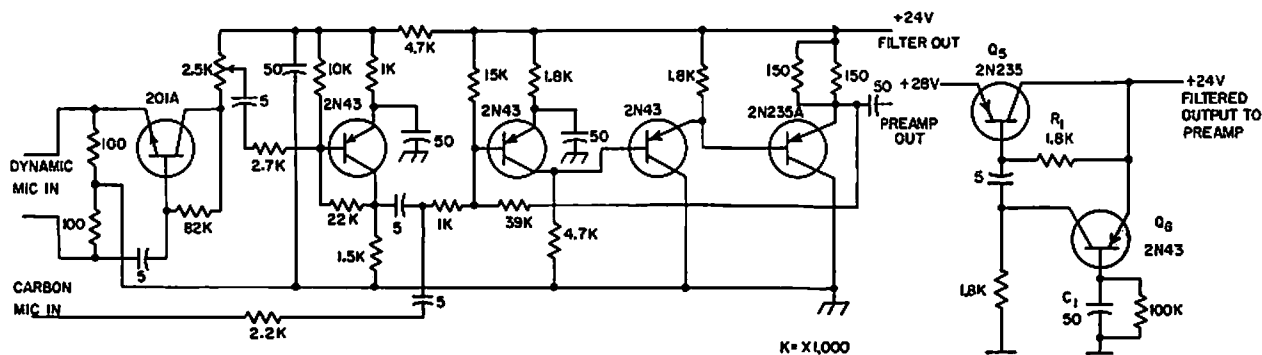
PREFERRED VOLTAGE AMPLIFIER—Amplifies 10 to 70-mv signals to level needed to drive audio power amplifier. For 5751, amplification is 335, maximum output is 23 v rms, and 3-db response is 10 cps to 20 kc. For 6112, maximum output is 26 v rms, amplification is 370, and 3-db bandwidth is 10 cps to 30 kc.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 60, p 60-2.



10-W DIRECT-COUPLED POWER AMPLIFIER—Has excellent low-frequency response, along with d-c feedback for temperature stabilization of all stages. Q2 and Q3 operate class B in Darlington connection to increase current gain. Fuses protect output transistors against continuous sine-wave output above 10 kc. Power response at 1 w is flat from 30 cps to 15 kc.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 260.



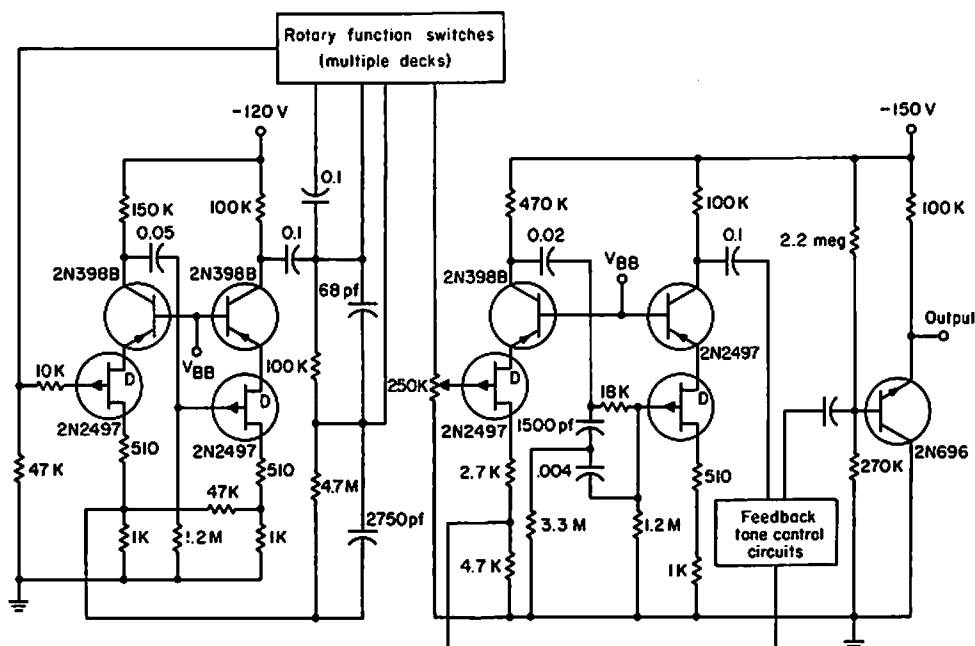
9-V PHONO AMPLIFIER—Provides 400 mw at 10% distortion. R1 is 5K audio taper and R2 is 25K linear taper.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 377.



ZONED PUBLIC ADDRESS IN PLANE—Uses single preamplifier and up to five power amplifiers and speakers to distribute sound uni-

formly throughout seating area of plane. Air-ground output switch acts on all amplifiers simultaneously to compensate for dif-

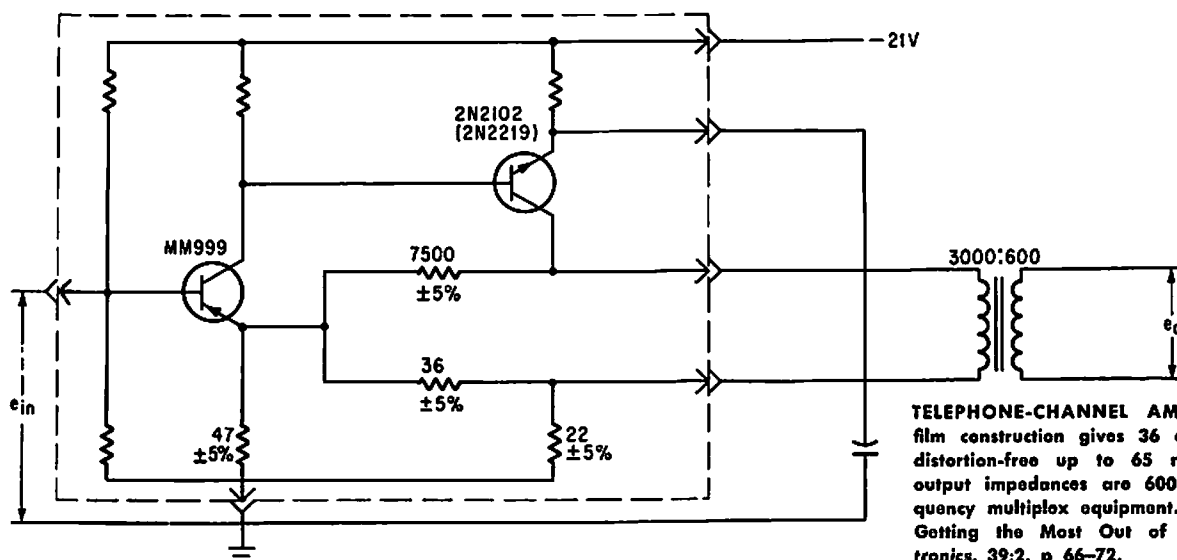
ferent noise levels on ground than in air.—**Transistorized P-A System Adjusts to Aircraft Noise.** *Electronics*, 31:7, p 106-107.



HI-FI FET PREAMP—Breakdown voltage of commercial fet's is extended by using direct-

coupled cascode connection with low-cost germanium transistor.—L. J. Sevin, Jr., "Field-

Effect Transistors," McGraw-Hill, N.Y., 1965, p 73.



TELEPHONE-CHANNEL AMPLIFIER—Thick-film construction gives 36 db gain and is distortion-free up to 65 mw. Input and output impedances are 600 ohms, for frequency multiplex equipment.—N. A. Zellmer, *Getting the Most Out of Feedback*, *Electronics*, 39:2, p 66-72.

RESISTORS: All 1/2 watt, T1 Type CD1/2MR
CAPACITORS:

C₁-0.1μf

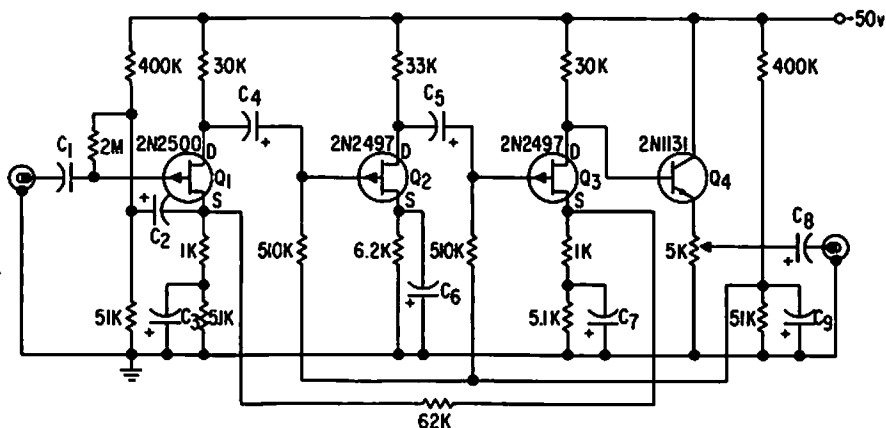
C₂-5.6μf-T1 Type SCM 565BP035C4

C₃, C₆, C₇-220μf-T1 Type SCM 227HP010C4

C₄, C₅-1.0μf-T1 Type SCM 105FP035C4

C₈-10μf-T1 Type SCM 106BP020C4

C₉-68μf-T1 Type SCM 686GP015C4

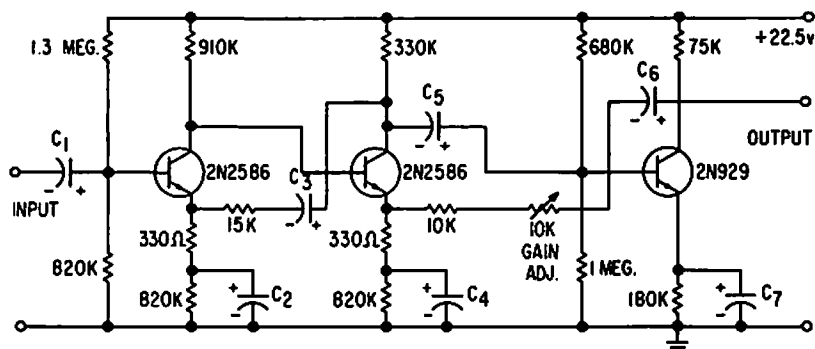
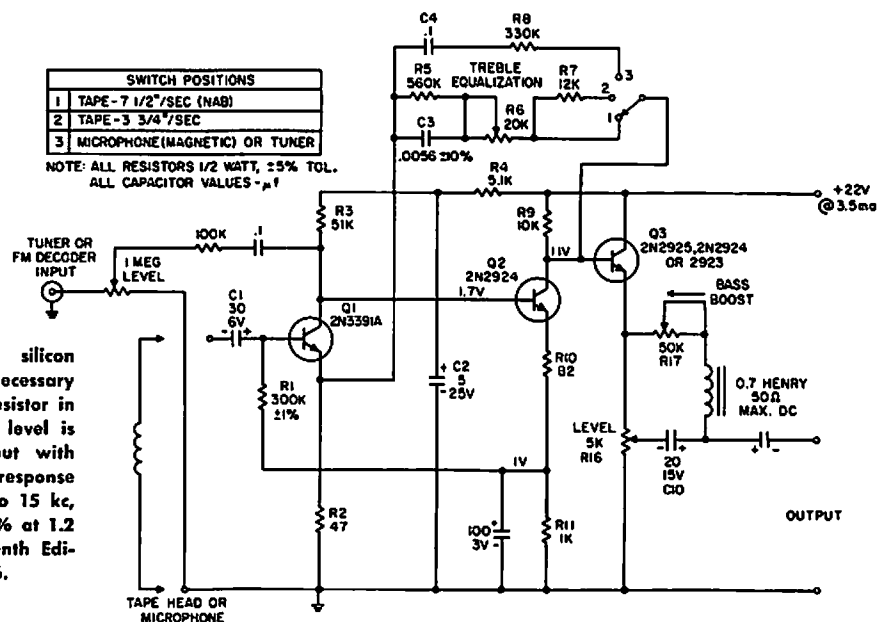


60-DB LOW-NOISE FET AMPLIFIER—Gives maximum voltage gain of 60 db, constant within 0.5 db from -55 to +125°C with built-in gain adjustment. Has good low-fre-

quency response, along with extremely low noise, as little as 5 db at 10 cps. Used with low-level transducers, null detectors, recorders, oscilloscopes, and medical research equip-

ment.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 293.

TAPE - MICROPHONE PREAMP—Uses silicon planar npn transistors, making it necessary to have temperature-compensating resistor in emitter circuit of first stage. Noise level is 66 db below reference level output with weighted measurement. Frequency response is flat within 0.25 db from 30 cps to 15 kc, and total harmonic distortion is 0.01% at 1.2 v output.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 256.



LOW-LEVEL LOW-NOISE HIGH-GAIN—Gives gains up to 1,000 (60 db) for high-impedance transducer applications, with typical noise figure of 1 db at emitter currents below 1

microamp and generator resistance above 1 meg. Such performance was previously available only with vacuum tubes and field-effect transistors. Ideal for space applications.—

RESISTORS—ALL 1/2 watt, T1 type CD1/2 MR

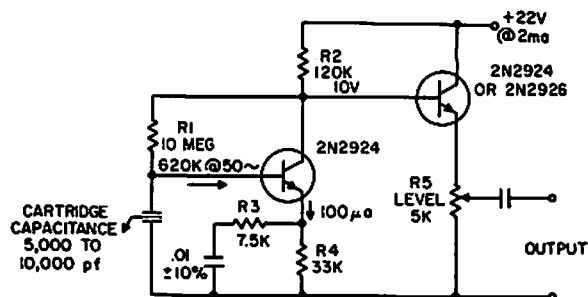
CAPACITORS

C₁, C₃, C₅, C₆ — 2μf, T1 type SCM 225FP020C4

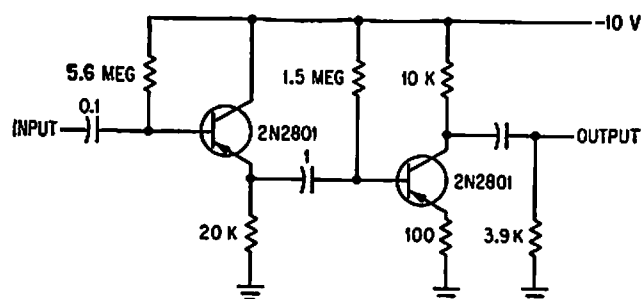
C₂, C₄ — 20μf, T1 type SCM 226BP015C4

C₇ — 20μf, T1 type SCM 226GP035C4

Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 291.

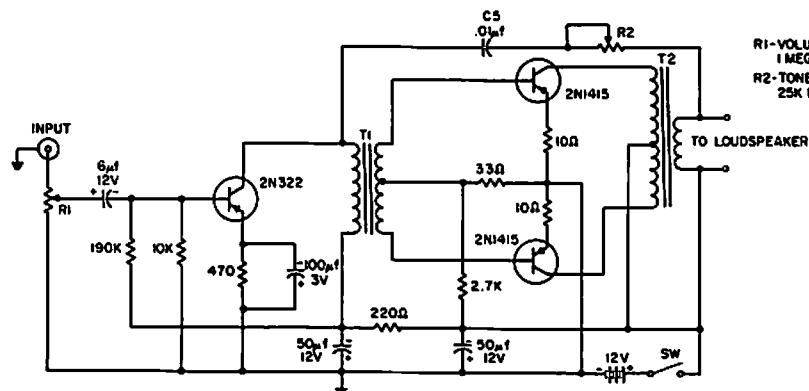
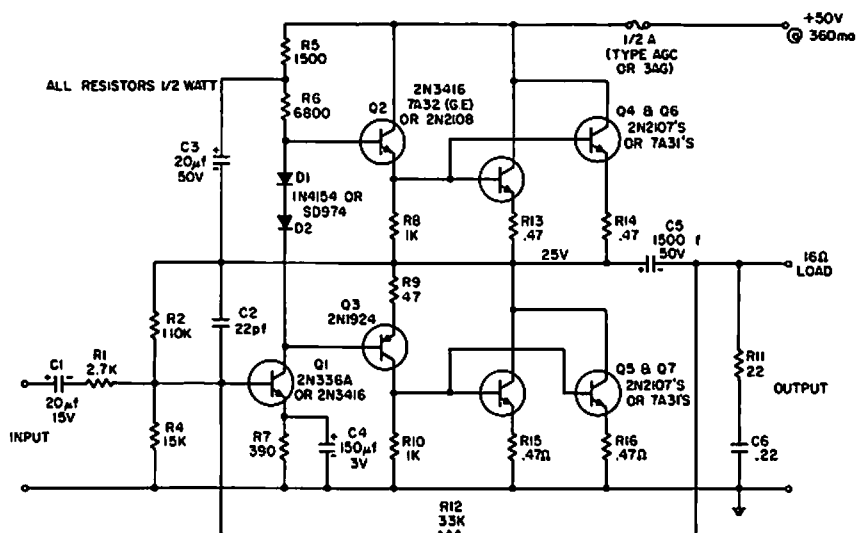


CERAMIC-CARTRIDGE PHONO PREAMP—Gives RIAA equalized output. With Astatic model 137 cartridge, output reference level of 1 v is 13 db below maximum output and 69 db above unweighted noise level.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 258.



LOW-NOISE AUDIO AMPLIFIER—Power gain is 44 db, input impedance 440,000 ohms, and hum and noise 57 db down for 3-db frequency response of 20 to 100,000 cps.—L. E. Clark, E. B. Mack, and R. C. Hojhall, *Highlights of Small-Signal Circuit Design*, Electronics, 36:49, p 46-50.

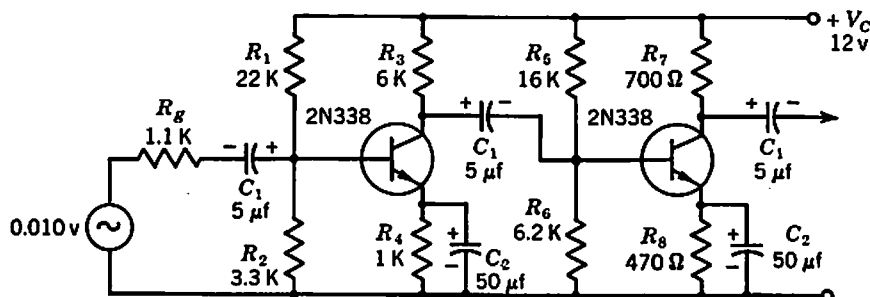
12-W TRANSFORMERLESS AMPLIFIER—Uses two transistors in parallel for each of the outputs, to cut saturation resistance in half and thereby increase power output. Parallel operation also serves to reduce distortion. Operating efficiency is 67% at 12 w.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 267.



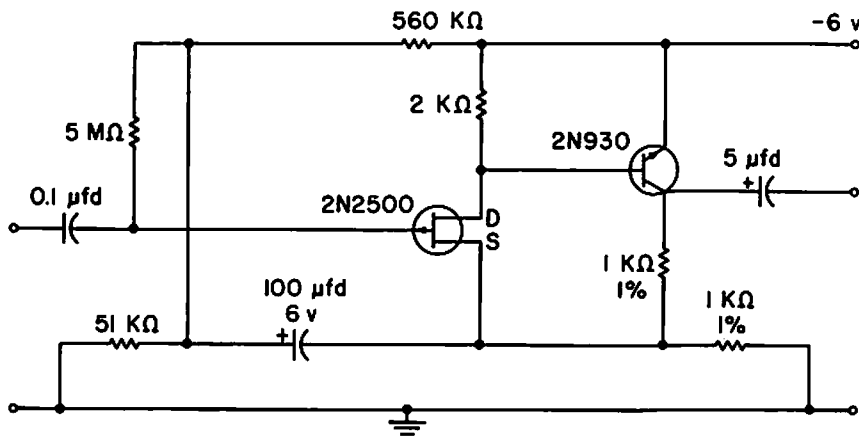
R1—VOLUME CONTROL
1 MEG AUDIO TAPER
R2—TONE CONTROL
25K LINEAR TAPER

T1—DRIVER TRANSFORMER
PRL 20K/SEC 2K CT
T2—OUTPUT TRANSFORMER
PRL 300Ω CT/SEC V.C.

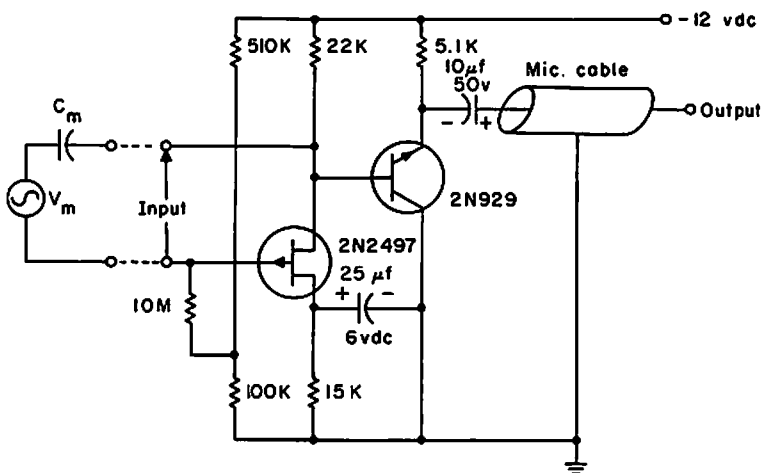
12-V AUDIO AMPLIFIER—Input sensitivity is 10 to 20 mv for 0.5 w output. Maximum power output at 10% harmonic distortion is 500 mw. Design calculations are given.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 246.



TWO-STAGE CASCADED COMMON-EMITTER—Design procedure is given for low-level amplifier that is one of the most-used circuits in all transistor applications, for boosting millivolt-range signals to workable level of several volts. Capacitance coupling is used for a-c operation along with d-c stability. Total power gain is 64 db. Voltage gain is 1,000.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 198.

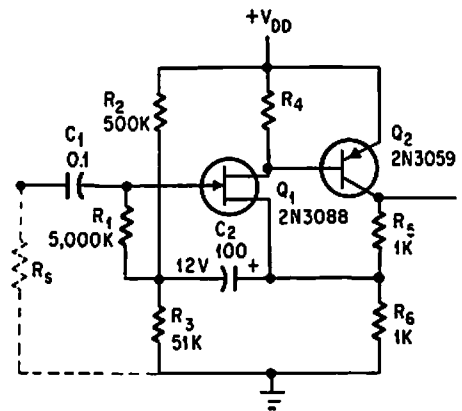


FET-PNP DARLINGTON—P-channel fet is combined with pnp transistor in equivalent to Darlington connection, for use with high-input-impedance low-frequency transducers. Spot noise figure is 7 db at 10 cps and 3 db at 100 cps. Broadband noise figure from 10 cps to 10 kc is 1.7 db with 200K generator resistance.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 136.

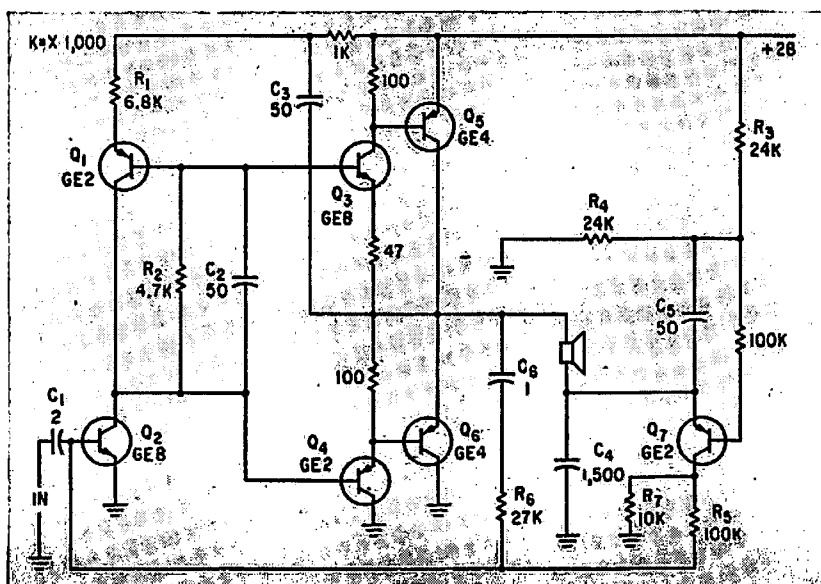


CAPACITOR MIKE PREAMP—Fet provides required high input impedance. Can easily be mounted in microphone. Emitter-follower with output impedance of about 100 ohms will

drive 500 feet of microphone cable without appreciably affecting frequency response.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 75.

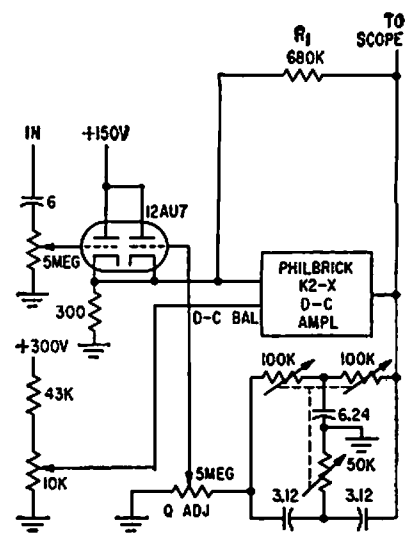


BOOTSTRAP FET—Input impedance is high at low frequencies (180 meg at 10 cps), but drops to 3 meg at 10 kc. High collector current contributes to high over-all noise level.—B. Smith, Low-Noise FETs Sound Good To Circuit Designers, *Electronics*, 37:31, p 58-62.

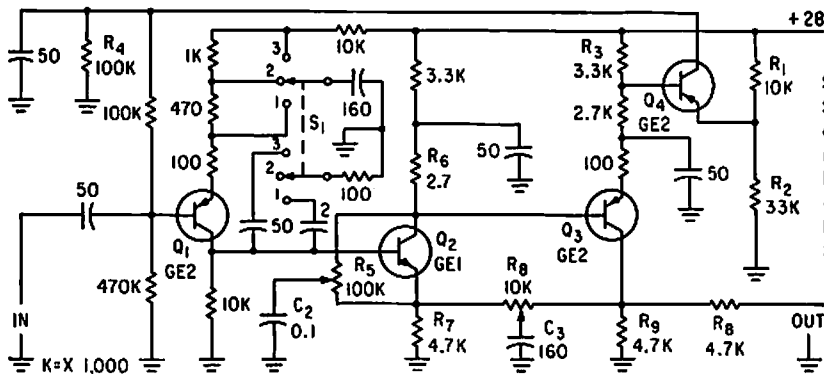


10-W POWER AMPLIFIER—Q1 provides bias current for low-power complementary-symmetry push-pull output stage Q3-Q4, which drives power transistors Q5-Q6. Q7 provides

voltage bias for Q2.—H. W. Parmer, Two Easy Ways to Stabilize Power-Transistor Hi-Fi Amplifiers, *Electronics*, 35:43, p 56-58.

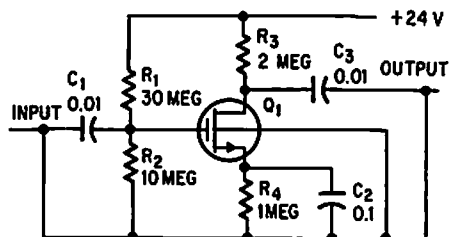
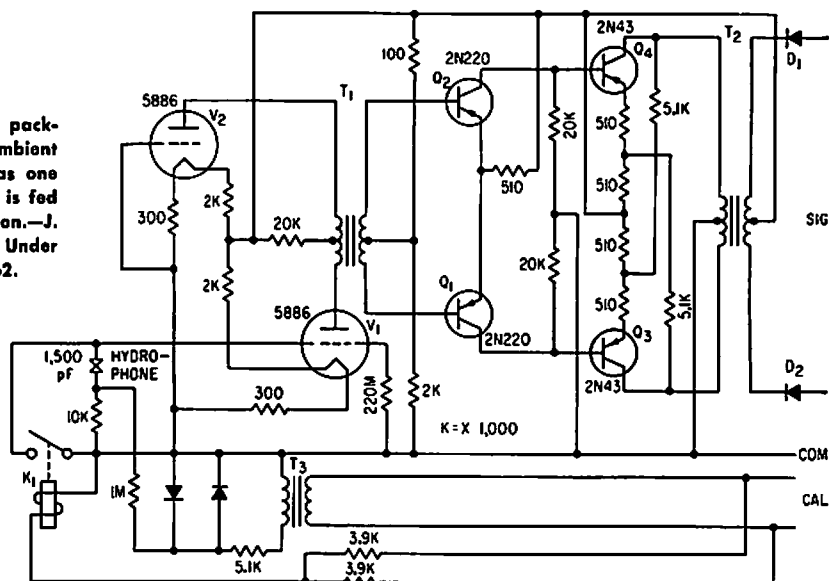


TUNABLE SUBAUDIO AMPLIFIER—Commercial d-c amplifier with twin-T feedback tuning element tunes from 0.5 to 100 cps, for analyzing low-frequency components of complex waveforms.—J. M. Reece, Subaudio Tunable Amplifier, *Electronics*, 32:45, p 72-74.

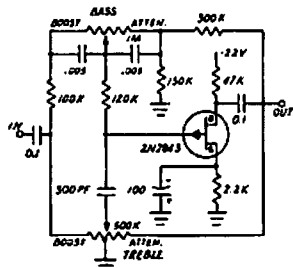


STABILIZED MULTI-INPUT AUDIO PREAMP—Switch gives choice of input impedance, frequency response, and level-compensation networks. R5 is tone control. Q4 provides bias current to base of Q1 for stabilization. —H. W. Parmer, *Two Easy Ways to Stabilize Power-Transistor Hi-Fi Amplifiers*, *Electronics*, 35:43, p 56-58.

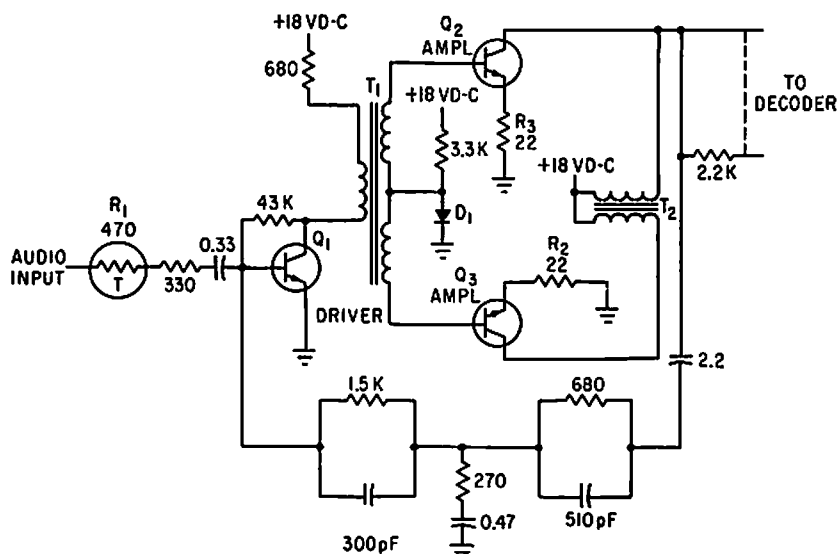
UNDERWATER PREAMPLIFIER—Used in package with hydrophone for measuring ambient ocean noise over periods of months, as one basis for sonar system design. Output is fed through cable pair to shore installation. —J. V. Schaefer, *Remote Preamplifiers for Under Ocean Work*, *Electronics*, 33:28, p 60-62.



MOS FET AMPLIFIER—Circuit draws only 6 microamp while providing voltage gain over 200. —G. G. Luetgenau and S. H. Barnes, *Designing With Low-Noise MOS FETs: A Little Different But No Harder*, *Electronics*, 37:31, p 53-58.



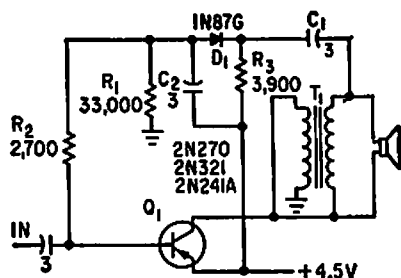
TONE CONTROL—Unlike bipolar transistor, fet maintains full dynamic range while loading R-C tone control network. —FET's and RC Networks (Siliconix ad), *Electronics*, 39:4, p 71.



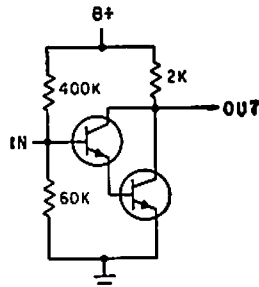
CLASS-AB PUSH-PULL AUDIO—Senseistor R1 in a-c coupled driver compensates for effects of temperature on amplifier gain. Negative feedback stabilizes frequency and phase re-

sponse. Circuit drives 20 decoders in Mercury spacecraft command receiver. —R. Elliott, *First Details on Mercury Spacecraft Command Receiver*, *Electronics*, 36:5, p 32-35.

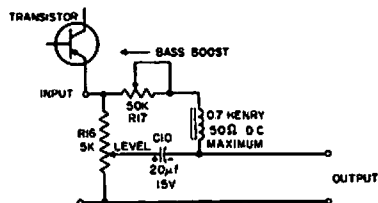
100-350 CPS RECEIVER—Input signal from electrodes of crevasse detector is attenuated to suitable level at constant impedance of 1,000 ohms by T-pad and passed to 2N107 preamplifier whose supply voltage is stabilized at 5.8 v by reversed TI620 silicon diode shunt operating at zener point. Signal is then fed through bandpass L-C filter to amplifier, driver, and final 355 class A stage that drives recorder pen motor and relay-meter.—H. P. Van Eckhardt, *Crevasse Detector Blazes Glacial Trails*, *Electronics*, 31:3, p 63-65.



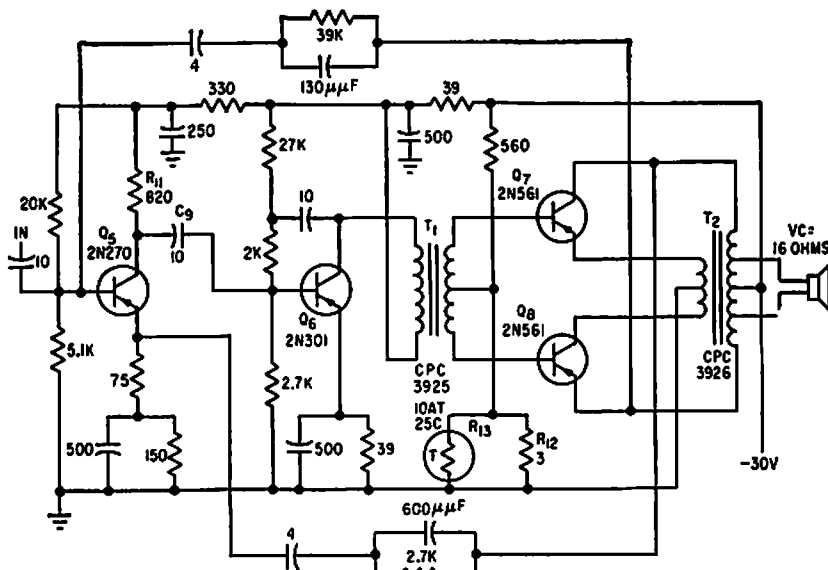
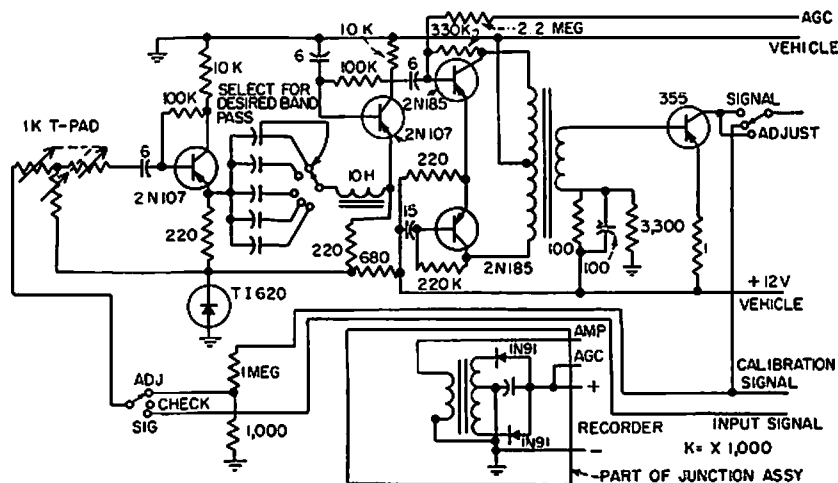
SYNTHETIC PUSH-PULL—Single transistor in sliding class-A output requires no input transformer, while approximating push-pull class-B output stage.—J. A. Worcester, *One-Transistor "Push-Pull,"* *Electronics*, 32:24, p 74.



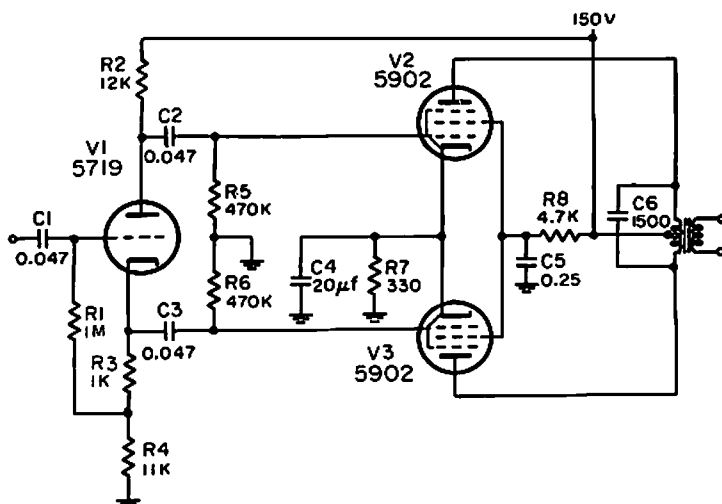
DARLINGTON AMPLIFIER—Useful up to 100 kc, but high input impedance makes it particularly desirable for audio preamps. Gives gain of two stages with dissipation of only one.—L. Pollock and R. Gutteridge, *Latest Design Techniques for Linear Microcircuits*, *Electronics*, 35:41, p 47-49.



BASS BOOST OR LOUDNESS CONTROL—Operates on output of preamp. Gives operator independent control of level of bass or amount of bass boost desired. May also be used as loudness control.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 254.

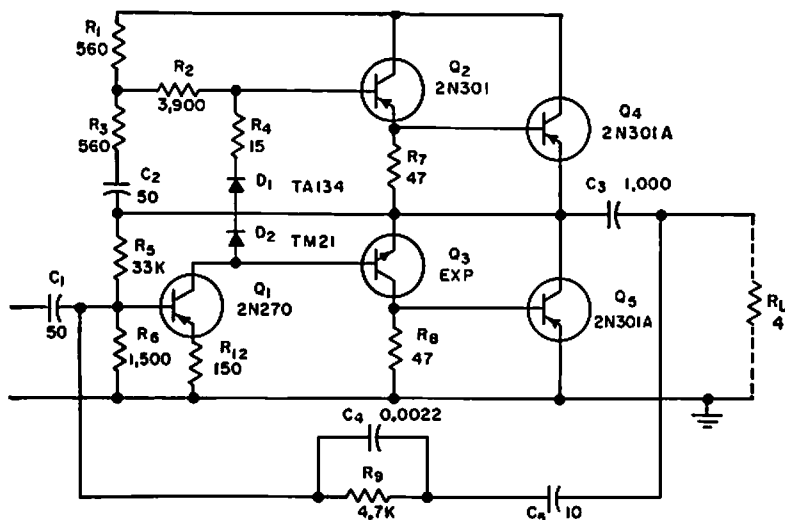


25-W CLASS-B POWER AMPLIFIER—Uses balanced negative feedback, with input Q5 and driver Q6 operating class A. Output stage is temperature-stabilized.—R. Minton, *Designing High-Quality A-F Transistor Amplifiers*, *Electronics*, 32:24, p 60-61.



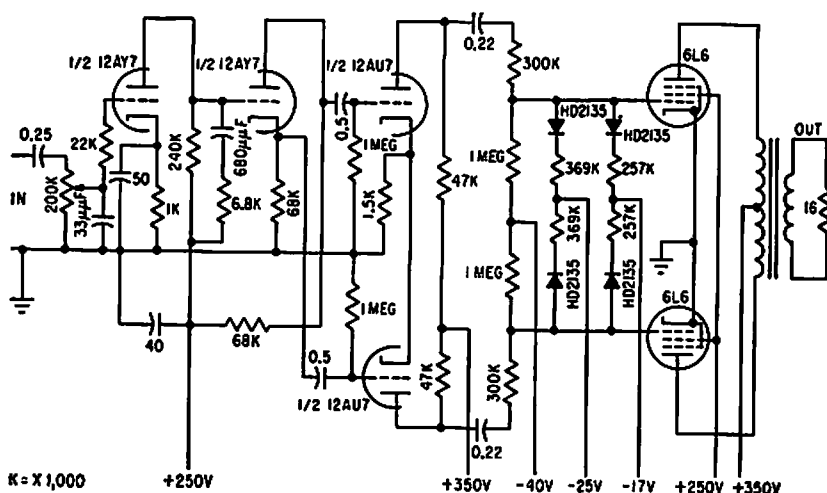
PREFERRED AUDIO POWER AMPLIFIER—Delivers 2 w with less than 5% distortion to suitably matched load. If push-pull tubes are dynamically matched, screen and cathode

bypass capacitors C4 and C5 may be omitted.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, *Electron Tube Circuits*, 1963, PC 64, p 64-2.



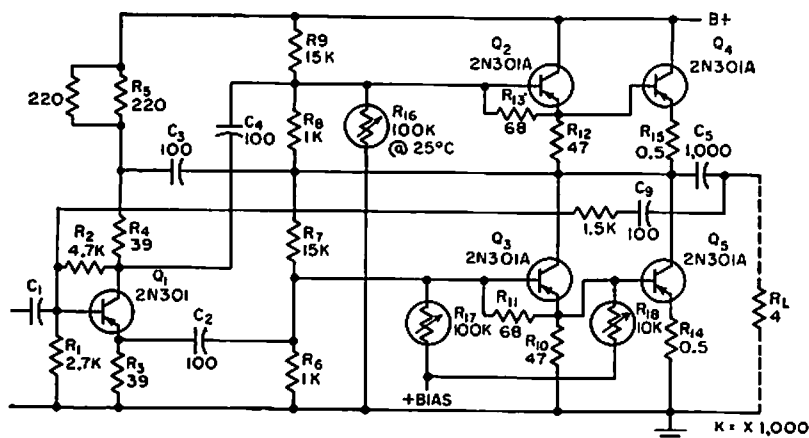
45-W A-F OUTPUT—Operates with convection cooling over temperature range of -10 to $+50^{\circ}\text{C}$. Forward voltage drop of diodes decreases with increasing temperature, to hold

emitter currents essentially constant. Uses quasicomplementary symmetry.—M. B. Herscher, *Designing Transistor A-F Power Amplifiers*, *Electronics*, 31:15, p 96-99.

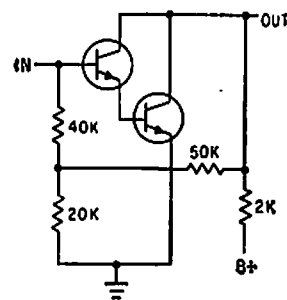


REDUCING ODD-HARMONIC DISTORTION—Grid-plate characteristic of class-B amplifier is linearized to eliminate harsh odd-harmonic distortion, through use of compon-

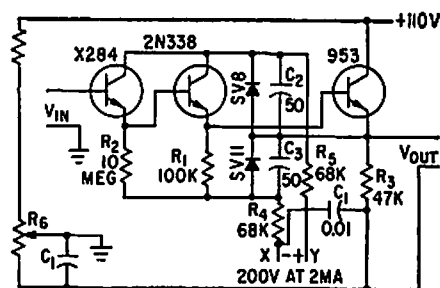
sation network having nonlinear transfer function. Distortion is cut to 2.6% at 16 w output.—B. Sklar, *Reducing Distortion in Class-B Amplifiers*, *Electronics*, 32:21, p 54-56.



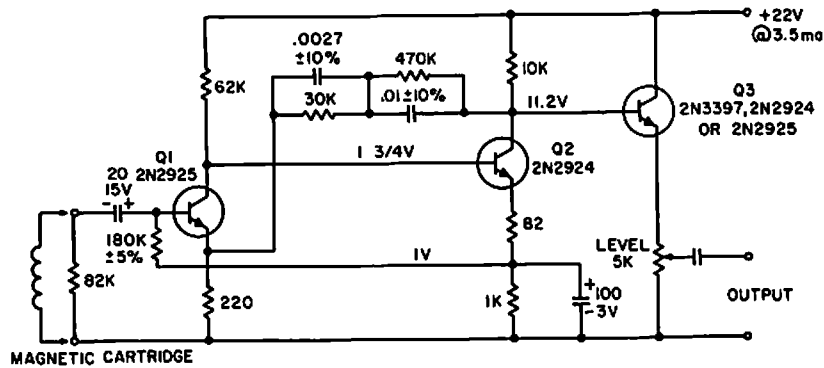
45-W SERIES-TYPE POWER AMPLIFIER—Uses split-load phase inverter, capacitance-coupled to common-collector class B driver, which in turn is direct-coupled to class-B common-emitter output stage. Driver and output stages are each in series for d-c collector supply. No transformers are required.—M. B. Herscher, *Designing Transistor A-F Power Amplifiers*, *Electronics*, 31:15, p 96-99.



DARLINGTON WITH VOLTAGE DIVIDER—Additional resistors in voltage divider reduce bias voltage, to simplify manufacture as integrated circuit. Useful up to 100 kc.—L. Pollock and R. Gutteridge, *Latest Design Techniques for Linear Microcircuits*, *Electronics*, 35:41, p 47-49.

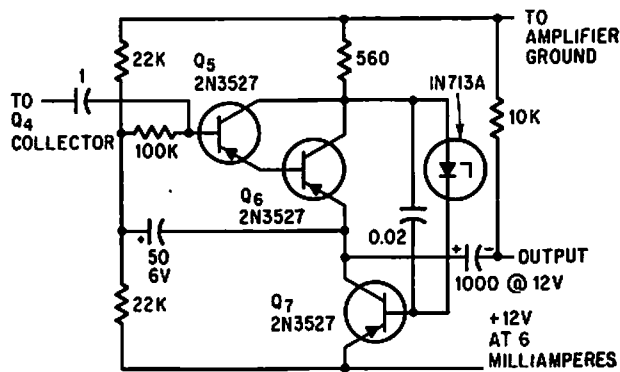


500-MEG D-C INPUT RESISTANCE—Bootstrap-collector circuit uses starved transistor to provide 500-meg d-c input resistance with 100-v input signal. A-c resistance is even higher, in the 1,000-megohm region at low audio frequencies.—B. M. Branson, *Starved Transistors Raise D-C Input Resistance*, *Electronics*, 32:5, p 54-55.



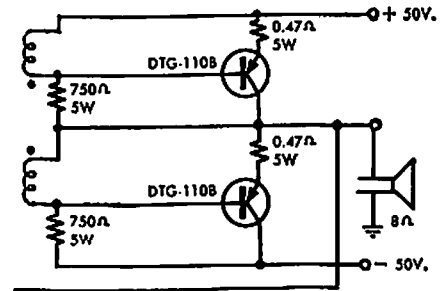
NPN PHONO PREAMP—Input of 6 mv at 1 kc from magnetic cartridge gives 1 v output, which is 15 db below clipping level and 72 db above unweighted noise level. RIAA

equalized output is within 1 db from 40 cps to 12 kc.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 257.

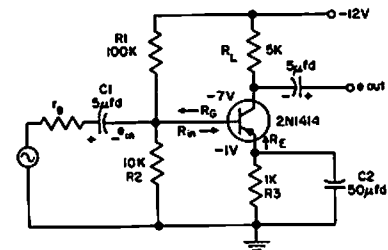


FET OUTPUT STAGE—Bootstrapped input, Darlington driver, and White follower give voltage gain of one, input impedance of 1

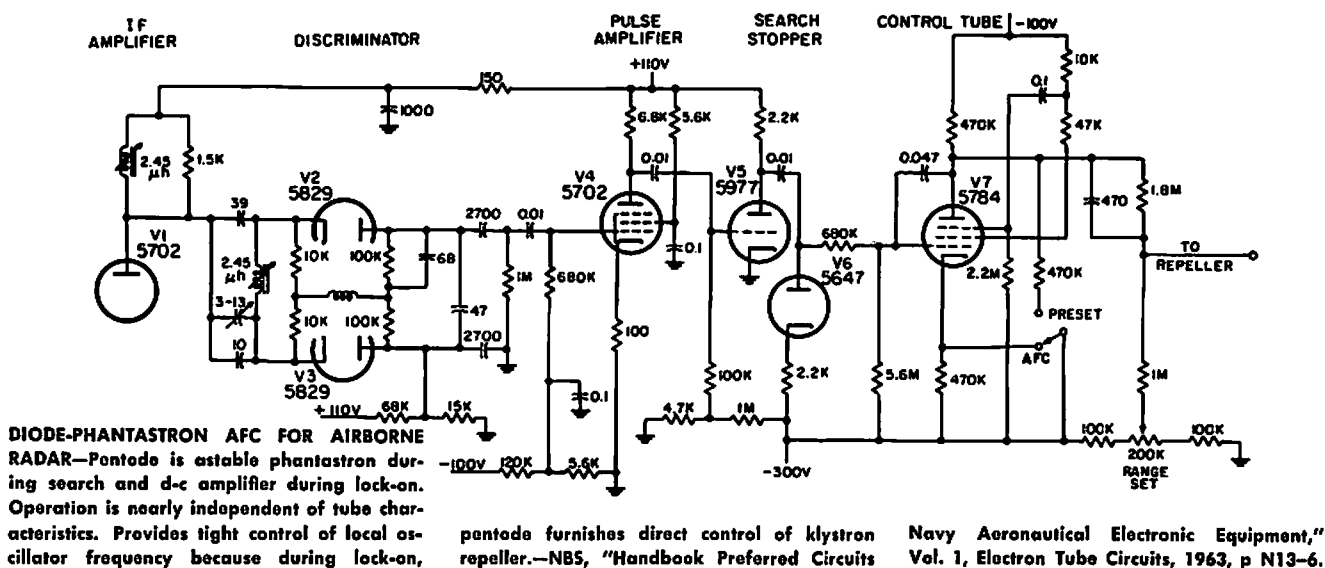
meg, and output impedance of 10 ohms.—B. Smith, Low-Noise FETs Sound Good To Circuit Designers, *Electronics*, 37:31, p 58-62.



50-W TWO-STAGE OUTPUT—Produces over 50-w rms audio power and has simple drive requirements.—High-Power Nu-Base Germanium Transistors (Dolco Radio ad), *Electronics*, 39:7, p 20-21.



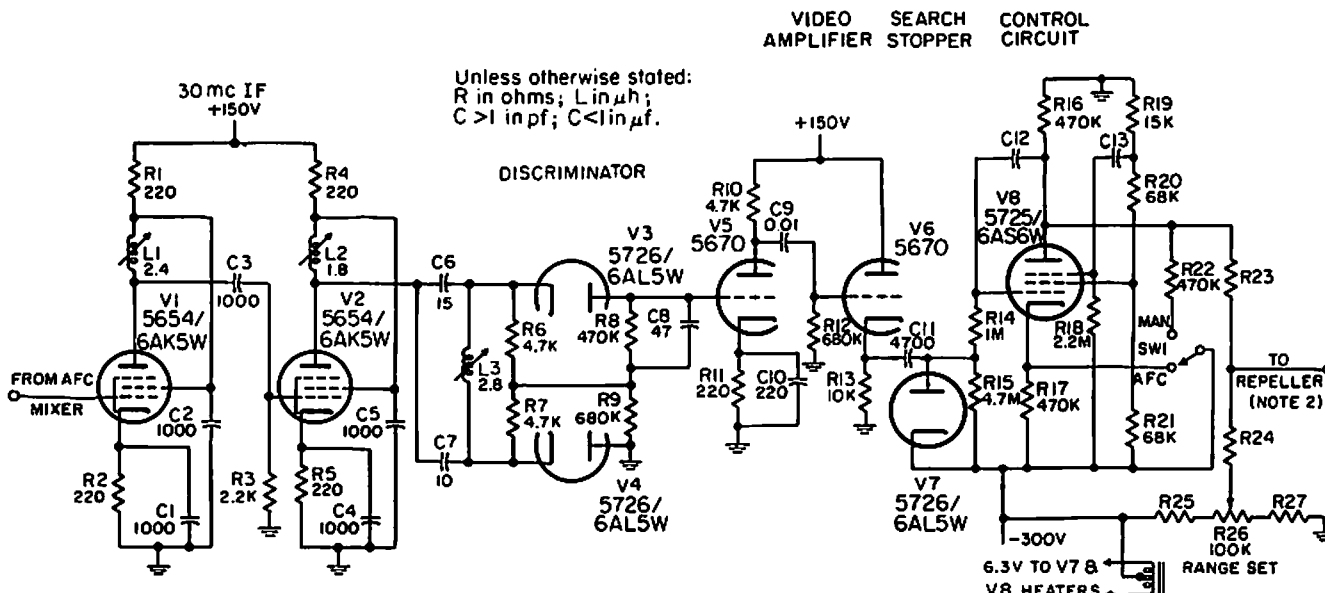
SINGLE-STAGE AUDIO AMPLIFIER—Design procedure is given for basic transistor stage.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 241.

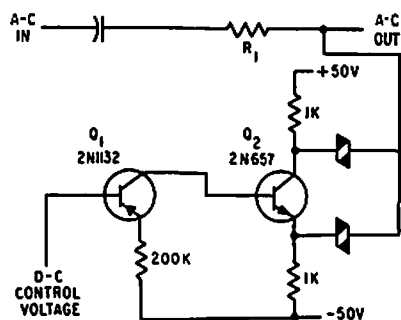


PREFERRED 30-MC I-F PULSE AFC—For use in pulse-modulated systems, to maintain a difference of 30 Mc between transmitter and local oscillator frequencies. If local oscillator is required to operate 30 Mc below transmitter, discriminator diodes V3 and V4 should

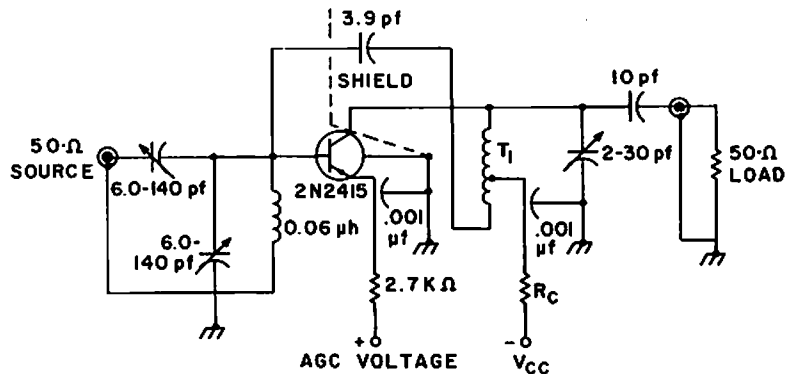
be reversed. Circuit is a hunting system, because local oscillator is swept over band of frequencies to find correct operating point. During search, phantastron V8 acts as sawtooth generator to provide sweep voltage for control element of local oscillator (repeller of

klystron).—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 53, p 53-2.





VOLTAGE-CONTROLLED GAIN—Response of two-transistor a-c amplifier, controlled by d-c voltage, can be made linear by adding suitable feedback. With no d-c control voltage on base of Q1, both transistors are saturated, and effective shunt resistance of circuit is about 500,000 ohms. When d-c control voltage is increased positively until Q1 is cut off, effective shunt resistance drops to 200,000 ohms.—L. C. Bowers, *Attenuator Controls Amplifier Gain*, *Electronics*, 34:39, p 150-153.



TYPICAL PERFORMANCE

$V_{CB} = -6v$ $I_C = -2ma$

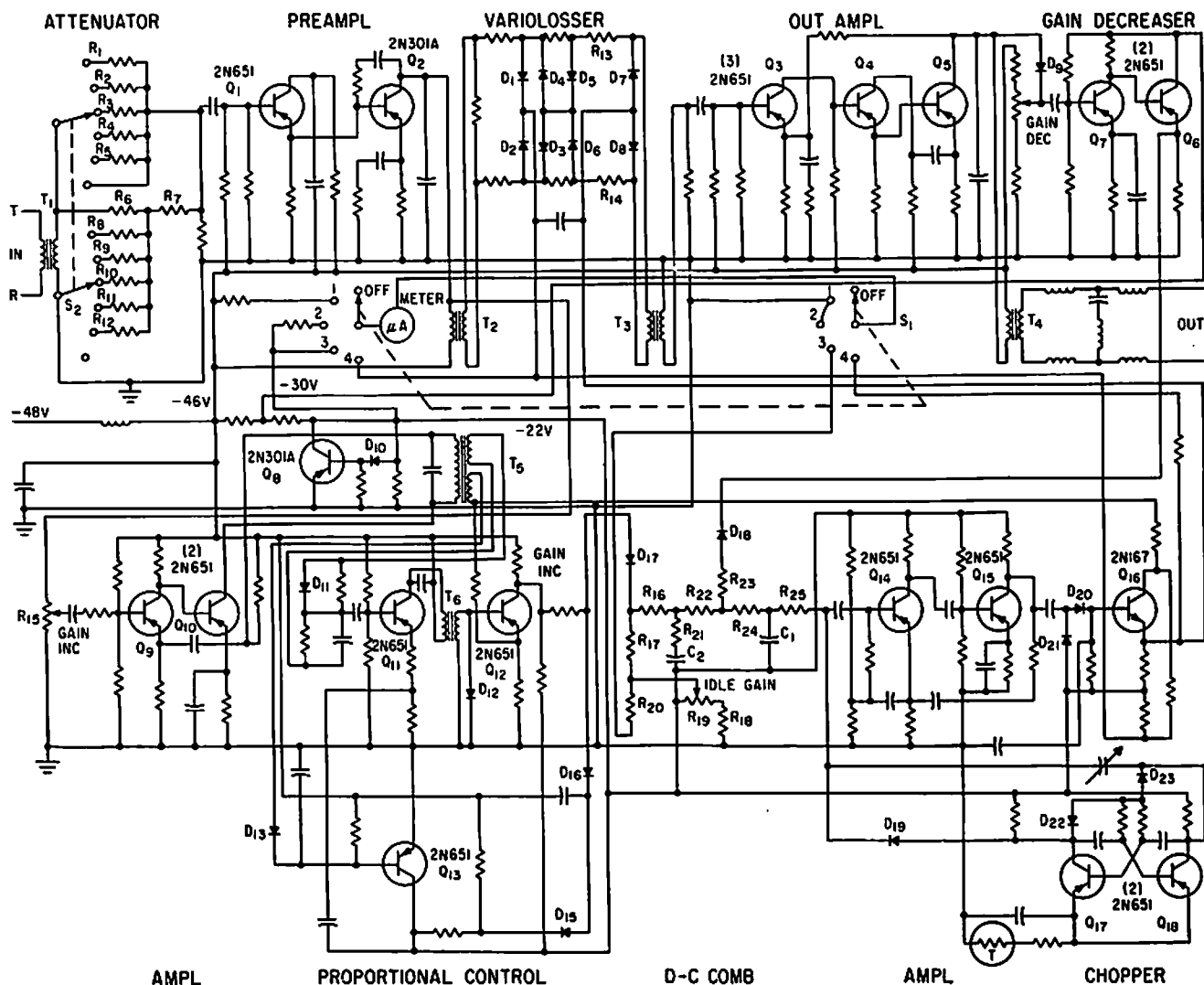
GAIN = 27 db

N. F. < 3 db

T_1 - 51 AIR DUX #516
TAPPED 4:1 FROM THE
COLLECTOR

70-MC NEUTRALIZED GAIN-CONTROLLED AMPLIFIER—Gain is 27 db, with typical noise figure below 3 db. RC for reverse gain control is 0 ohms, and for forward gain control

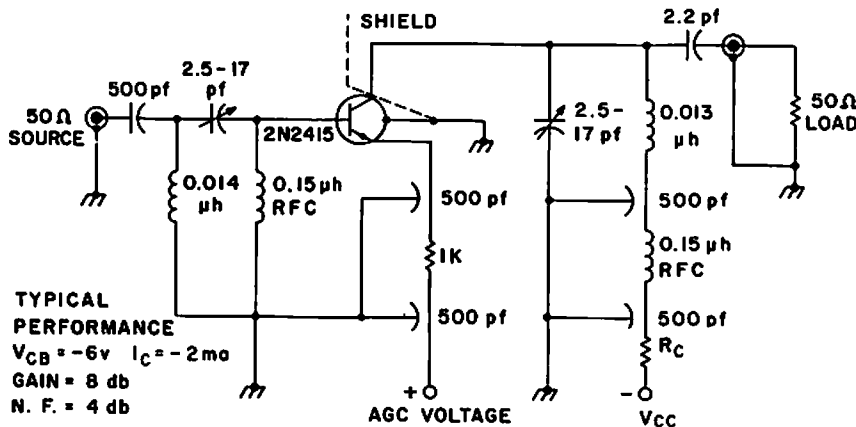
is 1,000 ohms. Reverse control range is 35 db, and forward gain control is 47 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 215.



AUDIO AGC FOR 40-DB RANGE—Automatic gain-adjusting amplifier produces constant output for speech level variations up to 40

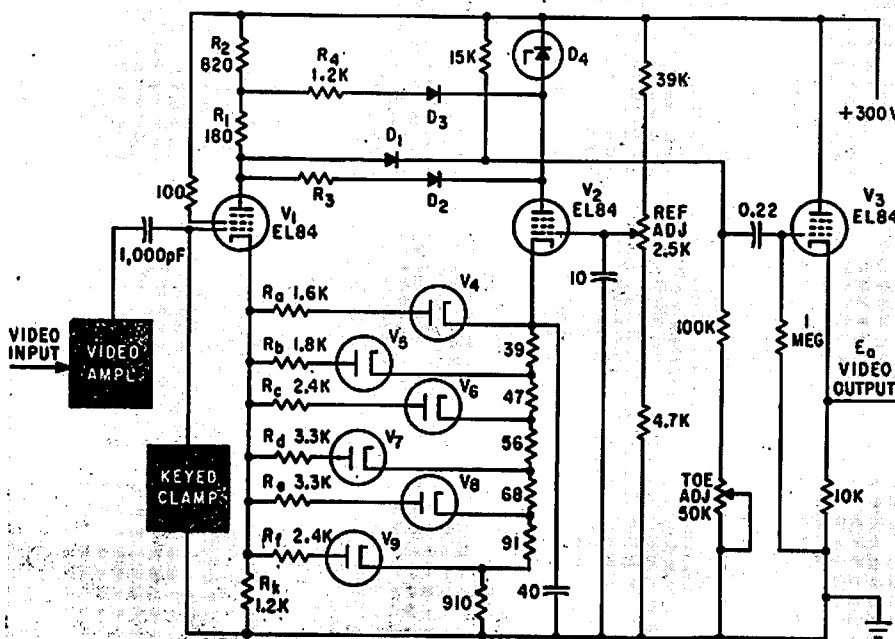
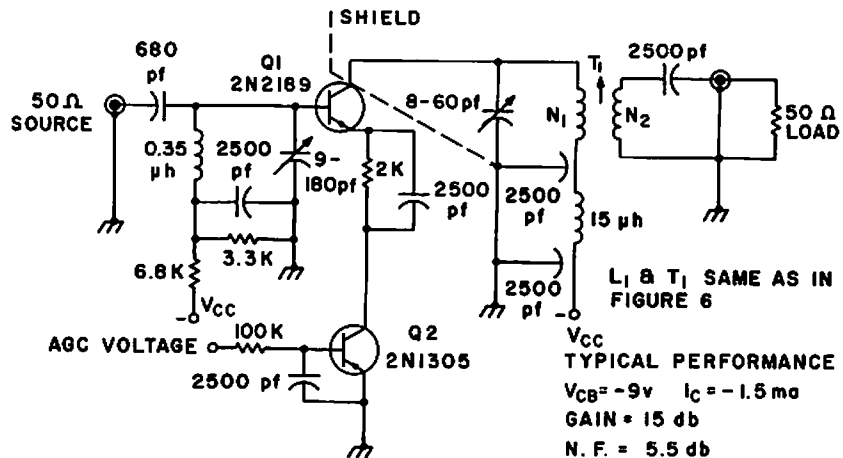
db. Intelligibility of speech is ensured by allowing instantaneous peaks to remain.—L. E. Getgen, *Amplifier Compensates for Speech-*

Level Variations, *Electronics*, 33:31, p 103-106.



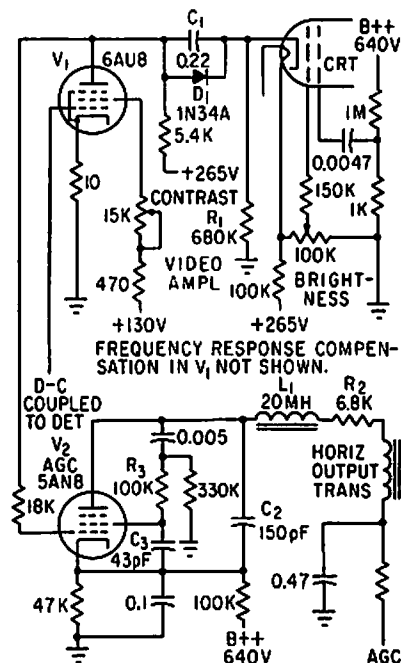
450-MC GAIN-CONTROLLED STAGE—Has gain of 8 db with typical noise figure of 4 db. Reverse gain control is 21 db for collector current of 20 microamp, and forward gain control is 26 db at 7 ma collector current.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 220.

30-MC HYBRID GAIN CONTROL—Q2 acts as variable impedance to give emitter degeneration, which is a form of external gain control. Q2 also controls collector current of Q1 to give reverse gain control action, which is internal gain control. Gain control range is 33 db, with 2:1 change in bandwidth.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 222.

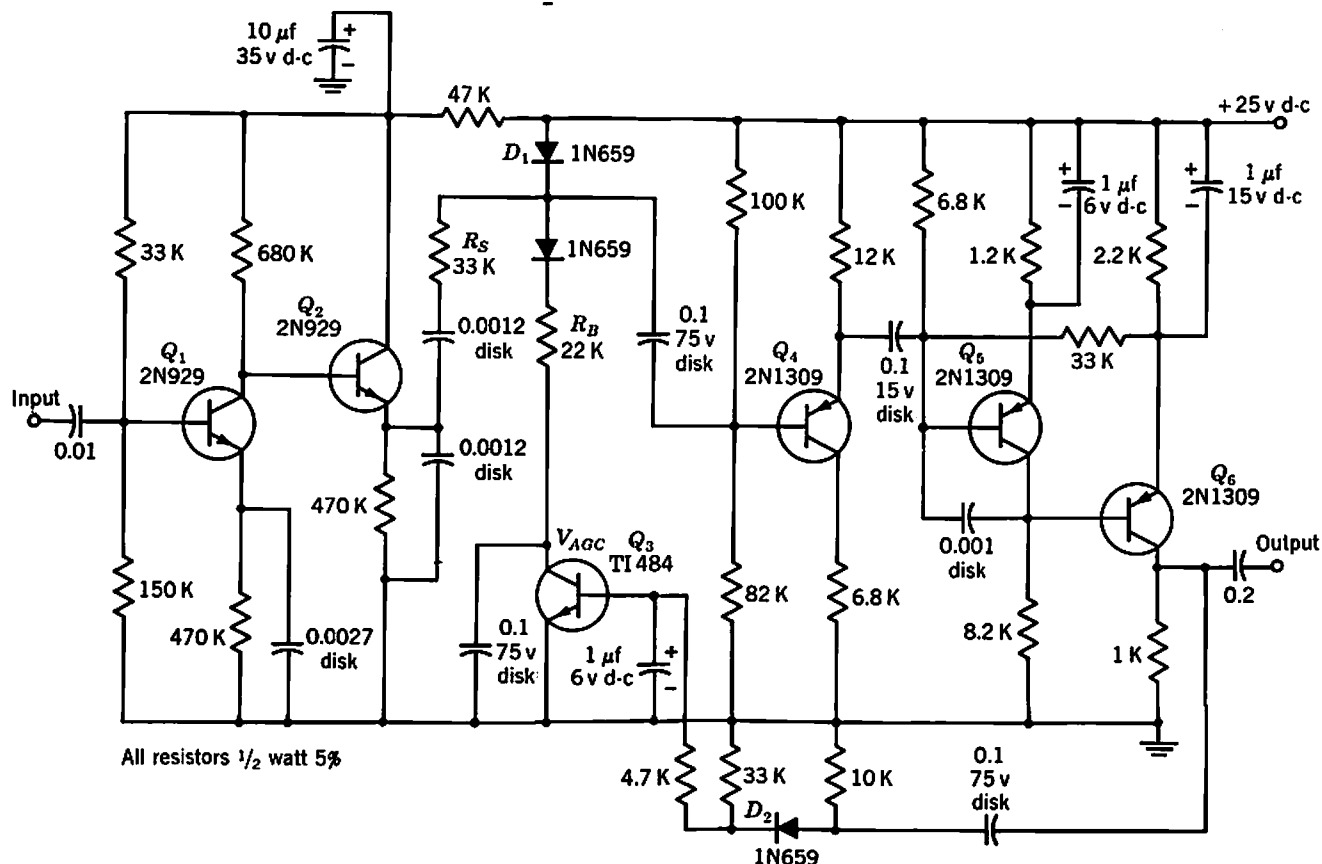
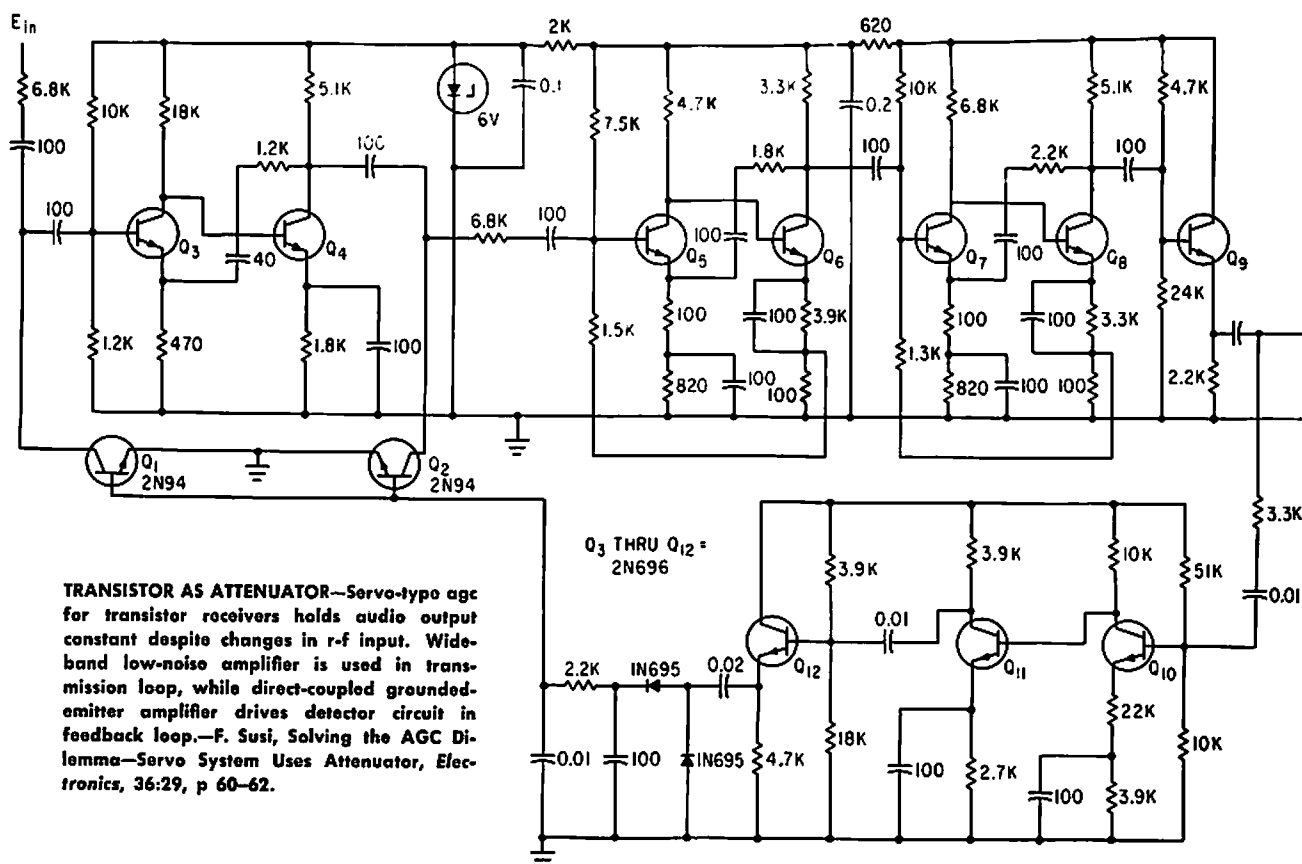


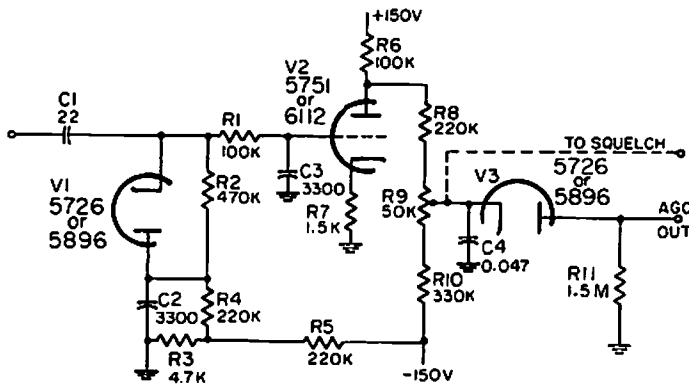
SIGNAL LEVEL CONTROLS GAIN—Amplifier is used with nonlinear circuit elements to get D-versus-log E characteristic approximating that of positive color film being scanned. When no signal is applied to grid of V1, all diodes in its cathode circuits are conducting, equivalent cathode resistance is lowest, and

stage gain is highest. As signal level increases, diodes V4 through V9 successively stop conducting, with V9 turning off last to make stage gain a minimum.—R. M. Farber and K. M. St. John, Scanner Analyzes Color Content of Movie Film, *Electronics*, 34:48, p 38-41.



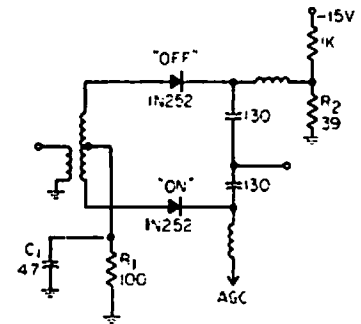
BACK-PORCH KEYED AGC—Composite of d-c coupling for dark scenes and a-c coupling for bright scenes, with agc referenced to back-porch (blanking level) rather than to sync tips, approaches ideal compromise for automatic control of tv picture.—L. Solomon, New Tubes and Circuits for Consumer Electronics, *Electronics*, 36:2, p 47-49.



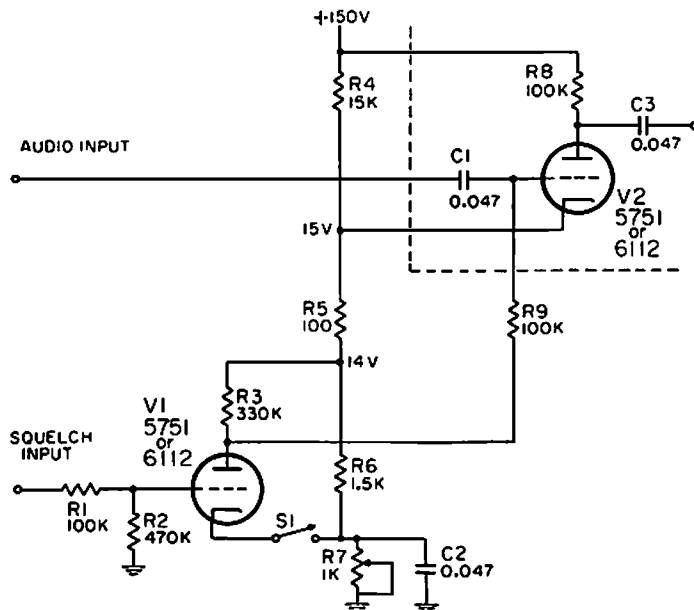


PREFERRED AGC AND SQUELCH CONTROL—Furnishes bias voltage for r-f and i-f stages of receiver, to minimize changes in output volume as input signal fades or as receiver is tuned to station having different signal strength. Additional output controls squelch tube that suppresses background noise in

absence of input signal. Maximum i-f input is 7 v rms. Maximum d-c output level is -27 v for miniature tube and -35 v for subminiature.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 63, p 63-2.

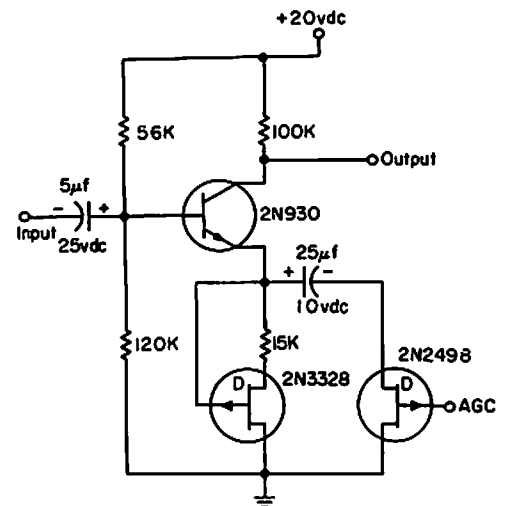


BRIDGE WITH AGC FOR ON DIODE—Input signal is applied in push-pull to two diodes, then combined by two capacitors. Reduction of agc bias increases attenuation of signal by bridge.—W. A. Rheinfolder, Designing Automatic Gain Control Systems, *EEE*, 13:1, p 53-57.

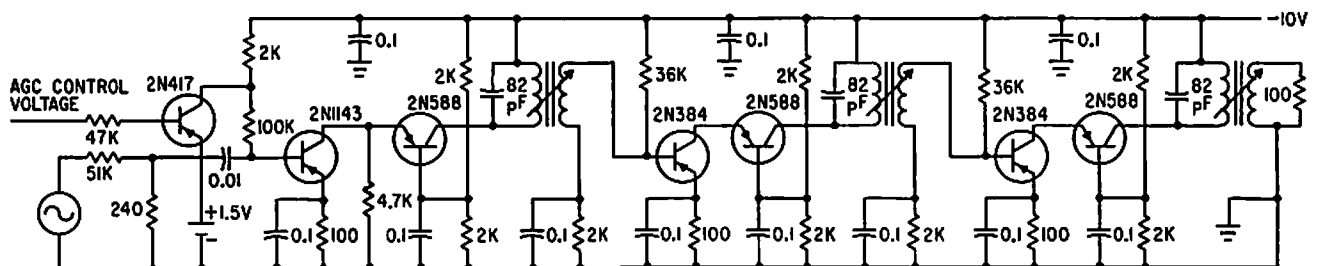


PREFERRED SQUELCH—Used in sensitive receivers having agc, to suppress objectionable increase in noise output when no signal is present, as when receiving intermittent transmissions. Uses d-c amplifier that is added to grid circuit of first audio stage to bias it

beyond cutoff and thereby silence it until usable signal arrives.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 64, p 64-2.



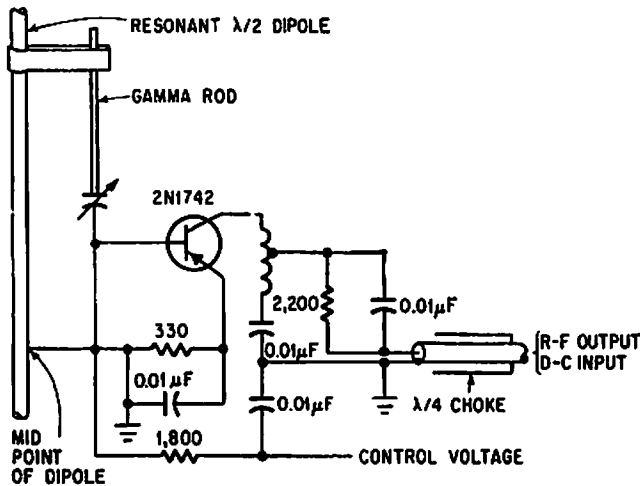
FET FOR AGC—Uses 2N2498 fet as variable-emitter resistor in common-emitter transistor amplifier. Low-current 2N3328 is used to supply constant emitter bias current and have very light dynamic loading on emitter for maximum agc range. Since variable resistor is capacitor-coupled to emitter of transistor, there is no change in bias current when strong agc voltage is suddenly applied. Absence of transient thump makes circuit desirable for broadcast speech compressors.—L. J. Sovin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 78.



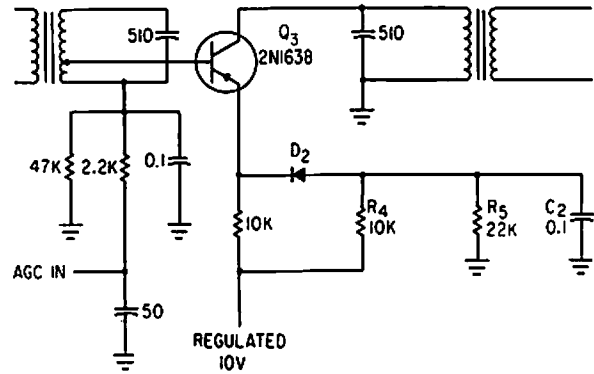
AGC FOR WIDE-BAND—2N417 transistor is gain control element for 10-Mc tuned i-f amplifier using transistor cascode circuit.

Bandwidth is 1.25 Mc and maximum gain is 91 db.—J. F. Perkins, Transistor Cascode Circuit Improves Automatic Gain Control in Am-

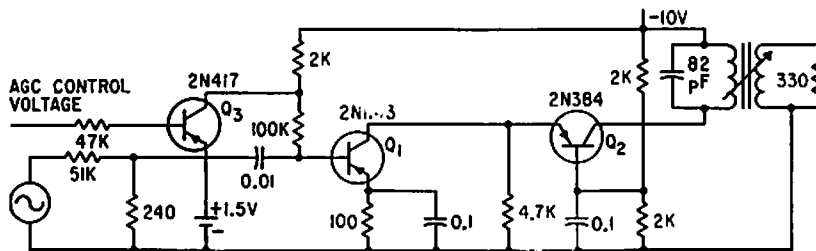
plifiers, *Electronics*, 34:22, p 49-51.



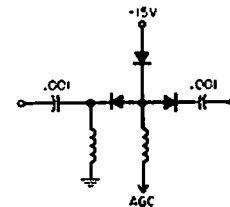
VARIABLE-GAIN ANTENNAFIER—Use of forward agc permits varying gain of amplifier mounted on dipole, as required for arrays.—J. F. Ripplin, *Making the Antenna an Active Partner*, *Electronics*, 38:16, p 93-96.



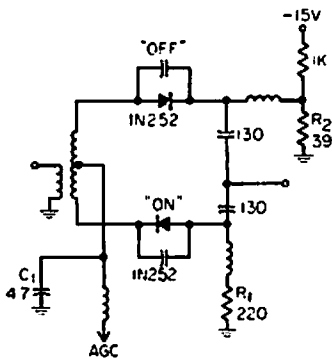
PREVENTING CLIPPING IN CONTROLLED STAGE—Agc bias controls negative current feedback in each controlled i-f stage. Diode D2 prevents clipping when forward bias falls below peak value of signal.—P. V. Sparks, *Servo Filter and Gain Control Improves Automatic Direction Finder*, *Electronics*, 34:23, p 110-113.



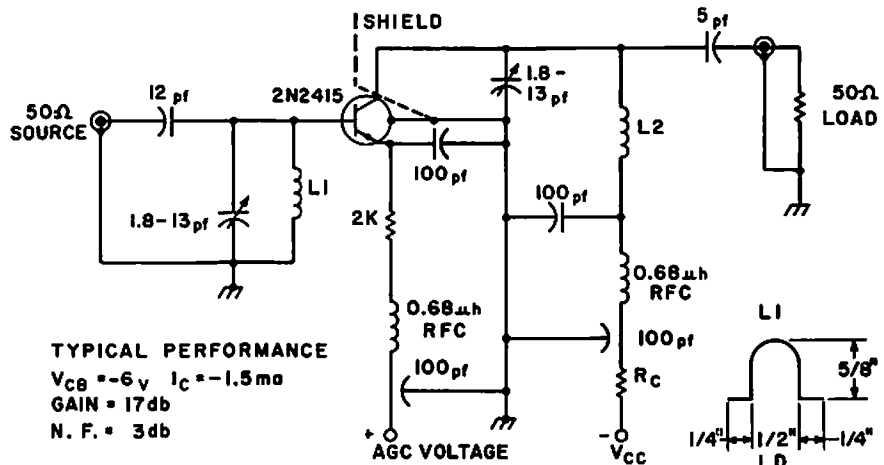
CASCODE I-F WITH AGC—Q3 is gain control element for cascode combination Q1-Q2 in 10-Mc i-f amplifier.—J. F. Perkins, *Transistor Cascode Circuit Improves Automatic Gain Control in Amplifiers*, *Electronics*, 34:22, p 49-51.



AGC WITH DIODE T-NETWORK VARICAP—Voltage-controlled capacitor circuit minimizes effect of shunt capacitance, thus reducing resonance peaks and preventing regeneration, but insertion loss is high (8 db).—W. A. Rhein-felder, *Designing Automatic Gain Control Systems*, *EEE*, 13:1, p 53-57.



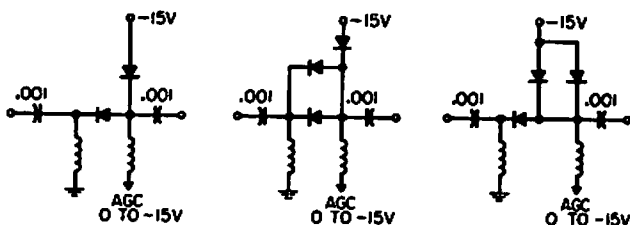
BRIDGE WITH AGC ON BOTH DIODES—Agc is applied to center tap of transformer, to turn one diode off while other is being turned on. Attenuation can be 40 db over bandwidth up to 250 Mc with agc bias of 0.5 to 3 v. Insertion loss is only a few db.—W. A. Rhoinfelder, *Designing Automatic Gain Control Systems*, *EEE*, 13:1, p 53-57.



TYPICAL PERFORMANCE
 $V_{CB} = -6V$ $I_C = -1.5mA$
 GAIN = 17db
 N. F. = 3db

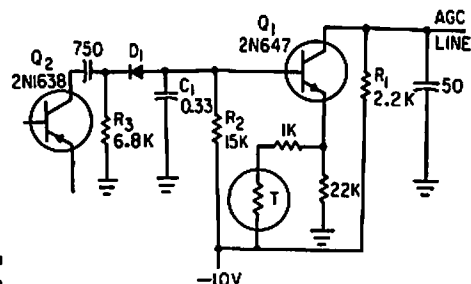
200-MC GAIN-CONTROLLED STAGE—Has gain of 17 db with typical noise figure of 3 db, 24 db of forward gain control, and 33 db of reverse gain control.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 218.

L1 = 1/4" X 1/32" COPPER STRAP BENT AS SHOWN ABOVE.
 L2 = 21 # 22 SOLDEREZE CLOSE WOUND ON C.T.C.
 PLS62C4L/200 63 NO SLUG.

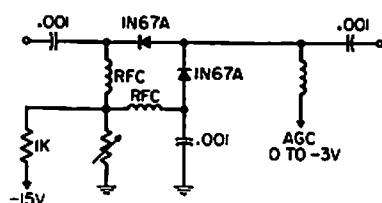


AGC WITH VARICAP—Basic voltage-controlled capacitor circuit uses capacitance variation with voltage of back-biased diode constructed to have large capacitance changes, such as Varicap. Circuits give different insertion losses and gain changes. Left: 5 db insertion loss and 16 db gain control range for agc bias

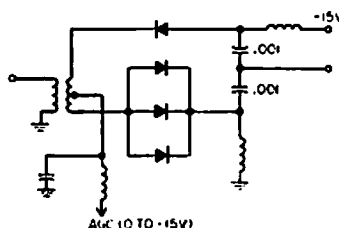
of 15 v. Center: 2 db loss and 11 db gain range. Right: 7 db loss and 18 db gain range. All can be reasonably flat for 200-Mc bandwidth.—W. A. Rheinfelder, *Designing Automatic Gain Control Systems*, EEE, 13:1, p 53-57.



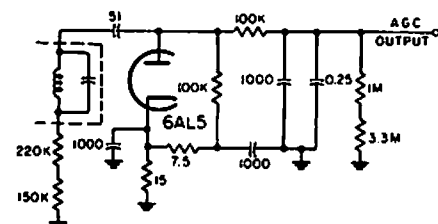
AGC FOR ADF—Gives uniform receiver response over wide dynamic range of input signal levels. Output of third i-f stage Q2 is applied across agc diode D1.—P. V. Sparks, *Servo Filter and Gain Control Improve Automatic Direction Finder*, *Electronics*, 34:23, p 110-113.



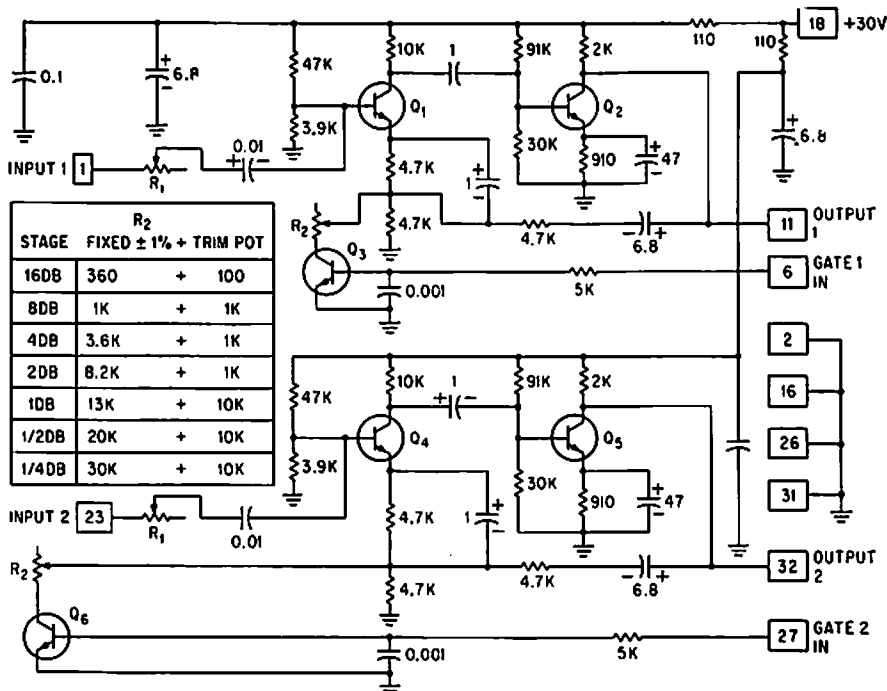
DIODE L-ATTENUATOR AGC—Two diodes provide maximum agc control. Circuit gives control range of about 15 db with agc voltages to 3 v. Above 200 Mc, frequency response changes irregularly with attenuation.—W. A. Rheinfelder, *Designing Automatic Gain Control Systems*, EEE, 13:1, p 53-57.



AGC WITH VARICAP DIODE BRIDGE—Uses voltage-controlled capacitors to provide very large gain control range, greater than 30 db.—W. A. Rheinfelder, *Designing Automatic Gain Control Systems*, EEE, 13:1, p 53-57.

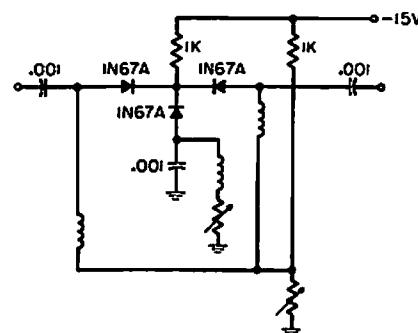


SIMPLE AGC—Used to keep output of communication receiver relatively constant with varying input signals.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, *Electron Tube Circuits*, 1963, p N12-4.

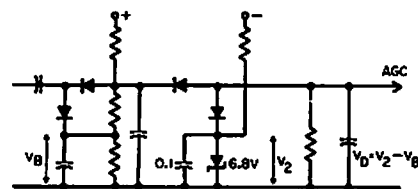


NINE FIXED-GAIN STAGES GIVE AGC—Monopulse radar amplifier stages are used in cascade, each with different fixed gain and a slicer that switches from that gain to unity gain if signal exceeds predetermined reference level. Give gain up to 80 db in 0.5-db

steps, equivalent to fast agc, to give constant 10-v output for signals ranging from 1 mv to 10 mv.—W. W. Smith, *Fast AGC Amplifier Locks Monopulse Radar on Target*, *Electronics*, 36:39, p 34-36.



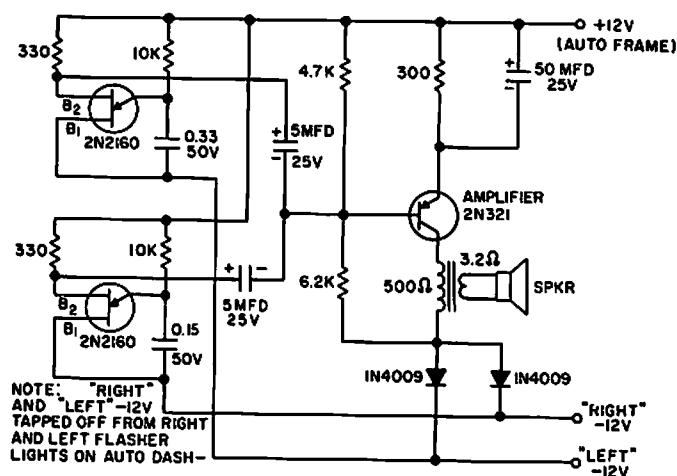
DIODE T-ATTENUATOR AGC—All three diodes are simultaneously controlled, to give excellent agc action over control range of 20 db, although insertion loss is high. Frequency response is excellent up to 150-Mc cutoff.—W. A. Rheinfelder, *Designing Automatic Gain Control Systems*, EEE, 13:1, p 53-57.



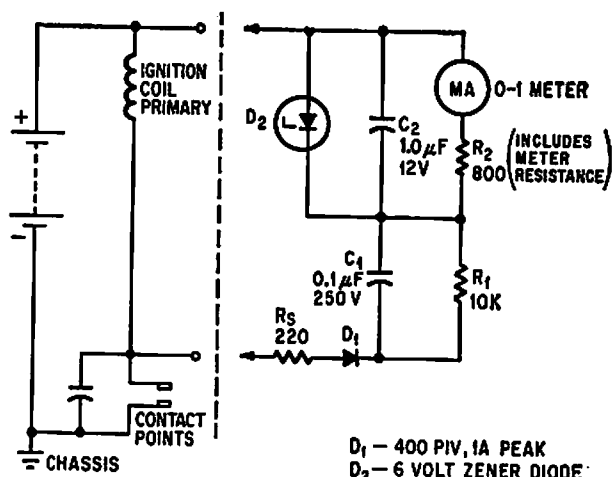
DOUBLE-GATED AGC—Uses zener diode to supply standing bias for agc bus.—W. A. Rheinfelder, *Designing Automatic Gain Control Systems*, EEE, 13:1, p 53-57.

CHAPTER 7

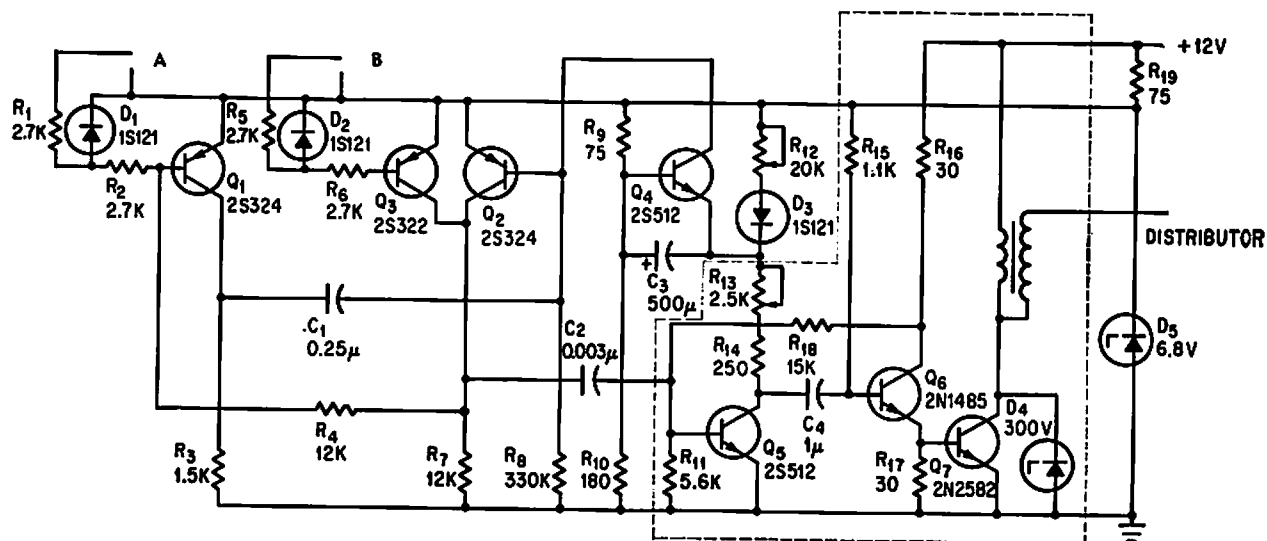
Automotive Circuits



AUDIBLE TURN-SIGNAL INDICATOR—Produces two different tones in synchronism with turn-signal flashers. Diodes prevent short-circuit. For autos with positive ground.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 381.



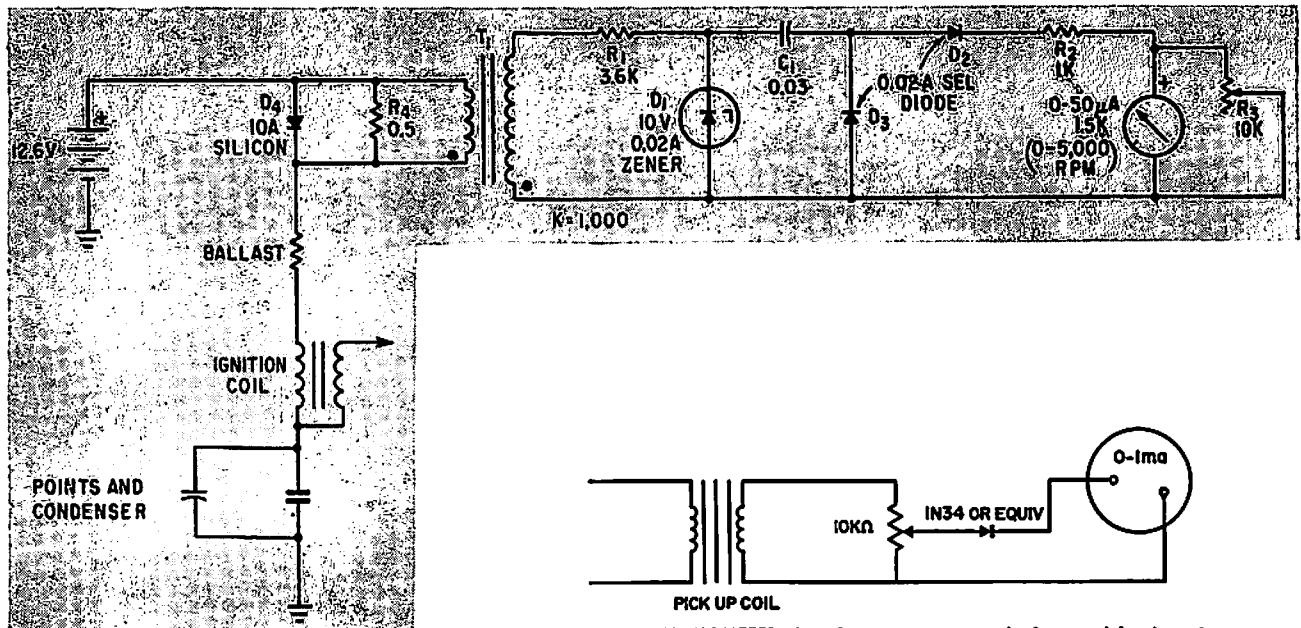
AUTO TACHOMETER—Connects to automobile circuit at battery and at distributor contact points. Zener diode D2 limits maximum charging voltage across C2.—J. A. Irvine, No Moving Parts in Auto Tachometer, *Electronics*, 39:9, p 77-78.



AUTOMATIC IGNITION ADVANCE—Inductive pickups on engine crankshaft feed to A and B, to make timing vary with engine speed by

triggering monostable delay placed ahead of basic Delcotron spark generator (dashed lines).—A. R. Hayes, *Electronically Controlling*

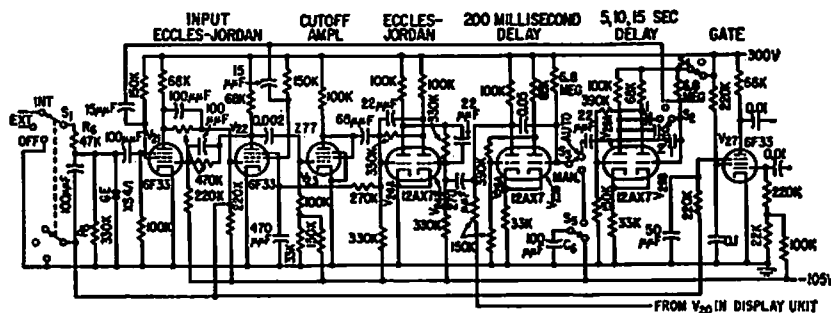
Auto's Engine Spark, *Electronics*, 37:32, p 43-44.



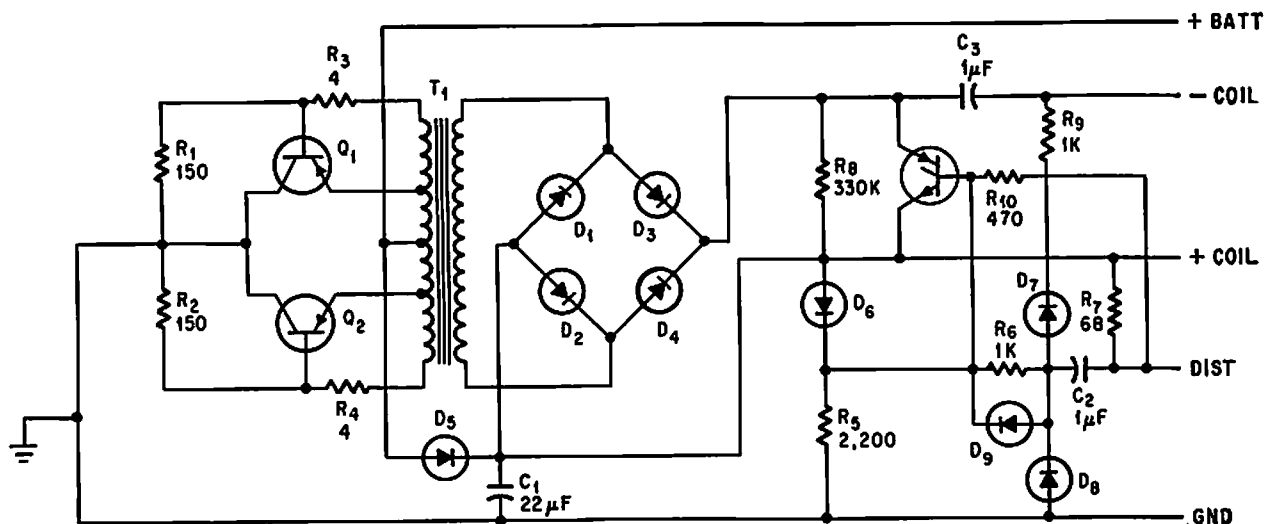
PASSIVE TACHOMETER—Circuit is placed in series with ignition coil to pick up ignition pulses and feed them to integrating rate-meter calibrated in rpm. Number of pulses per shaft revolution depends on number of cylinders.—F. Trainor, Unique Engine Tachometer Uses only Passive Components, *Electronics*, 35:30, p 40-41.

ZERO-POWER ENGINE TACHOMETER—Requires no battery or other power source. Coil from 10K relay serves as pickup for mounting near rotating magnets of flywheel of outboard motor or magneto. When rotating magnets are not available, as in most automotive engines, variable-reluctance pickup is used. This

can be made from old loudspeaker, mounted so fan blades of generator pass between pickup coil and permanent magnet. Coil mounting should be aluminum to maintain calibration that is made with commercial tachometer.—K. M. Bronscome, Engine Tachometer, *EEE*, 10:9, p 27.



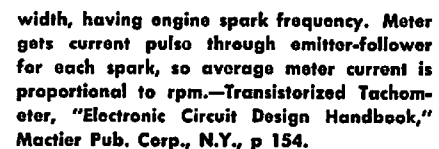
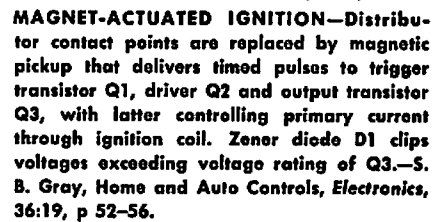
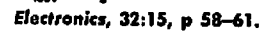
AUTO-LOCKING GATE FOR TACHOMETER DISPLAY—Permits pulses to pass to display unit during gating period. Display can be held for 5, 10, or 15 sec by switching different capacitors into delay mvbr. Additional 200-millisecc delay gives time for counting tubes to return to zero.—J. K. Goodwin, Digital Tachometer Aids in Turbine Design, *Electronics*, 32:15, p 58-61.

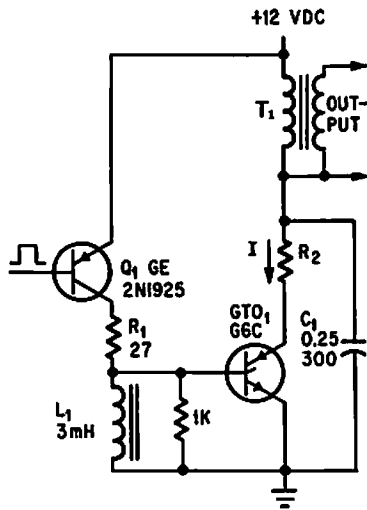


AUTO IGNITION—Capacitive-discharge ignition system uses scr as switch. Transistors

serve as d-c to d-c converter.—R. Van Houten and J. C. Schweitzer, A New Ignition System

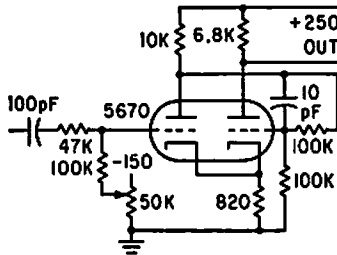
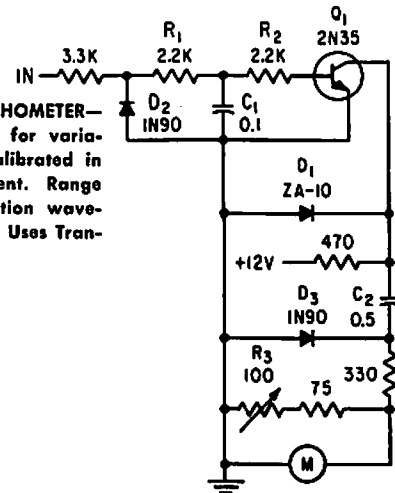
For Cars, *Electronics*, 37:26, p 68-72.



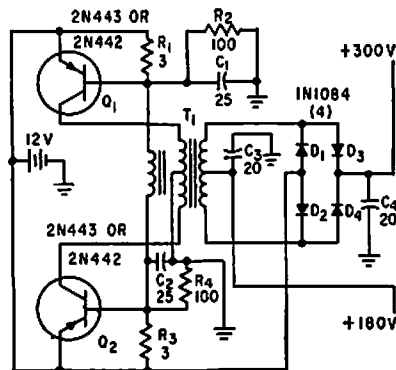


HIGH-VOLTAGE PULSE GENERATOR—Square-wave input to transistor Q1 triggers scr on and off, inducing high-voltage damped-oscillation pulse in secondary of T1. Used for auto ignition and other applications requiring up to 30 kv from 0 to 400 times per second. —D. R. Grafham, Now the Gate Turnoff Switch Speeds Up D-C Switching, *Electronics*, 37:12, p 64-71.

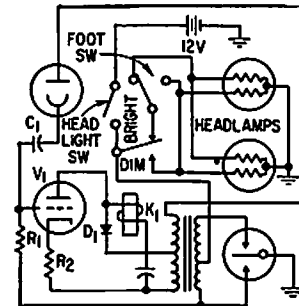
SINGLE-TRANSISTOR AUTO TACHOMETER—Uses zener diode to compensate for variations in 12-v supply. Meter is calibrated in rpm, using R3 for final adjustment. Range is 0 to 6,000 rpm. Input is ignition waveform.—J. Cowan, Auto Tachometer Uses Transistor, *Electronics*, 31:33, p 92-94.



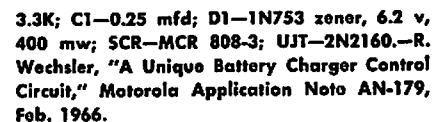
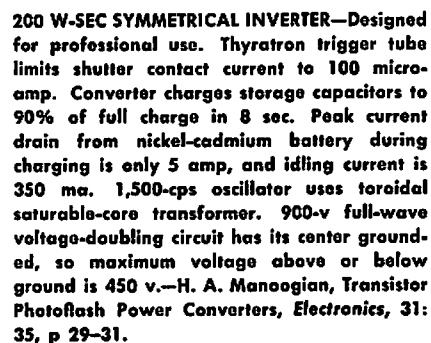
AMPLITUDE DISCRIMINATOR—High-speed trigger with adjustable bias network and cathode-follower output serves as amplitude discriminator for tachometer that responds to pulses produced by gamma radiation sources on sealed-in rotating parts not directly coupled to input or output shafts of transmissions or turbines.—R. R. Bockemuehl and P. W. Wood, Unique Two-Channel Tachometer uses Radioisotopes, *Electronics*, 35:49, p 44-45.

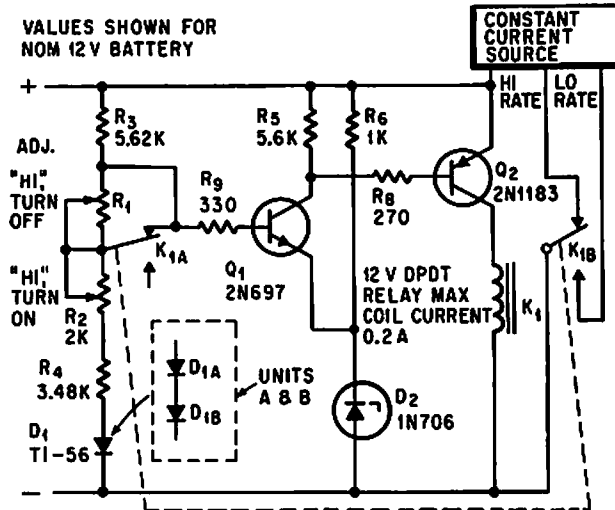


120-W, 300-V D-C AUTO CONVERTER—For 12-v auto systems. Develops square-wave voltage at 200 cps, for conversion to 300 v d-c by silicon-diode bridge rectifier. Each switching transistor requires 7-inch-square sheet of 1/4th-inch aluminum as heat sink.—W. E. Bushor, Electronics and the American Automobile, *Electronics*, 31:47, p 73-79.

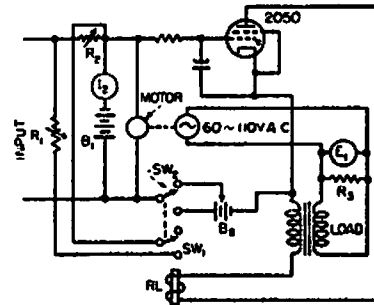


HEADLIGHT DIMMER—Automatically switches from high to low beam when oncoming headlight strikes photocell. Driver may dim lights manually at any time and leave them dim. When lights have been dimmed automatically, momentary reduction of light on photocell will not cause return to high beam.—W. E. Bushor, Electronics and the American Automobile, *Electronics*, 31:47, p 73-79.

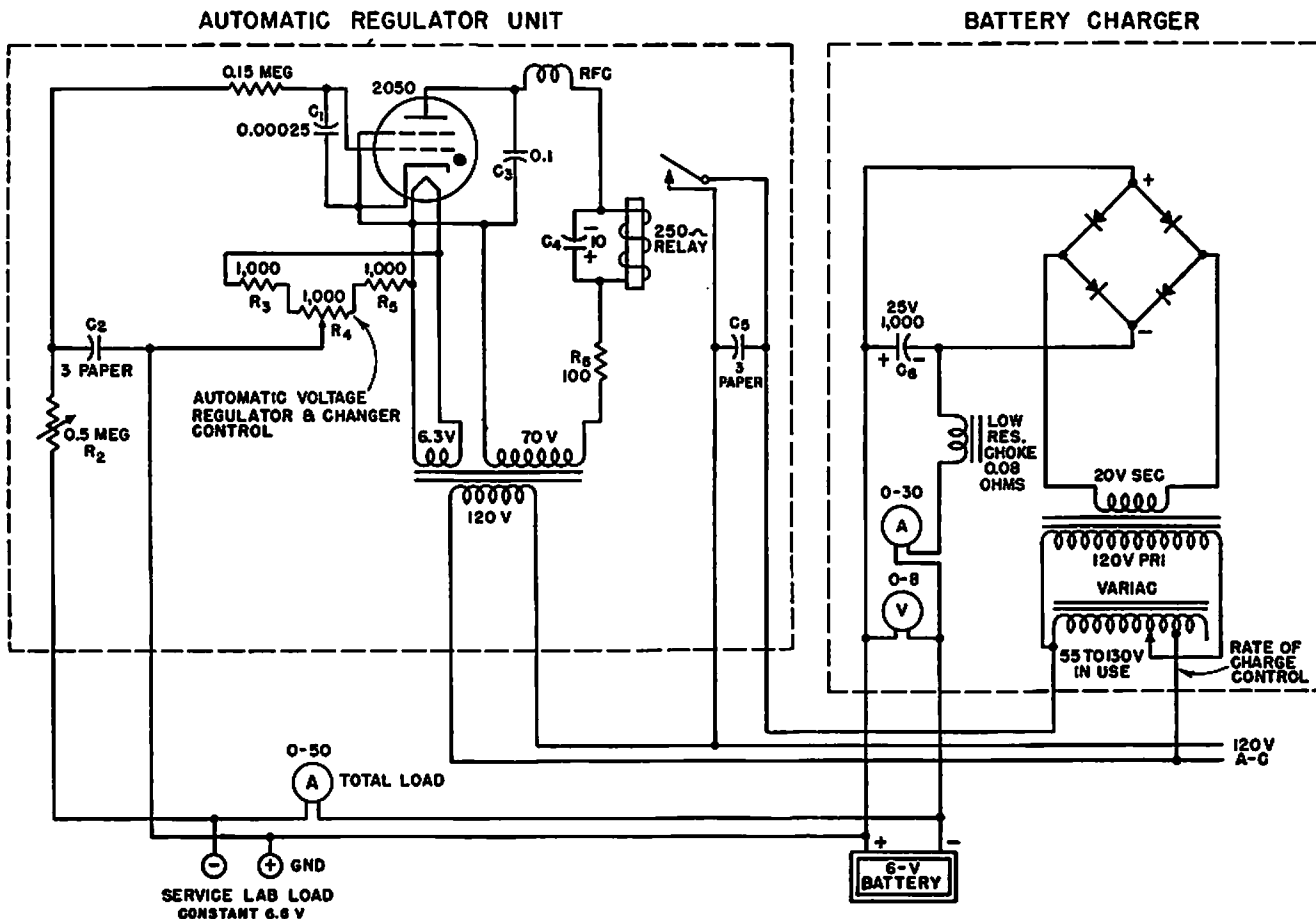




QUASI-CONSTANT-CURRENT BATTERY CHARGER—Circuit monitors state of charge of battery while charging at constant high rate, then transfers automatically to constant-current trickle charge when battery is fully charged.
—A. Anton, *Comparator Controls Battery Charging Rate*, *Electronics*, 37:12, p 72.



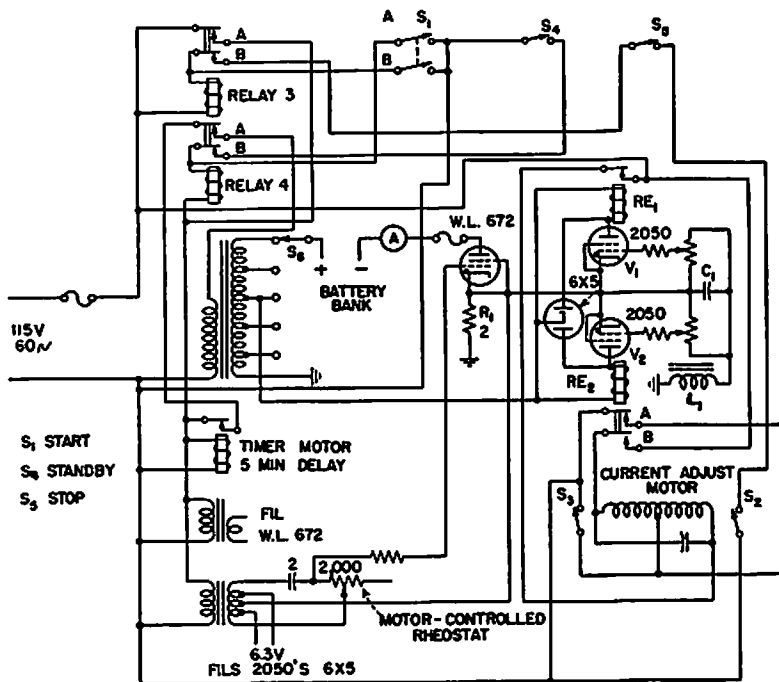
CHARGER CONTROL WITH REFERENCE BATTERY—Developed to insure constant d-c supply for rotary converters if ship's power supply fails. Control circuit operates SW1 at proper time intervals. B2, in series with B1 with polarity opposing, supplies reference voltage. When storage battery needs charge, gas tube ignites to pull in RL and initiate charging cycle.—V. Zeluff and J. Markus, "Electronics Manual for Radio Engineers," McGraw-Hill, N.Y., 1949, p 545.



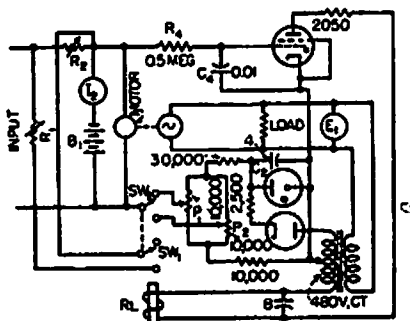
THYRATRON CONTROLS CHARGER RECTIFIER—Automatic regulator turns charger off when battery voltage exceeds predetermined value,

and turns charger on again automatically at any desired lower voltage from 5 to 7.5 v. Line voltage changes do not affect adjustment.—

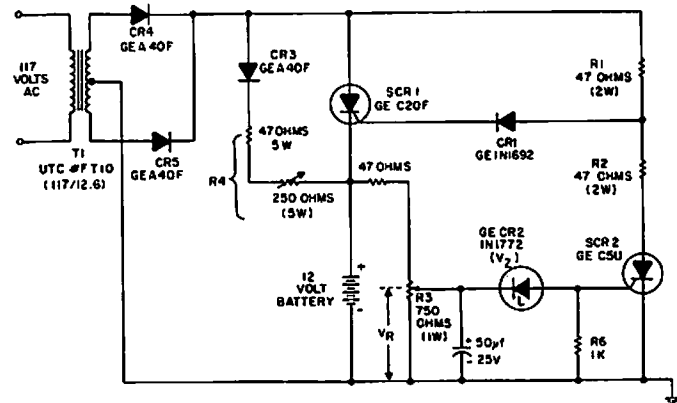
J. Markus and V. Zeluff, "Handbook of Industrial Electronic Circuits," McGraw-Hill, N.Y., 1948, p 257.



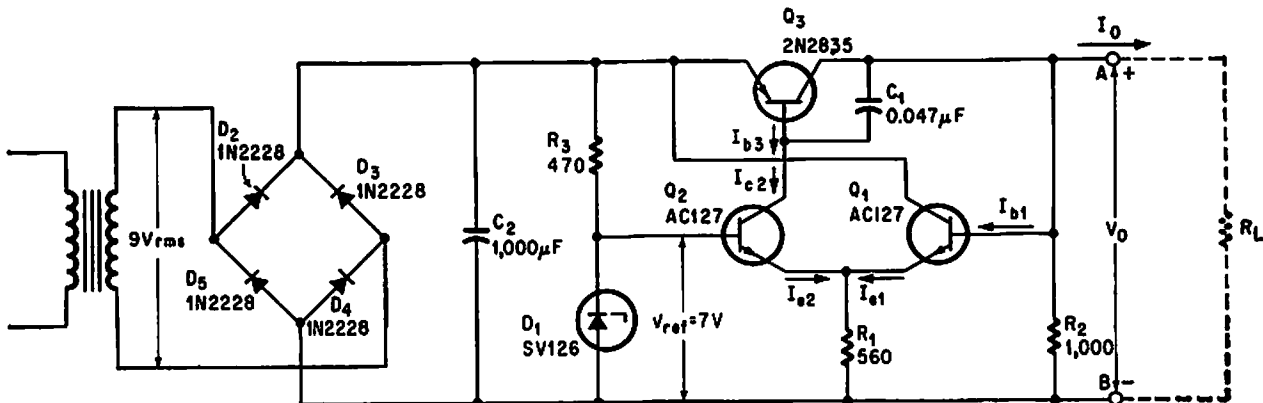
CONSTANT-CURRENT BATTERY CHARGER—Thyatron-controlled motor drives phasing control rheostat to give fully automatic charging of 50 2-v storage cells at constant rate of 2 amp. —J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 150.



CHARGER CONTROL WITH REGULATED D-C REFERENCE—Eliminates need for separate reference battery. Control fires thyatron to pull in or out and initiate charging cycle when battery voltage drops.—V. Zeluff and J. Markus, "Electronics Manual for Radio Engineers," McGraw-Hill, N.Y., 1949, p 545.



SCR BATTERY-CHARGING REGULATOR—Can charge 12-v battery at up to 6-amp rate. When battery voltage reaches charged level, charging scr shuts off, and trickle charge determined by R4 flows.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 109.



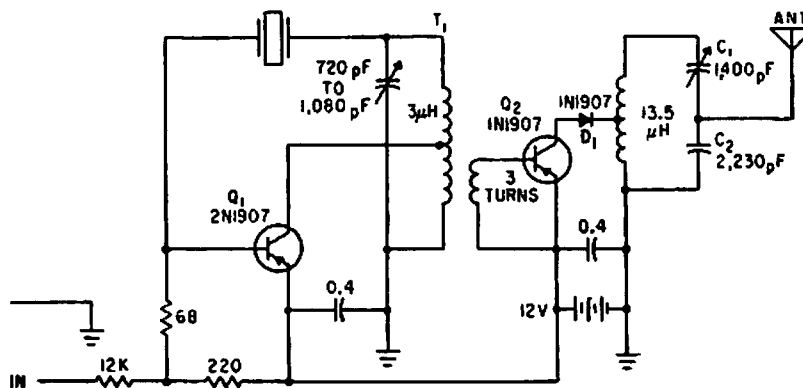
CHARGING CURRENT LIMITER—Series regulator transistor is driven by differential ampli-

fier using npn transistors, to control charging of nickel-cadmium battery.—G. H. P. Kohnke,

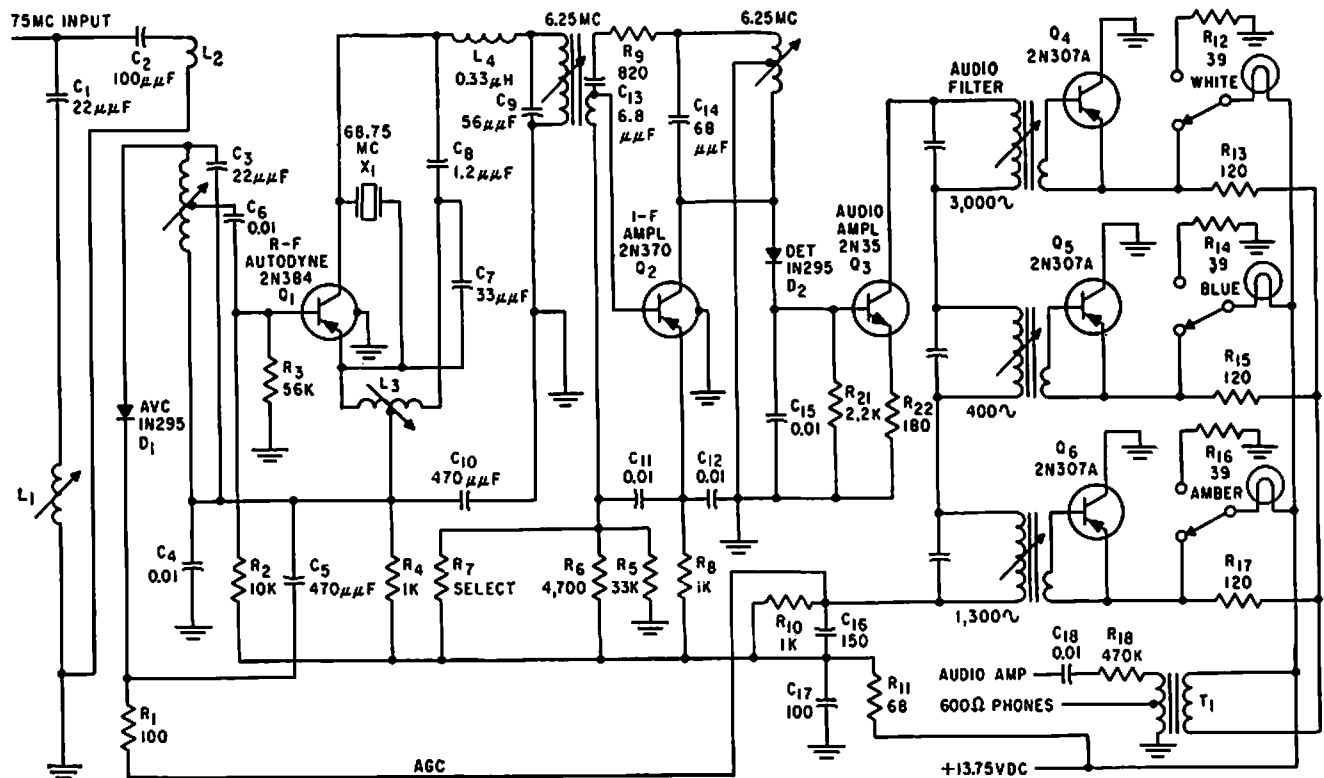
Simple Voltage Regulator Limits Load Current, *Electronics*, 37:28, p 63.

CHAPTER 9

Beacon Circuits



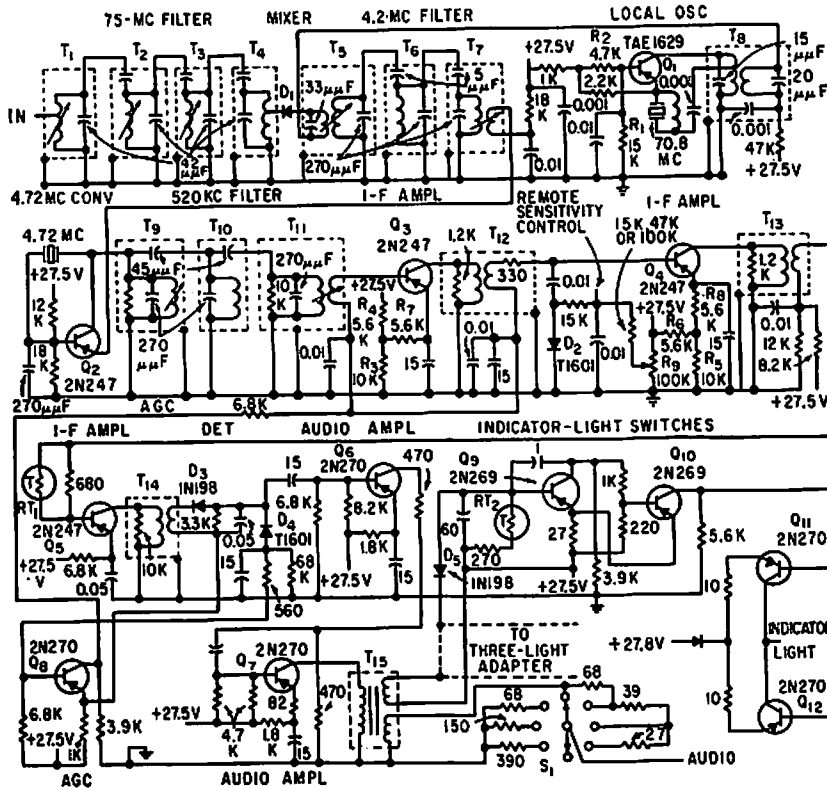
BALLOON TELEMETER AND BEACON—Delivers 10 w at 1,686 kc into 72 ohms at 60% efficiency. Can also operate at 7 Mc if crystal and tank are changed.—F. W. Frykman and A. R. Moore, *Lightweight Transmitter Provides Flight Data and Beacon Signal*, *Electronics*, 34:32, p 164.



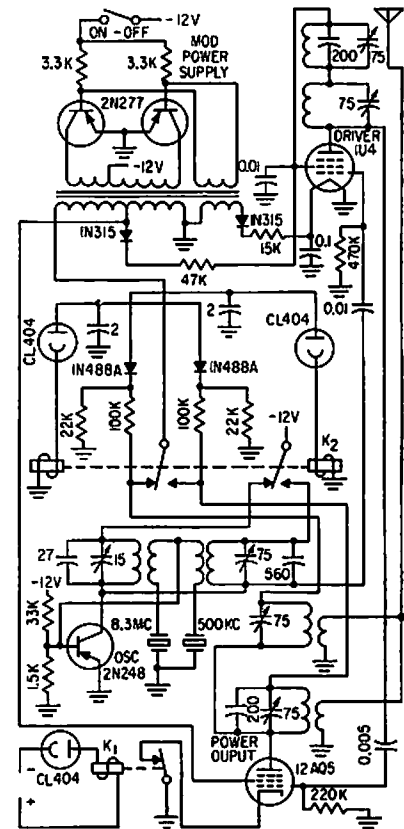
3-LIGHT SUPERHET MARKER BEACON—Gives audio output as well as colored-light presentation in aircraft. Superheterodyne provides

immunity to spurious activation of lamps by tv stations.—F. P. Smith, *Transistorized Receiver for Marker Beacon Use*, *Electronics*,

32:46, p 76-78.



1-LIGHT AIRBORNE MARKER BEACON—Dual-conversion 75-Mc receiver has high first i-f for good image rejection and lower second i-f for stable gain. Responds to any of three modulating frequencies (3,000 cps airways, 400-cps outer runway, and 1,300-cps middle runway).—R. G. Erdmann, Transistor Dual Conversion for Marker-Beacon Receivers, *Electronics*, 32:19, p 59-61.

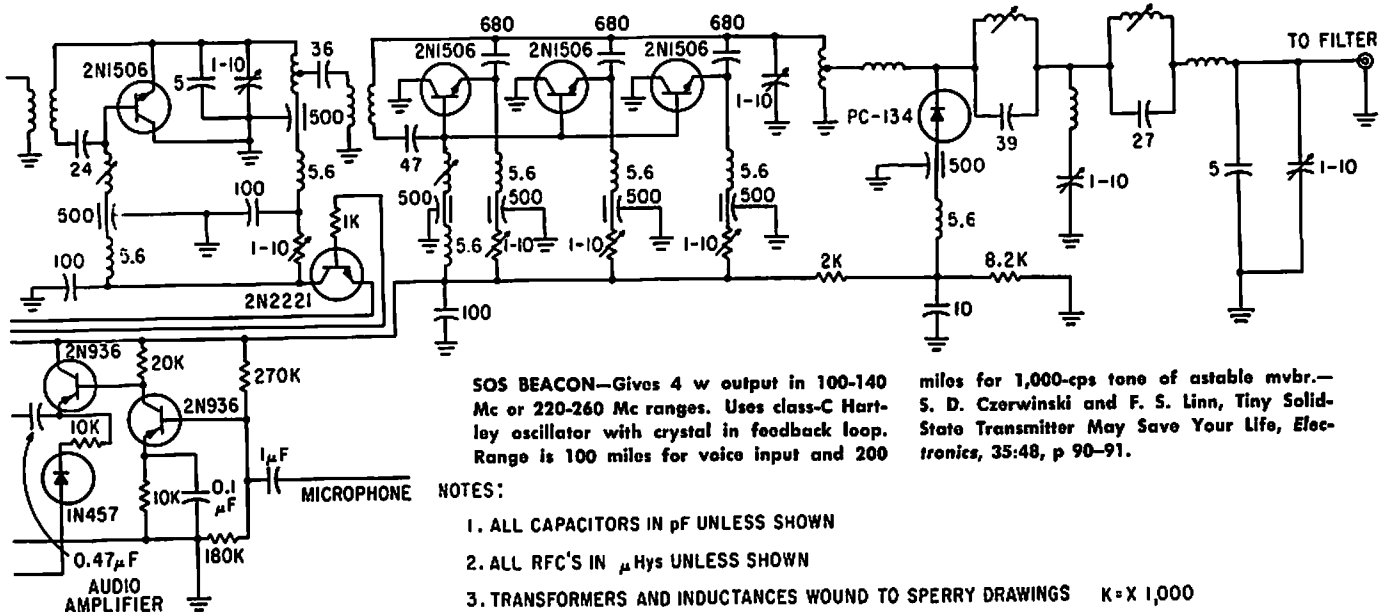


SOS ON TWO FREQUENCIES—Transistors and tubes are combined for maximum power efficiency at 5-w output. Operates on 500-kc and 8.326-Mc distress frequencies. Code-wheel-operated photoelectric flip-flop automatically switches bands and keys transmitter in SOS code.—H. B. Weisbecker, Distress Transmitter is Hybrid, *Electronics*, 31:31, p 98-100.

PREAMPLIFIER

POWER AMPLIFIER

TRIPLER



SOS BEACON—Gives 4 w output in 100-140 Mc or 220-260 Mc ranges. Uses class-C Hartley oscillator with crystal in feedback loop. Range is 100 miles for voice input and 200

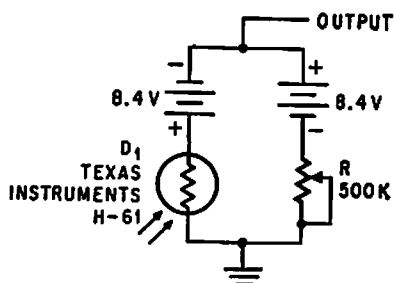
miles for 1,000-cps tone of astable mvbr.—S. D. Czerwinski and F. S. Linn, Tiny Solid-State Transmitter May Save Your Life, *Electronics*, 35:48, p 90-91.

NOTES:

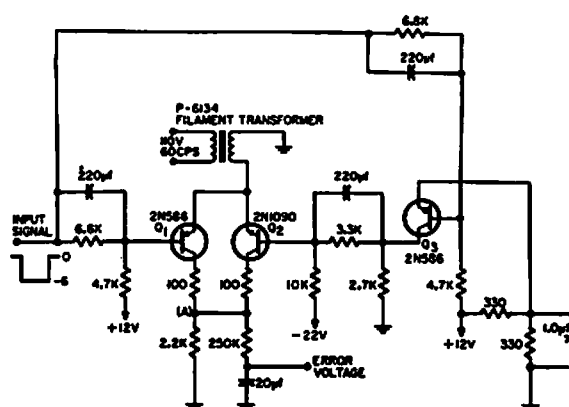
1. ALL CAPACITORS IN pF UNLESS SHOWN
2. ALL RFC'S IN μ Hs UNLESS SHOWN
3. TRANSFORMERS AND INDUCTANCES WOUND TO SPERRY DRAWINGS K=X 1,000

CHAPTER 10

Bridge Circuits

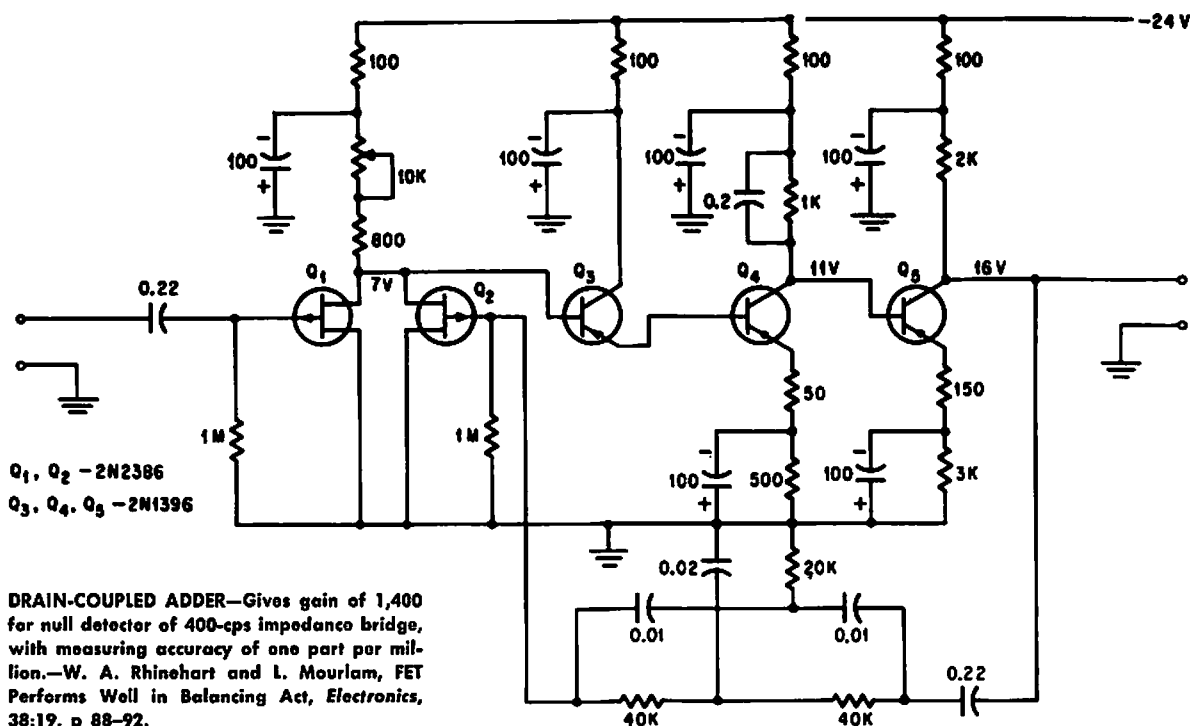


PULSE HEIGHT DETECTOR—Photodiode held against screen of scope unbalances bridge when illuminated by pulse on screen. Bridge output can control another scope for displaying and measuring pulses whose amplitude and period vary randomly.—I. Baird, *Pulse Frequency Measured by Photoconductor and Scopes*, *Electronics*, 38:13, p 77.

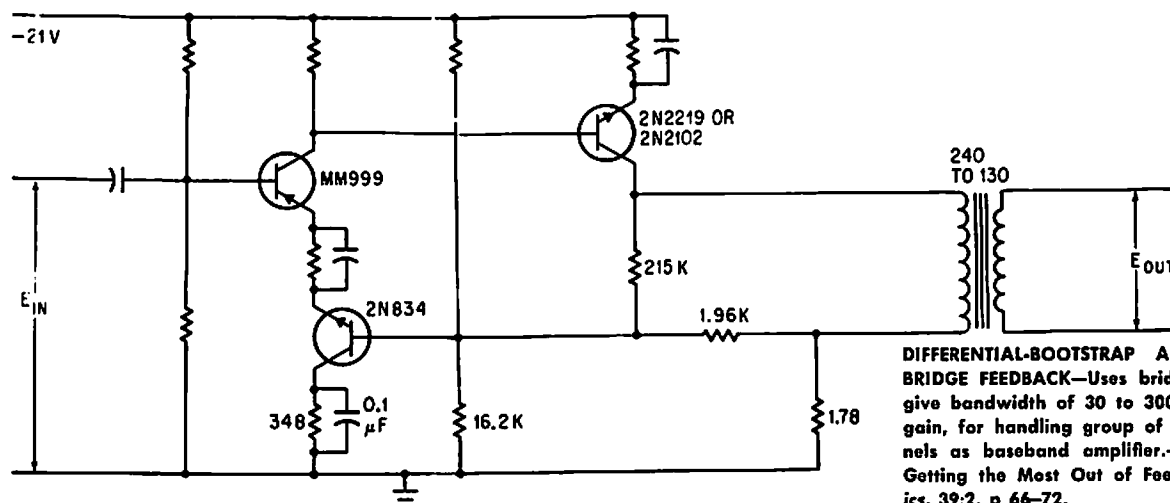


PHASE-DIFFERENCE BRIDGE—Develops d-c error voltage proportional to phase difference between two applied signals, one of which is 60-cps line-frequency reference. Can also

correct oscillator outputs and serve as pulse-width discriminator.—D. P. Dorsey, *Transistor Bridge Detector*, *EEE*, 13:1, p 75.

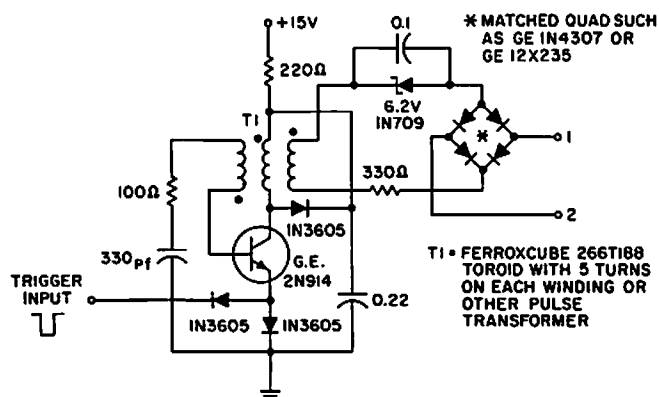


DRAIN-COUPLED ADDER—Gives gain of 1,400 for null detector of 400-cps impedance bridge, with measuring accuracy of one part per million.—W. A. Rhinehart and L. Mourlam, *FET Performs Well in Balancing Act*, *Electronics*, 38:19, p 88-92.

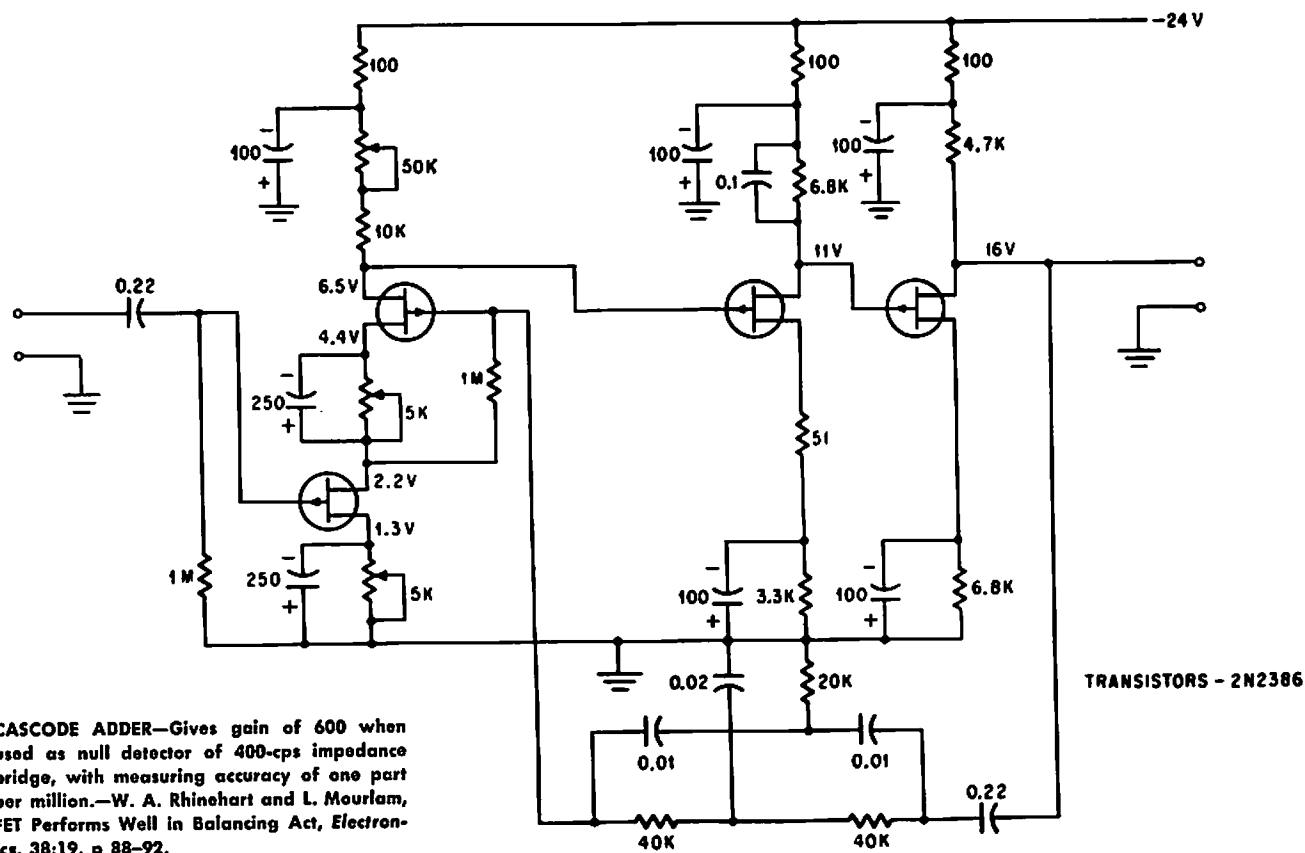


DIFFERENTIAL-BOOTSTRAP AMPLIFIER WITH BRIDGE FEEDBACK—Uses bridge feedback to give bandwidth of 30 to 300 kc with 40 db gain, for handling group of telephone channels as baseband amplifier.—N. A. Zellmer, *Getting the Most Out of Feedback, Electronics*, 39:2, p 66-72.

DIODE SAMPLING BRIDGE—Uses diode matched quad with blocking-oscillator driving circuit. Negative input pulse triggers oscillator, generating pulse about 100 nsec wide, to forward-bias bridge diodes and reduce impedance between terminals 1 and 2 to about 5 ohms. Between pulses, diodes are reverse-biased by capacitor charge and impedance between terminals rises to 1,000 meg.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 450.

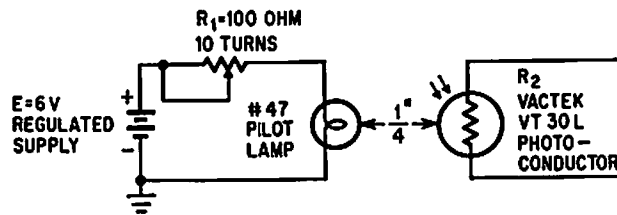


T1 = FERROXCUBE 266T188 TOROID WITH 5 TURNS ON EACH WINDING OR OTHER PULSE TRANSFORMER



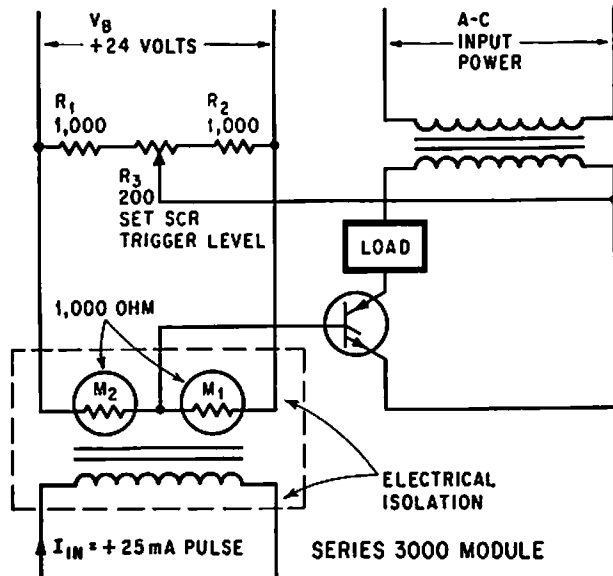
CASCODE ADDER—Gives gain of 600 when used as null detector of 400-cps impedance bridge, with measuring accuracy of one part per million.—W. A. Rhinehart and L. Mourlam, *FET Performs Well in Balancing Act, Electronics*, 38:19, p 88-92.

TRANSISTORS - 2N2386



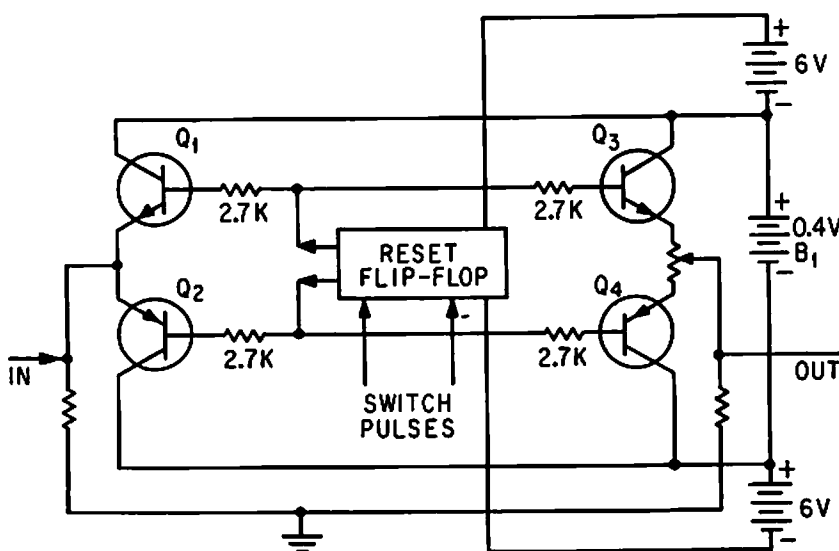
OPTOELECTRONIC BRIDGE ELEMENT—Lamp with rheostat varies resistance of photocell over range of 100 to 10,000 ohms to give stable nonreactive resistance element for r-f

bridge.—R. H. Wagner, Variable R-F Resistor Attained With Photocell, *Electronics*, 37:26, p 67.



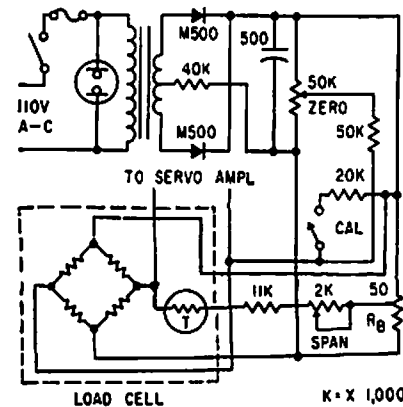
SCR CONTROL—Firing angle and trigger level are controlled by magnetoresistor bridge.—R.

M. Gitlin, Magnetoresistors Isolate Load From Control Circuit, *Electronics*, 38:3, p 54-59.

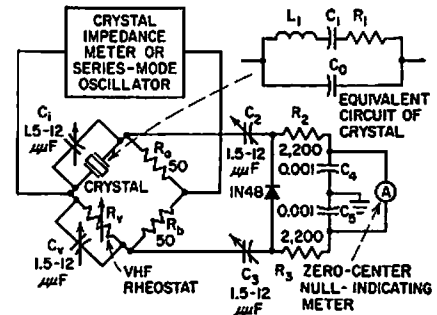


TRANSISTOR BRIDGE SWITCHES MICROVOLT SIGNALS—Circuit approaches infinite impedance during off condition by lowering emitter-to-collector conductance gap to zero. Conventional reset flip-flop controls on and

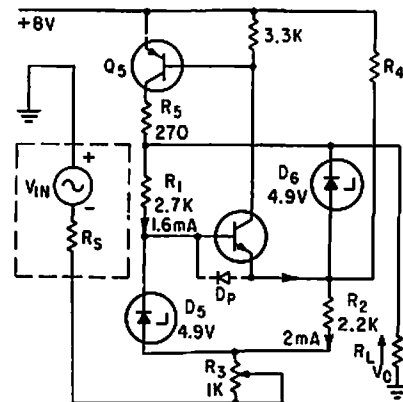
off operation of series-connected npn and pnp bridge transistors.—M. V. Kalfayan, Transistor Bridge Switches Microvolts, *Electronics*, 37:1, p 60.



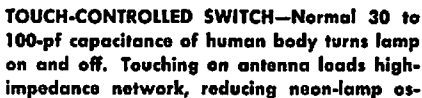
SELF-BALANCING TORQUE INDICATOR—Uses shunt bridge balancing technique. Amplifier and servo motor that drive 50-ohm balancing pot RB are standard commercial units. Highly stable power supply and reference voltage are not needed.—C. H. Haakana, Shunt Bridge Balancing in Strain-Gage Indicators, *Electronics*, 32:30, p 50-51.



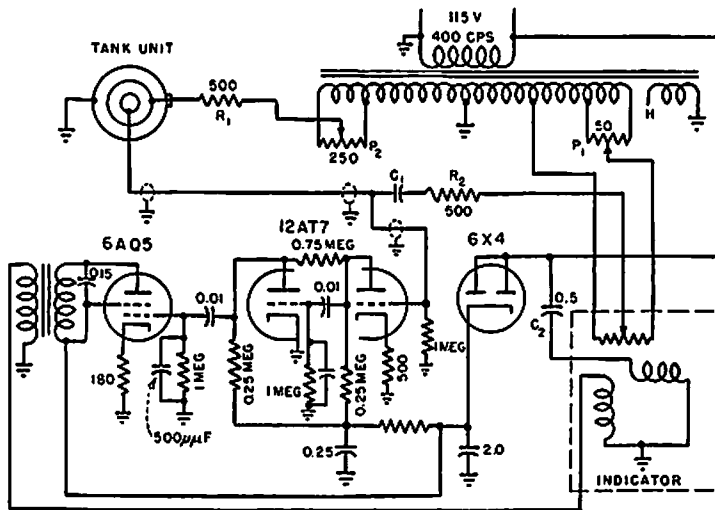
CRYSTAL-PARAMETER BRIDGE—Bridge plugs into crystal socket of standard crystal impedance meter, and crystal under test plugs into bridge. Only other instruments needed for measuring equivalent parameters of overtone crystals for 75 to 200 Mc are frequency meter and null-indicating meter.—D. W. Robertson, Plug-in Bridge Checks VHF Quartz Crystals, *Electronics*, 31:19, p 82-85.



D-C LEVEL SHIFTER—Bridge R1D5R2D6 and two transistors in d-c negative-feedback loop deliver output signal that is replica of input but at lower impedance and shifted in d-c voltage level a predetermined amount.—J. Willis, High Precision D-C Level Shifter Reduces Output Impedance, *Electronics*, 36:18, p 65-68.

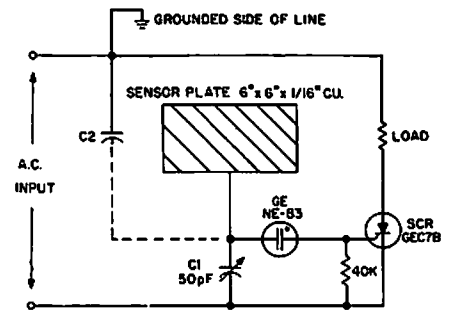


ing on lamp. Touching off antenna reverses all conditions.—S. B. Gray, Home and Auto Controls, *Electronics*, 36:19, p 52-66.

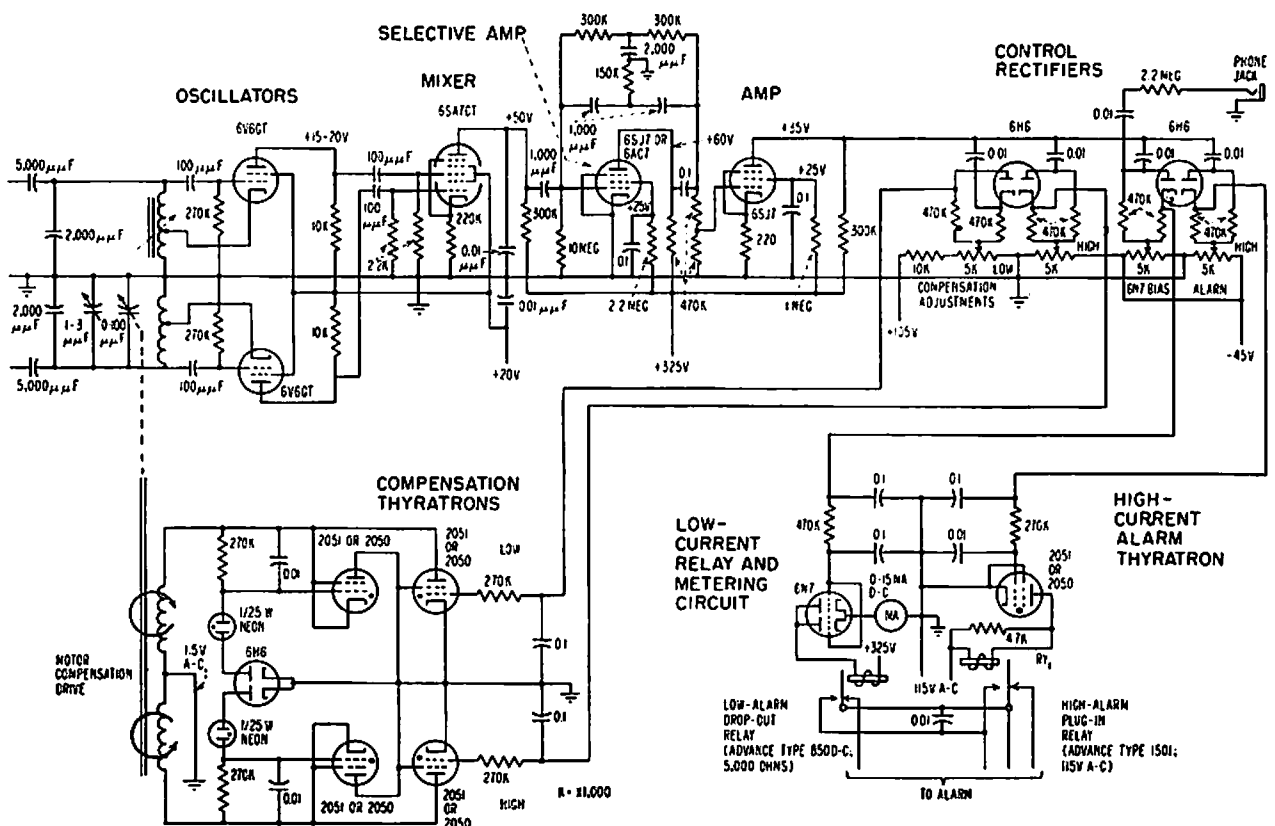


CAPACITANCE-TYPE AIRCRAFT FUEL GAGE—Indicates weight of fuel rather than volume. Uses self-balancing bridge, with concentric-tube capacitor mounted vertically in call of tank to serve as one arm. With fuel in tank, servo drives bridge-robalance potentiometer

and indicator to new position corresponding to amount of fuel in tank.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 21.



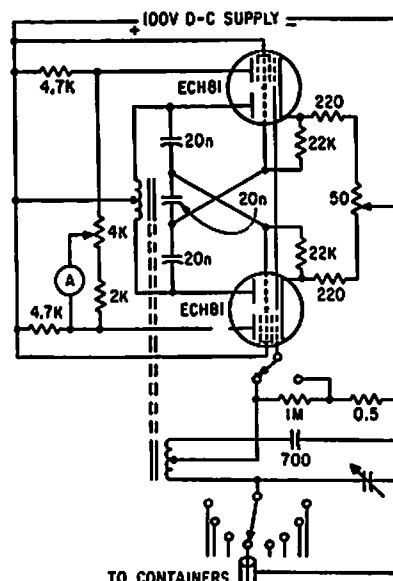
PROXIMITY SWITCH—Sensor plate and C2 form capacitive voltage divider across a-c supply. Value of C2 depends on proximity to sensor plate of human body, grounded object, or other reasonably conductive object. When voltage across C1 exceeds breakdown of neon, C1 and C2 discharge through scr gate, causing scr to trigger and energize load. Latching action is obtained by driving scr anode circuit with d-c, for such applications as elevator floor selector buttons and door safety controls.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 122.



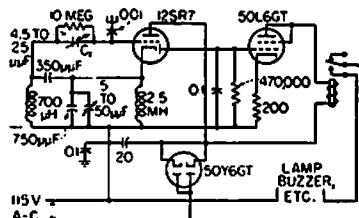
BALANCED-CAPACITANCE FENCE ALARM—Sets off alarm when anyone approaches barbed wire fence around power plant or substation. Automatically corrects for capacitance changes due to weed growth and

changing weather conditions. Two separate antennas and two oscillators are used, with lines along fence serving as part of tuning capacitance of each oscillator. Mixer produces beats between harmonics. Frequency-

selective network in low a-f range produces d-c voltages that trigger relay tubes and actuate alarm relays.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, N.Y., 1959, p 1.

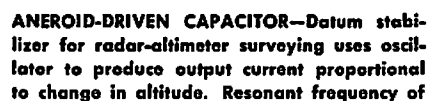


controls speed at which wire is pulled, to hold capacitance within desired limits. Sensing electrode is water trough, with water in contact with extruded insulation to form one side of unknown capacitor. Wire is grounded to form other side.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 18.



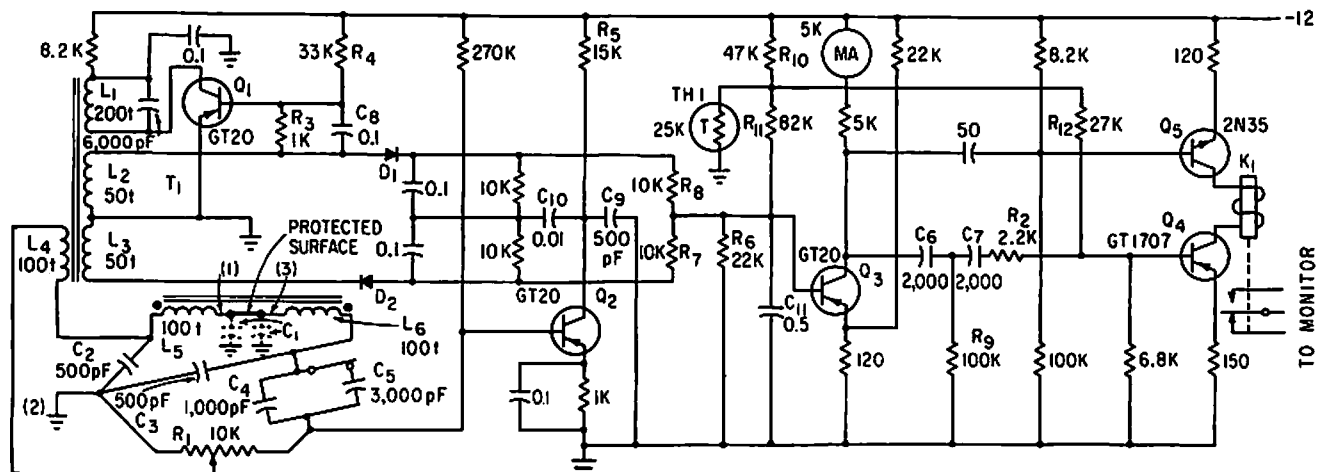
amplified to drive tachometer or frequency meter. Pickup is mounted close to moving blades on shaft whose speed is being measured.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 17.

INTRUSION ALARM—Circuit stops oscillating when intruder approaches antenna surrounding area being protected. Uses weak oscillator with relay tube that is biased to cutoff by some of oscillator output. Additional capacitance caused by intruder stops oscillator, removing bias and making tube conduct and actuate relay to sound alarm.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 19.



high-stability 3.5-Mc oscillator is varied by small capacitor plate driven by three-element aneroid. Discriminator and d-c amplifier transform resulting frequency changes to cur-

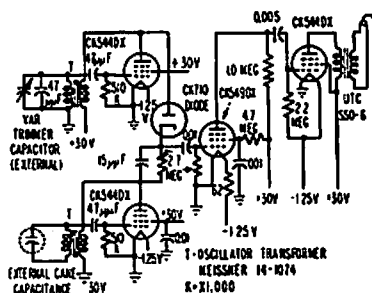
rent variations proportional to changes in height.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 23.



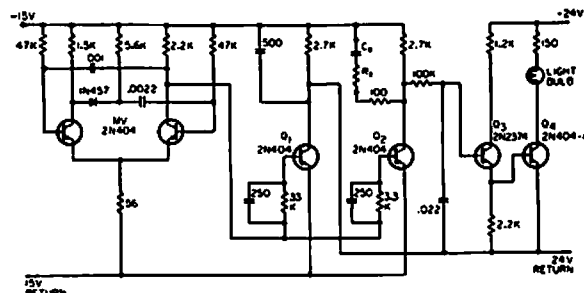
BODY-CAPACITANCE ALARM—Detects intruder by sensing body capacitance. Oscillator Q1 feeds 20 kc to capacitance bridge that contains C1, which is capacitance to ground of protected cabinet. When unbalanced, bridge feeds 20-kc signal to amplifier Q2, whose

output goes to phase-sensitive detector D1-D2, which converts unbalance signal into d-c voltage for amplification by Q3. At balance, Q4 and Q5 send about 1 ma through relay K1 to keep it energized. When intruder approaches protected cabinet, output of phase-

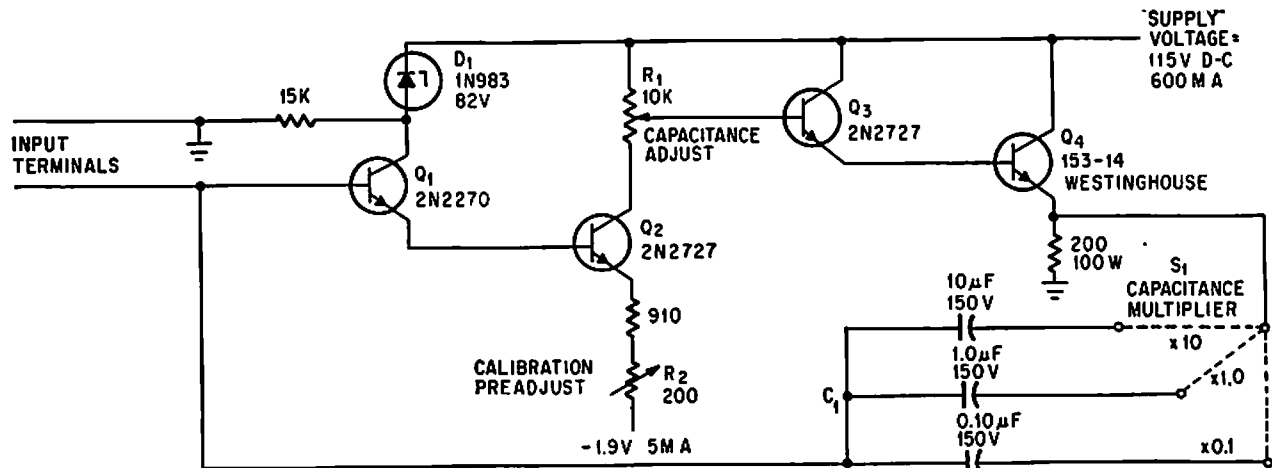
sensitive detector becomes more negative, causing K1 to drop out and sound an alarm. —S. M. Bagno, Sensitive Capacitance Intruder Alarm, *Electronics*, 33:38, p 65-67.



SENSING CANE FOR BLIND—Capacitance-sensing probe in tip of cane changes frequency of one oscillator in accordance with distance from ground, curb, or holes, to make beat-frequency oscillator produce audio tone in headset worn by blind person.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 24.



IN-CIRCUIT CAPACITOR TESTER—Permits checking capacitors dynamically for opens or shorts without disconnecting them. Indicator light is turned on for both faults.—E. L. Major, In-Circuit Capacitor Tester, *EEE*, 13:3, p 47.



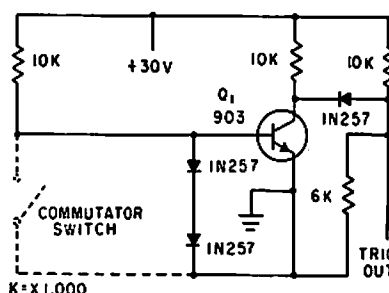
ELECTRONIC CAPACITOR—Two-terminal circuit provides capacitance values from 0.1 to 100 mfd, continuously variable in three ranges.

Voltage rating is +10 v and frequency range is d-c to 45 cps. Used in low-pass RC filter with adjustable cutoff frequency, in waveform

analyzer.—D. L. Bergman, Electronic Capacitor is Continuously Variable, *Electronics*, 32, p 89.

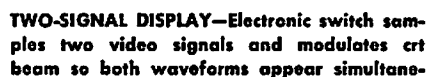
CHAPTER 12

Cathode Ray Circuits



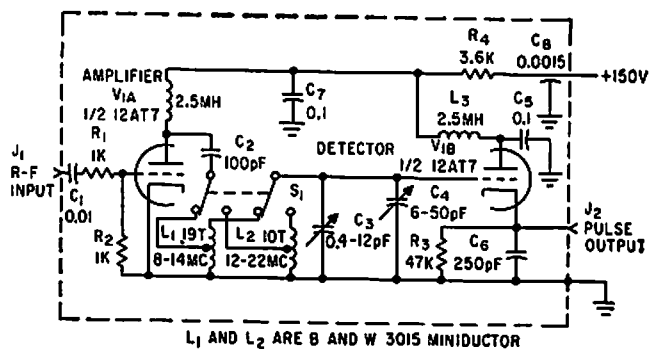
base frequency, and mark-space ratio controlled by variable d-c level input.—A. D. Runnalls, Bluebird Racor's Telemetry System, *Electronics*, 33:44, p 70-72.

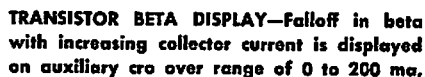
CRO SWEEP TRIGGER—Base of Q1 is grounded once during each revolution by commutator segment of gyro balancer, to make circuit produce sharp pulse that triggers oscilloscope sweep.—F. W. Kear, *Electronic System for Balancing Gyro Wheels*, *Electronics*, 33:43, p 82-85.



ously on screen. Matched diodes D1 and D2 serve as switches for the positive 2-v video signals.—A. E. Papedi, Reliable Repertoire Of

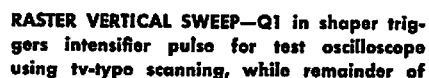
Display Circuits, Electronics, 38:2, p 60-66.





for constant collector voltages up to 8 v.—R. Zuleeg and J. Lindmayer, Sweep Equipment

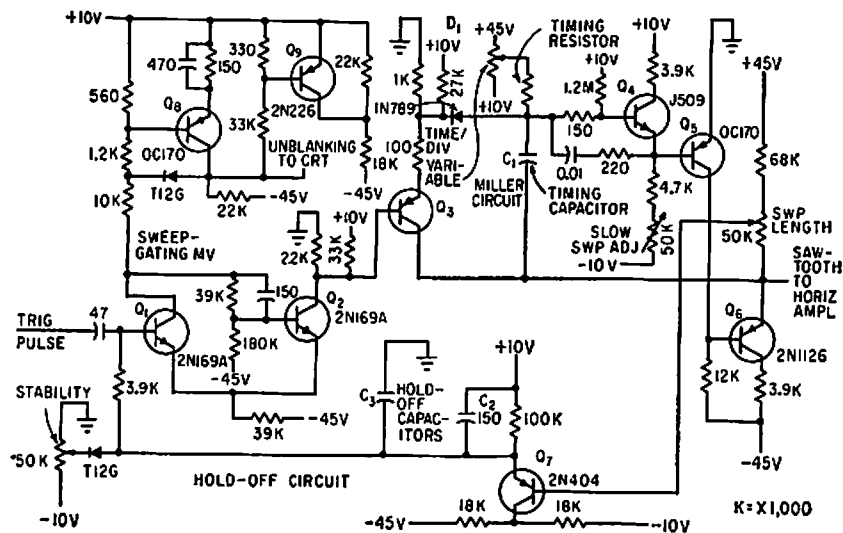
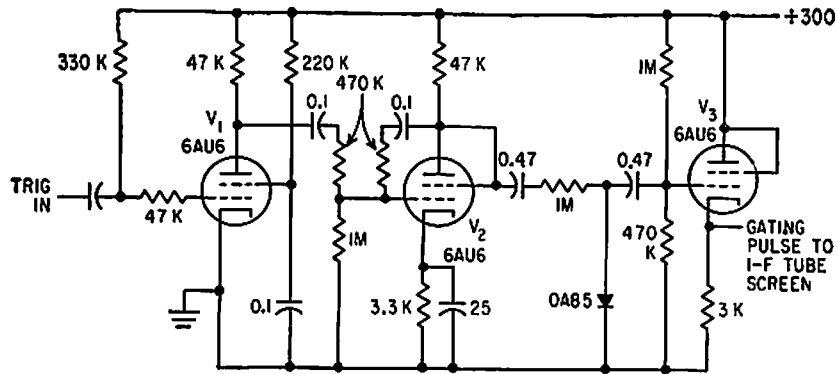
Displays Transistor Beta, *Electronics*, 31:49, p 100-101.



circuit generates vertical sweep for scope used to measure time intervals in range of 0.5 to 100 microsec with time resolution of

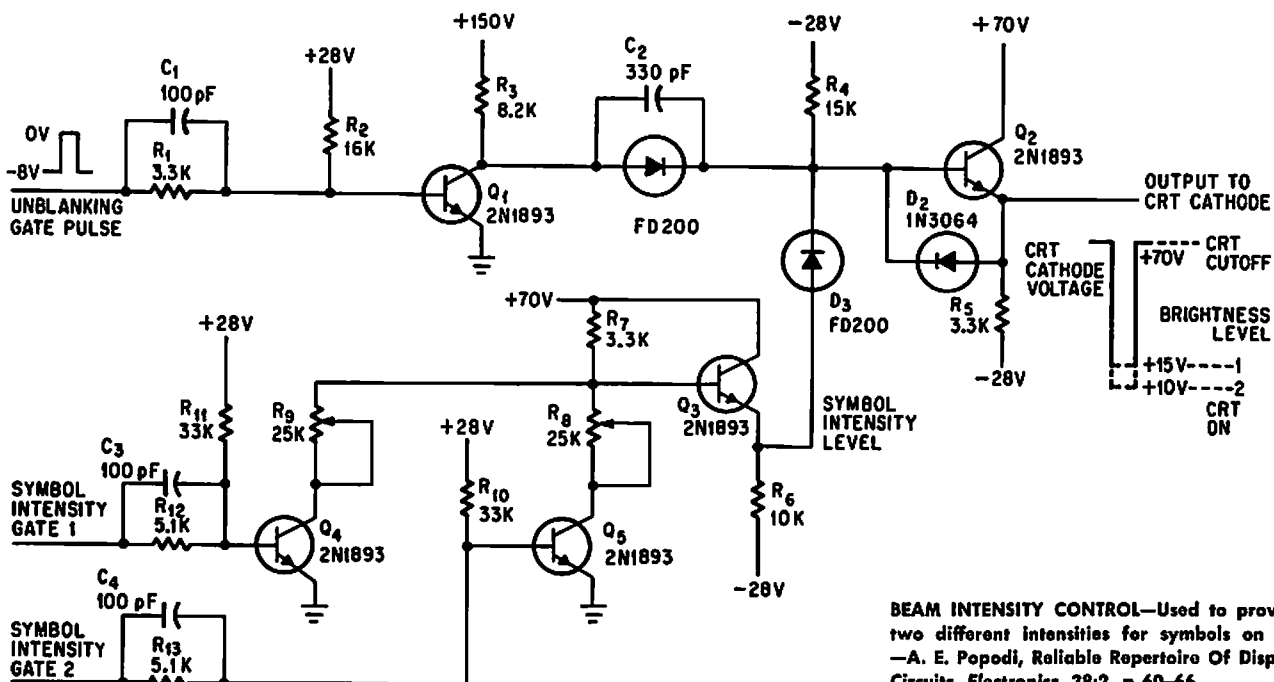
0.05 microsec.—R. P. Rufer and W. A. Karlot-
ski, Use Raster Oscilloscopes for Faster Time
Measurements, *Electronics*, 35:52, p 38-42.

PPI GROUND PULSE BLANKER—Used in backscatter receiver to produce gating pulse that can be applied to screen grid of final i-f stage to remove bright ground pulse (occurring because receiver is fed from transmitting antenna by way of transmit-receive switch) from ppi screen.—K. Perry, Reducing Interference in Ionospheric Sounding, 33:22, *Electronics*, p 118-120.

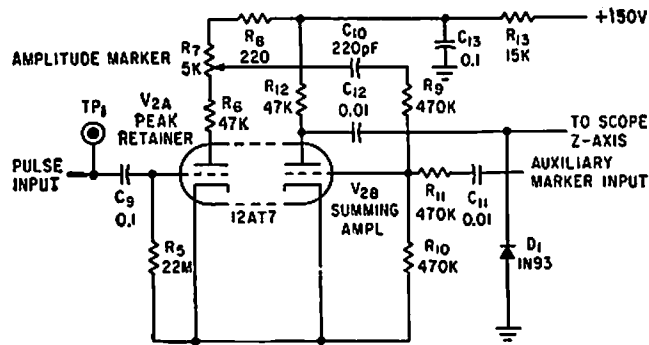


PORTABLE CRO UNBLANKING AMPLIFIER—
Supplies signal to turn on crt during sweep.
Hold-off circuit insures that trace starts from

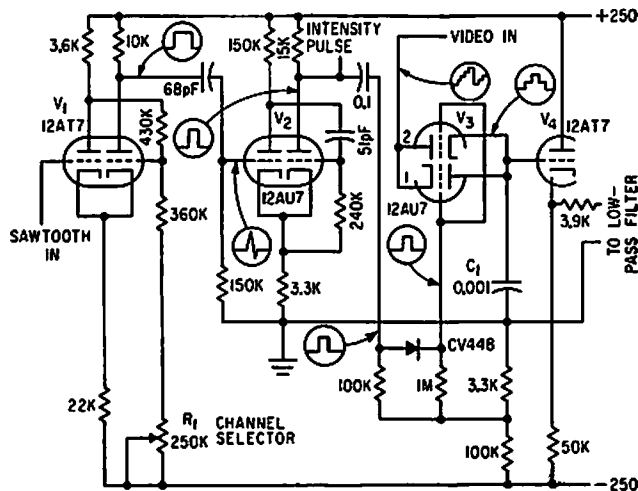
same point on every sweep.—O. Svehaug and J. R. Kobbe, Battery-Operated Transistor Oscilloscope, *Electronics*, 33:12, p 80-83.



BEAM INTENSITY CONTROL—Used to provide two different intensities for symbols on crt. —A. E. Popodi, *Reliable Repertoire Of Display Circuits*, *Electronics*, 38:2, p 60-66.

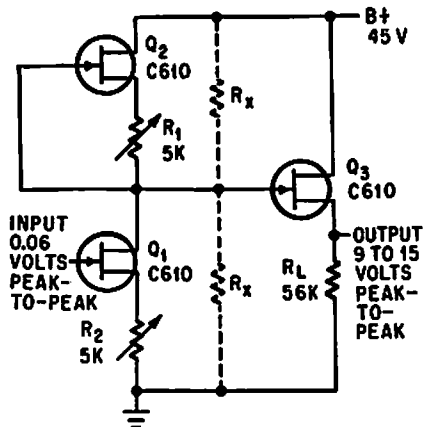


AMPLIFIER FOR MARKER GENERATOR—Triode V2A amplifies brightening pulse generated by marker circuit, and feeds amplified pulse to V2B for mixing with fixed or variable external marker pulse so both are applied to Z-axis of scope.—D. J. Oderizzi, Z-Axis Marker Generator for Bandpass Circuit Alignment, *Electronics*, 33:26, p 108-110.

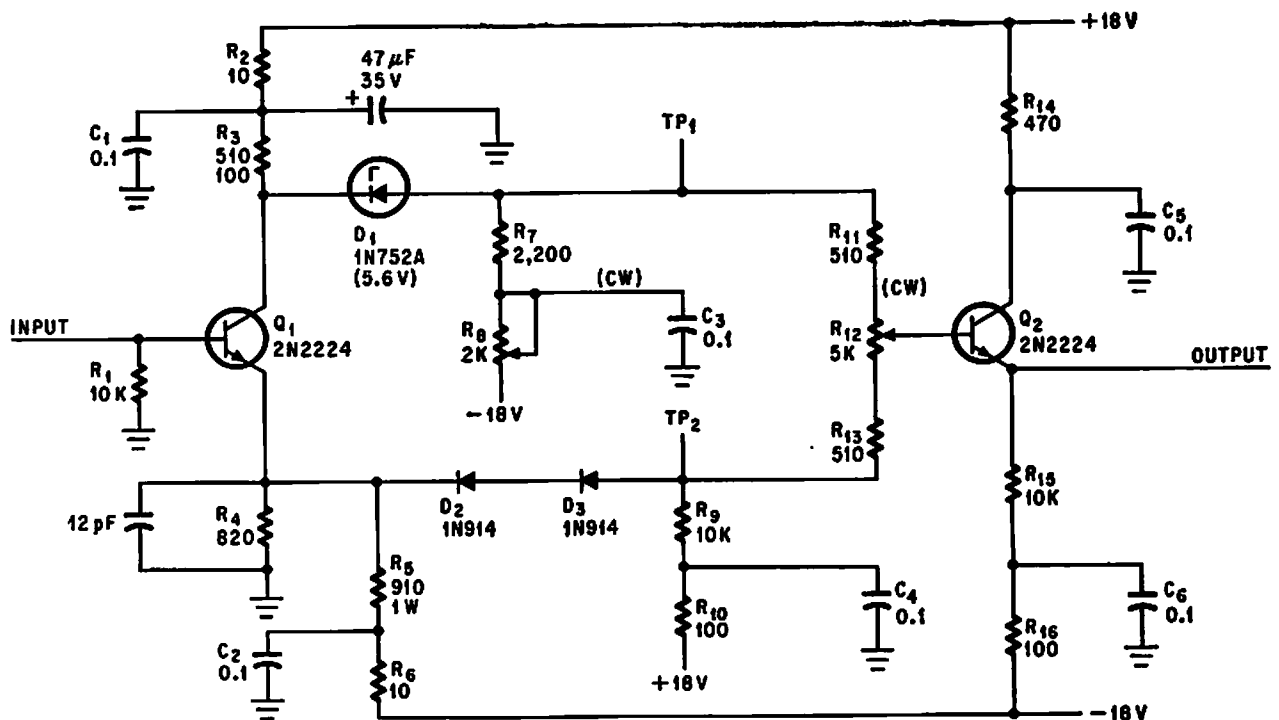


STROBE PULSE GENERATOR—Constant-amplitude sawtooth from timebase of display unit is fed to one control grid of Schmitt trigger V1, whose output triggers one-shot mvbr to produce gating pulse 0.1 millisecond wide that

also identifies strobed channel on crt by brightening trace at that point.—A. Petton, Telemetry System for Testing Automobiles, *Electronics*, 33:43, p 57-59.



HORIZONTAL DRIVE—Three field-effect transistors give 45 db voltage gain for 1-cps triangular wave in horizontal deflection circuit of crt.—F. J. Murphree and J. H. Hammond Jr., High-gain D-C Amplifier Drives CRT Display, *Electronics*, 37:19, p 53.

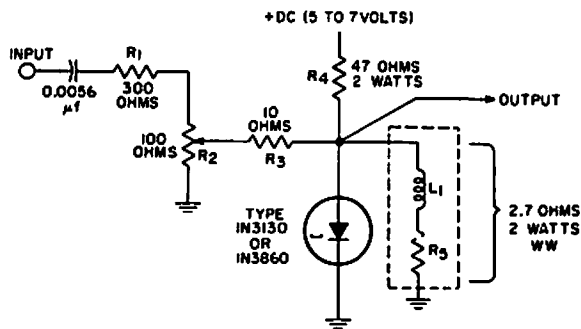
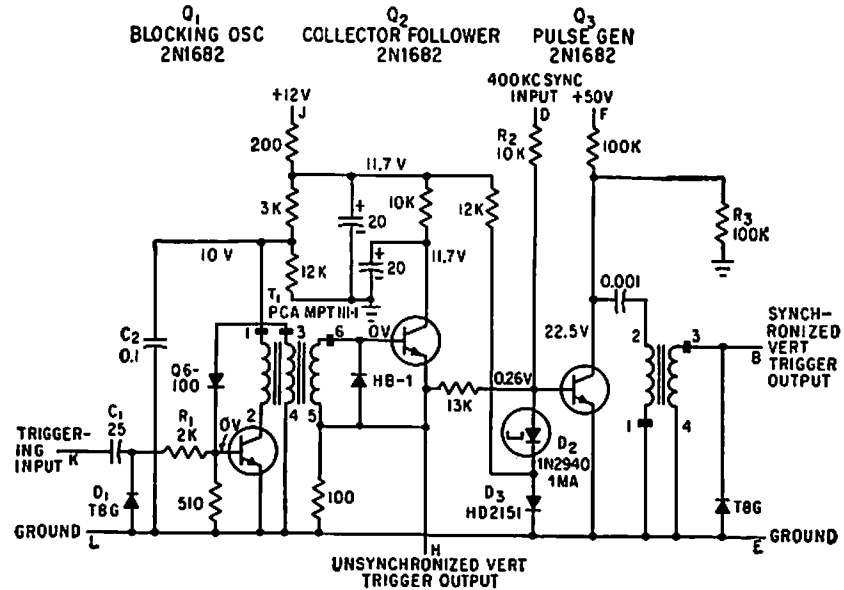


ALIGNMENT CORRECTION—Circuit developed for correcting alignment inaccuracies between electrostatic deflection plates and face of

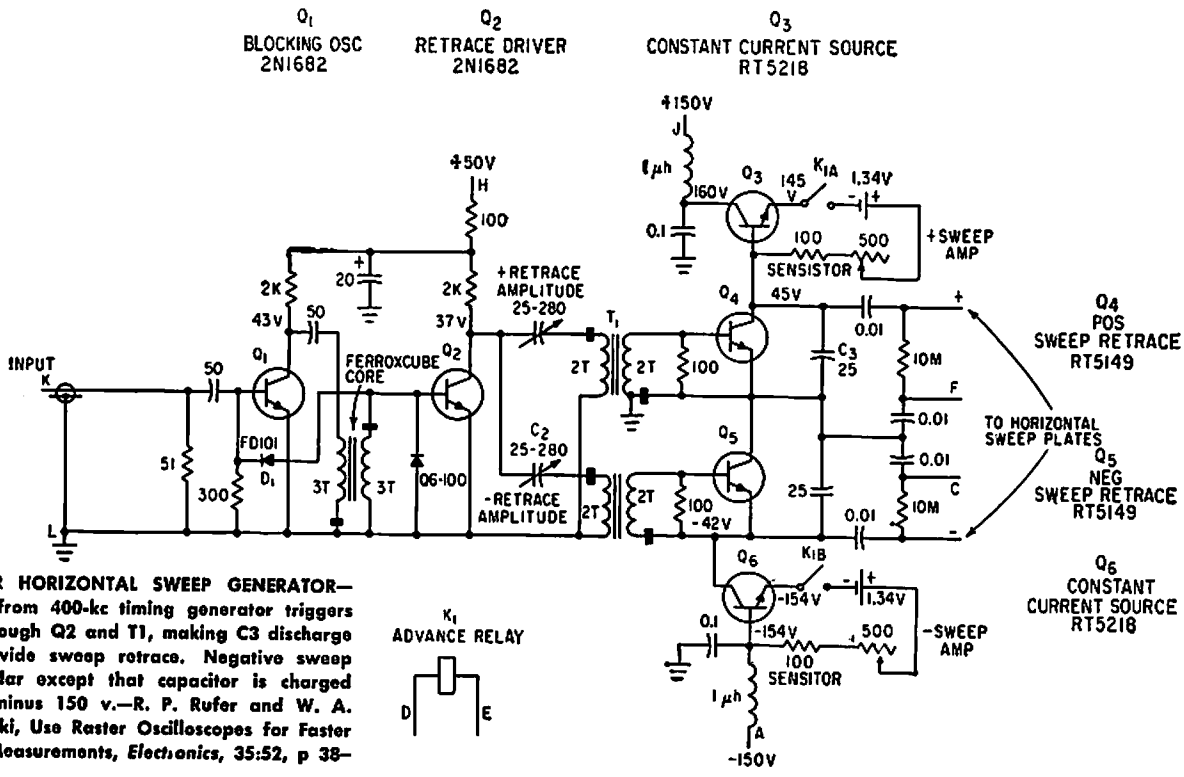
cathode-ray tube gives output varying from 0 to +3.5 v when input varies from 0 to -4 v.—F. E. Smith, Buffer Amplifier Supplies Bi-

polar Output, *Electronics*, 37:21, p 75.

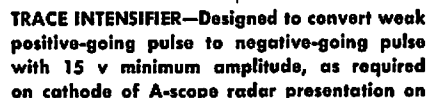
RASTER VERTICAL TRIGGERING GENERATOR—Output pulse width of blocking oscillator Q1 is over 3.5 microsec, determined by T1 and C2. Q2 couples this pulse to output H for use as unsynchronized output, while Q3 with 400-kc sync input serves with D2 as coincidence gate to give output only when both sync and gate pulses are present.—R. P. Rufer and W. A. Karlotski, Use Raster Oscilloscopes for Faster Time Measurements, *Electronics*, 35:52, p 38-42.



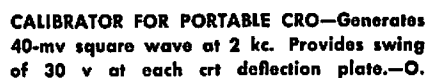
UHF SYNCHRONIZER—Simple tunnel-diode circuit can synchronize any scope to any constant frequency up to signal-bandwidth limits of scope, even though bandwidths are greater than crosync circuits can handle. Upper frequency limit of circuit is at least 1.2 gc. Diode oscillates at frequency controlled primarily by L1, but will lock onto uhf input signal and deliver exact subharmonic of input. Can provide countdowns at ratios exceeding 100:1.—F. M. Carlson, Tunnel-Diode UHF Synchronizer, *EEE*, 12:2, p 109.



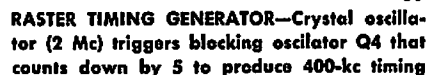
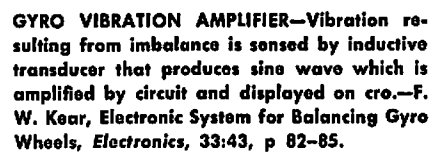
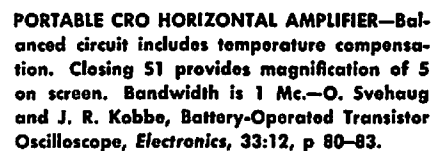
RASTER HORIZONTAL SWEEP GENERATOR—Input from 400-kc timing generator triggers Q4 through Q2 and T1, making C3 discharge to provide sweep retrace. Negative sweep is similar except that capacitor is charged from minus 150 v.—R. P. Rufer and W. A. Karlotski, Use Raster Oscilloscopes for Faster Time Measurements, *Electronics*, 35:52, p 38-42.



commercial oscilloscope. Minimum input amplitude is 250 mv.—L. J. Brocato, Scopo-Trace Intensification Converter, EEE, 13:2, p 62-63.

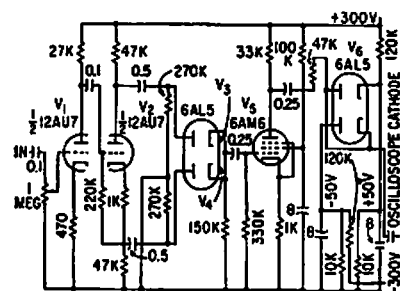


Svehaug and J. R. Kobbe, Battery-Operated Transistor Oscilloscope, *Electronics*, 33:12, p 80-83.

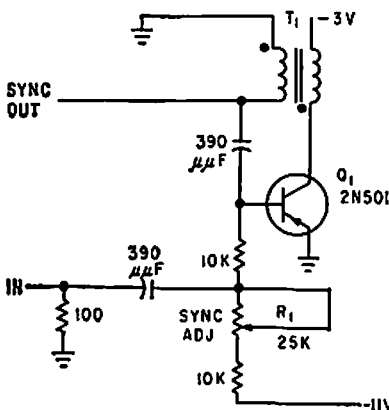


pulses for test oscilloscope using tv-type scanning.—R. P. Rufer and W. A. Karlotski, Use Raster Oscilloscopes for Faster Time Measure-

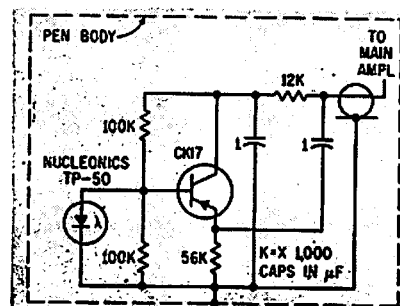
ments, *Electronics*, 35:52, p 38-42.



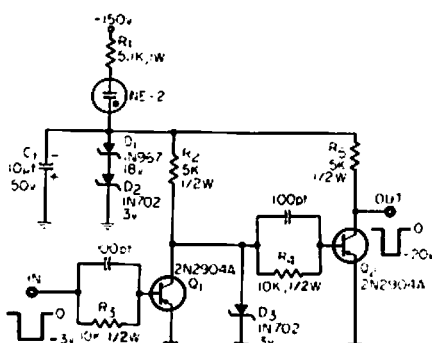
TRACE BRIGHTNESS EQUALIZER—Amplitude of signal to be displayed controls scope brightness by changing voltage on cathode of crt. Low-level signal and high-voltage pulses are automatically adjusted as to brightness, so both traces appear equally bright on photographic film.—J. K. Goodwin, *Circuit Evens Scope Brightness*, *Electronics*, 31:51, p 96-98.



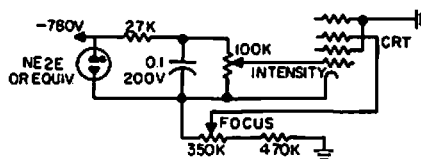
SYNC BLOCKING OSCILLATOR—Free-running period of grounded-emitter stage is made to lock in with frequency of pulse generator, to provide synchronizing signal for conventional oscilloscope during tests of high-speed computer circuits.—L. Neumann, *Transistorized Generator for Pulse Circuit Design*, *Electronics*, 32:14, p 47-49.



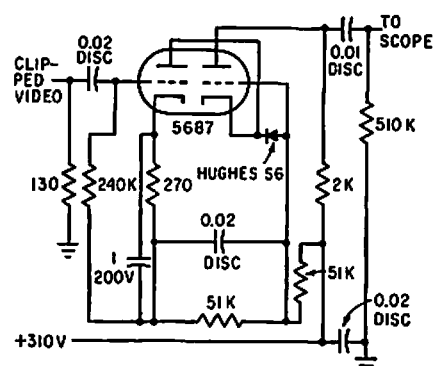
LIGHT-PEN PREAMP—Raises signal level of germanium photodiode before it is fed through coax to main amplifier. Special decoupling in collector circuit allows power and signal to be supplied simultaneously over single coaxial cable.—B. M. Gurley and C. E. Woodward, *Light-Pen Links Computer to Operator*, *Electronics*, 32:47, p 85-87.



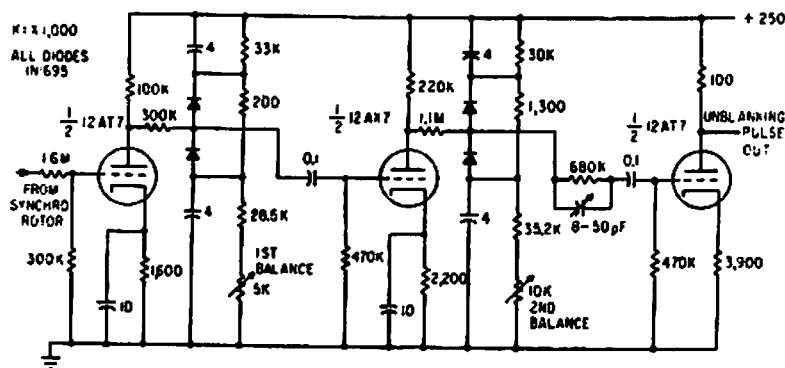
Z-AXIS MODULATION—Pulse amplifier allows crt beam-intensity modulation from 3-v logic levels. Requires only single -150 v supply. Intended as modification for Tektronix oscilloscope.—J. H. Cormack, *Pulse Amplifier for Beam Intensity Modulation*, *EEE*, 14:1, p 63.



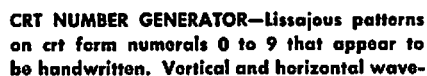
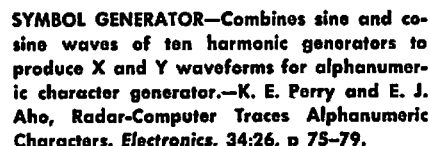
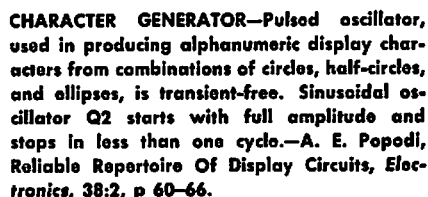
NEON CRT BIAS REGULATOR—Neon lamp serves as bias regulator for grid 1 of oscilloscope crt and as pilot lamp.—Moro Glow-Lamp Circuits, *EEE*, 12:2, p 106-108.



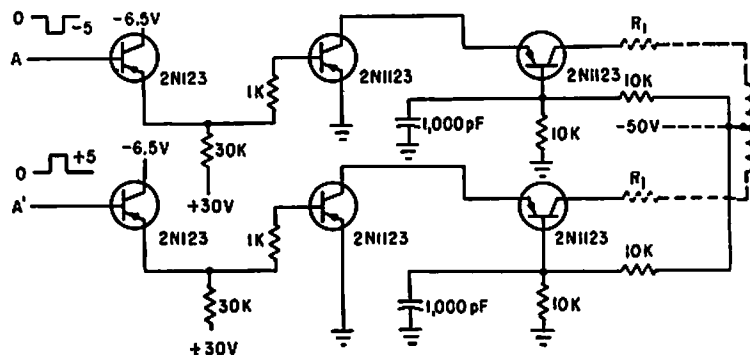
Z-AXIS AMPLIFIER—Accepts clipped video signal of microwave interferometer system and intensity-modulates electron beam of oscilloscope.—H. L. Bunn, *Determining Electron Density and Distribution in Plasmas*, *Electronics*, 34:14, p 71-75.



UNBLANKING PULSE GENERATOR—Unblanking signal is produced by repeated amplification and clipping of 500-cps signal from antenna synchro driver. Square-wave output is applied to control grid of display crt. Balancing controls are adjusted so unblanking strobe line starts from center.—R. T. Wolfram, *Improved Communications Using Groundscatter Propagation*, *Electronics*, 33:44, p 74-78.



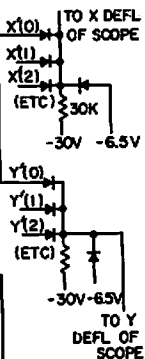
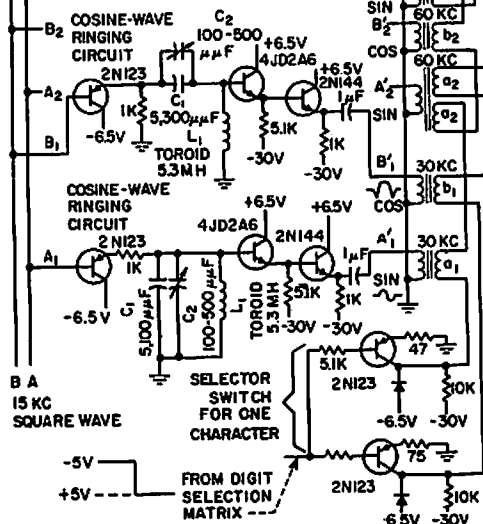
transformer, permitting waveforms to pass through to crt deflection plates and create pattern on screen. All waveshapes are de-



FINE-POSITIONING CHARACTER-GENERATOR SWITCH—Pairs of deflection switches operating push-pull into deflection yoke act with 16-point coarse positioning system to give 4,096 positions, generated in binary fashion.—K. E. Perry and E. J. Aho, *Radar-Computer Display Traces Alphanumeric Characters, Electronics*, 34:26, p 75-79.

FREQ	FUNCTION	L ₁	C ₁
30KC	SIN	5.3MH	3,000 μF
	COS		+200 μF
60KC	SIN	2.65MH	2,400 μF
	COS		+240 μF
90KC	SIN	1.77MH	1,600 μF
	COS		+160 μF
120KC	SIN	1.33MH	1,300 μF
	COS		+130 μF
150KC	SIN	1.06MH	1,000 μF
	COS		+58 μF

ALL FIVE PAIRS OF RINGING CIRCUITS ARE IDENTICAL EXCEPT FOR VALUES OF L₁ AND C₁



SECONDARY TURNS FOR CHARACTERS 0, 1, 2, 3, 4, 5, 6 AND 7

X(0)	Y(0)	X(1)	Y(1)	X(2)	Y(2)	X(3)	Y(3)	X(4)	Y(4)	X(5)	Y(5)	X(6)	Y(6)	X(7)	Y(7)
a ₁ +28	b ₁ -8	a ₁ 0	b ₁ +40	a ₁ -3	b ₁ -22	a ₁ +43	b ₁ +9	a ₁ +8	b ₁ -22	a ₁ +41	b ₁ +1	a ₁ -4	b ₁ -16	a ₁ 0	b ₁ -24
a ₂ -20	b ₂ 0	a ₂ 0	b ₂ -20	a ₂ -39	b ₂ 0	a ₂ 0	b ₂ +6	a ₂ -6	b ₂ -22	a ₂ +6	b ₂ 0	a ₂ +15	b ₂ +32	a ₂ 0	b ₂ -32
a ₃ 0	b ₃ 0	a ₃ 0	b ₃ 0	a ₃ 0	b ₃ 0	a ₃ 0	b ₃ +6	a ₃ 0	b ₃ -22	a ₃ -6	b ₃ 0	a ₃ 0	b ₃ 0	a ₃ 0	b ₃ 0
a ₄ 0	b ₄ 0	a ₄ 0	b ₄ 0	a ₄ 0	b ₄ 0	a ₄ 0	b ₄ +6	a ₄ 0	b ₄ -22	a ₄ -6	b ₄ 0	a ₄ 0	b ₄ 0	a ₄ 0	b ₄ 0
a ₅ 0	b ₅ 0	a ₅ 0	b ₅ 0	a ₅ 0	b ₅ 0	a ₅ 0	b ₅ +6	a ₅ 0	b ₅ -22	a ₅ -6	b ₅ 0	a ₅ 0	b ₅ 0	a ₅ 0	b ₅ 0
a ₆ 0	b ₆ 0	a ₆ 0	b ₆ 0	a ₆ 0	b ₆ 0	a ₆ 0	b ₆ +6	a ₆ 0	b ₆ -22	a ₆ -6	b ₆ 0	a ₆ 0	b ₆ 0	a ₆ 0	b ₆ 0
a ₇ 0	b ₇ 0	a ₇ 0	b ₇ 0	a ₇ 0	b ₇ 0	a ₇ 0	b ₇ +6	a ₇ 0	b ₇ -22	a ₇ -6	b ₇ 0	a ₇ 0	b ₇ 0	a ₇ 0	b ₇ 0
a ₈ 0	b ₈ 0	a ₈ 0	b ₈ 0	a ₈ 0	b ₈ 0	a ₈ 0	b ₈ +6	a ₈ 0	b ₈ -22	a ₈ -6	b ₈ 0	a ₈ 0	b ₈ 0	a ₈ 0	b ₈ 0
a ₉ 0	b ₉ 0	a ₉ 0	b ₉ 0	a ₉ 0	b ₉ 0	a ₉ 0	b ₉ +6	a ₉ 0	b ₉ -22	a ₉ -6	b ₉ 0	a ₉ 0	b ₉ 0	a ₉ 0	b ₉ 0
a ₁₀ 0	b ₁₀ 0	a ₁₀ 0	b ₁₀ 0	a ₁₀ 0	b ₁₀ 0	a ₁₀ 0	b ₁₀ +6	a ₁₀ 0	b ₁₀ -22	a ₁₀ -6	b ₁₀ 0	a ₁₀ 0	b ₁₀ 0	a ₁₀ 0	b ₁₀ 0
a ₁₁ 0	b ₁₁ 0	a ₁₁ 0	b ₁₁ 0	a ₁₁ 0	b ₁₁ 0	a ₁₁ 0	b ₁₁ +6	a ₁₁ 0	b ₁₁ -22	a ₁₁ -6	b ₁₁ 0	a ₁₁ 0	b ₁₁ 0	a ₁₁ 0	b ₁₁ 0
a ₁₂ 0	b ₁₂ 0	a ₁₂ 0	b ₁₂ 0	a ₁₂ 0	b ₁₂ 0	a ₁₂ 0	b ₁₂ +6	a ₁₂ 0	b ₁₂ -22	a ₁₂ -6	b ₁₂ 0	a ₁₂ 0	b ₁₂ 0	a ₁₂ 0	b ₁₂ 0
a ₁₃ 0	b ₁₃ 0	a ₁₃ 0	b ₁₃ 0	a ₁₃ 0	b ₁₃ 0	a ₁₃ 0	b ₁₃ +6	a ₁₃ 0	b ₁₃ -22	a ₁₃ -6	b ₁₃ 0	a ₁₃ 0	b ₁₃ 0	a ₁₃ 0	b ₁₃ 0
a ₁₄ 0	b ₁₄ 0	a ₁₄ 0	b ₁₄ 0	a ₁₄ 0	b ₁₄ 0	a ₁₄ 0	b ₁₄ +6	a ₁₄ 0	b ₁₄ -22	a ₁₄ -6	b ₁₄ 0	a ₁₄ 0	b ₁₄ 0	a ₁₄ 0	b ₁₄ 0
a ₁₅ 0	b ₁₅ 0	a ₁₅ 0	b ₁₅ 0	a ₁₅ 0	b ₁₅ 0	a ₁₅ 0	b ₁₅ +6	a ₁₅ 0	b ₁₅ -22	a ₁₅ -6	b ₁₅ 0	a ₁₅ 0	b ₁₅ 0	a ₁₅ 0	b ₁₅ 0
a ₁₆ 0	b ₁₆ 0	a ₁₆ 0	b ₁₆ 0	a ₁₆ 0	b ₁₆ 0	a ₁₆ 0	b ₁₆ +6	a ₁₆ 0	b ₁₆ -22	a ₁₆ -6	b ₁₆ 0	a ₁₆ 0	b ₁₆ 0	a ₁₆ 0	b ₁₆ 0
a ₁₇ 0	b ₁₇ 0	a ₁₇ 0	b ₁₇ 0	a ₁₇ 0	b ₁₇ 0	a ₁₇ 0	b ₁₇ +6	a ₁₇ 0	b ₁₇ -22	a ₁₇ -6	b ₁₇ 0	a ₁₇ 0	b ₁₇ 0	a ₁₇ 0	b ₁₇ 0
a ₁₈ 0	b ₁₈ 0	a ₁₈ 0	b ₁₈ 0	a ₁₈ 0	b ₁₈ 0	a ₁₈ 0	b ₁₈ +6	a ₁₈ 0	b ₁₈ -22	a ₁₈ -6	b ₁₈ 0	a ₁₈ 0	b ₁₈ 0	a ₁₈ 0	b ₁₈ 0
a ₁₉ 0	b ₁₉ 0	a ₁₉ 0	b ₁₉ 0	a ₁₉ 0	b ₁₉ 0	a ₁₉ 0	b ₁₉ +6	a ₁₉ 0	b ₁₉ -22	a ₁₉ -6	b ₁₉ 0	a ₁₉ 0	b ₁₉ 0	a ₁₉ 0	b ₁₉ 0
a ₂₀ 0	b ₂₀ 0	a ₂₀ 0	b ₂₀ 0	a ₂₀ 0	b ₂₀ 0	a ₂₀ 0	b ₂₀ +6	a ₂₀ 0	b ₂₀ -22	a ₂₀ -6	b ₂₀ 0	a ₂₀ 0	b ₂₀ 0	a ₂₀ 0	b ₂₀ 0
a ₂₁ 0	b ₂₁ 0	a ₂₁ 0	b ₂₁ 0	a ₂₁ 0	b ₂₁ 0	a ₂₁ 0	b ₂₁ +6	a ₂₁ 0	b ₂₁ -22	a ₂₁ -6	b ₂₁ 0	a ₂₁ 0	b ₂₁ 0	a ₂₁ 0	b ₂₁ 0
a ₂₂ 0	b ₂₂ 0	a ₂₂ 0	b ₂₂ 0	a ₂₂ 0	b ₂₂ 0	a ₂₂ 0	b ₂₂ +6	a ₂₂ 0	b ₂₂ -22	a ₂₂ -6	b ₂₂ 0	a ₂₂ 0	b ₂₂ 0	a ₂₂ 0	b ₂₂ 0
a ₂₃ 0	b ₂₃ 0	a ₂₃ 0	b ₂₃ 0	a ₂₃ 0	b ₂₃ 0	a ₂₃ 0	b ₂₃ +6	a ₂₃ 0	b ₂₃ -22	a ₂₃ -6	b ₂₃ 0	a ₂₃ 0	b ₂₃ 0	a ₂₃ 0	b ₂₃ 0
a ₂₄ 0	b ₂₄ 0	a ₂₄ 0	b ₂₄ 0	a ₂₄ 0	b ₂₄ 0	a ₂₄ 0	b ₂₄ +6	a ₂₄ 0	b ₂₄ -22	a ₂₄ -6	b ₂₄ 0	a ₂₄ 0	b ₂₄ 0	a ₂₄ 0	b ₂₄ 0
a ₂₅ 0	b ₂₅ 0	a ₂₅ 0	b ₂₅ 0	a ₂₅ 0	b ₂₅ 0	a ₂₅ 0	b ₂₅ +6	a ₂₅ 0	b ₂₅ -22	a ₂₅ -6	b ₂₅ 0	a ₂₅ 0	b ₂₅ 0	a ₂₅ 0	b ₂₅ 0
a ₂₆ 0	b ₂₆ 0	a ₂₆ 0	b ₂₆ 0	a ₂₆ 0	b ₂₆ 0	a ₂₆ 0	b ₂₆ +6	a ₂₆ 0	b ₂₆ -22	a ₂₆ -6	b ₂₆ 0	a ₂₆ 0	b ₂₆ 0	a ₂₆ 0	b ₂₆ 0
a ₂₇ 0	b ₂₇ 0	a ₂₇ 0	b ₂₇ 0	a ₂₇ 0	b ₂₇ 0	a ₂₇ 0	b ₂₇ +6	a ₂₇ 0	b ₂₇ -22	a ₂₇ -6	b ₂₇ 0	a ₂₇ 0	b ₂₇ 0	a ₂₇ 0	b ₂₇ 0
a ₂₈ 0	b ₂₈ 0	a ₂₈ 0	b ₂₈ 0	a ₂₈ 0	b ₂₈ 0	a ₂₈ 0	b ₂₈ +6	a ₂₈ 0	b ₂₈ -22	a ₂₈ -6	b ₂₈ 0	a ₂₈ 0	b ₂₈ 0	a ₂₈ 0	b ₂₈ 0
a ₂₉ 0	b ₂₉ 0	a ₂₉ 0	b ₂₉ 0	a ₂₉ 0	b ₂₉ 0	a ₂₉ 0	b ₂₉ +6	a ₂₉ 0	b ₂₉ -22	a ₂₉ -6	b ₂₉ 0	a ₂₉ 0	b ₂₉ 0	a ₂₉ 0	b ₂₉ 0
a ₃₀ 0	b ₃₀ 0	a ₃₀ 0	b ₃₀ 0	a ₃₀ 0	b ₃₀ 0	a ₃₀ 0	b ₃₀ +6	a ₃₀ 0	b ₃₀ -22	a ₃₀ -6	b ₃₀ 0	a ₃₀ 0	b ₃₀ 0	a ₃₀ 0	b ₃₀ 0
a ₃₁ 0	b ₃₁ 0	a ₃₁ 0	b ₃₁ 0	a ₃₁ 0	b ₃₁ 0	a ₃₁ 0	b ₃₁ +6	a ₃₁ 0	b ₃₁ -22	a ₃₁ -6	b ₃₁ 0	a ₃₁ 0	b ₃₁ 0	a ₃₁ 0	b ₃₁ 0
a ₃₂ 0	b ₃₂ 0	a ₃₂ 0	b ₃₂ 0	a ₃₂ 0	b ₃₂ 0	a ₃₂ 0	b ₃₂ +6	a ₃₂ 0	b ₃₂ -22	a ₃₂ -6	b ₃₂ 0	a ₃₂ 0	b ₃₂ 0	a ₃₂ 0	b ₃₂ 0

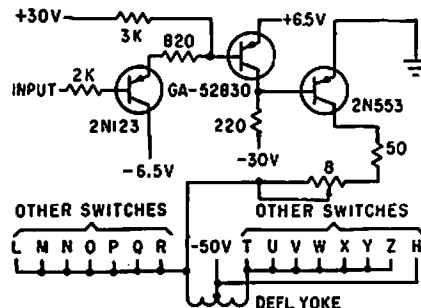
TEN TOROIDAL TRANSFORMERS WITH ONE SET OF SECONDARY WINDINGS SHOWN

ONE SET OF SECONDARIES, ONE 2-TRANSISTOR SELECTOR SWITCH (AS BELOW) AND ONE PAIR OF SCOPE INPUT DIODES (AS ABOVE) NEEDED FOR EACH CHARACTER

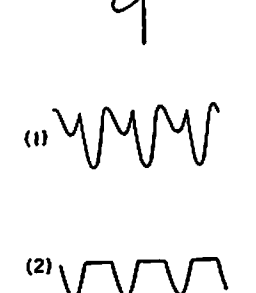
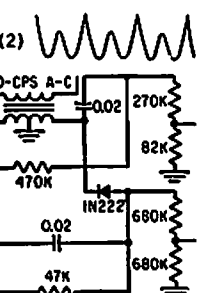
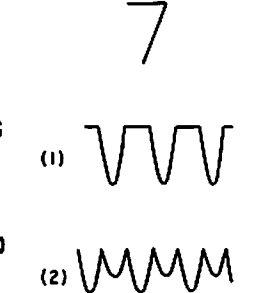
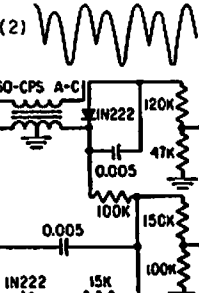
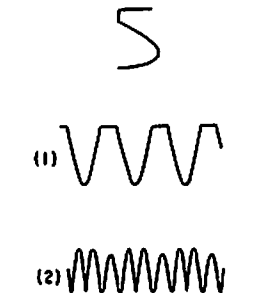
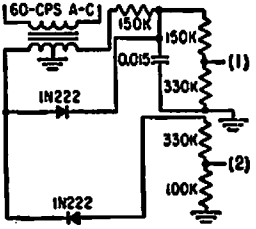
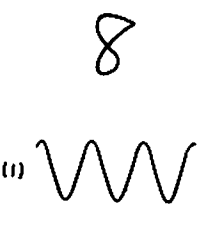
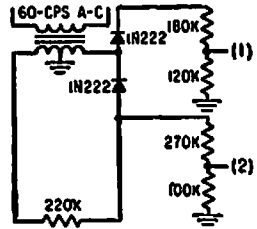
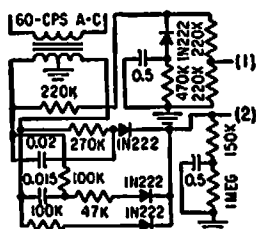
MINUS SIGNS INDICATE REVERSED CONNECTIONS TO WINDING

ZERO MEANS NO WINDING NEEDED

ANALOG CHARACTER GENERATOR—Displays numeric characters 1 through 7 on cathode-ray tube by deflecting spot to trace out each character continuously. X and Y deflection voltages are obtained by combining sine and cosine terms of first five harmonics of 30-kc fundamental. Transistorized gated oscillators, flip-flop serial counters, and emitter-followers feed 10 toroidal transformers having one set of secondary windings for each character.—K. E. Perry and E. J. Aho, *Generating Characters for Cathode-Ray Readout, Electronics*, 31:1, p 72-75.



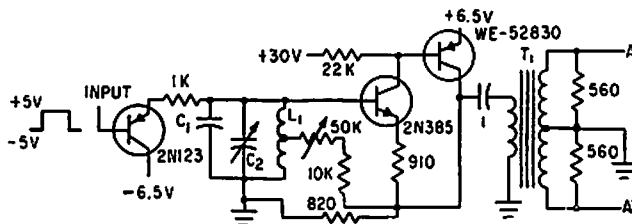
CHARACTER-GENERATOR DEFLECTION SWITCH—Coarse deflection system uses 32 identical power transistor circuits in switching configuration to drive low-inductance main deflection yoke. Half of these control X deflection to give 16 discrete positions, and the remainder serve for Y deflection.—K. E. Perry and E. J. Aho, *Radar-Computer Display Traces Alphanumeric Characters, Electronics*, 34:26, p 75-79.



rived from 60-cps centertapped sine-wave source. Reliability is insured through use of passive elements (resistors, capacitors, and

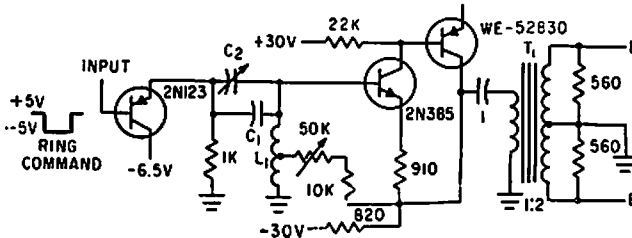
diodes) and standard techniques of clipping, limiting, and/or phase shifting to generate required waveshapes.—R. L. White, *Forming*

Handwritten-like Digits on CRT Display, *Electronics*, 32:11, p 138-140.



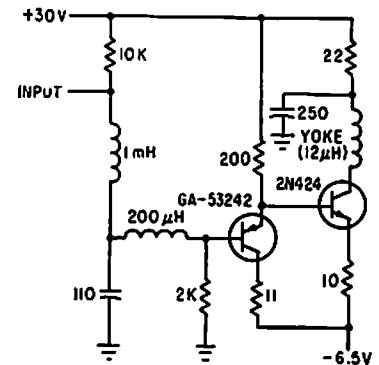
SHOCK-EXCITED SINE-WAVE OSCILLATOR—Rectangular pulse turns on five identical harmonic generators for alphanumeric character

generator.—K. E. Perry and E. J. Aho, Radar-Computer Display Traces Alphanumeric Characters, *Electronics*, 34:26, p 75-79.

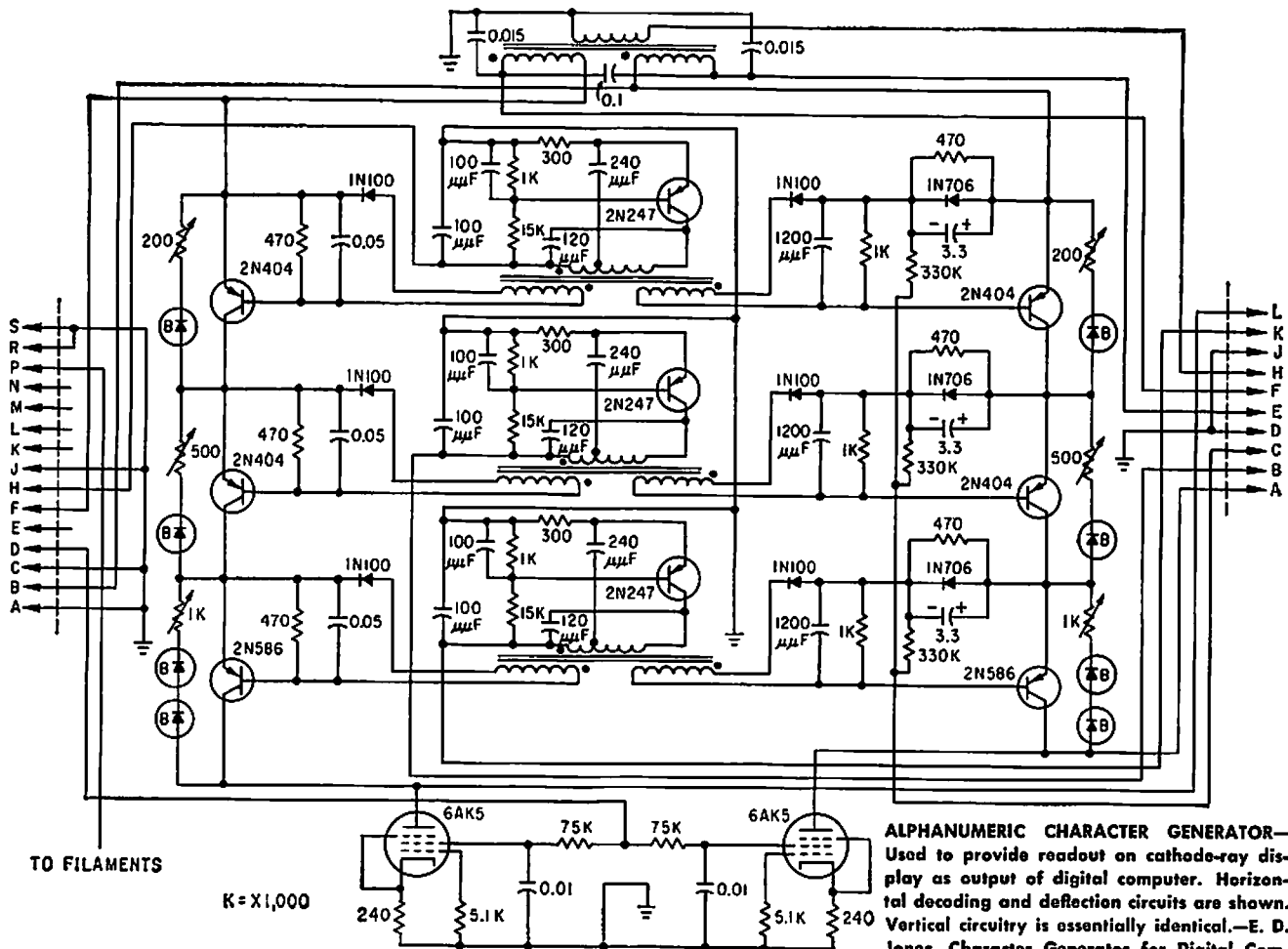


SHOCK-EXCITED COSINE-WAVE OSCILLATOR—Single 34.1-microsec ring command turns on five identical harmonic generators for alphanumeric character generator.—K. E. Perry and

E. J. Aho, Radar-Computer Display Traces Alphanumeric Characters, *Electronics*, 34:26, p 75-79.



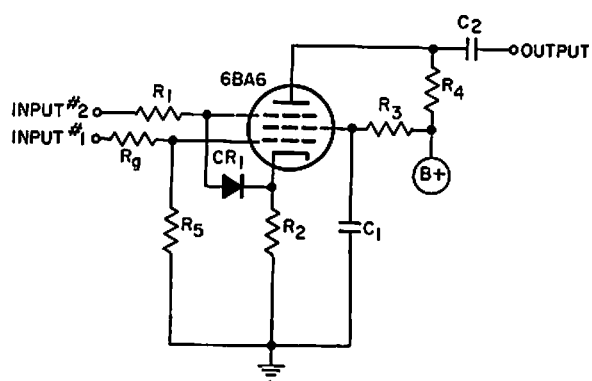
CHARACTER DEFLECTION AMPLIFIER—Converts X or Y voltage waveform into equivalent current waveform of up to 0.5 amp peak to peak, to drive low-inductance deflection yoke of cathode-ray character generator.—K. E. Perry and E. J. Aho, Radar-Computer Display Traces Alphanumeric Characters, *Electronics*, 34:26, p 75-79.



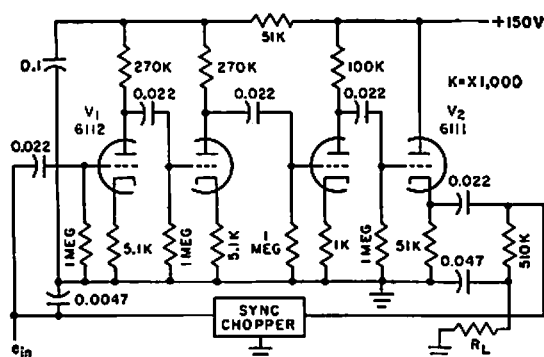
ALPHANUMERIC CHARACTER GENERATOR—Used to provide readout on cathode-ray display as output of digital computer. Horizontal decoding and deflection circuits are shown. Vertical circuitry is essentially identical.—E. D. Jones, Character Generator for Digital Computers, *Electronics*, 33:7, p 117-120.

CHAPTER 14

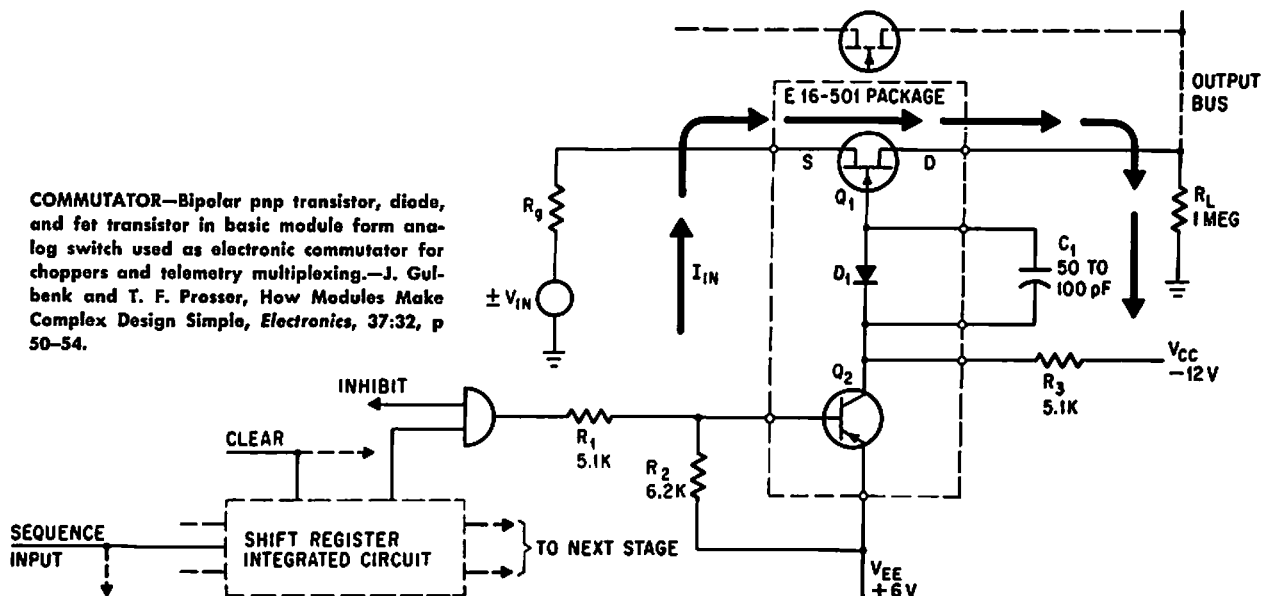
Chopper Circuits



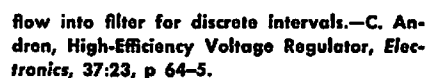
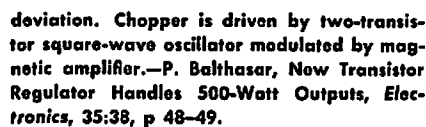
PENTODE CHOPPER—Designed for use as first stage of wide-band amplifier (d-c up to several kc). Design procedure is given. For 150-v plate supply, typical values are $R_2 = 100$ ohms, $R_4 = 5K$, $R_3 = 110K$, $R_1 = 15K$ with 1N34A diode, $R_5 = 1$ meg, $R_g = 1K$, and C_1 depends on lowest frequency to be amplified. —D. G. Knox, *Electronic Chopper*, *EEE*, 10:11, p 27-28.

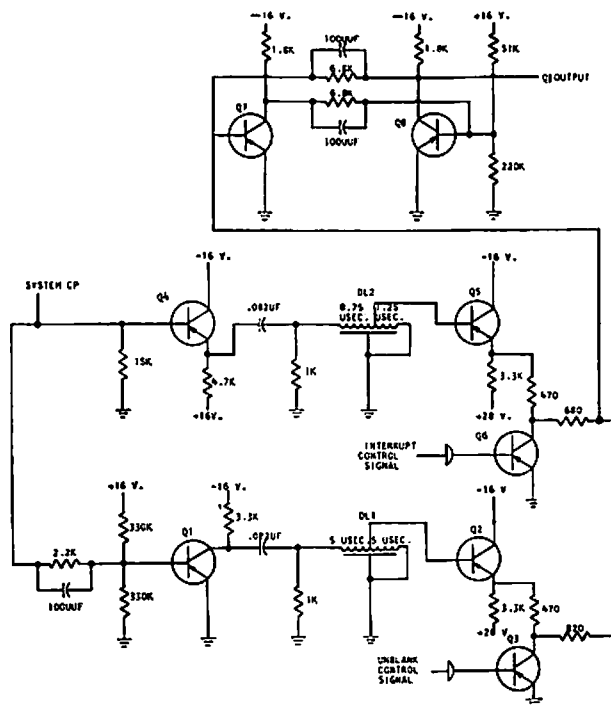


400-CPS MECHANICAL CHOPPER AMPLIFIER—Chopper modulates incoming d-c signal for a-c amplification, then demodulates output synchronously. Conversion gain is above 5,000. Suitable for high-gain low-level strain-gage thermocouple, and similar signals where amplifier drift must be minimized without using regulated power supply.—L. S. Klivans, *D-C Amplifiers for Control Systems*, *Electronics*, 31:47, p 96-100.



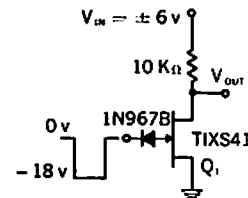
COMMUTATOR—Bipolar pnp transistor, diode, and fet transistor in basic module form analog switch used as electronic commutator for choppers and telemetry multiplexing.—J. Gulbenk and T. F. Prosser, *How Modules Make Complex Design Simple*, *Electronics*, 37:32, p 50-54.



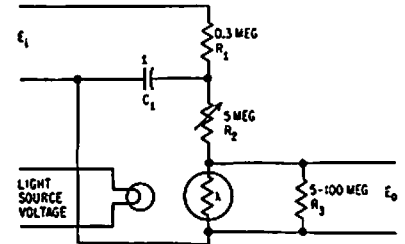


ASYNCHRONOUS SQUARE-WAVE CHOPPER—Used to interrupt or chop square wave generated with or in between regular system clock pulses, at specified times. Clock pulses are applied to bases of Q1 and Q4. Can be

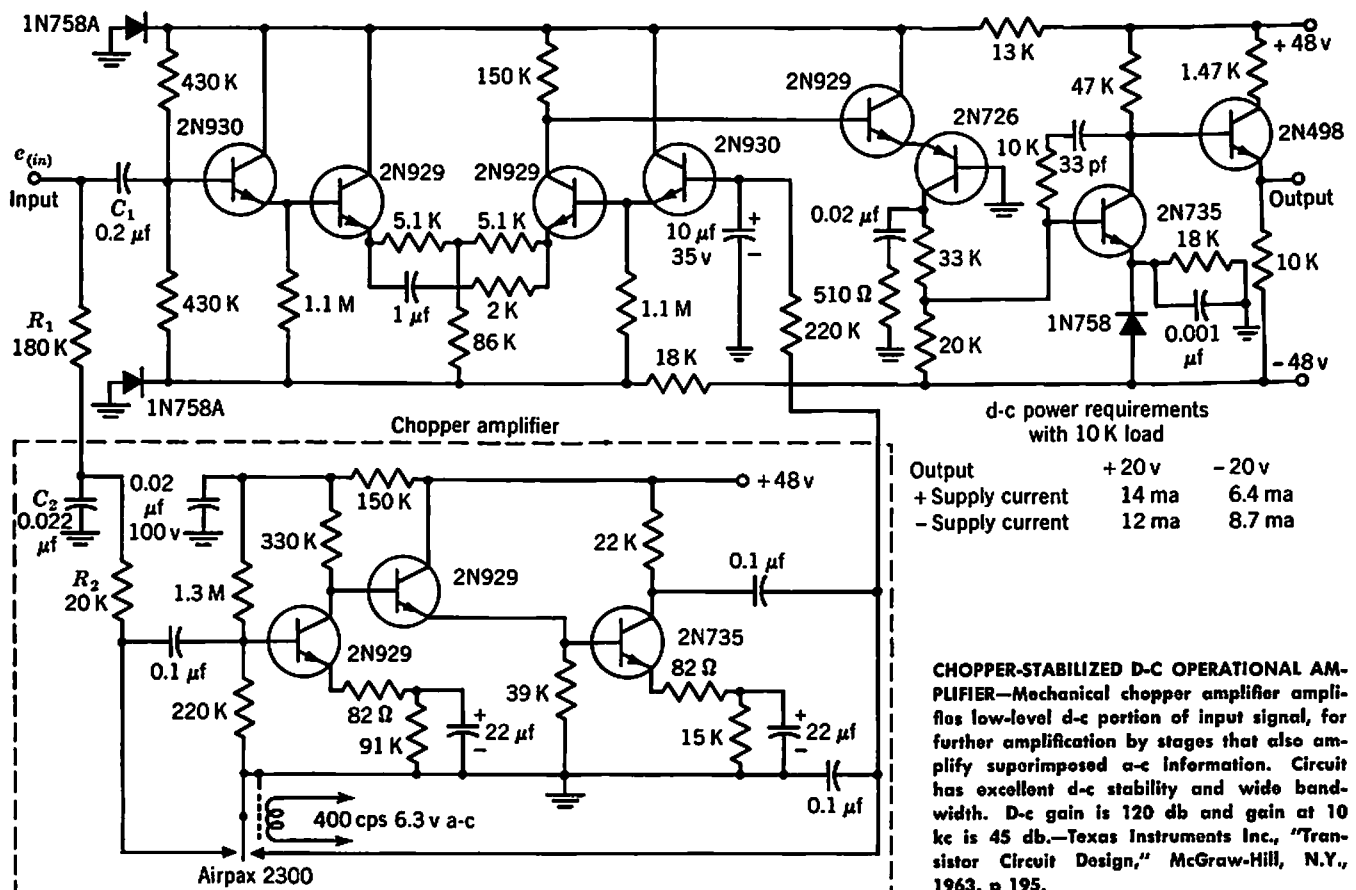
used to generate unblanking pulses without rise time deterioration, for intensifying sine and cosine waves on a radar display.—J. McGruder, Square Wave Chopper, *EEE*, 10:12, p 26-27.



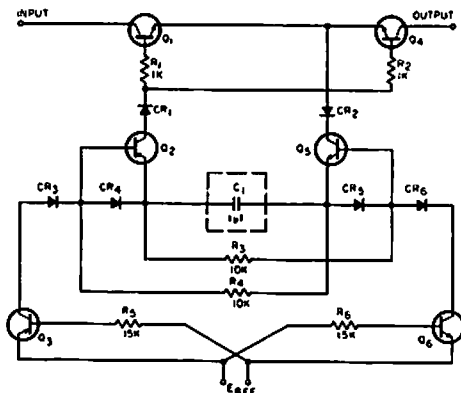
SHUNT FET CHOPPER—Shunt connection of silicon fet gives excellent performance because on resistance is only 20 ohms and drain-gate leakage current is less than 0.1 nanoamp.—Six More Semiconductor Advances from TI, (Texas Instruments ad), *EEE*, 14:8, p 120-121.



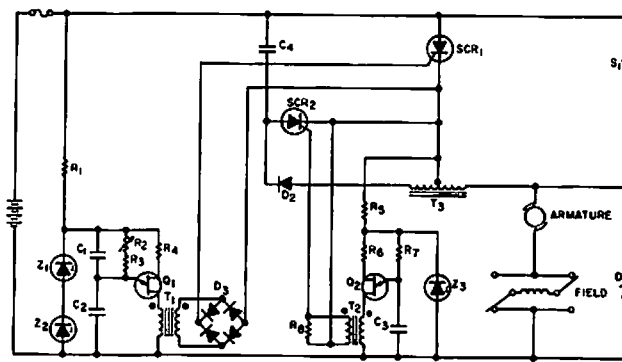
PHOTOCONDUCTIVE CHOPPER—Combines low noise level with resistance to vibration. R1 prevents burnup of photocell. R2 gives maximum conversion efficiency at setting of about 2.2 meg. C1 averages d-c input signal fluctuations so they do not exceed 120-cps chopping frequency of light source.—R. G. Seed, Chopper Uses New Photocells, *Electronics*, 31: 21, p 90-98.



CHOPPER-STABILIZED D-C OPERATIONAL AMPLIFIER—Mechanical chopper amplifier amplifies low-level d-c portion of input signal, for further amplification by stages that also amplify superimposed a-c information. Circuit has excellent d-c stability and wide bandwidth. D-c gain is 120 db and gain at 10 kc is 45 db.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 195.



PRINTED-CIRCUIT CHOPPER—Uses conventional Bright chopper connected to reference supply through coupler, constructed on ceramic chip. Q1 and Q4 are 2N914 matched pairs, Q2 and Q3 are 2N2412, and Q5 and Q6 are 2N914. All diodes are 1N914.—D. D. Robinson, Application of Integrated Circuits: An Evolutionary Approach, *EEE*, 12:4, p 42-47.



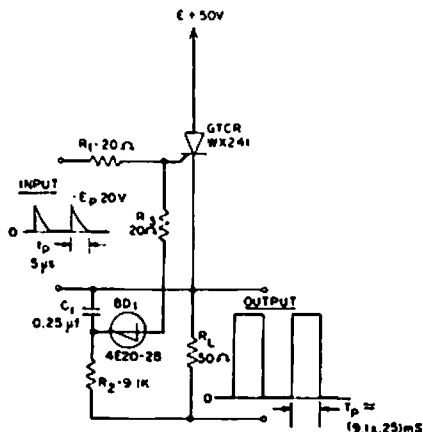
C1—0.1 μ F, 100V
C2—0.1 μ F, 100V
C3—0.22 μ F, 100V
C4—100 μ F, 15VAC, G-E 28F103
D1—G-E 1N2153
D2—G-E A27P
D3—G-E 1N3584
D4—G-E 2N2646
Q1—G-E 2N2647

R1—220 Ω , 2W
R2—0 TO 100K POTENTIOMETER (TAPER D)
R3—20K, 1/2W
R4—330 Ω , 1/2W
R5—270 Ω , 2W
R6—330 Ω , 1/2W
R7—15K, 1/2W
R8—100 Ω , 1/2W

SCR1—G-E C358
SCR2—G-E C358
T1—P-E 2229
T2—P-E 2229
T3—G-E 9T45VT009
Z1—G-E 1N1771
Z2—G-E 1N1771
Z3—G-E 1N1773

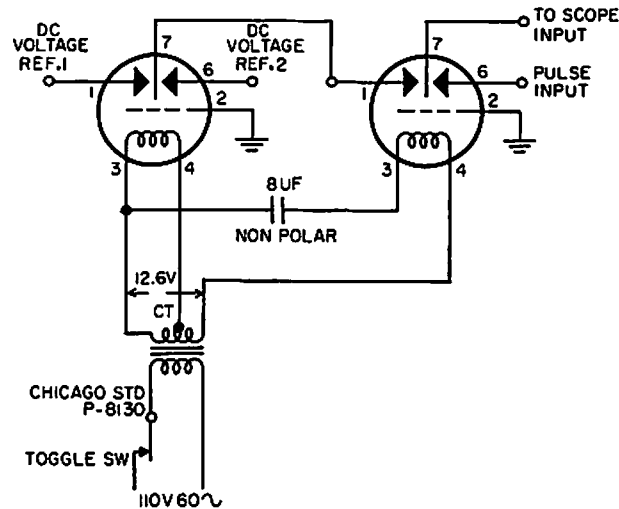
JONES CHOPPER FOR BATTERY-POWERED VEHICLE—Uses variable-frequency constant-pulse-width system that starts reliably and provides smooth acceleration. At low speeds, on time of chopper, in series with d-c series

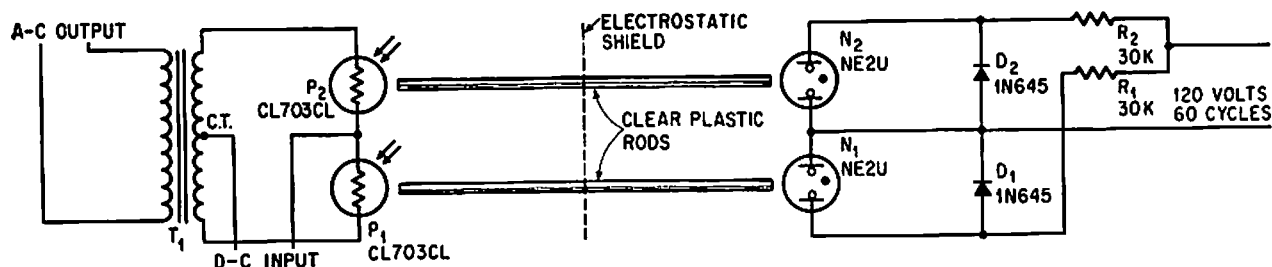
motor, is much less than off time so average motor voltage is low. Potentiometer R2 controls ratio of on to off times for speed control. —“Silicon Controlled Rectifier Manual,” Third Edition, General Electric Co., 1964, p 173.



NORMALLY-OFF GTO CHOPPER—Small trigger at input generates high-power pulse with duration determined by time constant R2-C1. Gate-turnoff controlled rectifier in this circuit will chop 1 kw at 1 kc.—J. W. Motta, Jr., Switching Circuits Using the Gate Turnoff Controlled Rectifier, *EEE*, 11:3, p 52-55.

ARNOLD CHOPPERS A-175

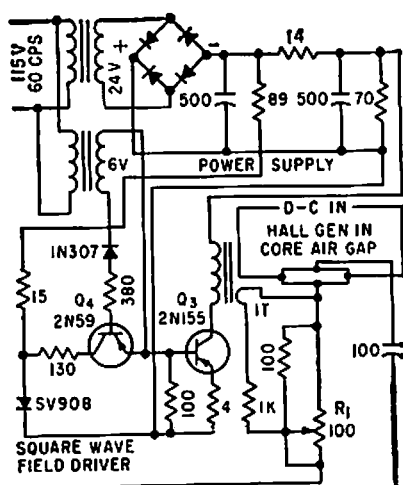




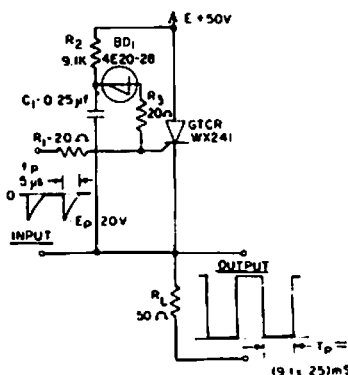
OPTOELECTRONIC CHOPPER WITH NEONS—Used with amplifier of sensitive potentiometer recorder. Diodes D1 and D2 short-circuit

lamps on alternate half-cycles. Photoelectric chopping eliminates stray interference from a-c line and minimizes heat dissipation prob-

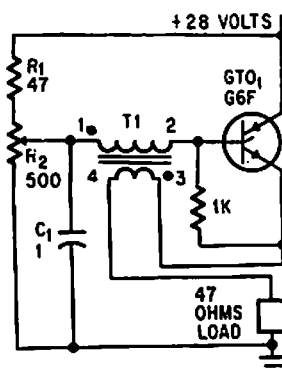
lems.—W. Moore, Photoconductors Chop D-C Signal Levels, *Electronics*, 38:9, p 61-62.



HALL-GENERATOR CHOPPER AMPLIFIER—D-c signal voltage to be chopped is applied as control current of Hall generator. Magnetic field for generator is pulsed at 60 cps by driver circuit. Output is pulsating d-c voltage that is product of the two inputs. Error in input d-c versus output a-c is 2.5% for temperature range of -20 to 50°C . Can be used as d-c, a-c, or r-f microammeter.—T. J. Marcus, Highly Sensitive Electronic Chopper, *Electronics*, 32:40, p 67-68.



NORMALLY-ON GTO CHOPPER—Small trigger at input removes applied voltage from load for duration of time constant R_2-C_1 . Handles 1 kw at 1 kc.—J. W. Motto, Jr., Switching Circuits Using the Gate Turnoff Controlled Rectifier, *EEE*, 11:3, p 52-55.

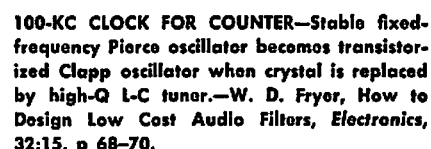


CHAPTER 15

Clock Circuits

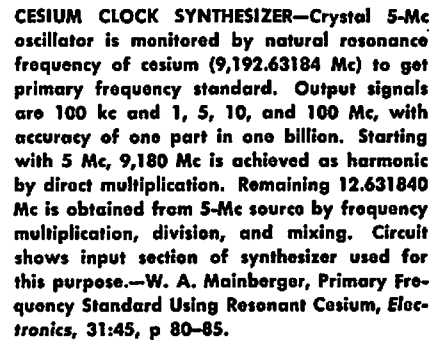


generator Q3-Q4.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 202.

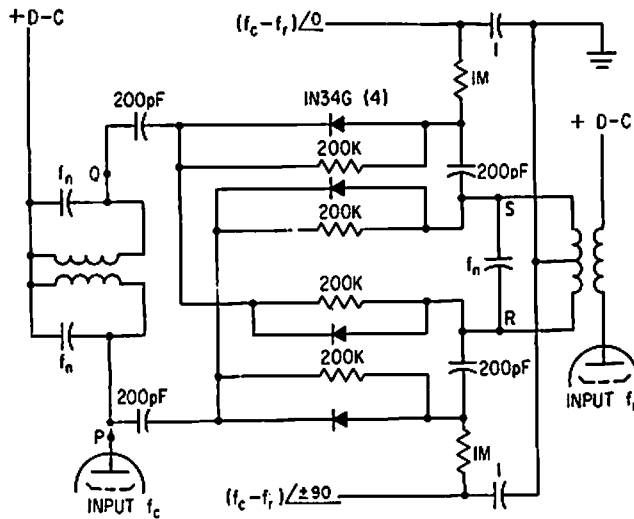


sufficient for short-time drifts over several days. When C1 reaches either end of its range, cam closes S1 or S2 and energizes drive motor M1 for C2. Large capacitor is then driven in direction that will make small

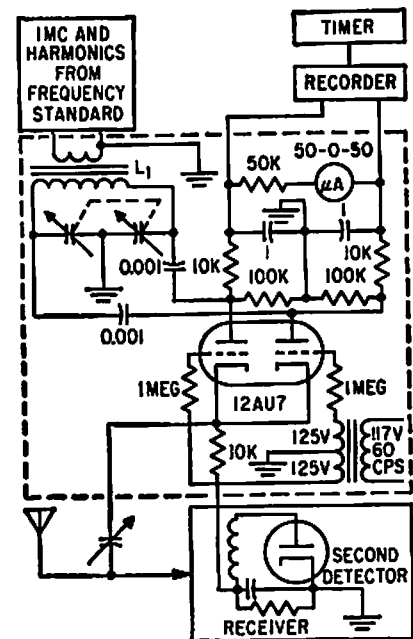
capacitor return to middle of its range.—W. A. Mainberger, Primary Frequency Standard Using Resonant Cesium, *Electronics*, 31:45, p 80-85.



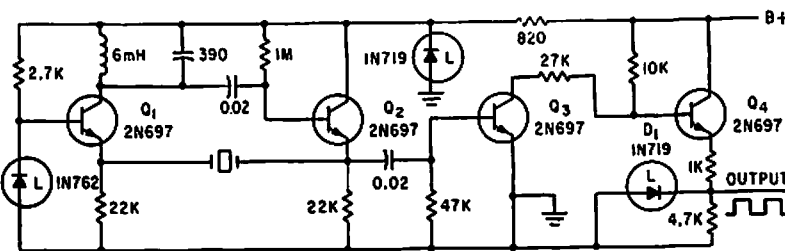
moving rectifier gate from driver output.—R. S. Reed, Rugged Arming-Fuzing Timer for Atomic Artillery Missile, *Electronics*, 34:38, p 48-51.



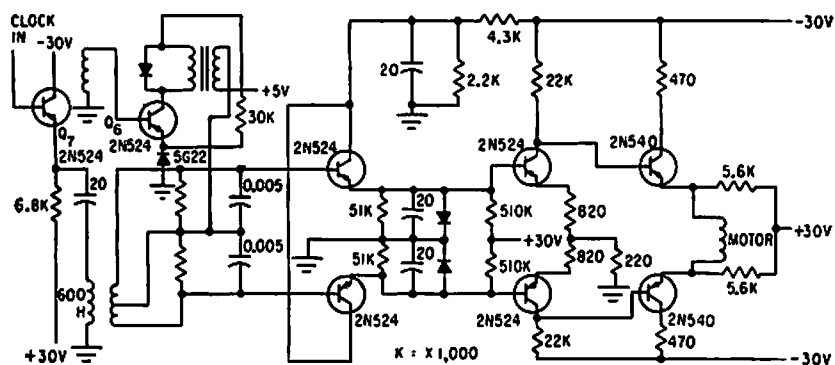
in phase. These output signals are amplified for synchronous motor that drives trimmer capacitor of crystal oscillator in servo loop that brings difference frequency to zero.—K. Nygaard, Atomic Clock Accuracy for Crystal Oscillators, *Electronics*, 33:46, p 82-83.



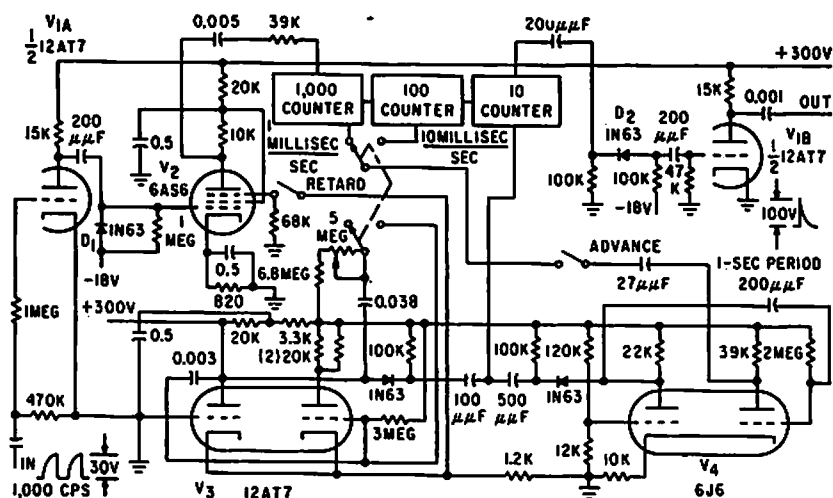
SIMPLE WWV CHECK—Permits making accurate check of local frequency standard quickly and easily by direct comparison with WWV signals. Uses one receiver. Two signals, at 0 and 180°, are obtained from local standard clock. Switch 12AU7 alternately connects one triode detector output and 0° signal simultaneously to antenna, and then other triode detector output simultaneously with 180° signal. Doppler error is minimized by averaging hourly 3-minute readings over 8-hour period. Accuracy is one part in 100,000,000.—J. F. Brumbach, *Fast WWV Check of Frequency Standard*, *Electronics*, 32:13, p 76-79.



circuit to crystal. Output amplitude is limited by zener diode D1.—R. S. Reed, Rugged Arming-Fuzing Timer for Atomic Artillery Missile, *Electronics*, 34:38, p 48-51.

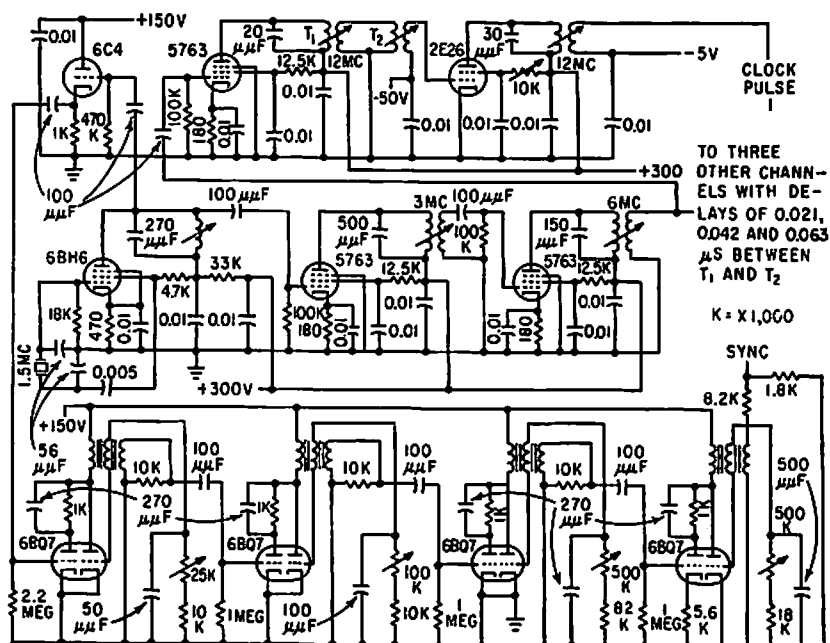


PHASE SYNCHRONIZER—Used to synchronize 2,500-cps local time standard with that of transmitting end of wire-line system. When zero crossings of pulses do not coincide, error signal is produced which, after amplification, is applied to motor that rotates phase shifter until there is phase synchronization between local clock and incoming information. This insures sampling of recovered information at middle of incoming bit.—J. L. Hollis, *Sending Digital Data Over Narrow-Band Lines*, *Electronics*, 32:23, p 72-74.



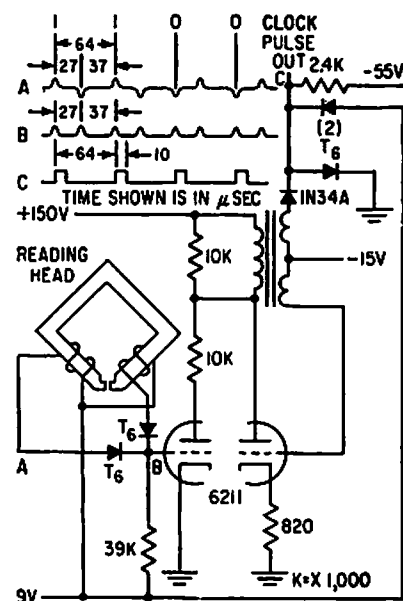
SETTING CLOCK TO WWV—Locally generated pulses can be easily synchronized with pulses from WWV to set clocks associated with frequency standard. Changes in period of pulses from counter-type frequency dividers are

made with precision vernier. Any 10-stable divider can be used in counter circuit.—E. F. Wilson, *Using Divider Vernier to Synchronize Pulses*, *Electronics*, 32:27, p 44-45.

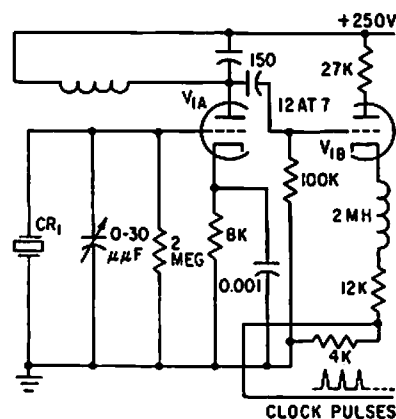


FOUR-PHASE 12-MC CLOCK—To minimize jitter of synchronizing signals, fundamental frequency is chosen at one-eighth of clock rate or 1.5 Mc. Oscillator output is fed through two doubler stages and split into three chan-

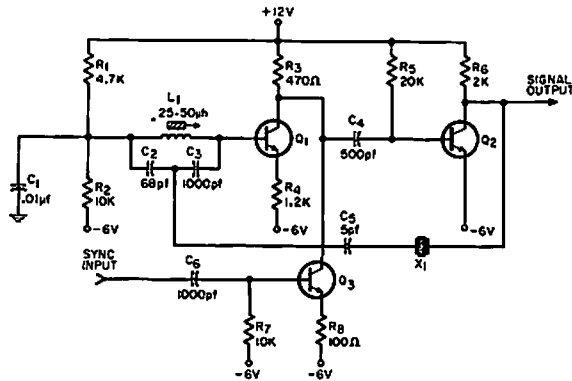
nels, each having additional doubler followed by power amplifier, to provide low output impedance at any reasonable power level.—G. O. Olson, *Design of High-Frequency Clock Pulse Generators*, *Electronics*, 32:35, p 56-57.



CLOCK PULSE GENERATOR—Derives pulse from reading head that scans data track of magnetic-spoke disk memory, eliminating need for separate timing track. Generates pulse for each magnetic spoke passing over gap of head, regardless of whether spoke is written with 1 or 0.—T. C. Chen and O. B. Stram, *Digital Memory System Keeps Circuits Simple*, *Electronics*, 32:11, p 130-133.

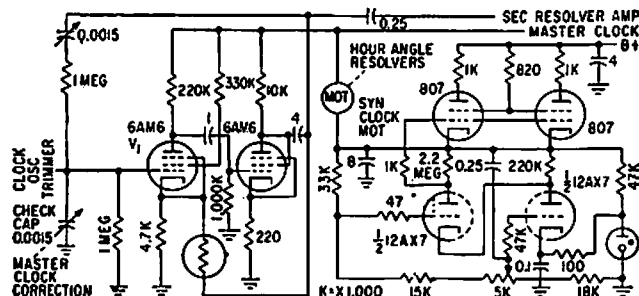


809.11-KC CLOCK OSCILLATOR—Crystal-controlled oscillator V1A and shaping circuit V1B produce pulses with repetition rate equal to frequency of crystal oscillator.—H. Vantine Jr. and E. C. Johnson, *Modified Transceivers Compute Distance*, *Electronics*, 31:37, p 94-98.



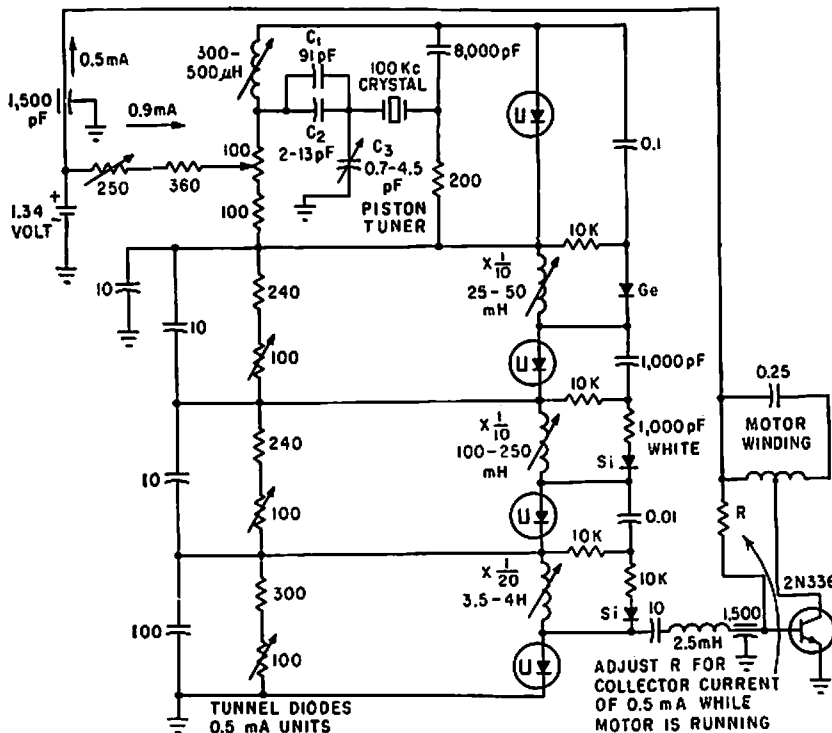
SYNCHRONIZING 3.3-MC CLOCK TO 300-PPS TRIGGER—Synchronizes crystal-controlled train of 3.3-Mc clock pulses to unrelated sync trigger having nominal repetition rate of 300

pulses per second, to provide constant delay between end of sync pulse and first clock pulse.—P. Danzer, Synchronized, Crystal-Controlled Oscillator, *EEE*, 12:5, p 90.



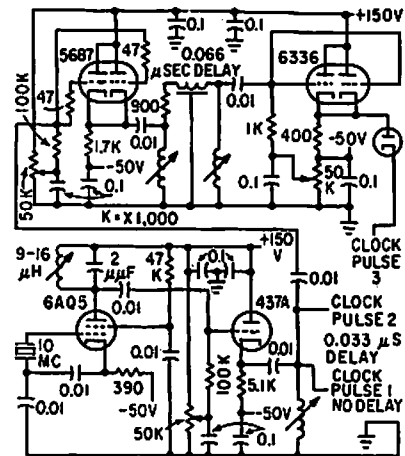
SIDEREAL REGENERATIVE OSCILLATOR—Compares synchronous clock motor speed at 30-sec intervals with pendulum-driven master sidereal-time clock. Motor runs slightly faster than sidereal time. If too fast at checking time, check capacitor at input of V1 is closed

to reduce frequency of oscillation and speed of motor to bring it back into coincidence with master.—C. N. Kingston, Radio Telescope Sees 2 Billion Light Years, *Electronics*, 31:23, p 70-75.

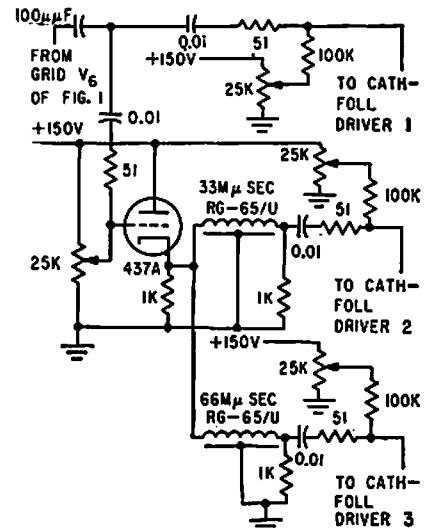


ELECTRONIC CHRONOMETER—Trimmers permit adjusting timing by a few seconds per year. Major error is due to aging of quartz crystal. Tunnel diodes divide 100-kc crystal frequency

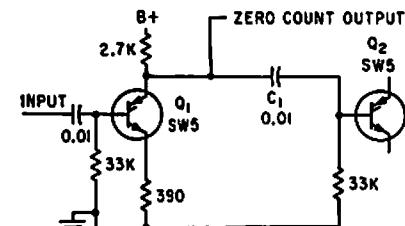
by 2,000 to give 50-cps output for driving motor of clock.—R. L. Watters, Tunnel Diodes Control Quartz-Crystal Chronometer, *Electronics*, 34:39, p 129-131.



10-MC CLOCK OSCILLATOR—Output through cathode follower is split into three channels (two are not shown) to get clock pulses at three phases.—G. O. Olson, Design of High-Frequency Clock Pulse Generators, *Electronics*, 32:35, p 56-57.



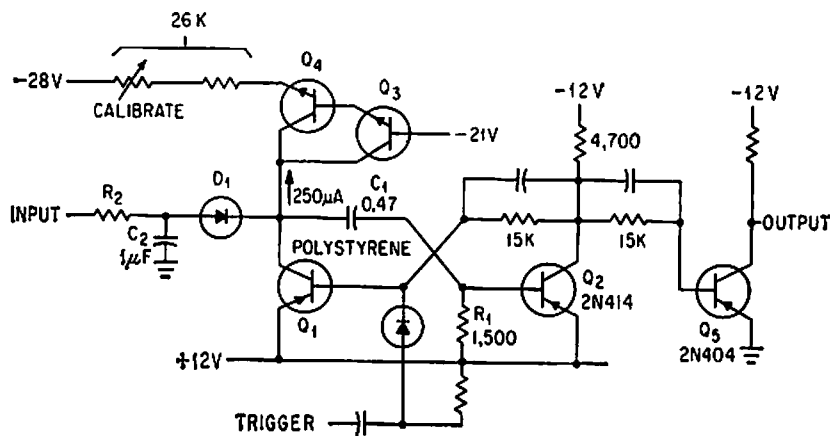
THREE-PHASE 10-MC CLOCK—Used in digital computer circuit testing when recirculation of pulses and pulse regeneration and shaping are required. Delay line provides required impedance match.—R. W. Buchanan and B. Kautz, Dynamic Testing of Computer Building Blocks, *Electronics*, 32:33, p 66-68.



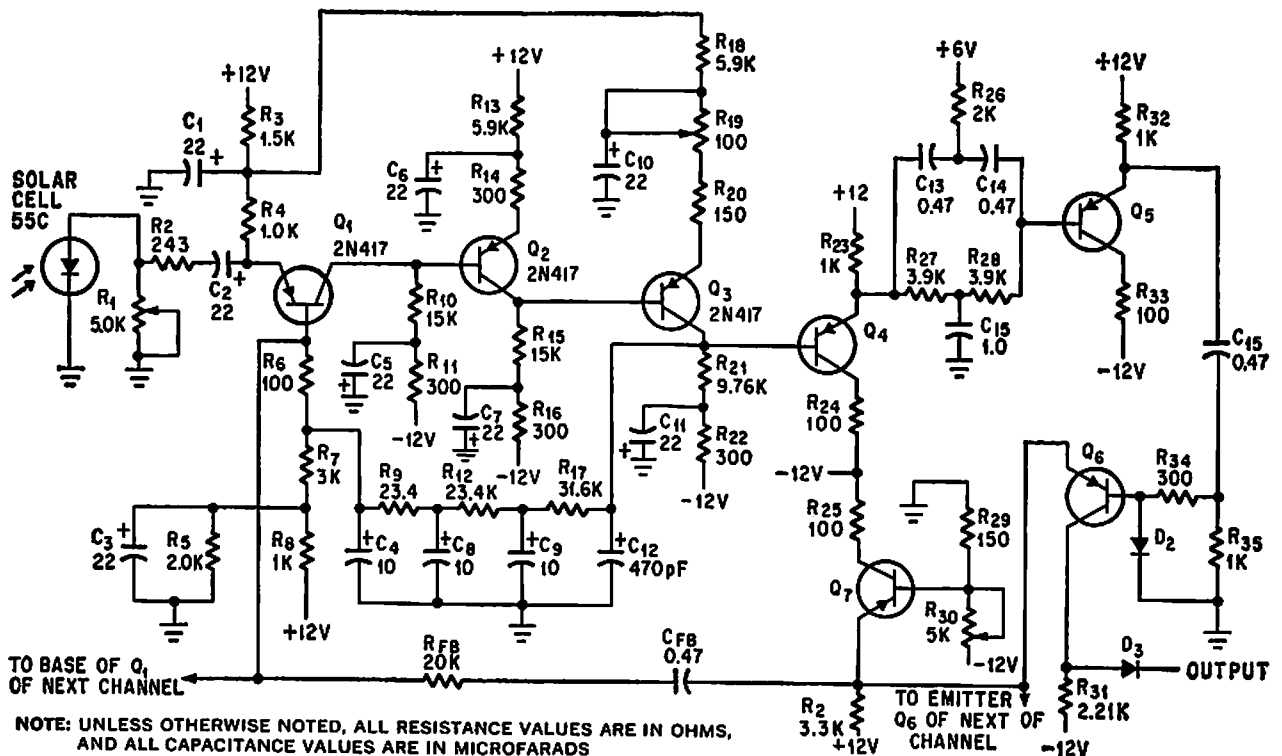
CLOCK RING COUNTER—Switching element is silicon controlled switch that approximates flip-flop, turning on when low-level positive pulse is applied to its base, and remaining on until turned off by negative pulse. Used as memory device to retain registered count until next input pulse makes bit transfer to following stage.—R. S. Reed, Rugged Arming-Fuzing Timer for Atomic Artillery Missile, *Electronics*, 34:38, p 48-51.

CHAPTER 16

Comparator Circuits



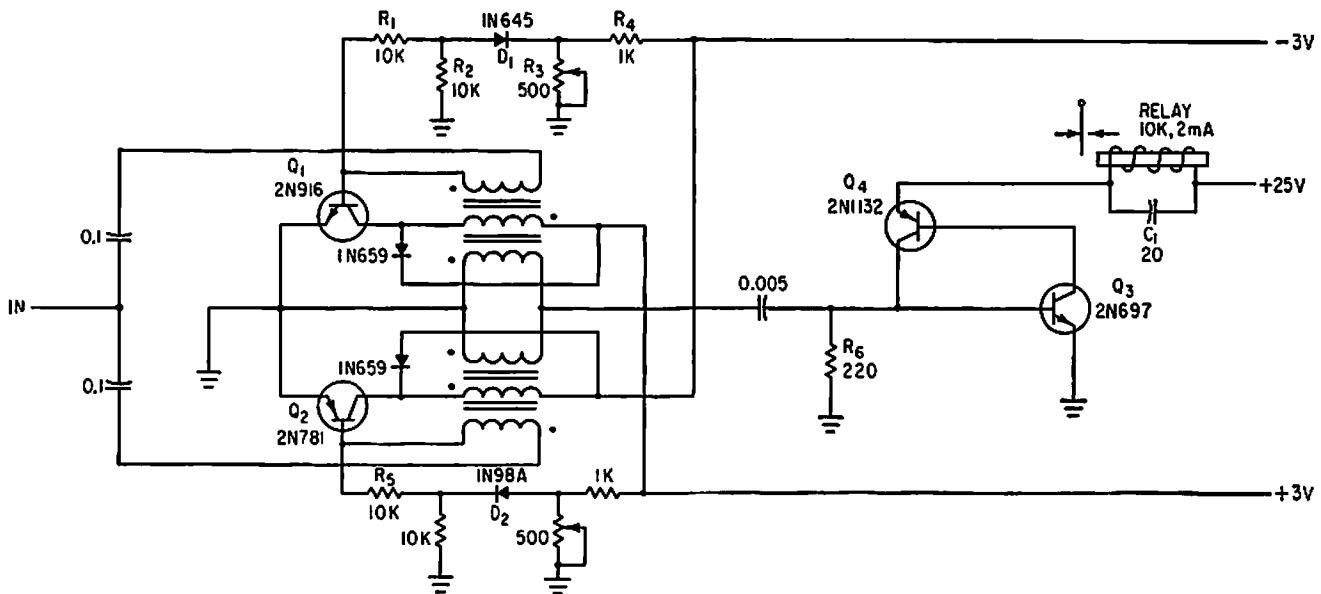
COMPARATOR FOR SOLID-STATE DIGITAL VOLTMETER—Circuit determines when output of ramp generator crosses 0 v and crosses unknown voltage. Transistor Q5 isolates mono Q1-Q2 from output logic. Transistors Q3 and Q4 provide constant current for charging C1 linearly to produce high-accuracy ramp.—R. C. Weinberg, Modified Ramp Generator Develops High D-C Input Impedance, *Electronics*, 37:8, p 33-35.



CHARACTER READER—Circuit shows one channel of solar-cell signal amplifier and section

of switching block for experimental character recognition system.—P. H. Howard, *Feedback*

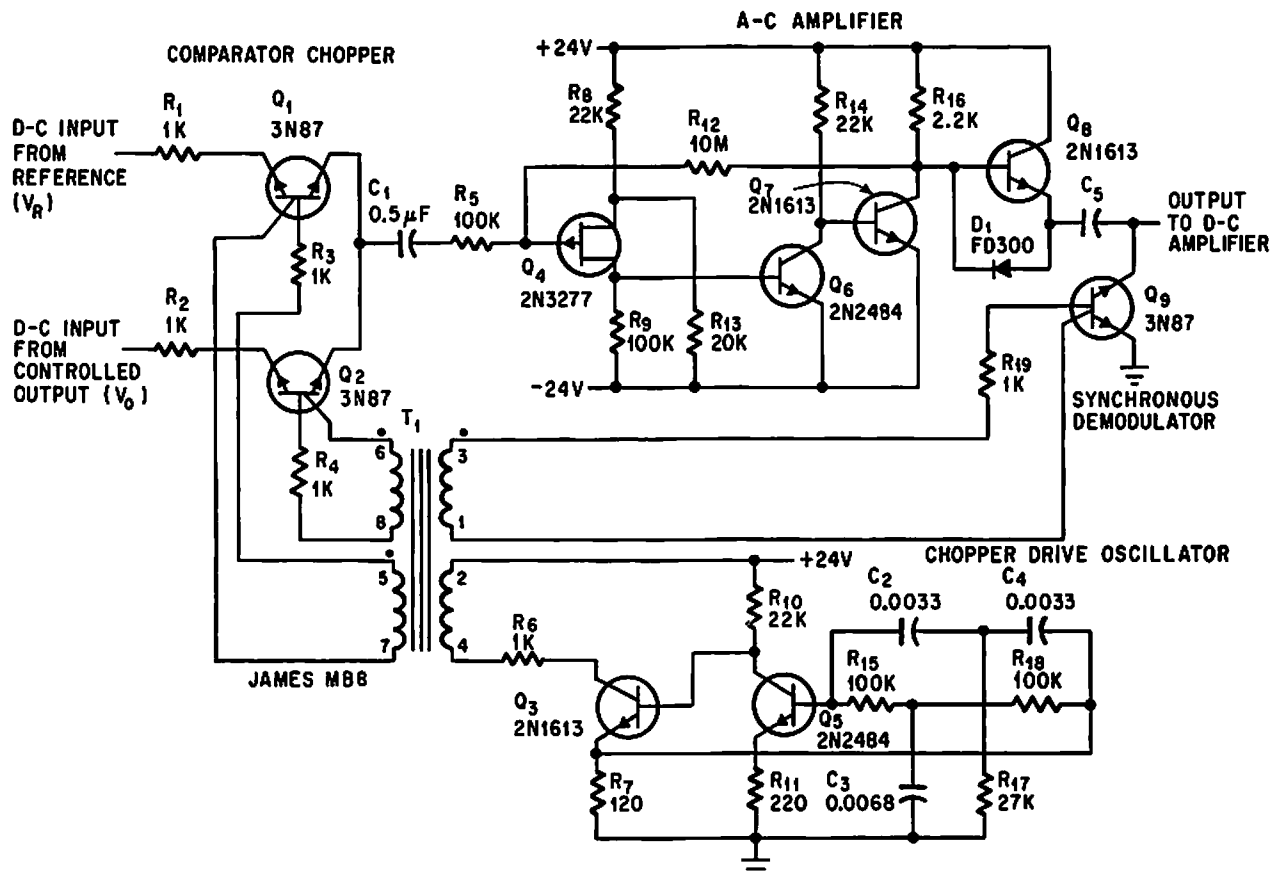
System Detects 1% Amplitude Difference, *Electronics*, 38:10, p 68-70.



PULSE ANALYZER—Either positive or negative pulses equal to or greater than adjustable threshold voltage operate relay, thus meas-

uring pulse height of either polarity. Responds to pulse widths as narrow as 50 nsec. Q1 responds to positive pulses, and Q2 to

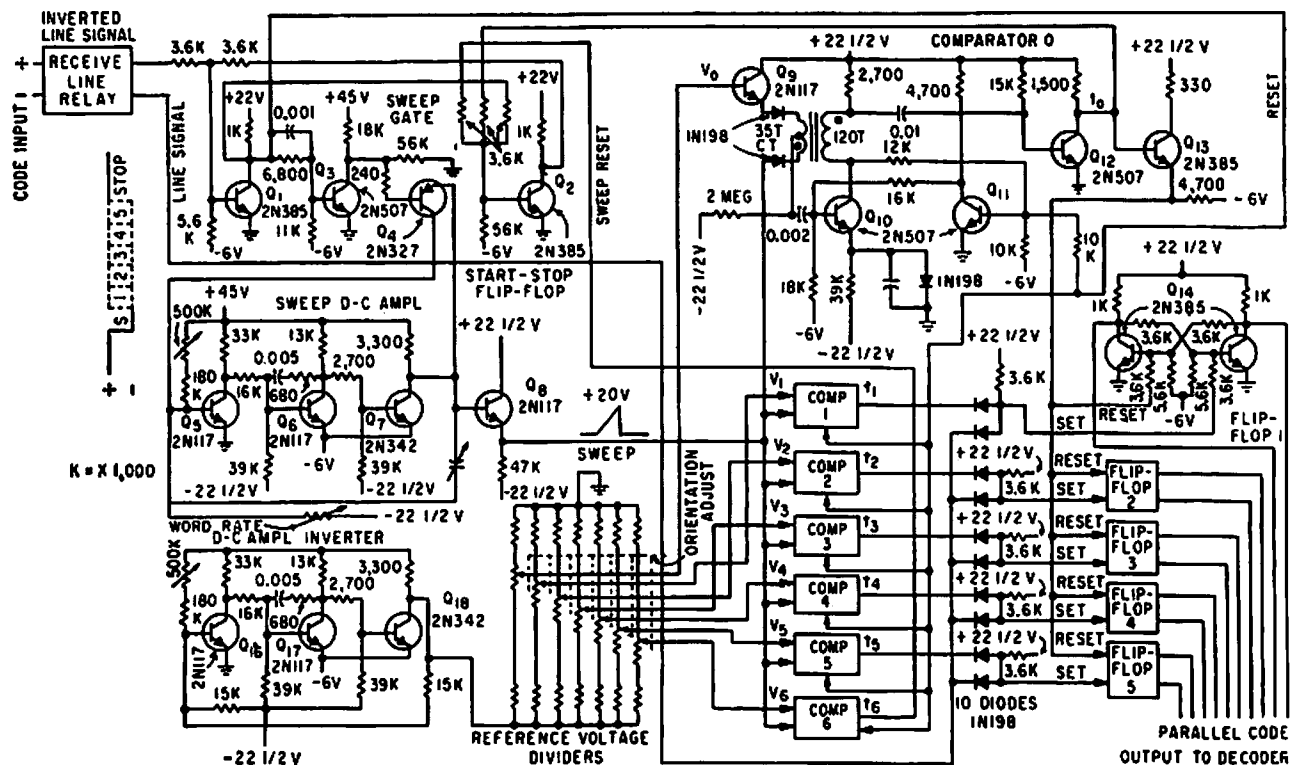
negative pulses.—O. B. Laug, Pulse Voltage Comparator Measures Height of Positive or Negative Pulses, *Electronics*, 34:36, p 70-71.



CHOPPER TRANSISTORS SIMULATE SPDT SWITCH—Comparator chopper senses difference between reference voltage and control

signal while drawing very little current from reference. Sine-wave drive frequency is determined by C2 and C3, which should have

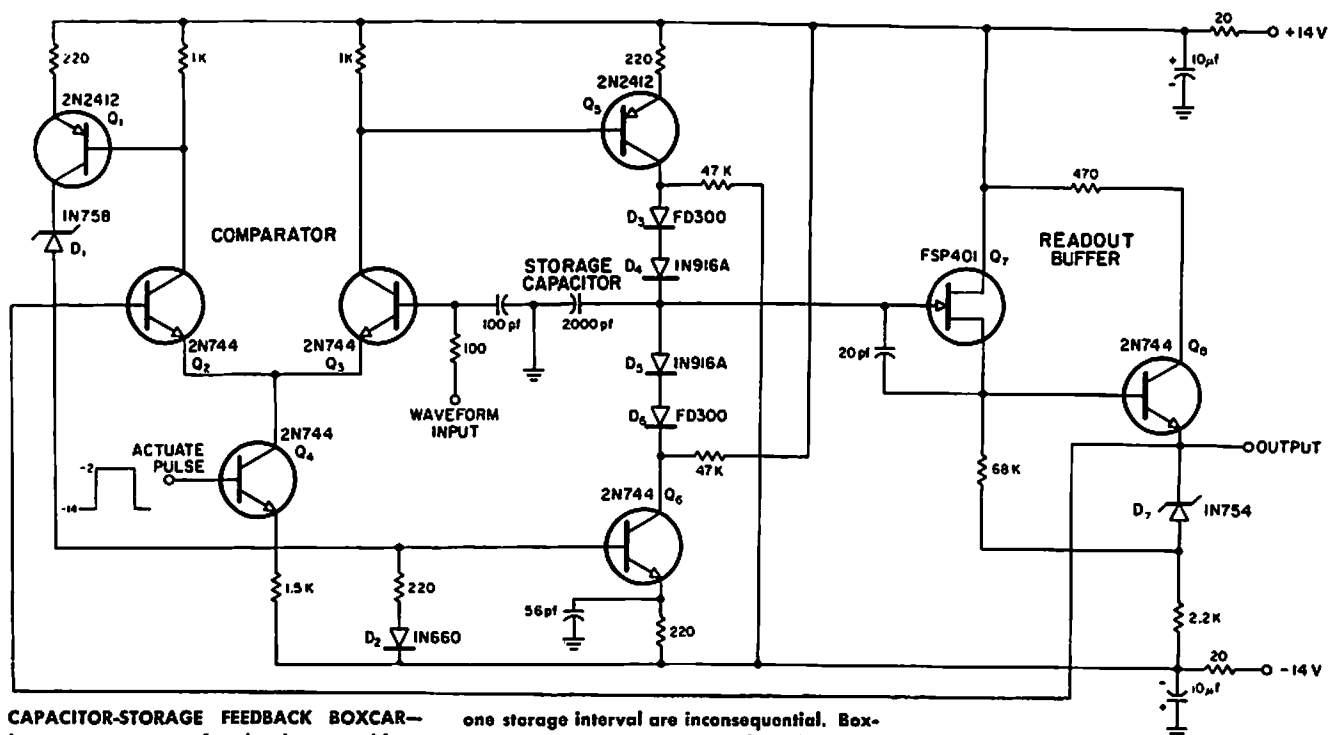
2 to 1 ratio.—J. S. Mac Dougall, Servo Comparator Amplifier Handles High Voltages, *Electronics*, 37:22, p 75-76.



RECEIVING DISTRIBUTOR—Converts tape-code input to 5-bit parallel code at five conventional flip-flops. Rising sweep voltage successively triggers six comparator stages. If line

signal is positive at triggering time, flip-flop corresponding to triggered comparator is set to its one state. Used in high-speed electrostatic printer in which each of 72 print heads

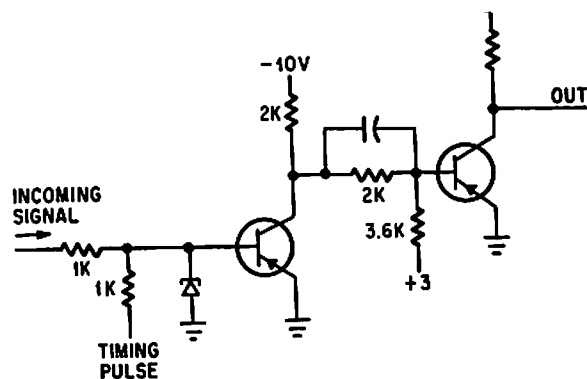
has 35 print pins.—R. E. West, High-Speed Readout for Data Processing, *Electronics*, 32: 22, p 83-85.



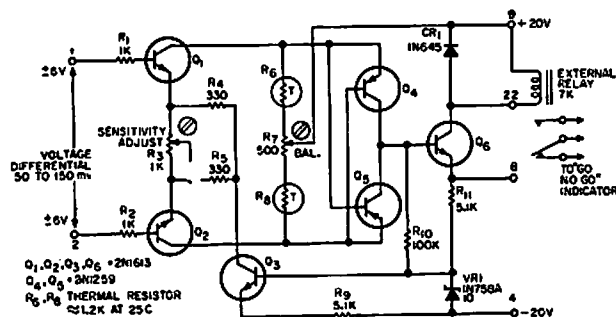
CAPACITOR-STORAGE FEEDBACK BOXCAR—Improves accuracy of radar boxcar without sacrifice in speed, by using feedback principles to overcome instabilities in d-c offset voltages. Feedback forces boxcar output to equal input voltage during sampling, so internal d-c offsets and instabilities longer than

one storage interval are inconsequential. Boxcar input goes to one terminal of difference amplifier, and boxcar output goes to other terminal. Difference amplifier output is then used to control capacitor charge. By gating on difference amplifier during sampling interval, boxcar output is made equal to value

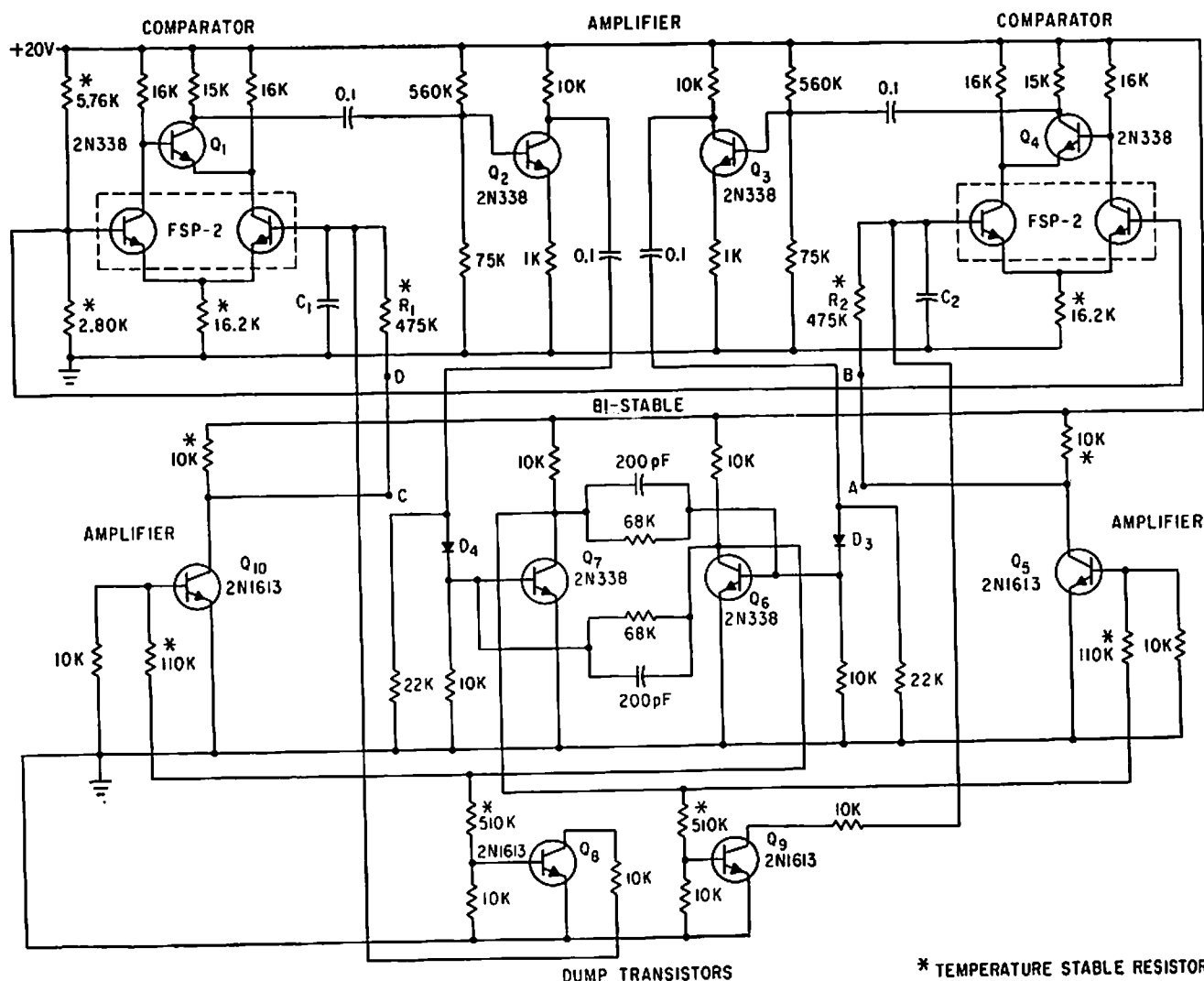
of waveform at that time.—P. E. Harris and B. E. Simmons, DC Accuracy in a Fast Boxcar Circuit Via a Comparator, *IEEE Transactions on Electronic Computers*, June 1964, p 285-288.



TUNNEL-DIODE VOLTAGE COMPARATOR—When incoming signal exceeds predetermined level, pulse with predetermined amplitude appears at output.—T. Kojima and M. Watanabe, *When You're Second, You Try Harder, Electronics*, 38:25, p 81-89.



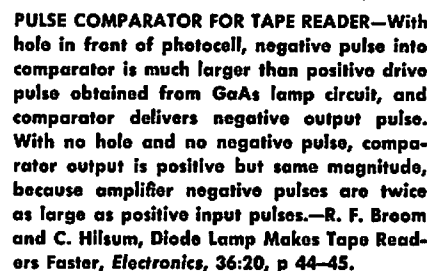
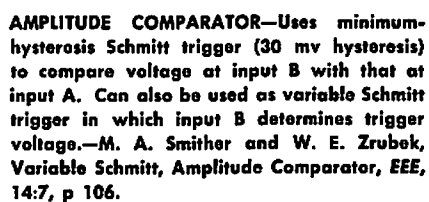
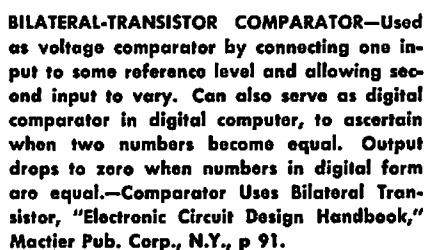
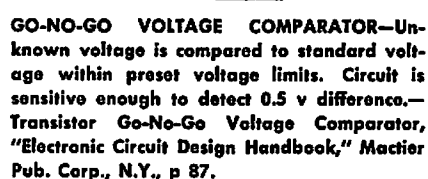
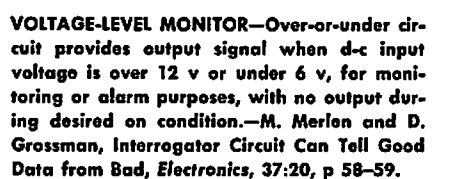
DIFFERENTIAL VOLTAGE COMPARATOR—If the two input signals are within preset differential voltage, relay is not actuated and GO indicator comes on. When the two signals differ too much, relay is actuated and NO-GO indication is provided. Used in comparing telemetered data received from satellite vehicle.—P. A. Walter, *Differential Voltage Comparator*, *EEE*, 10:8, p 24-25.



STABILIZED LOW-FREQUENCY OSCILLATOR—Transistors Q1 and Q4 compare charging voltages of mvbr timing capacitors C1 and C2 to fixed reference voltage. When a capacitor voltage is greater than reference voltage, its

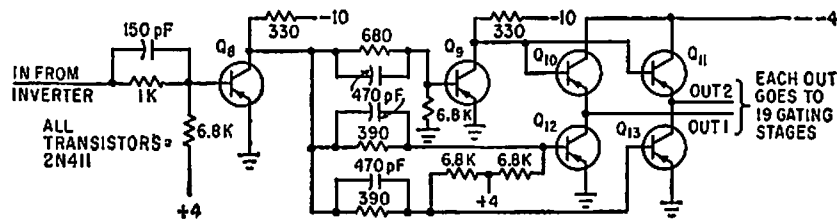
comparator switches its bistable mvbr to opposite state, so capacitor is discharged by dump transistor. Arrangement makes output frequency essentially independent of temperature from -25°C to $+75^{\circ}\text{C}$, for frequency

* TEMPERATURE STABLE RESISTORS range of 0.01 to 100,000 cps.—J. D. Long, *Novel Differential Amplifier Stabilizes Multi-vibrator*, *Electronics*, 35:24, p 53-54.

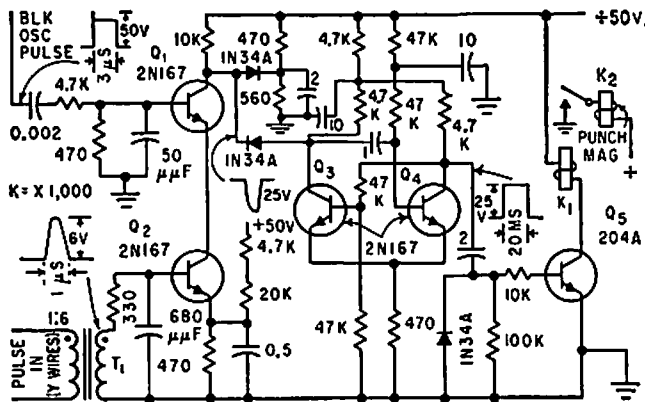


output to 95-ohm load. Circuit gain is about 3 db.—A. E. Popodi, Reliable Repertoire Of

Display Circuits, Electronics, 38:2, p 60-66.

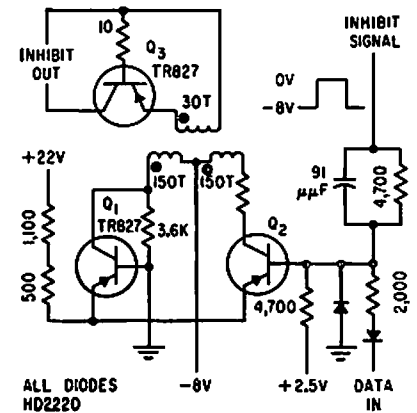


MEMORY STROBING-PULSE GATE—Used in generating precisely defined strobes for coincident-current memory. Uses drive-sampling core instead of clock signal to produce strobe at time when signal-to-noise ratio is highest. —A. H. Ashley and E. U. Cohler, Solving Noise Problems in Digital Computer Memories, *Electronics*, 33:13, p 72-74.

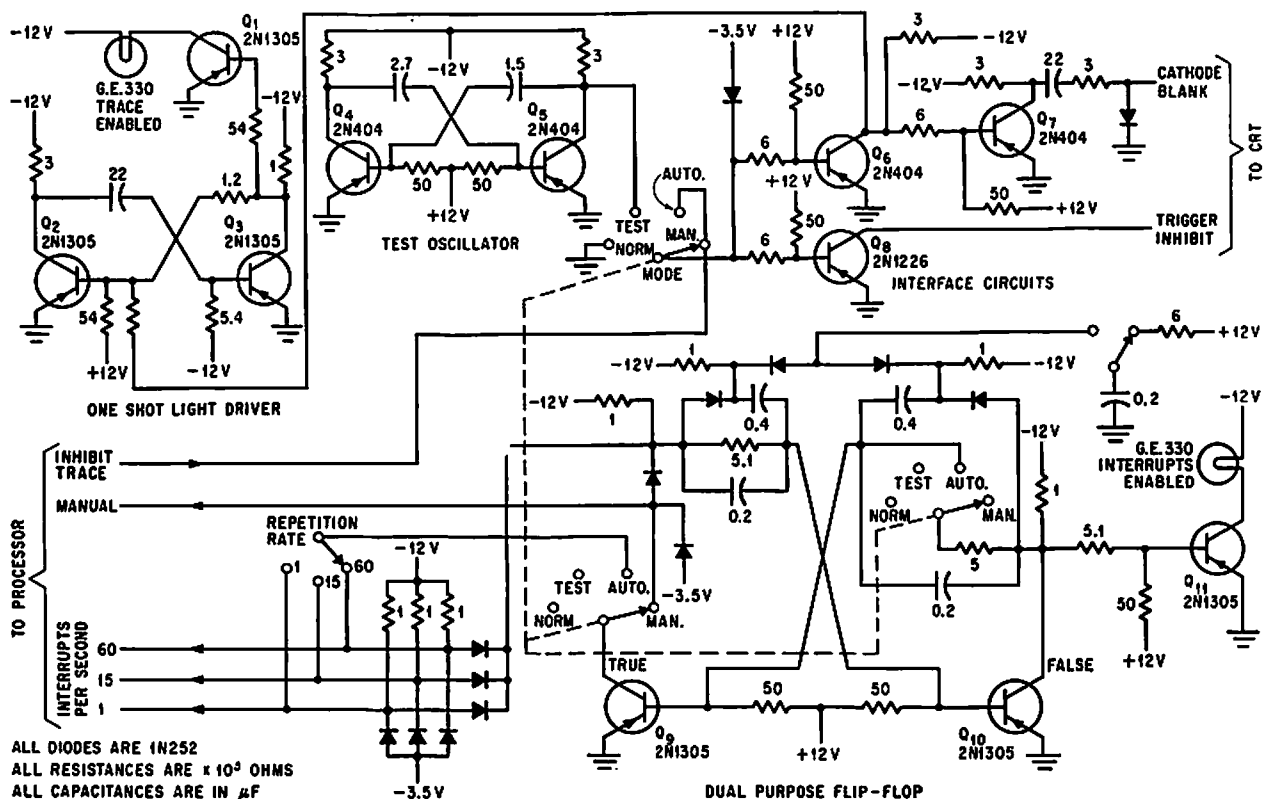


MATRIX READOUT—Used for data reduction in telephone traffic data recorder system to permit recording all information on a call as one entry. Coincidence circuit Q1-Q2 provides reliable sensing of matrix output in

presence of noise generated by rotary switches and relays. —J. W. Blanchard, E. C. Belle, and J. Smith, Ferrite Memories Simplify Telephone Data Analysis, *Electronics*, 32:41, p 68-70.



INHIBIT DRIVER—Used in coincident-current digital data buffer memory. —D. Haagens, Compact Memories Have Flexible Capacities, *Electronics*, 32:40, p 50-53.

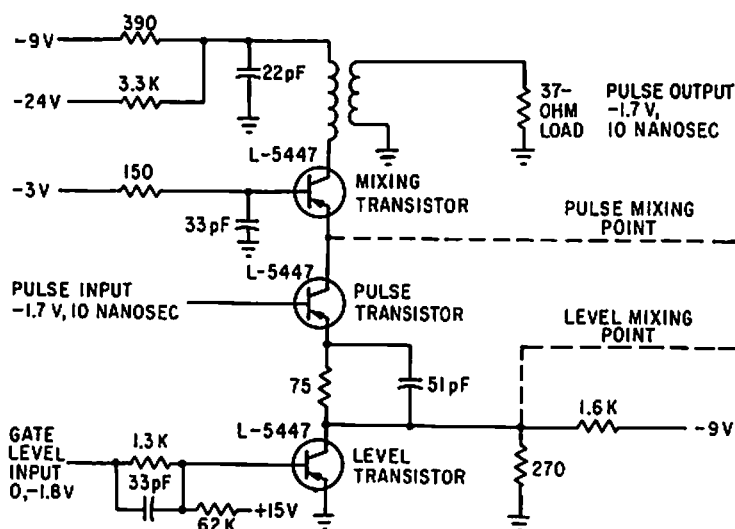
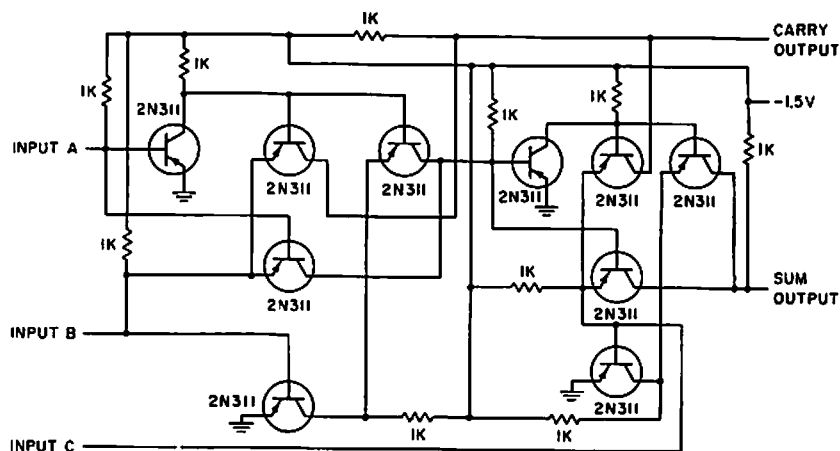


TIME-SHARED TROUBLESHOOTER SCOPE—Oscilloscope modifications shown permit computer to control cto display for diagnosing

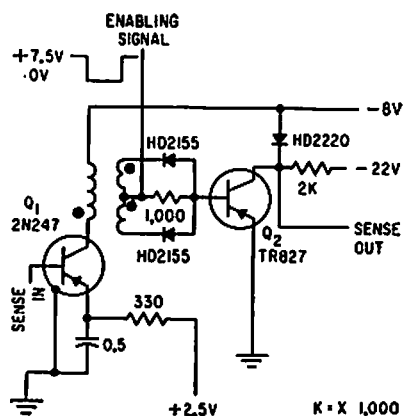
trouble in faulty section of time-sharing computer while users continue working with computer. —J. T. Quatso, Time-Shared Trouble-

shooter Repairs Computers On-Line, *Electronics*, 39:2, p 97-101.

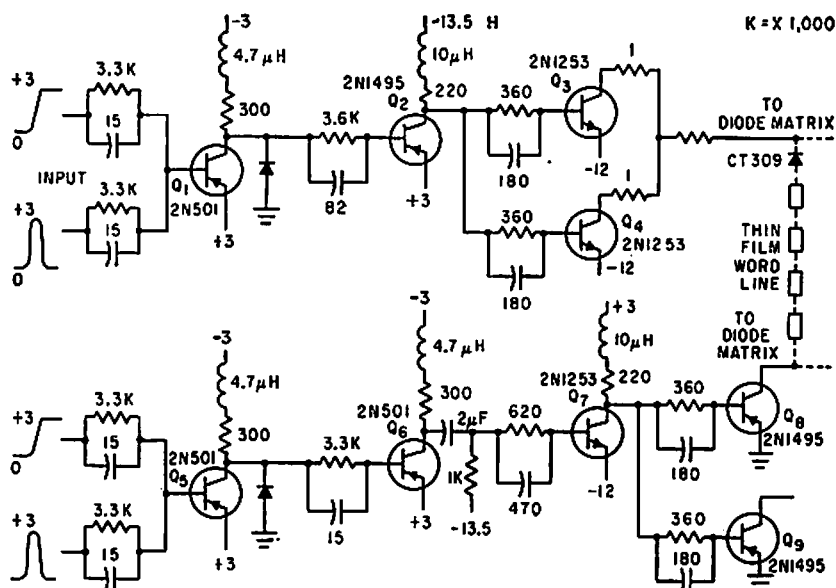
FULL ADDER—Made by joining two half-adders. Push-pull inverting amplifiers serve as switches to provide completely automatic operation. Carry from full adder can derive from either half-section but never both. No inhibitor signal is required to suppress unwanted sum signal.—F. B. Maynard, Half-Adders Drive Simultaneous Computer, *Electronics*, 31:29, p 80-82.



GATE AND MIXER FOR 10-NSEC PULSES—Used in 50-megapulse computer. Propagation delay time of circuit is only 4.5 nsec. Transistors may also be 2N769 or 2N976.—K. H. Kankle and J. E. Laynor, Key to Faster Computers: Ten-Nanosecond Amplifier, *Electronics*, 35:50, p 39-41.

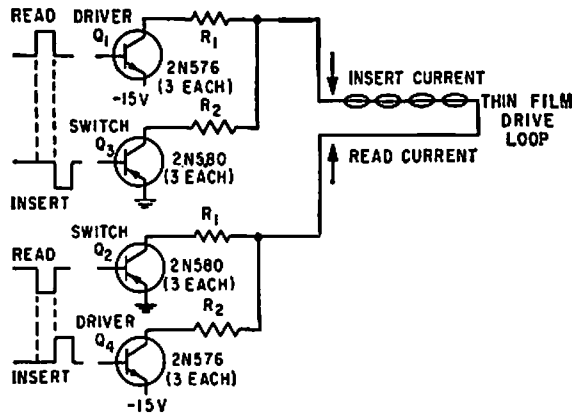


BUFFER MEMORY SENSE AMPLIFIER—Uses coincident-current technique. Full-wave rectifier is required at secondary of transformer because sense output can be of either polarity. Enabling signal turns on sense amplifier, to permit discrimination between memory core outputs during unload and load cycles.—D. Haagens, Compact Memories Have Flexible Capacities, *Electronics*, 32:40, p 50-53.

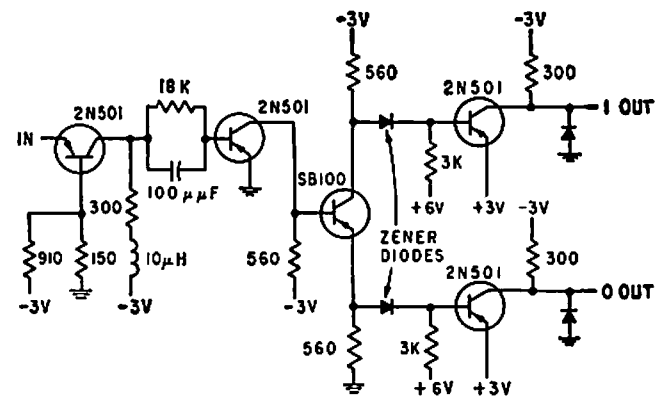


THIN-FILM MEMORY DRIVER—Generates 1-amp pulses with 35-nsec rise and fall times, at rates up to 1 Mc, for driving 2,560-bit memory plane using 2,000-angstrom nickel-

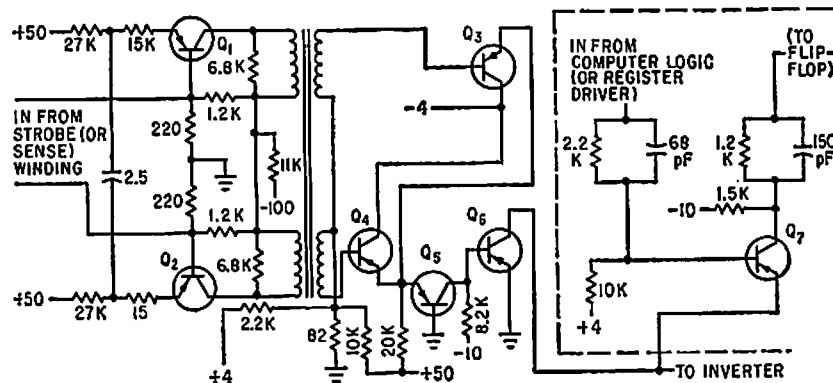
iron films.—E. E. Bittmann, Thin Magnetic Film Memories for High-Speed Computers, *Electronics*, 34:9, p 39-41.



THIN-FILM CURRENT DRIVER—Three 2N576 driver transistors in parallel, each rated 400 ma, deliver 1-amp pulses with 0.15 microsec rise time. Three 2N580 pnp transistors in parallel serve as current switches.—E. E. Bittmann, *Using Thin Films in High-Speed Memories*, *Electronics*, 32:23, p 55-57.

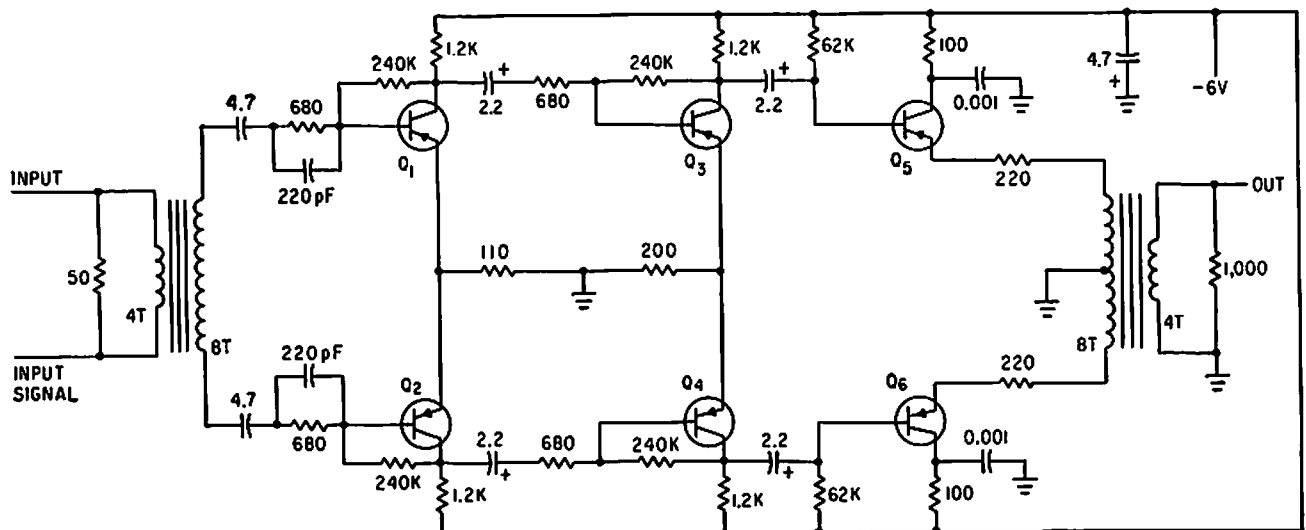


THIN-FILM SENSE AMPLIFIER—Common-base input stage matches low input impedance of sense winding. 5-mv input signal is boosted to 3-v level. Zener diodes shift d-c levels of output signal to desired 0 to +3 v level.—E. E. Bittmann, *Using Thin Films in High-Speed Memories*, *Electronics*, 32:23, p 55-57.



SENSE AMPLIFIER AND GATE—Uses drive-sampling core to generate precisely defined strobes for coincident-current memory.—A. H.

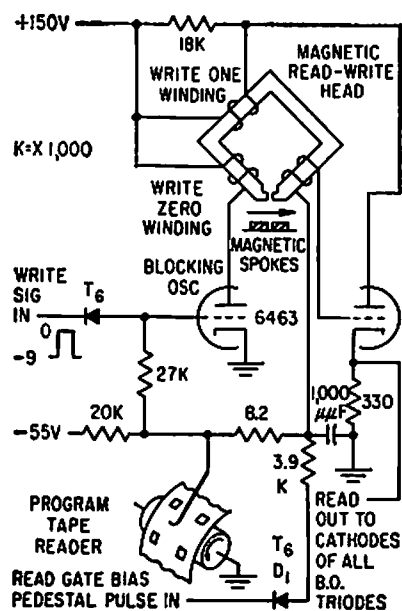
Ashley and E. U. Cohler, *Solving Noise Problems in Digital Computer Memories*, *Electronics*, 33:13, p 72-74.



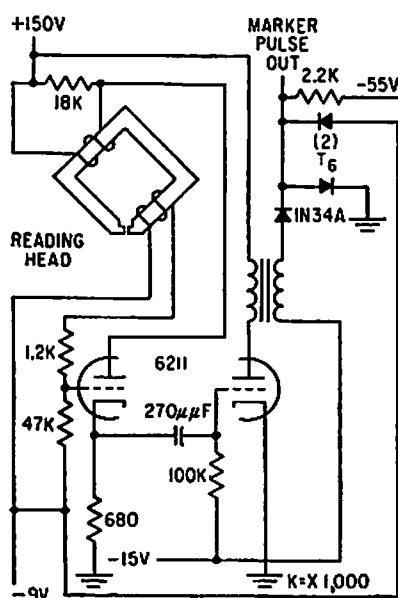
SENSE AMPLIFIER READS MEMORY DATA—Small signals stored in thin-film memory are amplified while rejecting noise from partially

selected bits on same sense line. Circuit is completely isolated differential amplifier operating from low-impedance source.—A. A.

Fleischer and E. Johnson, *New Digital Conversion Method Provides Nanosecond Resolution*, *Electronics*, 36:18, p 55-57.



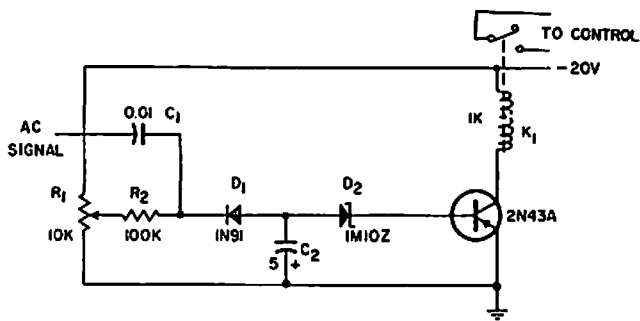
DISK READ-WRITE—Magnetic head is transformer of twin-triode blocking oscillator circuit used with aluminum disk having radial magnetic spokes that can store from 50 to 100 words.—T. C. Chen and O. B. Stram, Digital Memory System Keeps Circuits Simple, *Electronics*, 32:11, p 130-133.



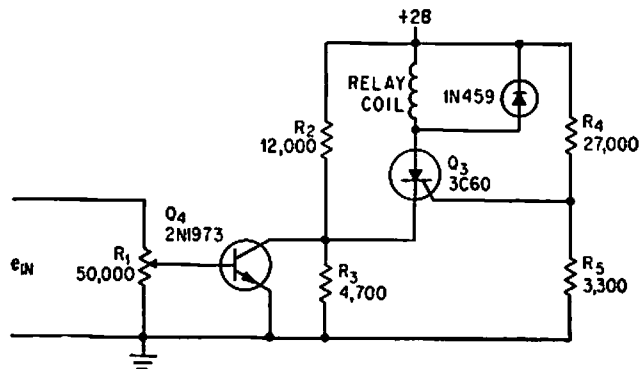
SPACECRAFT COMPUTER POWER SWITCH
 —Conserves battery power by automatically disconnecting sections of computer when are not in use. Q1 and Q2 serve as switches while Q3 generates automatic reset when power turns on. When control line at ground, no power is supplied on output line. Q4 delays power turnoff for short after control signal drops.—R. A. Clift, P

CHAPTER 18

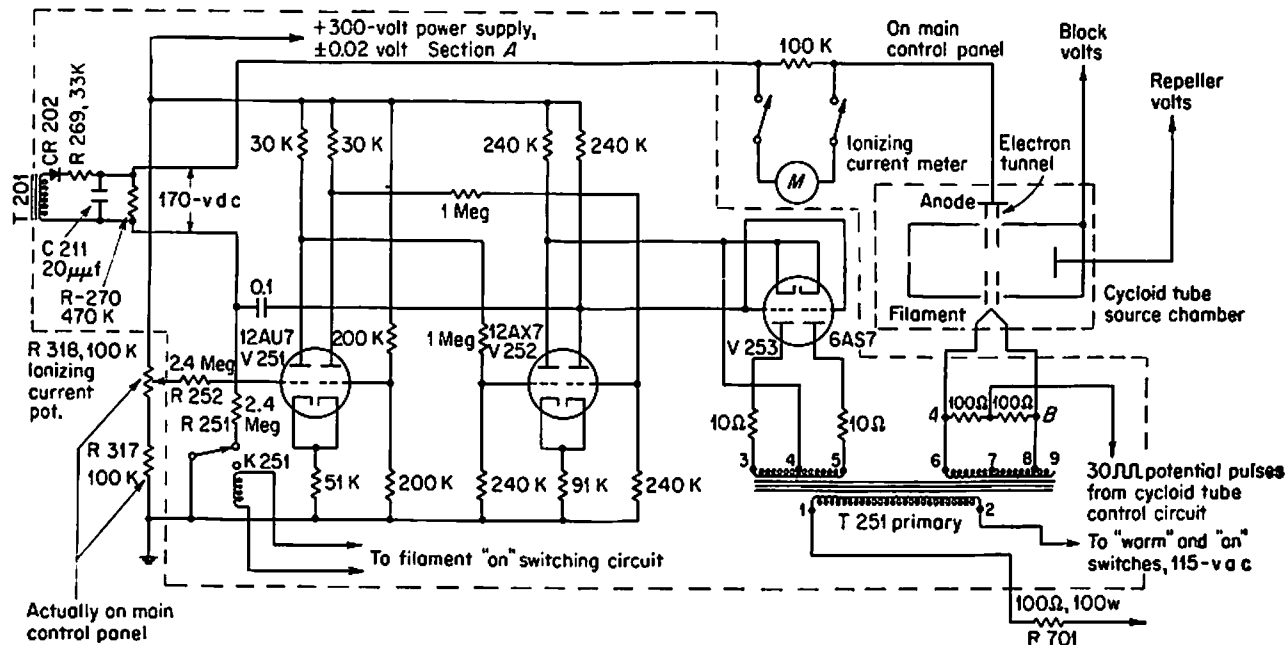
Control Circuits



SNAP-ACTION A-F POWER LEVEL SWITCH—When integrated voltage reaches point where zener diode breaks from nonconduction to conduction, transistor goes from cutoff to saturation suddenly, to provide fast relay operation.—Snap Action Level Switch, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., p 30.



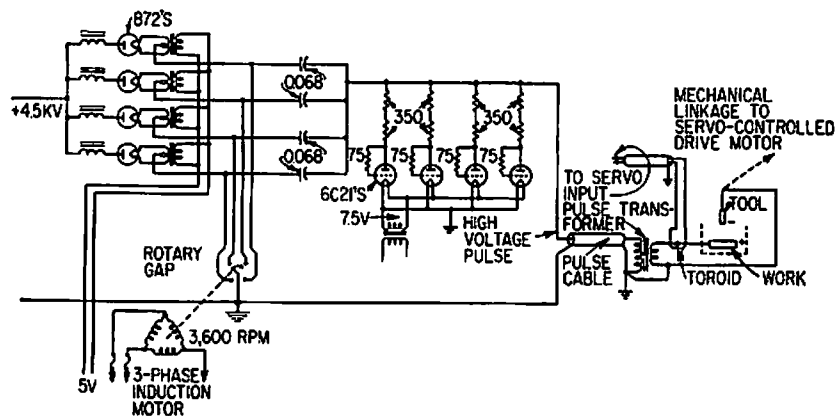
ACQUISITION RELAY DRIVER—Scr Q3 and transistor Q4 provide stable triggering point for acquisition signal used in aligning missile guidance systems, without hysteresis effect.—W. S. Zukowsky, *Aligning Saturn Missile's Guidance System, Electronics*, 37:8, p 26-27.



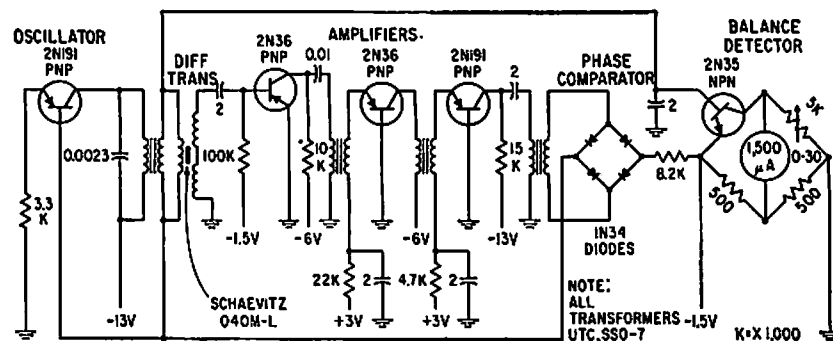
FILAMENT EMISSION REGULATOR—Used in Consolidated Electrodynamics gas analyzer to control ionizing current strength by regulating filament temperature of cycloid tube.

Potentiometer used for control is in grid circuit of V251. Circuit maintains ionizing current automatically at desired level.—G. C. Carroll, "Industrial Instrument Servicing

Handbook," McGraw-Hill, N.Y., 1960, p 8-122.

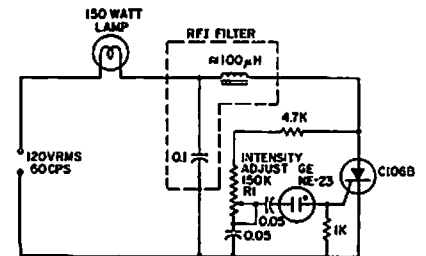


SPARK MACHINING CONTROL—Servo-controlled high-power electric spark machine produces repeated discharges between tool electrode and workpiece to cause erosion of metal to desired shape. Power source is 4.5-kv three-phase rectifier providing peak discharge current of 4,500 amp at pulse repetition rate of 2,880 pps. Rotary gap is used for pulse switching.—E. M. Williams and C. P. Porterfield, Spark Machine Tool has Servo Control, *Electronics*, 31:43, p 90-92.

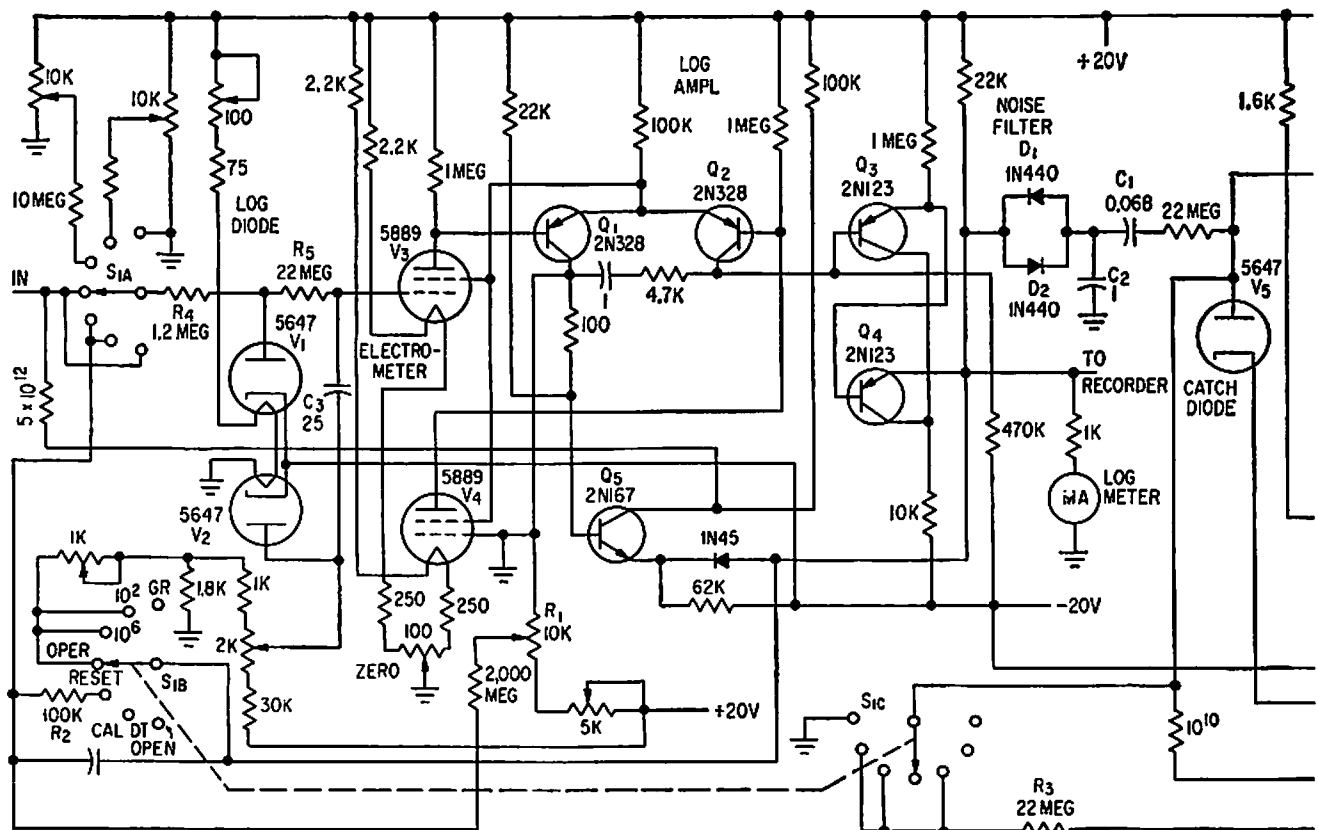


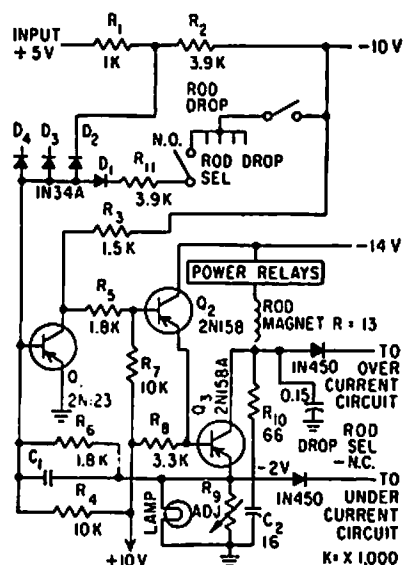
LIQUID-DENSITY GAGE—Peltier-effect semiconductor thermoelements maintain uniform cooling temperatures required for accurate specific gravity measurements. Null-position detecting circuit uses differential transformer

to sense position of movable core at end of float.—C. W. Hargens, Semiconductors Cool and Control Density Gage, *Electronics*, 31:49, p 80-81.

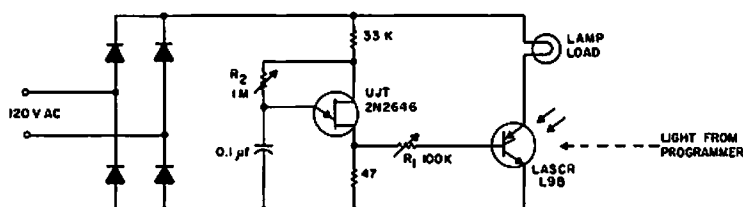


SCR LAMP BRIGHTNESS CONTROL—Will control up to 1.6 amp rms while operating directly from power line, yet is sufficiently compact to fit into base of common household lamp socket.—Low-Cost, Low-Power SCR's to Invade Commercial Market, *EEE*, 13:8, p 21-22.



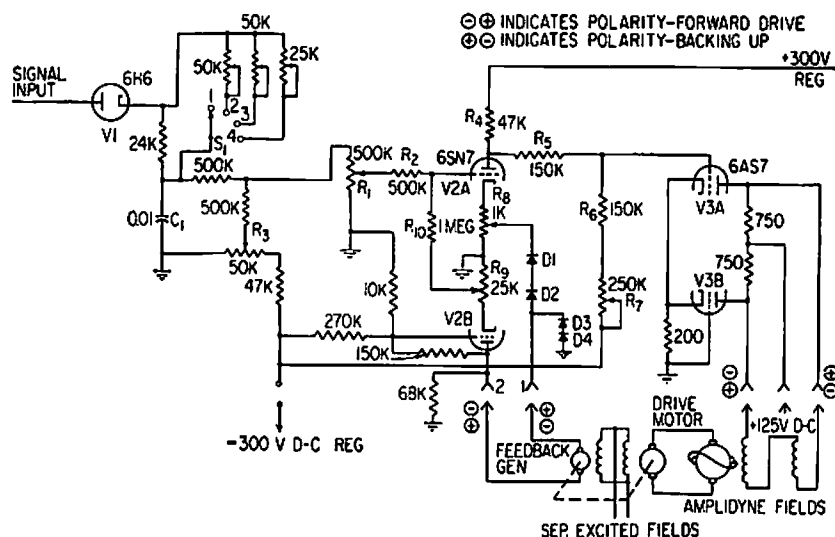


NUCLEAR REACTOR REGULATOR CONTROL—Feedback-type regulator holds magnet currents constant for control rods, at values set by R9 to 0.6 amp. When selected rods must be tripped for certain tests, regulator buses for these rods are connected to -10 v, to drop the rods. Amplifier uses $+10$ v supply as reference to hold output at -2 v.—E. J. Wade and D. S. Davidson, How Transistor Circuits Protect Atomic Reactors, *Electronics*, 31:29, p 73-75.

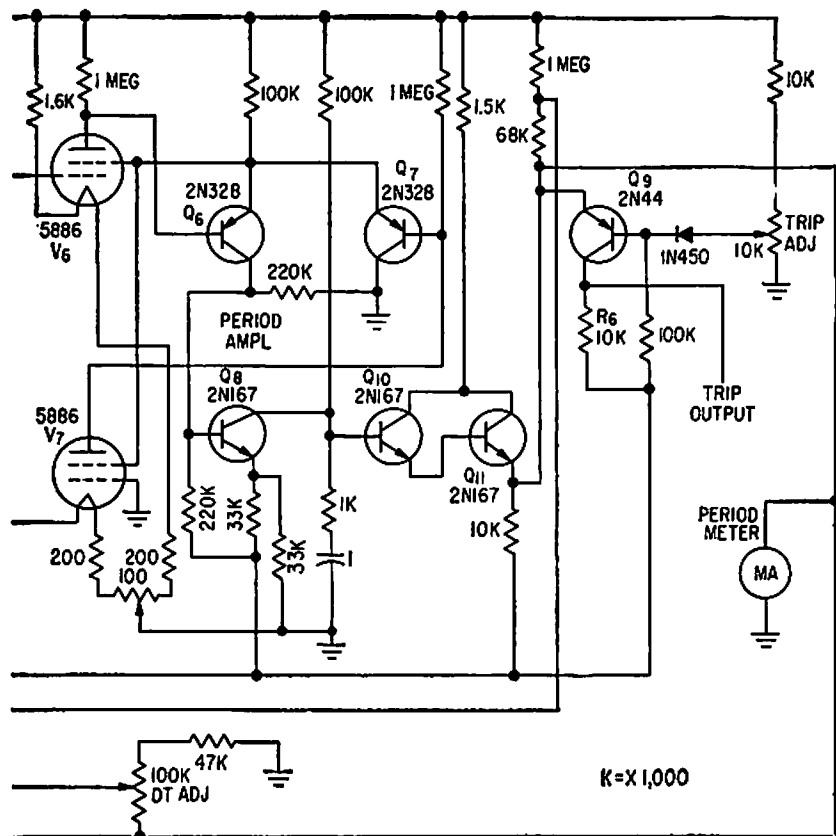


PREHEATING PROGRAMMED LAMPS—Minimizes thermal stresses on lamps and controls when programmed operation is repetitive for large number of cycles. Ujit control circuit provides preheating of lamp by triggering light-activated scr late in each half-

cycle. Setting of R2 determines minimum lamp current to maintain filament temperature just below visible level.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 216.

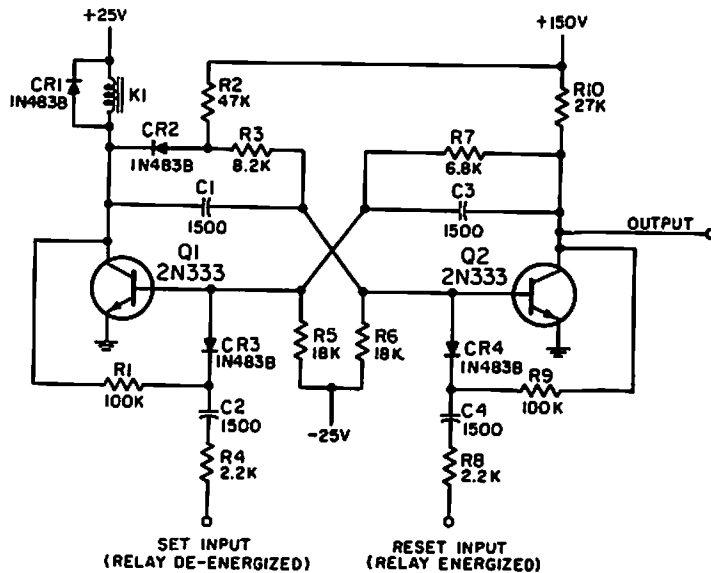
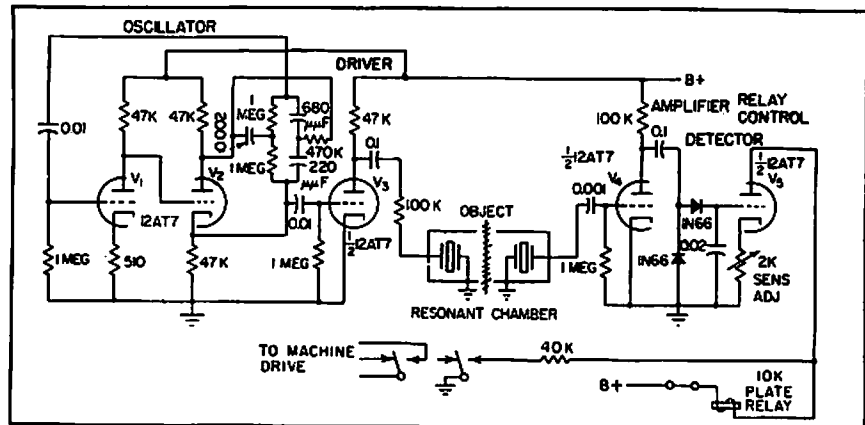


SPARK MACHINING SERVO DRIVE—Positioning information is derived from gap voltage, fed to integrating circuit through diode V1, and resulting output fed through V2 to cathode-coupled push-pull d-c amplifier that excites field of amplydne generator to move tool electrode toward or away from workpiece, as required to permit sparkover at voltage selected for machining conditions desired. Used chiefly for work on high-temperature alloys and otherwise unmachinable materials.—E. M. Williams and C. P. Porterfield, Spark Machine Tool has Servo Control, *Electronics*, 31:43, p 90-92.

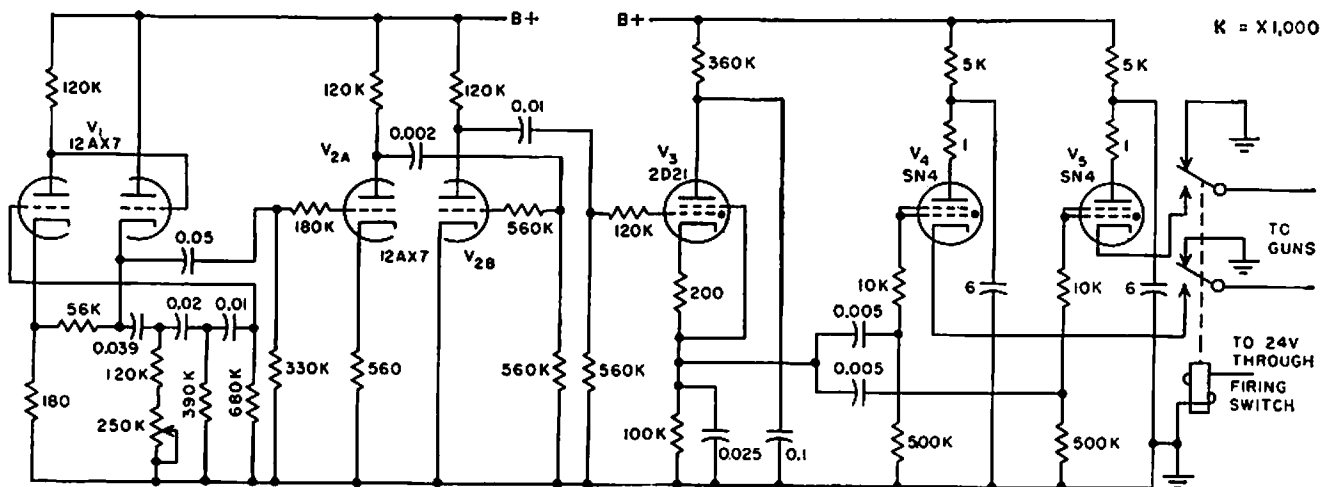


NUCLEAR REACTOR STARTUP CONTROL—Logarithmic and period amplifiers provide required wide indicating range without switching. Use of log diode V1 in series back to back with V2 provides nonlinear element in which effects of changes in cathode temperature and supply voltage are balanced out. V1 drives log amplifier consisting of balanced electrometer tubes V3 and V4, differential stage Q1-Q2, and cascaded emitter-follower Q3 and Q4. Period amplifier is a feedback-type differentiating circuit.—E. J. Wade and D. S. Davidson, *Transistor Amplifiers for Reactor Controls, Electronics*, 32:21, p 52-53.

FILM-BREAK DETECTOR—Photographic film in processor is run through resonant acoustic chamber in which presence of film affects energy transfer between crystal transducers. Change in transducer output when film breaks is used to operate relay through amplifier and detector, to control automatic film processor and thereby minimize rethreading and film spoilage. Can also be used to detect bubbles in rubber tubing during blood transfusions, and detect similar changes in other films, liquids, and gases.—E. L. Withey and R. G. Seed, *Acoustic Cavity Detects Breaks in Film*, *Electronics*, 31:13, p 50-51.



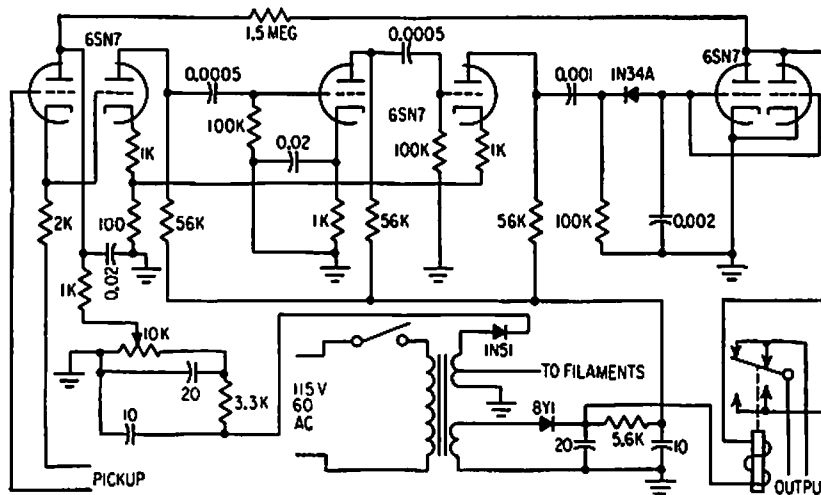
SATURATING-MVBR ON-OFF CONTROL—Used with relays and other electromechanical devices where current ratio must be 10 between on and off conditions. Operation is same as Eccles-Jordan, except that current drive for holding Q2 on is furnished through R2 and R3 rather than through Q1 collector load. Designed for relay that is energized by 8 ma and drops out at 0.5 ma. Trigger input should be between 14 and 20 v, with rise time of 10 microsec. Input circuit needs 5 millisecc to recover from positive signal before next trigger is applied. 2N333 has been dropped from Preferred List, but 2N335 can be used if operating point is adjusted for its higher beta.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 17 (originally PC 251), p 17-2.



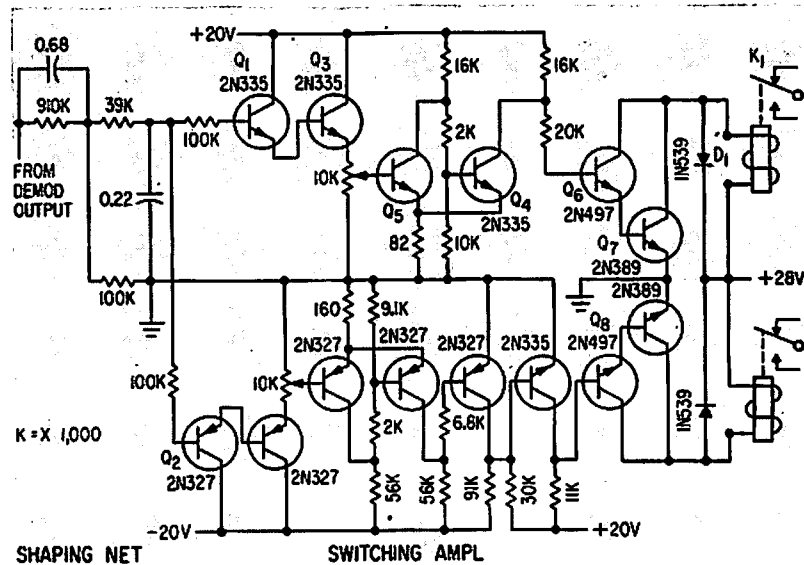
MACHINE-GUN CONTROL OSCILLATOR—Phase-shift oscillator firing circuit for airborne 20-mm guns permits operation anywhere in

range of 600 to 900 rounds per minute. Accuracy is improved by adjusting firing rate away from natural gun-mount vibration fre-

quency.—M. Hallio, *Firing Circuits Trigger Airborne Machine Guns*, *Electronics*, 31:31, p 86-89.

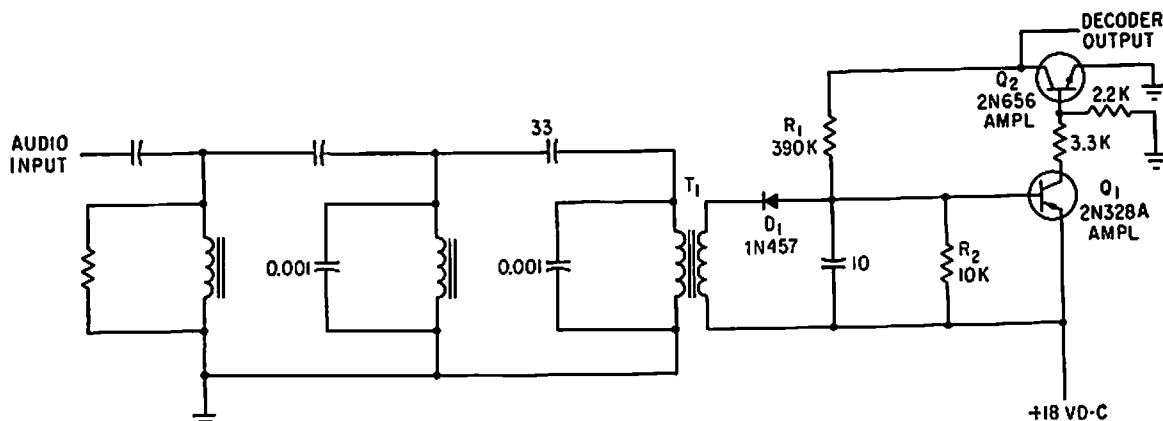


PROXIMITY CONTROL—Gain of Hartley oscillator is set so oscillation is maintained only when Q of resonant circuit is normal. When ferrous or nonferrous materials come near pickup coil, Q is reduced, oscillation stops, and output tube conducts, to pull in relay for counting or for controlling industrial machinery. Can be set for operating range of from 1/8th inch to 1 foot.—D. Elam, Proximity Transducer Uses Rapid Relay, *Electronics*, 31:25, p 73.



ROCKET ROLL CONTROL—Signal from roll-channel demodulator is shaped for two-section switching amplifier that energizes roll

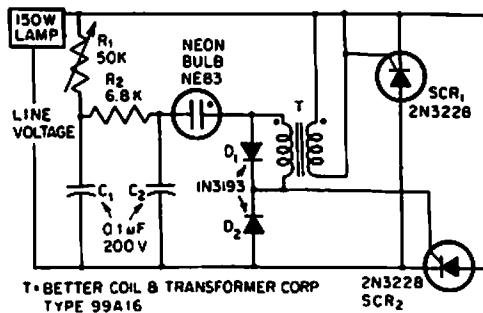
jet relays.—R. E. King and H. Low, Solid-State Guidance For Able-Series Rockets, *Electronics*, 33:5, p 60-63.



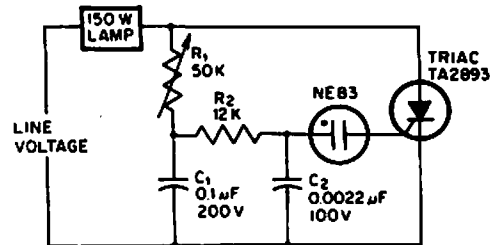
20-CHANNEL DECODER FOR AUDIO TONES—L-C filter in each decoder is tuned to channel tone, with 35-db adjacent-channel rejection.

Rectified output of detector D1 drives two-stage d-c amplifier having relay load. Used in Mercury spacecraft command receiver.—R.

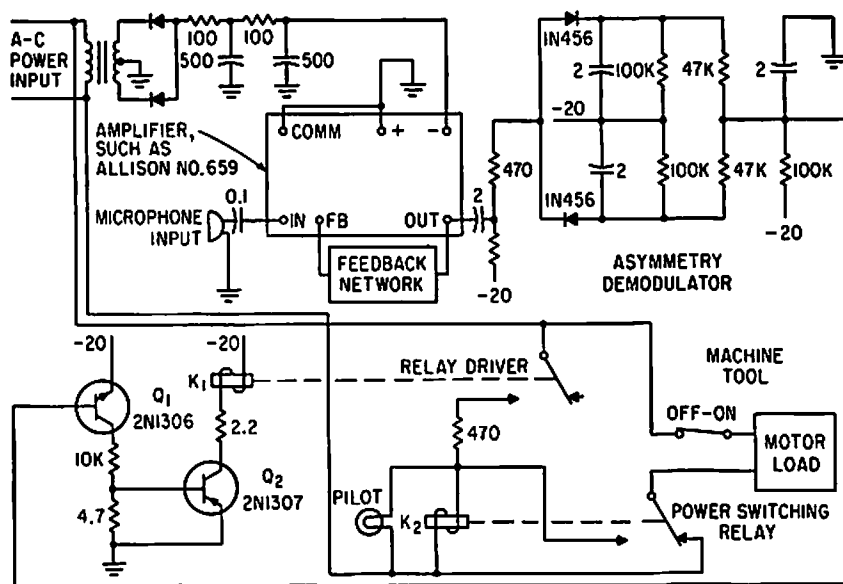
Elliott, First Details on Mercury Spacecraft Command Receiver, *Electronics*, 36:5, p 32-35.



SCR LAMP DIMMER—Can easily be built into lamp socket or fixture.—J. Eimbinder, SCRs In The Consumer Market, *EEE*, 14:8, p 100-103.

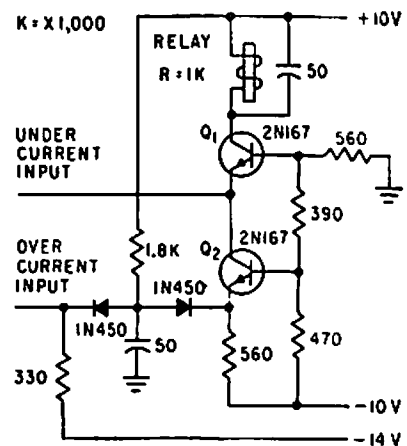


TRIAC LAMP DIMMER—Can easily be built into lamp socket or fixture. Uses minimum number of components.—J. Eimbinder, SCRs In the Consumer Market, *EEE*, 14:8, p 100-103.

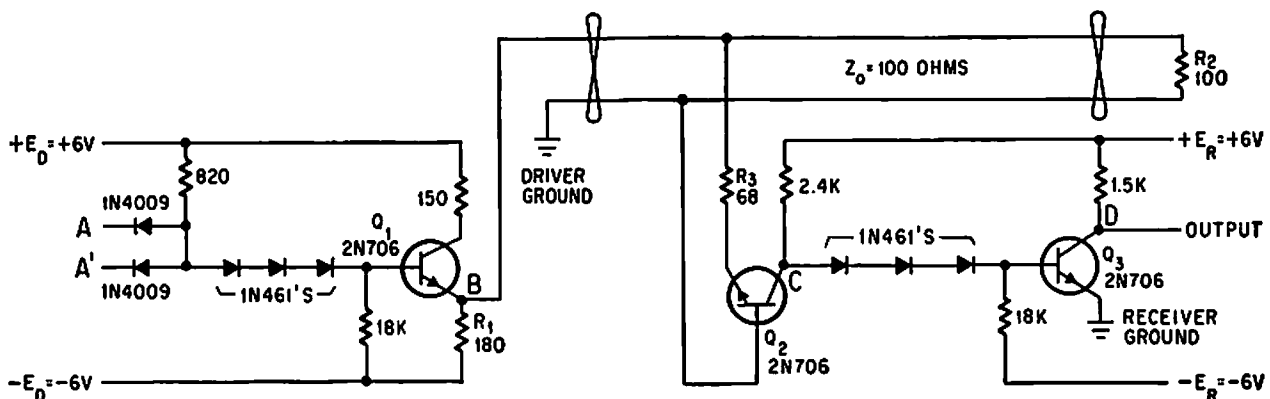


VOICE-OPERATED MACHINE CONTROL—Circuit rejects ambient noise or normal speech but responds to sharply spoken commands during emergency, to open motor circuit of machine tool. Asymmetry demodulator rejects sym-

metrical noise while accepting speech vowels having strong asymmetry.—W. C. Dersch, Speech Operates Safety Switch, *Electronics*, 36:25, p 78-82.



UNDERCURRENT-OVERCURRENT PROTECTION—Guards against improper operation of control amplifier in nuclear reactor scram system. If rod currents vary beyond predetermined limits, circuit initiates reactor scram. Either transistor may open relay coil circuit.—E. J. Wade and D. S. Davidson, How Transistor Circuits Protect Atomic Reactors, *Electronics*, 31:29, p 73-75.

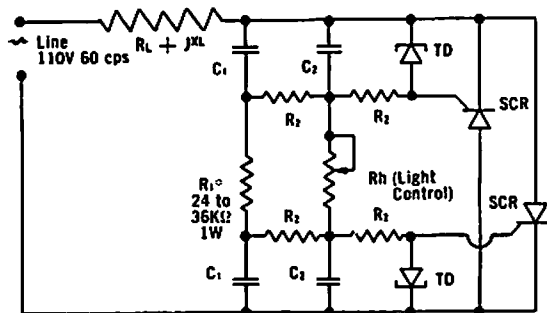


GROUND-LINE CABLE DRIVER—Twisted 100-ohm d-c coupled transmission line solves ground-differential problem between driving and receiving subsystems up to 120 feet

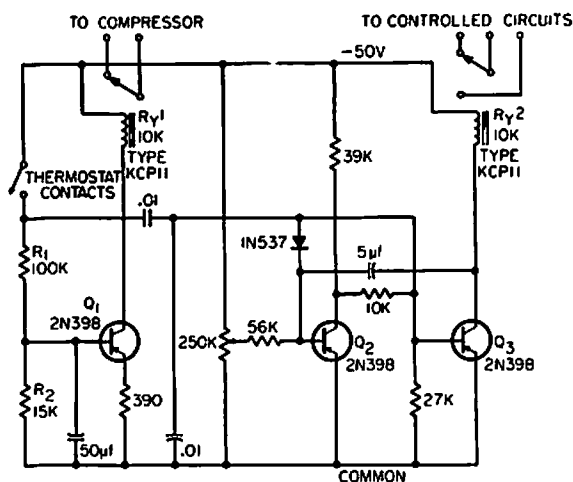
apart, while eliminating costly transformer coupling. Holding line excursion to 1.5 v minimizes capacitive crosstalk. Up to four receivers can be used on one line.—R. C.

Geravalia, Transmission Lines Couple Multiple-Drive Receivers, *Electronics*, 39:16, p 121-122.

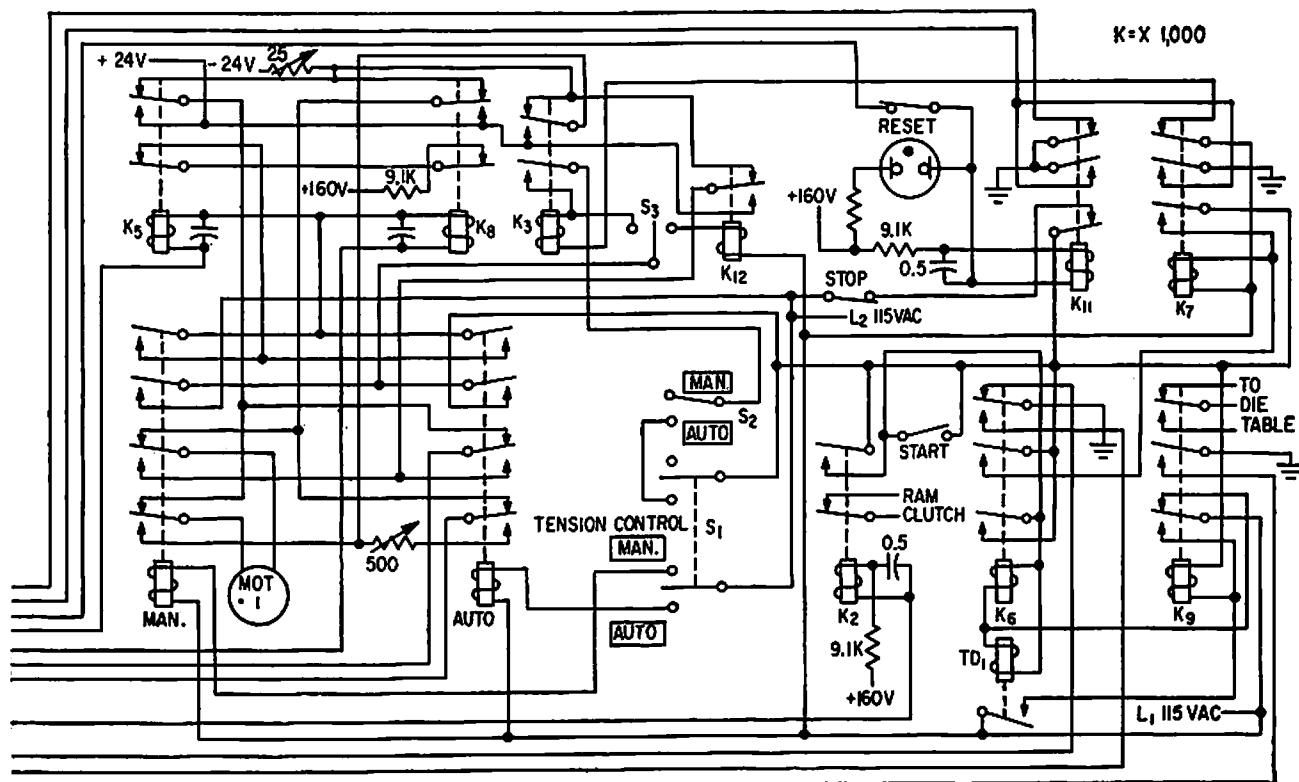
MOTOR TRANSIENT ANTICIPATOR—Disconnects battery supply of sensitive counters for preset interval during switching period of nearby air conditioner, to avoid extraneous counts by switching transients from compressor motor and control relays.—C. H. Harris, *Motor Transient Anticipator*, *EEE*, 13:5, p 45-46.



R₁ = 24 to 36KΩ — Resistor adjusted for the Lowest Desired Level
C₁ = .47μF, 30V, metallized paper ±20%
C₂ = .47μF, 12V, metallized paper ±20%
R₂ = 10KΩ, ¼W, ±10% SCR = K-1040, Hoffman
TD = T-1077, Hoffman Rh = 250KΩ linear rheostat, ½ watt

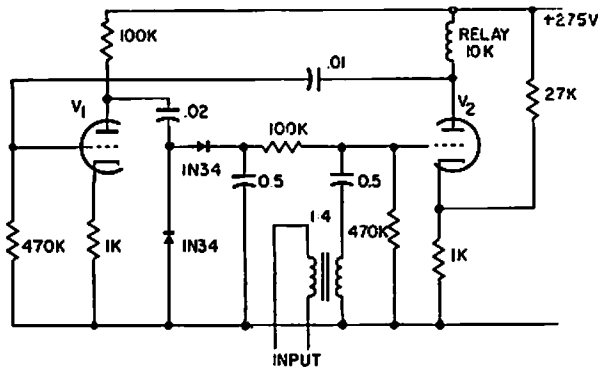


FULL-WAVE TD-SCR CONTROL—Use of tunnel diode between gate and cathode of each scr improves control performance of scr, to give triggering range of 10° to 175° . Input sensor Rh may be photocell or any other resistive transducer.—TD/SCR Combos for Sale, EEE, 12:3, p 62-64.



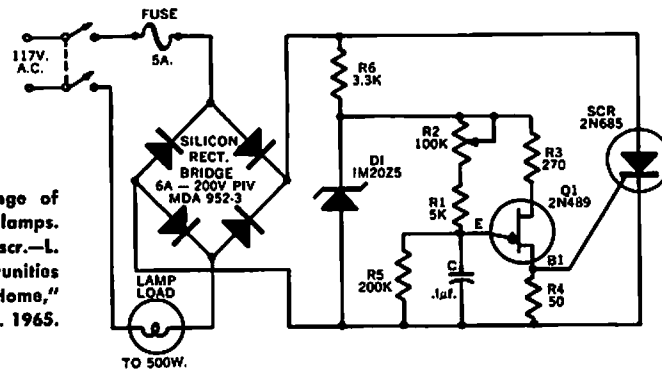
system lowers tension as dies are applied to metal. At end of cycle, operator opens stop switch, resetting relays that are energized by power line.—G. J. Crowdes, Automatic Con-

trols for Metal Working Machines, Electronics,
32:10, p 41-43.



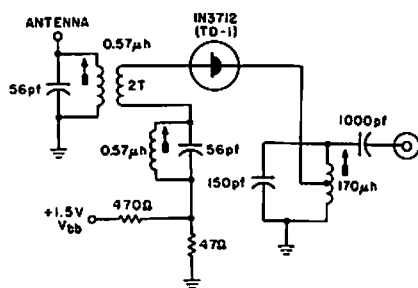
SENSITIVE RELAY CONTROL—Reflex circuit ensures full use of available gain of two-stage relay control amplifier using 12AT7 twin triodes. Tube V2 controls relay in its plate circuit and also serves as a-c amplifier, increasing over-all sensitivity by factor approximately equal to a-c gain. Performs best about 400 cps.—Sensitive Relay Control Amplifier, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., p 103.

SCR LIGHT DIMMER—Provides full range of brightness control for up to 500 w of lamps. Ujt Q1 provides turn-on pulses for scr.—L. Stern, "Thyristors Provide New Opportunities for Electronic Applications in the Home," Motorola Application Note AN-141, Dec. 1965.

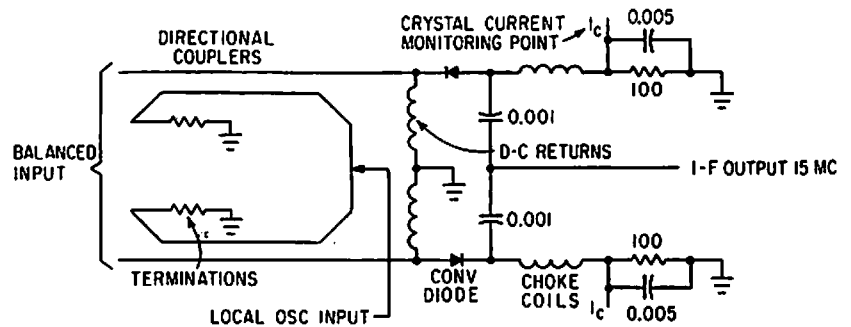


CHAPTER 19

Converter Circuits

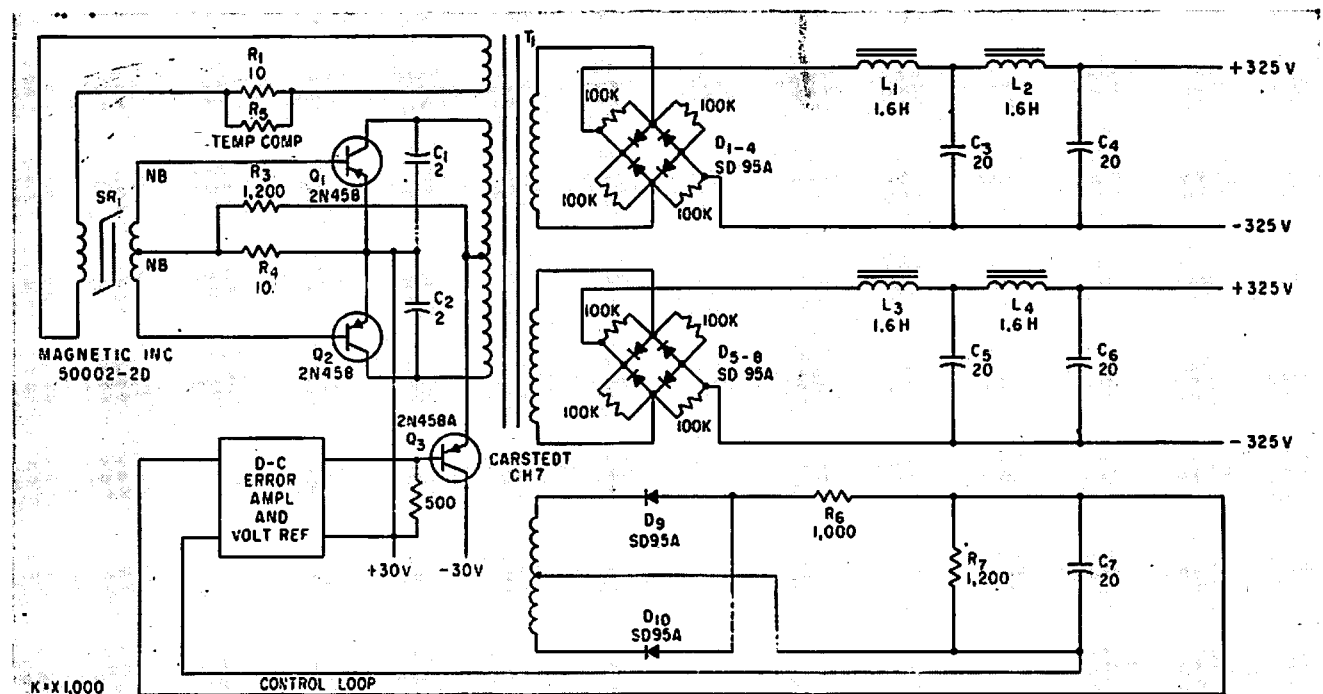


CB CONVERTER—Uses tunnel-diode oscillator.
—“Transistor Manual,” Seventh Edition, General Electric Co., 1964, p 358.



BALANCED-INPUT MIXER—Used with frequency-independent antennas to provide noise cancellation as balanced-input convert-

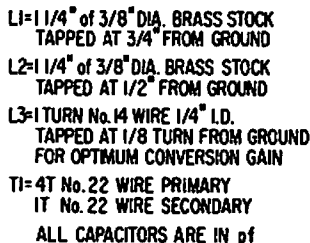
er.—C. Strother, Jr. and C. R. Lundquist, Bal-inverter—Frequency-Insensitive Balanced Converter, *Electronics*, 35:44, p 46–47.



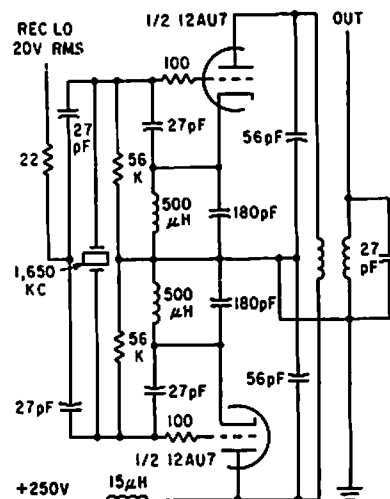
30 TO 325 V D-C—Use of transistors with high alpha cutoff frequency, along with loading networks across output bridge rectifiers,

minimizes switching spikes in output. Control-loop amplifier provides overall regulation.
—C. J. Biggerstaff, Reducing Spikes in D-C to

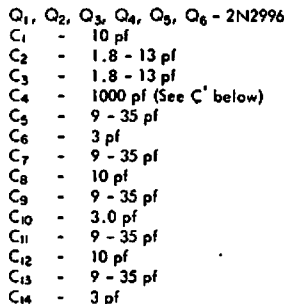
D-C Converter Outputs, *Electronics*, 34:42, p 64–65.



Conversion gain of mixer is 12 db. Total antenna to i-f conversion gain is 32 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 307.



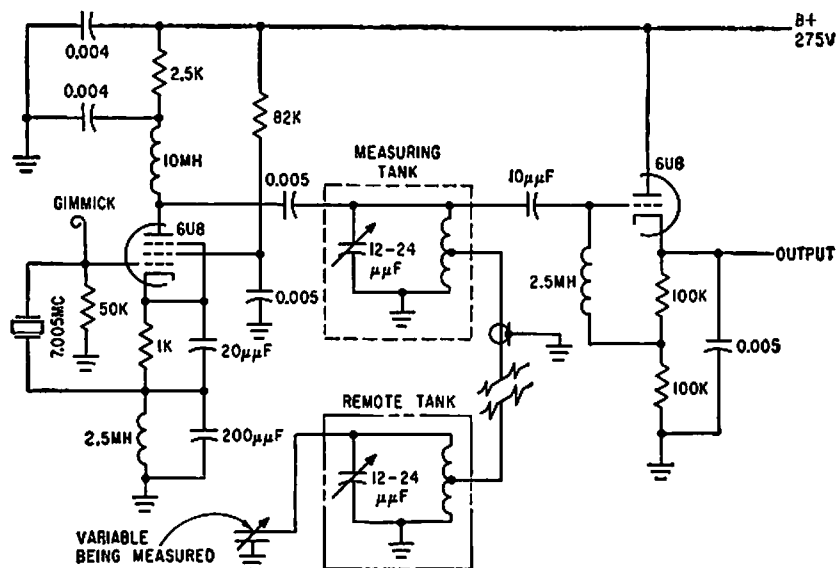
CB TRANSMITTER MIXER—Requires only one crystal per channel in 27-Mc citizens band radiotelephones, by combining beating oscillator with balanced mixer. Receiver local oscillator signal and even harmonics of beating oscillator frequency are cancelled in tuned symmetrical plate circuit.—M. E. Baird, *Mixer Circuit Lowers Radiotelephone Costs*, *Electronics*, 34:47, p 71.



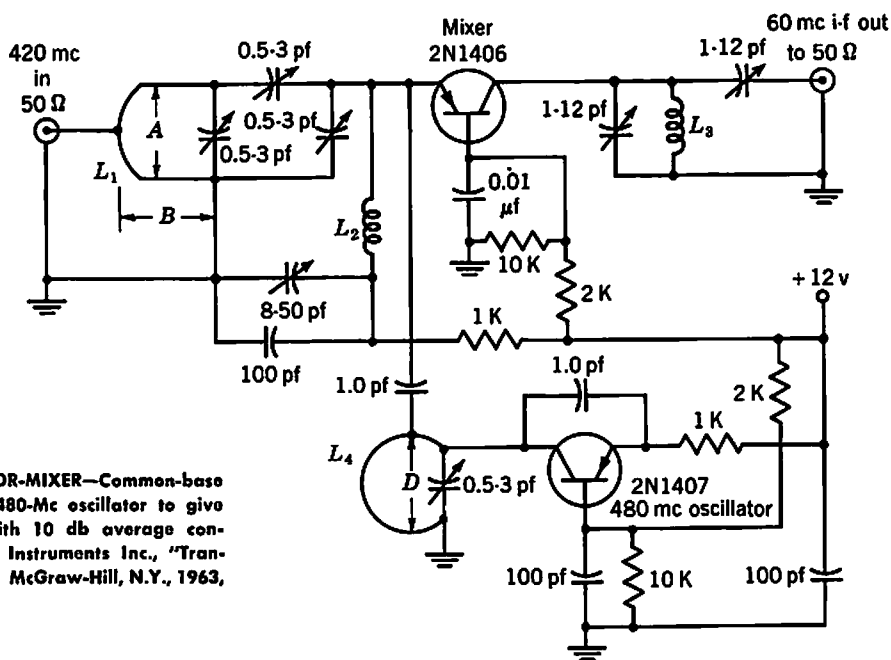
C ₁₅	-	9 - 35 pf
C ₁₆	-	10 pf
C ₁₇	-	2.5 pf
C ₁₈	-	0.9 - 7 pf
C ₁₉	-	0.9 - 7 pf
C ₂₀	-	220 pf
C ₂₁	-	110 pf
C ₂₂	-	1.8 - 13 pf
C	-	1000 pf
R ₁	-	1.5 k
R ₂	-	3.0 k
R ₃	-	3.0 k
R ₄	-	51 Ω
R ₅	-	1.5 k
R ₆	-	3.0 k

R ₇	-	3.0 k
R ₈	-	3.0 k
R ₉	-	3.0 k
R ₁₀	-	1.5 k
R ₁₁	-	3.0 k
R ₁₂	-	3.0 k
R ₁₃	-	1.5 k
R ₁₄	-	1.5 k
R ₁₅	-	3.0 k
R ₁₆	-	3.0 k
R ₁₇	-	1.5 k
R ₁₈	-	3.0 k
R ₁₉	-	3.0 k
R ₂₀	-	510 Ω

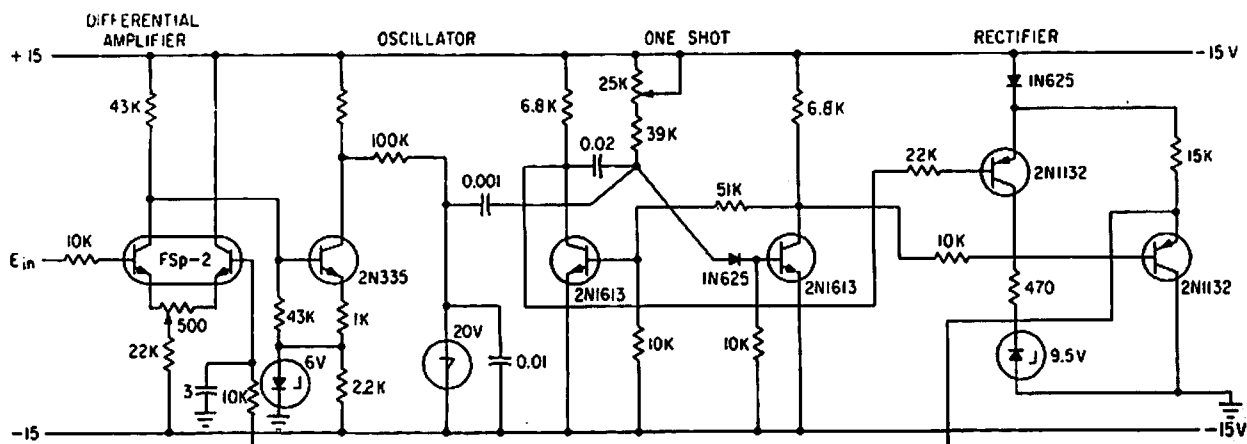
of only 5.9 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 305.

**TRANSDUCER-DRIVEN CRYSTAL OSCILLATOR**

—Sensitive 70-Mc one-tube oscillator feeds local and remote tank circuits to which capacitive or inductive transducers may be connected, for conversion of displacement, temperature, pressure, and other variables to corresponding changes in d-c output voltage. Will give up to 250 v change per micromicrofarad of transducer capacitance change.—L. J. Rogers, *Sensitive Transducers Use One-Tube Crystal Oscillator*, *Electronics*, 32:40, p 48-49.



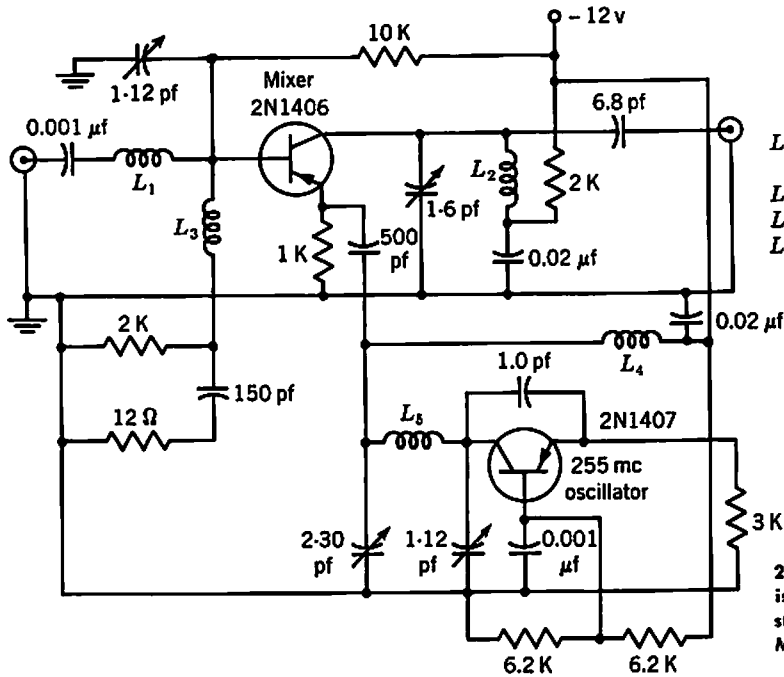
420-MC OSCILLATOR-MIXER—Common-base mixer is used with 480-Mc oscillator to give 60-Mc i-f output, with 10 db average conversion gain.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 327.



VOLTAGE-CONTROLLED OSCILLATOR—Simple circuit, using feedback to maintain accuracy, converts 0 to 3 v d-c linearly to 0 to 400 cps.

Uses differential amplifier that amplifies difference between input and feedback signals and feeds frequency-determining output to

Shockley four-layer diode oscillator.—J. D. Long, *Feedback Linearizes Voltage-To-Frequency Converter*, *Electronics*, 34:35, p 48.



L_1, L_2 4 turns #18 tinned buss on $\frac{3}{8}$ " dia Teflon[®] rod
Length $\frac{9}{16}$ "

L_3 25 turns #36 copper enamel on CTC LS 9

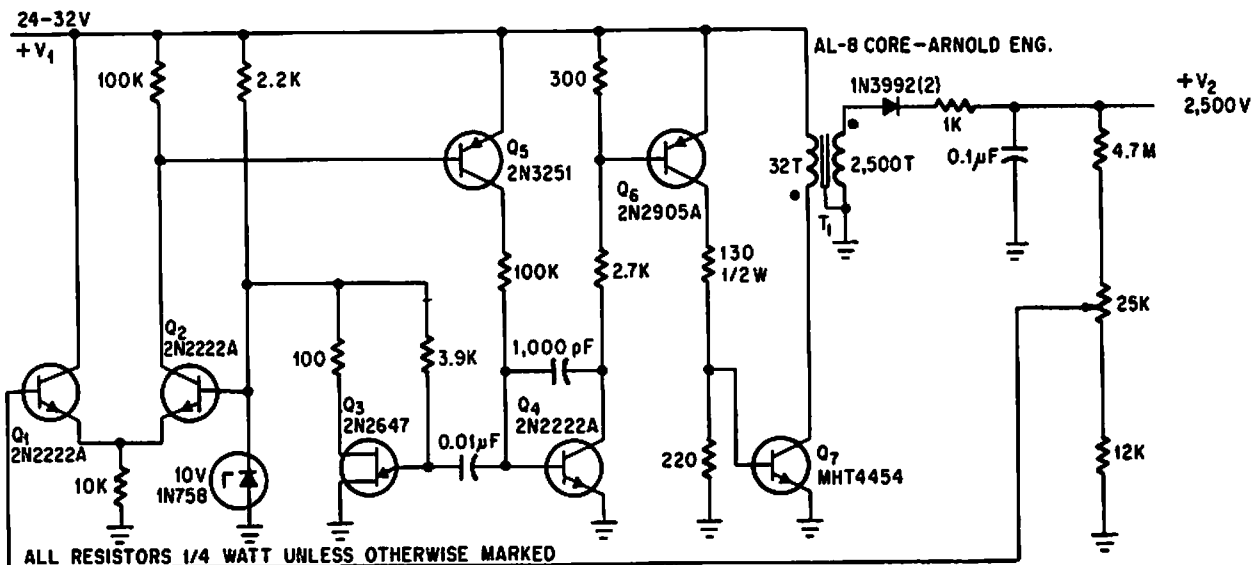
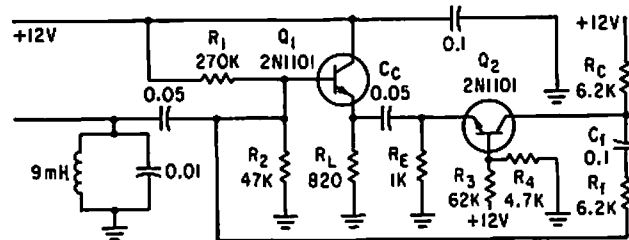
L_4 0.68 μ h RFC

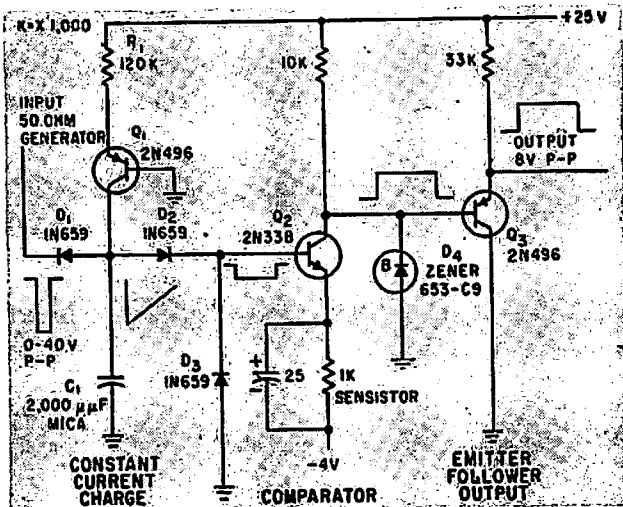
L_5 1 turn #18 tinned buss on $\frac{3}{8}$ " dia Teflon[®] rod
Length $\frac{1}{2}$ "

L_3 25 turns #36 copper enamel on CTC LS 9

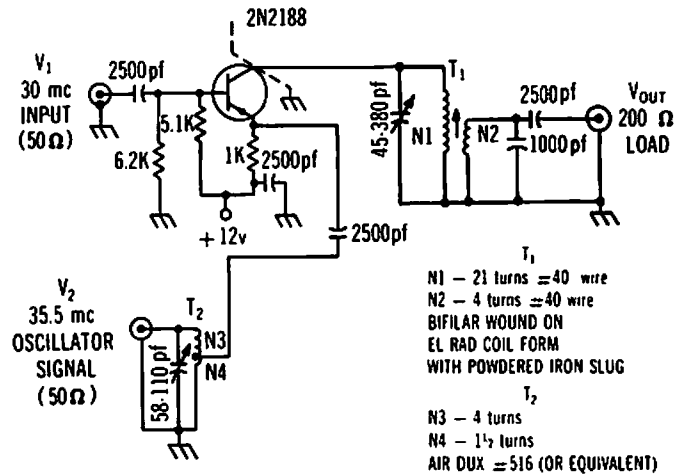
L_4 0.68 μ h RFC

L_5 1 turn #18 tinned buss on $\frac{3}{8}$ " dia Teflon® rod
Length $1\frac{1}{2}$ "



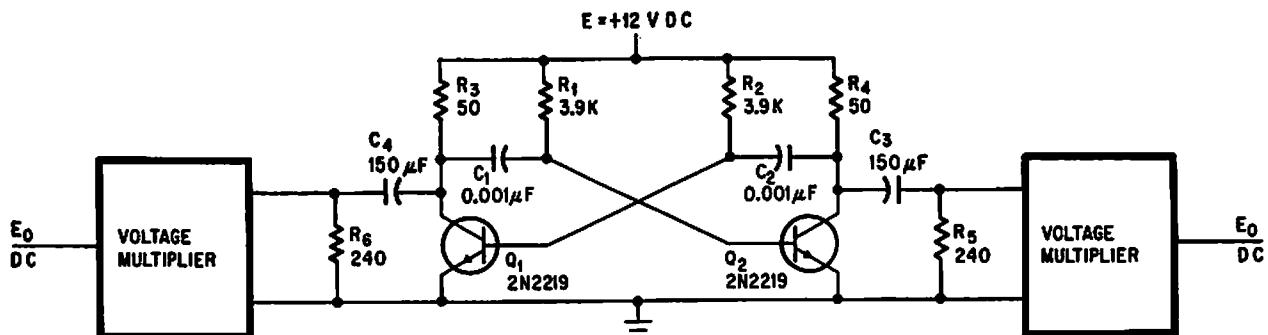
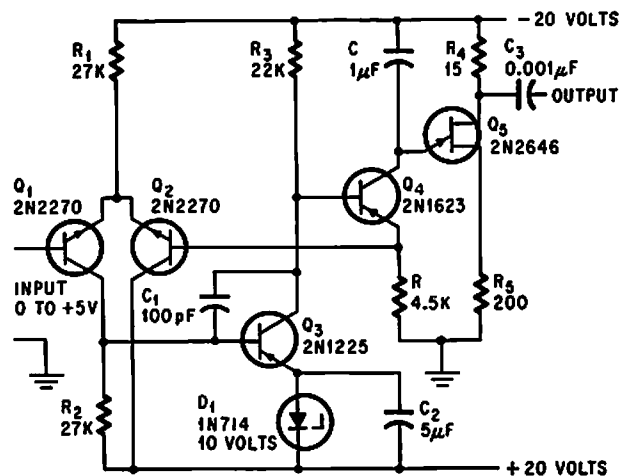


PULSE-HEIGHT-TO-TIME CONVERTER—Output width is proportional to input height. Last stage of constant-current charge circuit can be eliminated if output impedance can be high.—D. N. Carson and S. K. Dhawan, Data Conversion Circuits for Earth Satellite Telemetry, *Electronics*, 33:3, p 82-84.



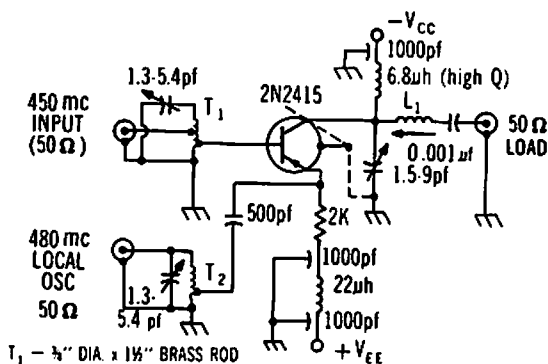
30 MC TO 5.5 MC—Single transistor serves as mixer to give 5.5-Mc i-f signal from 30-Mc signal input and 35.5-Mc oscillator input. Output is 100 mv for 10-mv signal input, with 630-mv oscillator signal.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 300.

VOLTAGE TO FREQUENCY CONVERTER—Q4 supplies charging current for C in relaxation oscillator Q5. Differential amplifier Q1-Q2 compares charging current with input voltage. Difference signal, amplified by Q3, controls charging current through Q4 and thus makes output frequency proportional to input voltage. For 0 to +5 v input, output is 0 to 100 cps with 0.1% linearity.—W. H. Voelker, Transistor Circuit Converts Voltage to Regulated Frequency, *Electronics*, 37:29, p 73-74.



D-C TO D-C—Free-running multivibrator (250 kc) generates unidirectional square waves having amplitude of d-c input voltage. R-C

circuit at output blocks average value of unidirectional pulso, and standard diode-capacitor voltage multiplier boosts output voltage to desired new value.—A. J. Durocher, D-c Voltage Converter Needs No Transformer, *Electronics*, 37:28, p 64-65.

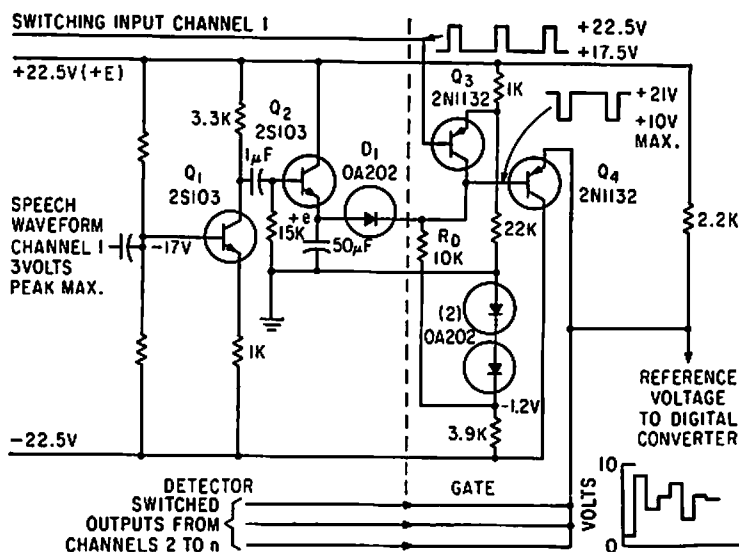


T_1 - $\frac{3}{8}$ " DIA. x $1\frac{1}{8}$ " BRASS ROD
TAPPED AT 0.75" and 0.375"
FROM GROUND

T_2 - $\frac{3}{8}$ " DIA. x $1\frac{1}{8}$ " BRASS ROD
TAPPED 0.375" FROM BOTTOM

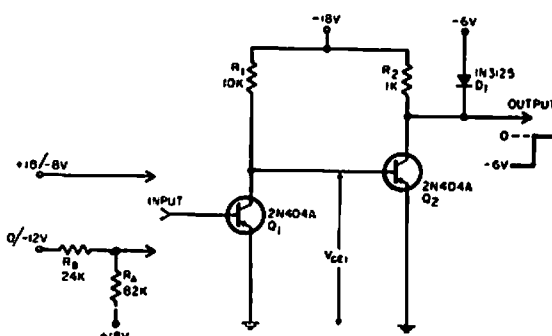
L_1 = 18 TURNS #30 WIRE ON CTL COIL
FORM #PL562C4L/20063 0 CLOSE
WOUND FROM BOTTOM. SLUG GROUND

450 MC TO 30 MC—With local oscillator feeding 1 mw, conversion gain is 15 db and noise figure 10 db. With 2N2415 r-f stage ahead of mixer, combined power gain is 25 db and noise figure 6 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 302.



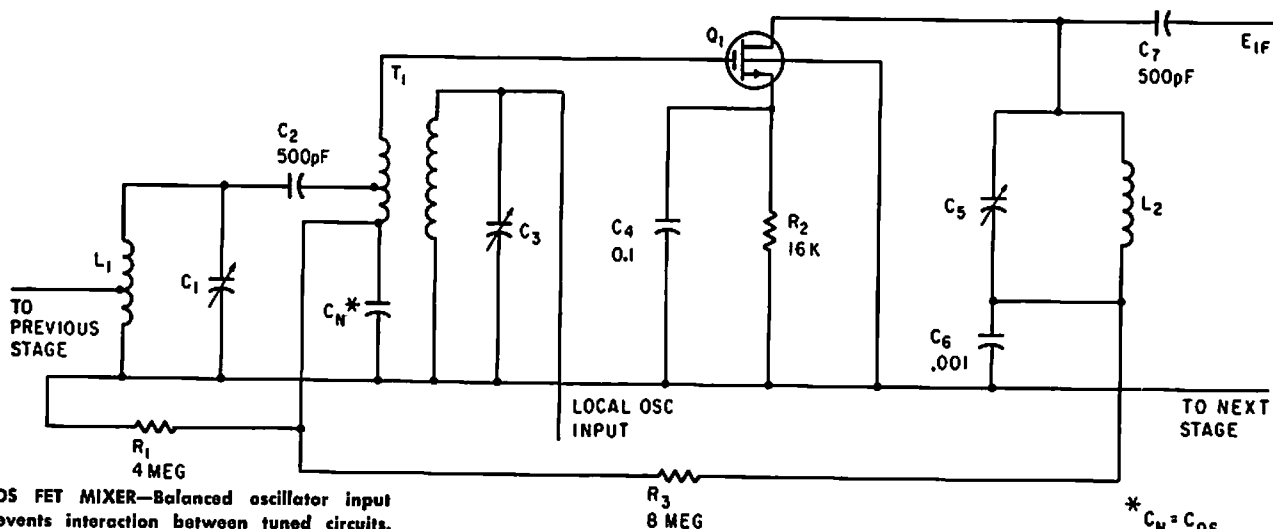
REFERENCE VOLTAGE FOR DIGITAL CONVERTER—Rectifies speech signals over 30-db range, and allows charge of memory capacitor to rise in millisec when speech input is applied, to form reference input voltage to

digital converter for ratio quantizing unit.—J. D. Howells, Better Speech Quantizing for Pulse-Code Modulation, *Electronics*, 35:48, p 84-88.



LOGIC-LEVEL CONVERTER—Converts from +18/-8 v logic levels of some frequency converters and other digital test equipment to

commonly used 0/-6 v logic levels of data-logging system.—C. M. Jackson, Logic-Level Converter, *EEE*, 12:9, p 61.

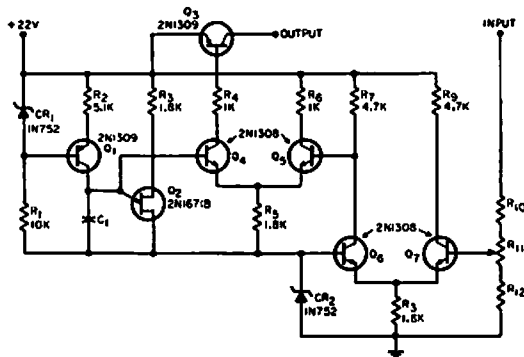


MOS FET MIXER—Balanced oscillator input prevents interaction between tuned circuits. Transconductance of mixer is directly proportional to oscillator voltage, permitting use

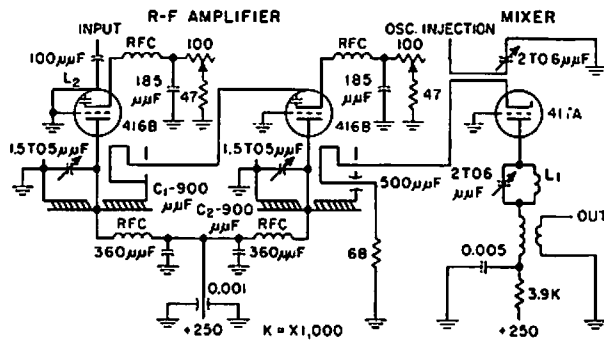
for agc.—G. G. Luetgenau and S. H. Barnes, Designing With Low-Noise MOS FETs: A Little

Different But No Harder, *Electronics*, 37:31, p 53-58.

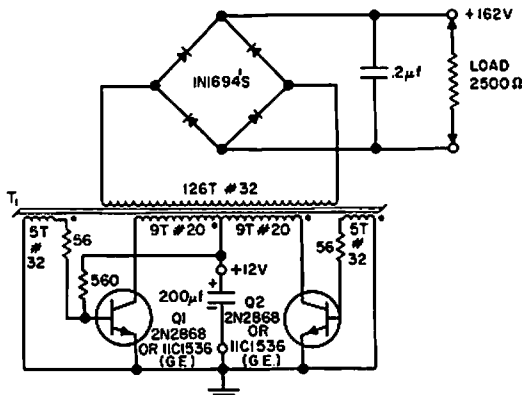
* $C_N = C_{OS}$



VOLTAGE TO PULSE-WIDTH CONVERTER—Converts d-c level linearly to pulse width at preset frequency. Used as switching-type series d-c regulator. C1 is selected to give desired preset frequency, and R10, R11, and R12 are chosen for desired voltage division of input.—M. C. Ellis, Linear Voltage to Duty-Cycle Converter, *EEE*, 12:3, p 72.

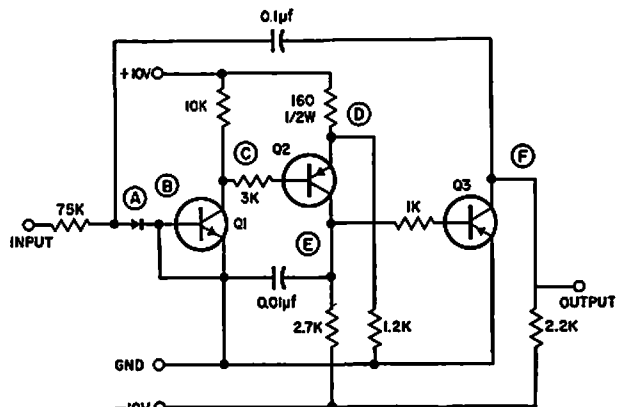


LOW-NOISE 400-MC CONVERTER—Voltage gain is 36 db, bandwidth 4 Mc, and noise figure only 2.5 db. Uses grounded-grid r-f and mixer stages. Applications include meteor, aurora, and forward scatter propagation studies.—L. F. Garrett, Low Noise Converter for IGY Propagation Study, *Electronics*, 31:5, p 52-54.

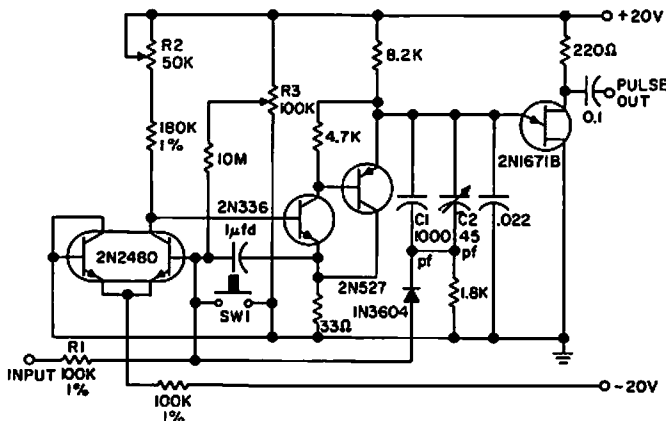


T₁ - TWO #3C3 E CORE #206F440 FERROXCUBE BOBBIN #595F425 SAUGERTIES, NY.

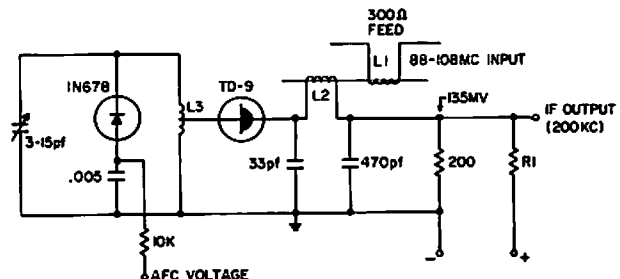
12 TO 162 V D-C CONVERTER—Simple and efficient saturating-core inverter provides 10 w output with efficiency of 80%, using operating frequency of about 8.5 kc.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 237.



D-C TO AUDIO FREQUENCY CONVERTER—Response is nonlinear; 1.7 v input voltage gives 100 cps; 1.9 v gives 200 cps; 2.25 v gives 400 cps; 3.3 v gives 800 cps; 6.2 v gives 1,600 cps; 10 v gives 2,000 cps. Output waveform approximates rectangular shape.—D. Busby, Jr., DC to Frequency Converter, *EEE*, 10:11, p 31.

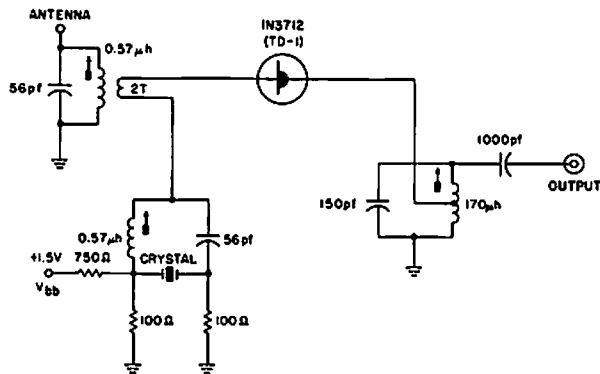


VOLTAGE-TO-FREQUENCY CONVERTER—Output frequency is proportional to input voltage, with 1 volt producing 1 kc. Linearity is better than 0.1%.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 346.

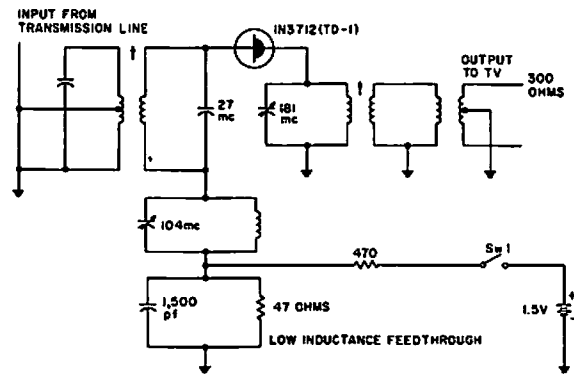


L1 - 4 TURNS #18-3/8" ID, APPROX. 1/2" LONG
L2 - 4 TURNS #18-3/8" ID, APPROX. 1/2" LONG
L3 - 8 1/2" TURNS #18-3/8" ID, APPROX. 3/4" LONG TAPPED AT 5 TURNS
LIL2 - COUPLED END TO END, SPACED ~ 1/8" APART
R1 - DEPENDS ON SUPPLY VOLTAGE. SELECT FOR BIAS OF APPROX. 135MV ACROSS 200Ω RESISTOR.

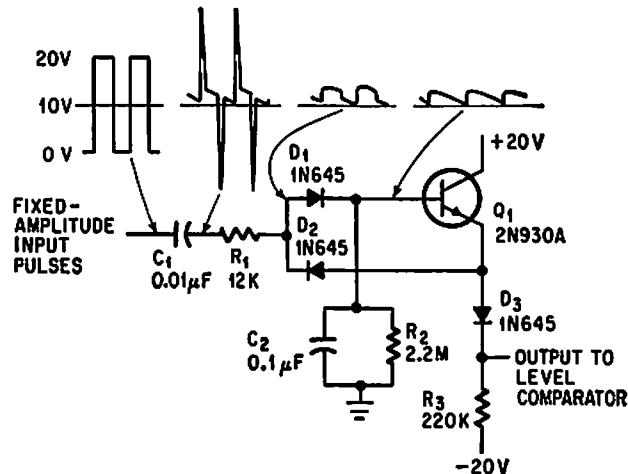
88-108 MC F-M CONVERTER WITH AFC—Variable-capacitance diode provides frequency control of tunnel-diode oscillator.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 361.



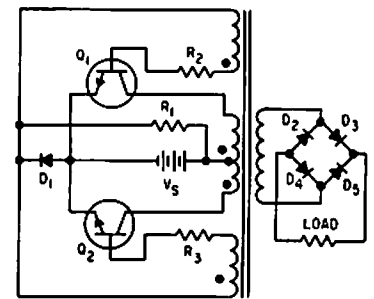
CRYSTAL-CONTROLLED CB CONVERTER—Uses tunnel-diode oscillator.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 358.



COMMUNITY-TV UP CONVERTER—Uses tunnel-diode oscillator.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 359.

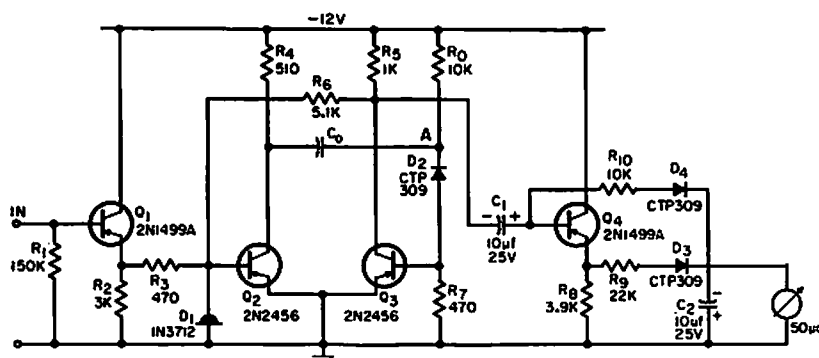


FREQUENCY-TO-VOLTAGE CONVERTER—Linear staircase generator delivers d-c output voltage proportional to repetition frequency of input pulses while rejecting short-duration spurious noise pulses.—M. Morlen and D. Grossman, Interrogator Circuit Can Tell Good Data from Bad, *Electronics*, 37:20, p 58-59.

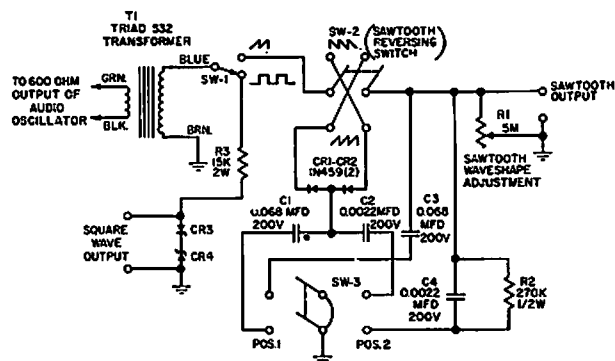


	SILICON	GERMANIUM
Q ₁ , Q ₂	2N389	2N457
D ₁	1N645	1N538
R ₁	5,600	8,200
R ₂ , R ₃	10	3.3
D ₂ , D ₃ , D ₄ , D ₅	1N647	1N540

POWER CONVERTER—Silicon transistor version gives 15 w output from 24-v supply at 70% efficiency, while germanium version gives over 100 w at 90% efficiency. With germanium transistors, diode D1 and supply voltage must be reversed.—T. R. Pye, Design of Transistor Power Converters, *Electronics*, 32:36, p 56-58.

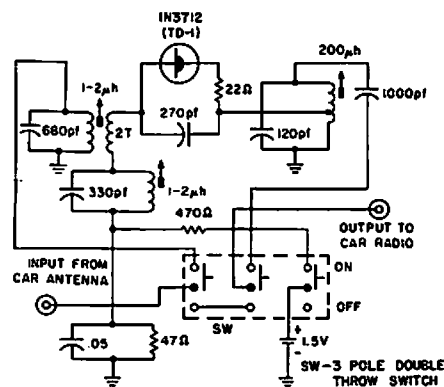


FREQUENCY TO D-C CONVERTER—Used in place of costly frequency meter for frequency measurements from 10 cps to 1 Mc, having almost any input waveform. Overall accuracy is 3%. Medium-power 12-v zener should be used to stabilize supply voltage.—T. Molliga, Frequency-to-DC Converter for Lab Measurements, *EEE*, 12:8, p 84.

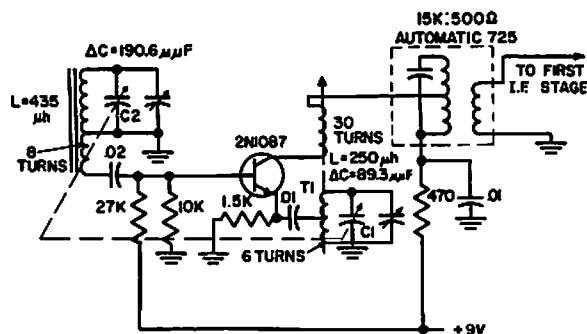


SINE TO SAWTOOTH OR SQUARE WAVES—Changes 50 to 17,000 cps sine waves to either waveform, using only power of signal itself. Sawtooth is obtained from sine wave by linear charging of capacitors. Switch position 1 covers 50 to 2,000 cps, and position 2 1,800 to 17,000 cps. CR3 and CR4 are IRC 60-1505 zener diodes (8 v - 0.1 v).—M. W. Raybin, Converts Sine Waves to Sawtooth or Square Waves, *EEE*, 10:11, p 28.

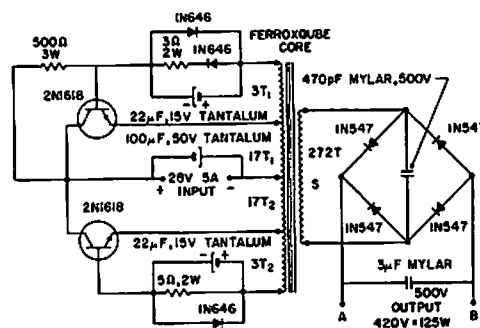
ition 1 covers 50 to 2,000 cps, and position 2 1,800 to 17,000 cps. CR3 and CR4 are IRC 60-1505 zener diodes (8 v - 0.1 v).—M. W. Raybin, Converts Sine Waves to Sawtooth or Square Waves, *EEE*, 10:11, p 28.



CIVIL AIR PATROL CONVERTER—Tunnel-diode oscillator in self-oscillating converter permits reception on aviation band with auto radio. —"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 358.



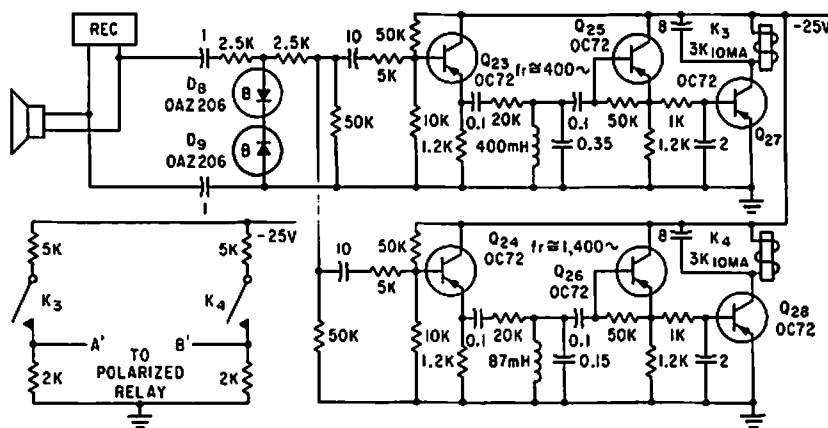
AUTODYNE CONVERTER—Single transistor stage serves as combination local oscillator, mixer, and i-f amplifier in transistor radio. Mixer operates in grounded-emitter configuration. I-f value is 455 kc. —"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 284.



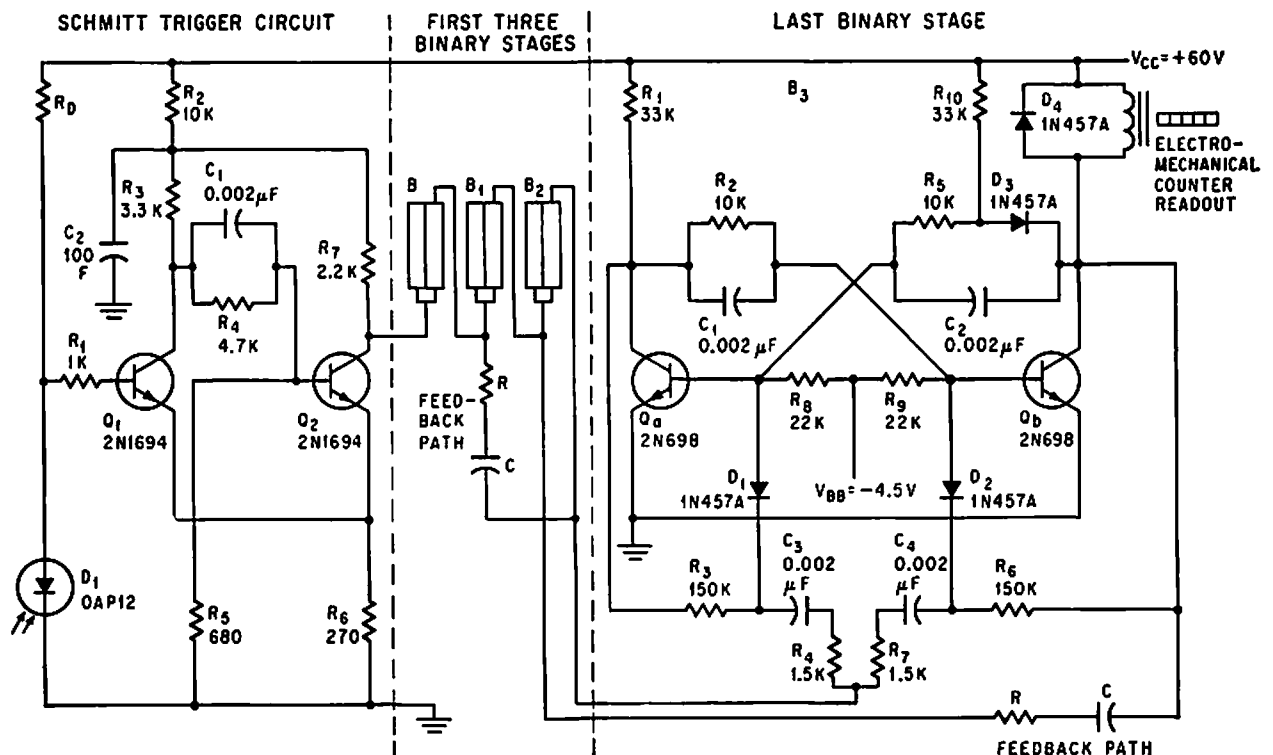
28 TO 420 V D-C CONVERTER—Efficiency is 87%, power output 125 w, output voltage ripple 0.7 v p-p, and operating frequency 10 kc. Ambient temperature range is -50 to 125°C. Transformer winding data is given. —"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 238.

CHAPTER 20

Counter Circuits—Objects



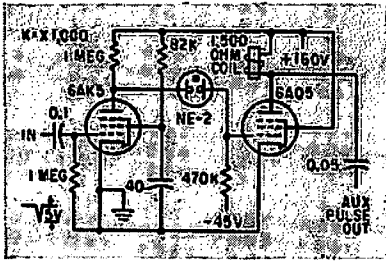
27-MC RECEIVER FOR TONE-MODULATED DATA—Two selective amplifiers, one tuned to 400 cps and other to 1,400 cps, drive relays K3 and K4 to control printing register for recording remote events such as passage of birds through infrared curtain.—P. A. Tove and J. Czokajewski, *Infrared Curtain System Detects and Counts Moving Objects*, *Electronics*, 34:31, p 40-43.



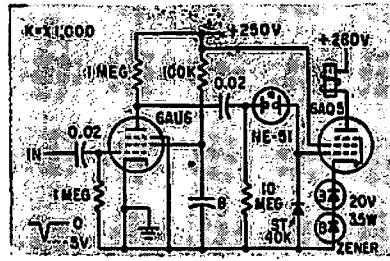
BUTTON AND BEAD COUNTER—Tiny objects passing before photodiode are counted by Schmitt trigger and four binary stages at up

to 30 counts per second, with electromechanical counter readout.—E. J. Brach, *Photocell Triggers Counting Circuit*, *Electronics*,

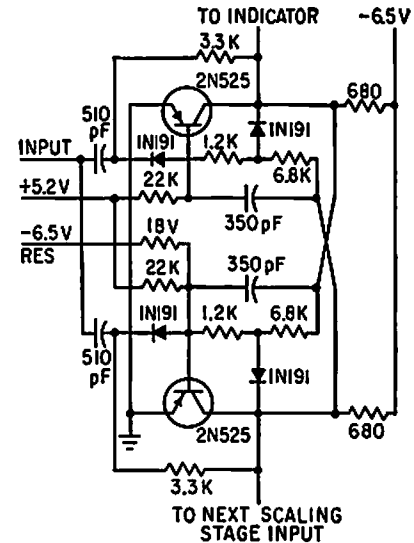
38:13, p 74-75.



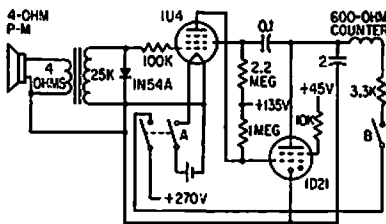
AMPLIFIER-DRIVER SPEEDS UP COUNTER—Arrangement using neon coupling between tubes can increase operating speed of well-designed electromechanical counter up to 2.5 times, by providing combination of pulsed and sliding overvoltage.—R. L. Ives, *Circuit Modifications for Boosting Counter Speed*, *Electronics*, 33:7, p 112-114.



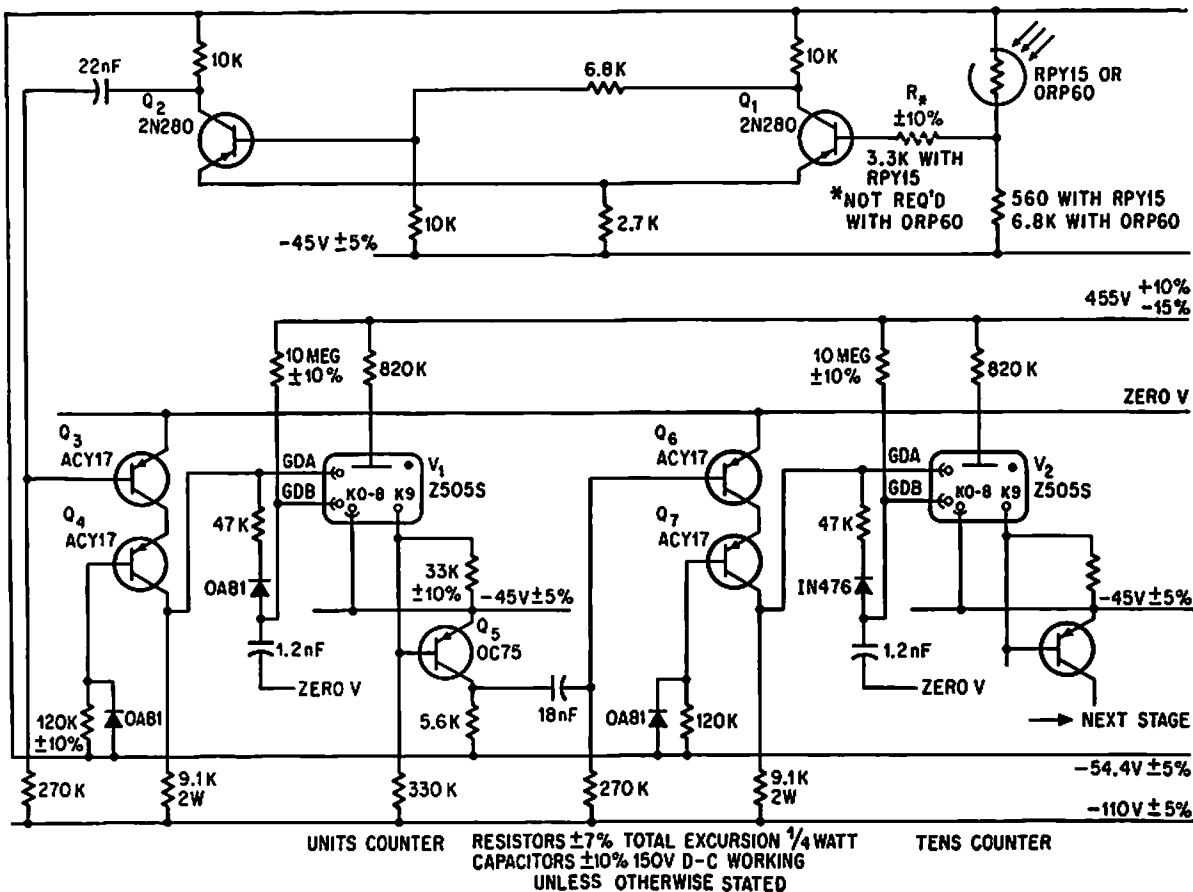
SPEED-TRIPLING COUNTER DRIVE—Increased speed of 25-cps electromechanical counter to 75 cps without affecting accuracy of count. Circuit arrangement simplifies power supply requirements. Zener diodes eliminate need for bias supply.—R. L. Ives, *Circuit Modifications for Boosting Counter Speed*, *Electronics*, 33:7, p 112-114.



NEON INDICATOR SCALER—Battery-powered stage uses binary scaling circuit with non-saturated temperature-compensated transistors, as elements of scale-of-64 circuit driving 4-digit mechanical register.—F. E. Armstrong, *Battery Powered Portable Scaler*, *Electronics*, 33:19, p 74-75.



GUNSHOT COUNTER—Loudspeaker serves as microphone for picking up loud noises such as from blasts, noisy auto engines, and children on roller skates. Each input shock wave drives grid of 1U4 negative and fires 1D21 stroboson, thereby dumping charge on 2-mfd capacitor from plate to ground through counter. Operates at up to 40 shots per second, fast enough for most automatic weapons fire.—R. L. Ives, *Shot Counter Uses Stroboson*, *Electronics*, 31:33, p 94-96.



CONVEYOR COUNTER—Senses objects as small as 0.2 sq cm on moving conveyor at

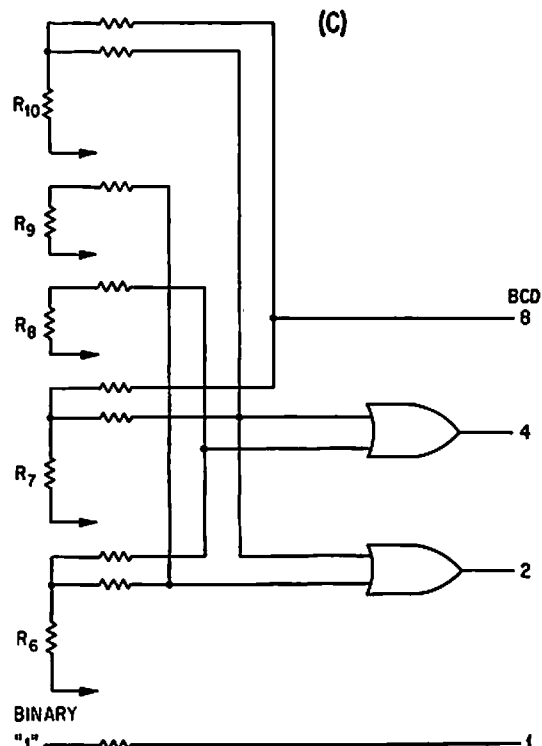
speeds up to 25 pps.—G. Jeayes, *Using Cold-Cathode Tubes to Count and Store*, *Electron-*

ics, 38:8, p 80-89.

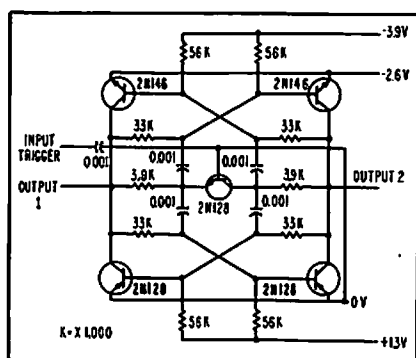
The schematic diagram illustrates the internal electronic circuit of the receiver. It features four vacuum tube stages: V1A (12AT7), V2 (6X4GB/S), V3 (12AT7), and V4 (6X4GB/S). The circuit is powered by a +300V supply and includes a phasemeter for signal analysis. Key components include resistors (e.g., 2.2 MEG, 10K, 33K, 150K, 820K, 39K, 150K, 820K, 47K, 100K, 4.7K, 10K, 47K, 10K, 22K), capacitors (e.g., 0.05, 0.02, 0.25, 0.01, 0.001, 0.005, 0.02), and a Zener reference voltage (-20V). The circuit is designed to process signals from the antenna through various stages of amplification and detection, ultimately outputting to the next coupling tube.

(A)

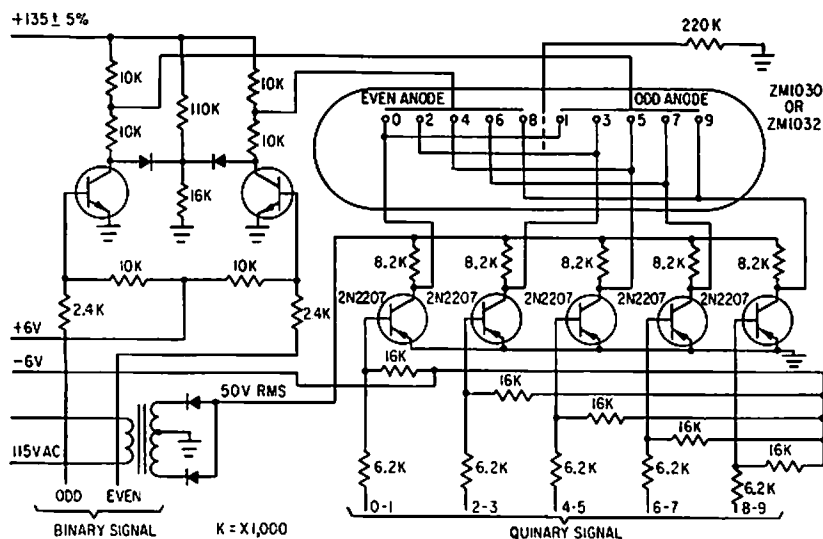
The circuit diagram (A) illustrates a 5x5 grid of transistors. The top row of transistors has base terminals labeled 8, 6, 4, 2, and 0. The bottom row of transistors has base terminals labeled 9, 7, 5, 3, and 1. Each transistor's emitter is connected to a common ground line at the bottom. The collector terminals of the top row are connected to a series of resistors labeled $R_6, R_7, R_8, R_9, R_{10}$. Each resistor is connected to a common horizontal line at the top. A $2K$ resistor is connected between the top horizontal line and the collector of the first transistor (base 8). A $10K$ resistor is connected between the top horizontal line and the collector of the fifth transistor (base 0). A $-20V$ supply is connected to the bottom horizontal line.



permit easy conversion to bcd.—R. Engelmann, B-Quinary Scaling: Accuracy and Simplicity at 500 Mc, *Electronics*, 36:46, p 34-36.

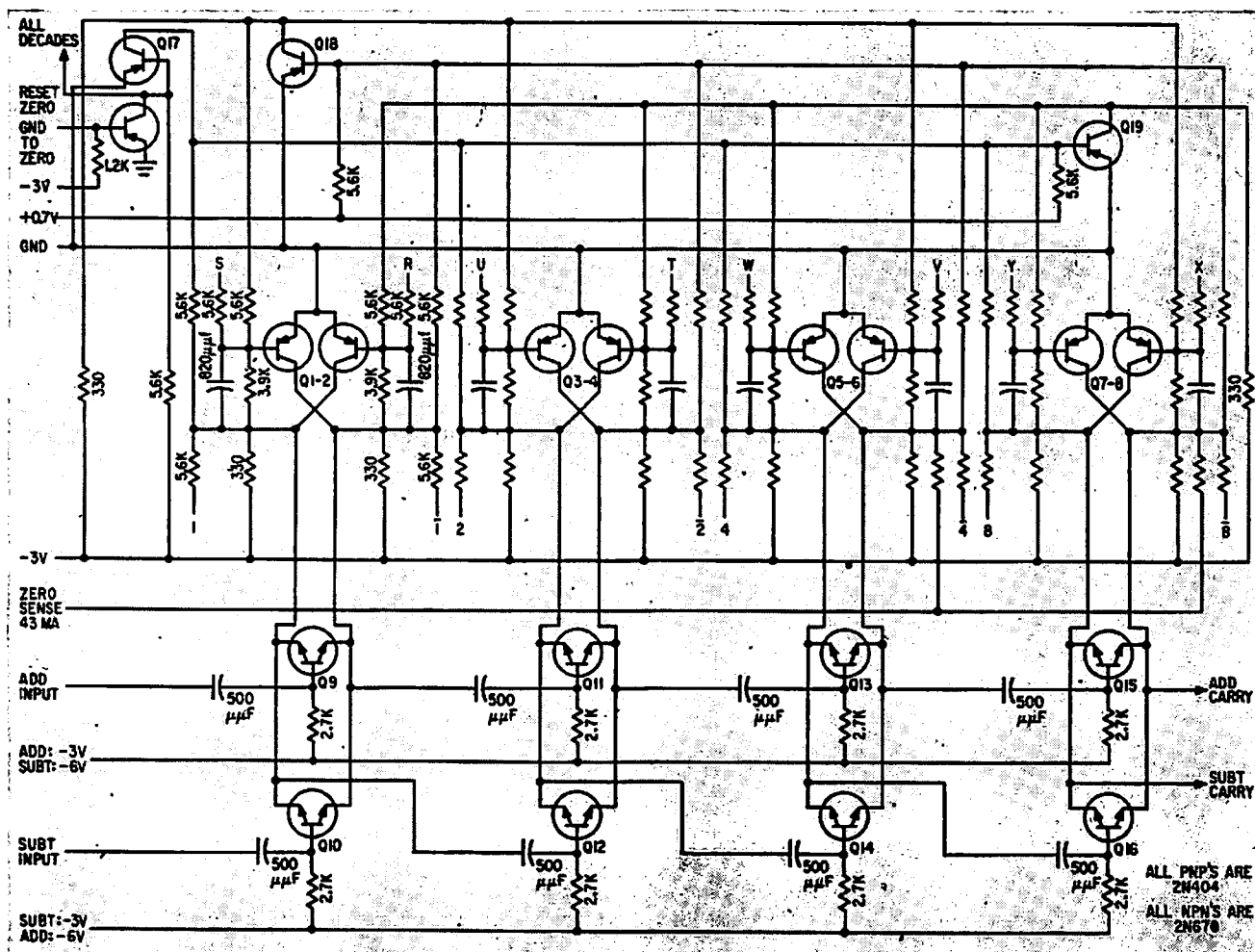


BINARY WITH STEERING-CIRCUIT TRANSISTOR—Input goes to steering transistor that replaces two back-to-back diodes normally used to drive bases of binary transistors through capacitive coupling. Four-transistor binary flip-flop reduces total quiescent drain on batteries.—R.W. Rochelle, *Cyclops Cores Simplify Earth-Satellite Circuits*, *Electronics*, 31:9, p 56-63.



BIQUINARY COUNTER WITH READOUT—Simplified driver circuits require only seven transistors rather than ten, when used with

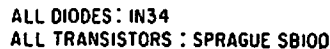
special Amperex ZM1032 tube.—Biquinary Indicator Uses 7 Transistors, *Electronics*, 36:28, p 58.



STEERING FOR REVERSIBLE DECADE COUNTER—Consists of four R-C coupled complementary saturated flip-flops Q1-Q8. Symmetrical npn

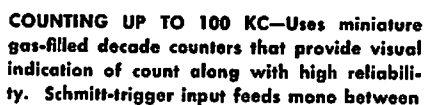
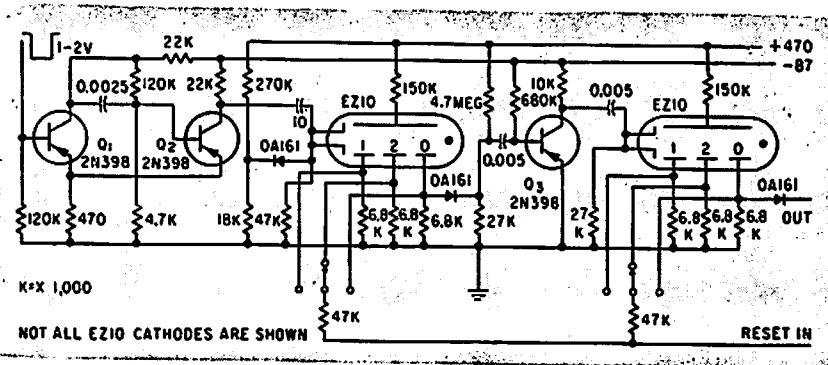
steering transistors Q9 through Q16, coupled between collectors of flip-flops, are used as trigger current amplifiers and for steering

when counting up or down.—R. D. Carlson, *Steering Circuits Control Reversible Counters*, *Electronics*, 33:1, p 86-88.



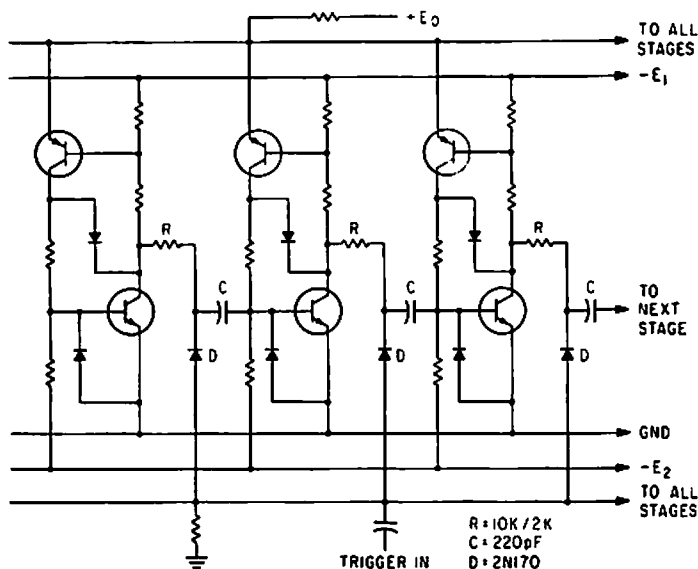
eight count; at ninth count, stages 1 and 4 generate positive leading coincidence trigger and apply it to stages 2 and 3 to change them from 0 to 1, so all stages are at 1 on count 9. Next count then clears all stages to 0. Dash-dot lines show connections for

advancing the counter six units for the fourth count.—P. K. Malhotra and R. Parshad, Novel Coincidence Technique for Transistor Decade Counter, *Electronics*, 36:7, p 71-72.

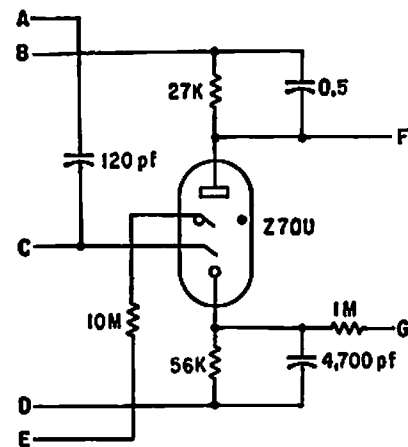


counter tubes. Can produce output after desired count if counter is initially reset to complement of the desired number.—K. Apel and P. Berweager. *Miniature Gas-Filled Tubes For*

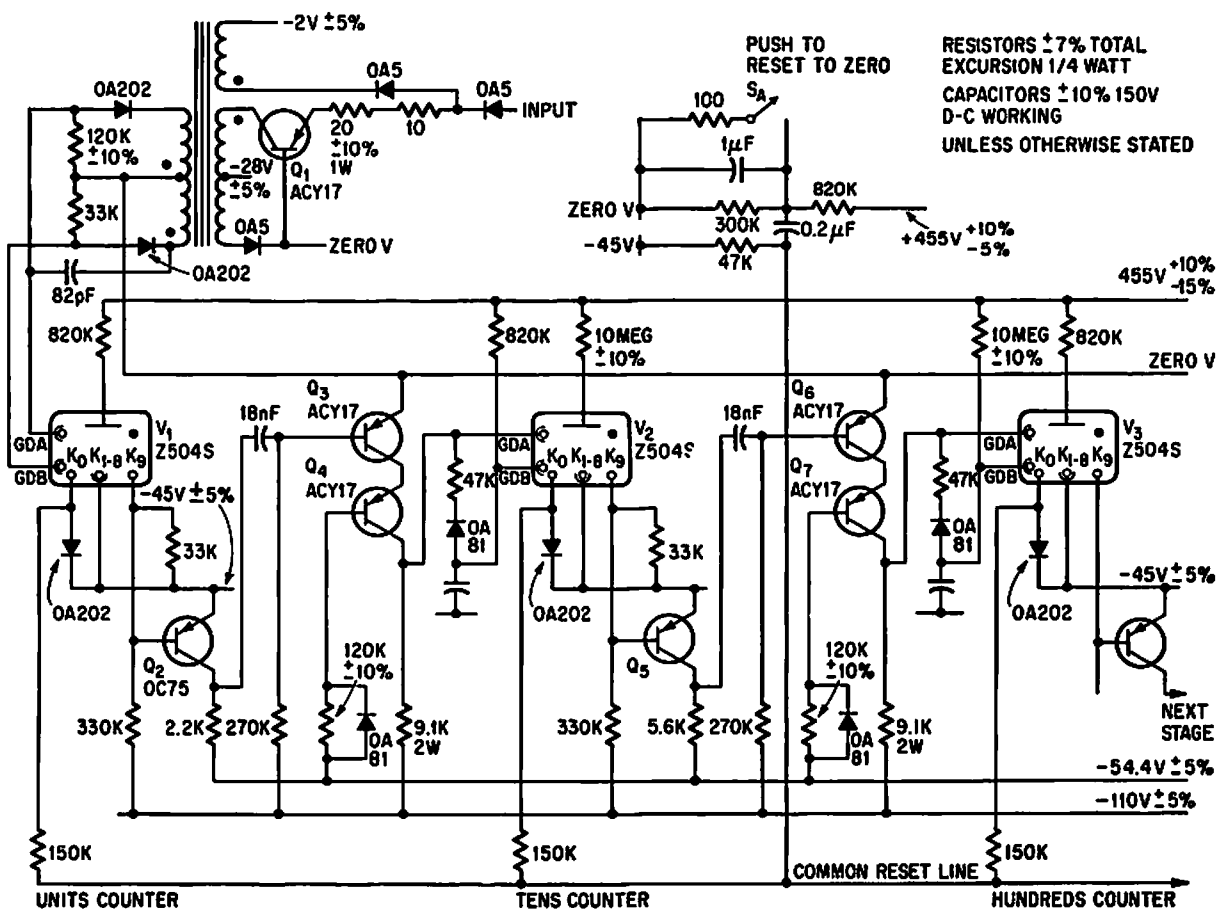
High-Speed Counting, *Electronics*, 33:8, p 46-47.



REVERSIBLE TEN-STAGE RING COUNTER—Can be operated above 100 kc. Reversible operation requires binary control, such as by bistable mvbr, to determine direction. Upper transistors are 2N414 and lower are 2N488. Other resistors are 1K.—N. C. Hekimian, PNP-NPN CIRCUITS: New Look at a Familiar Connection, *Electronics*, 35:47, p 42-46.

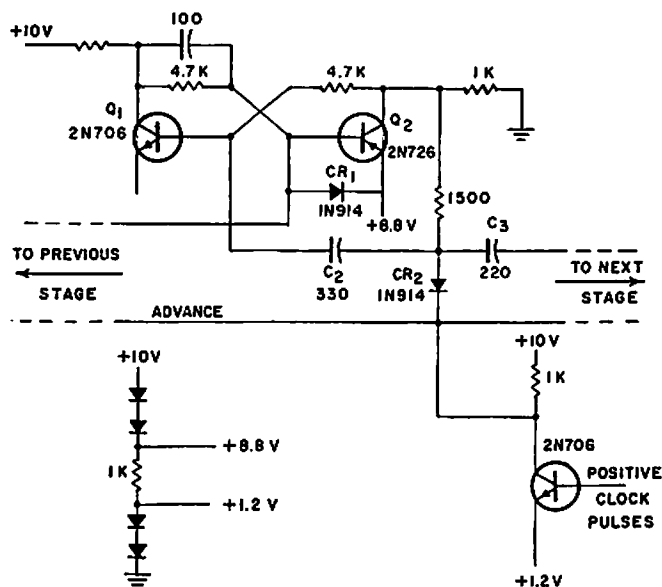


GAS-TUBE RING COUNTER—Uses Philips trigger tubes in decade counter having maximum speed of 2,500 pps. Bias developed at cathode is fed through G to prime following stage. Readout display can be Burroughs Nixie HB106 or other numerical indicator. —P. G. Hodgson, Cold-Cathode Ring-Counter Drives Numerical Indicator, *Electronics*, 33:14, p 80.

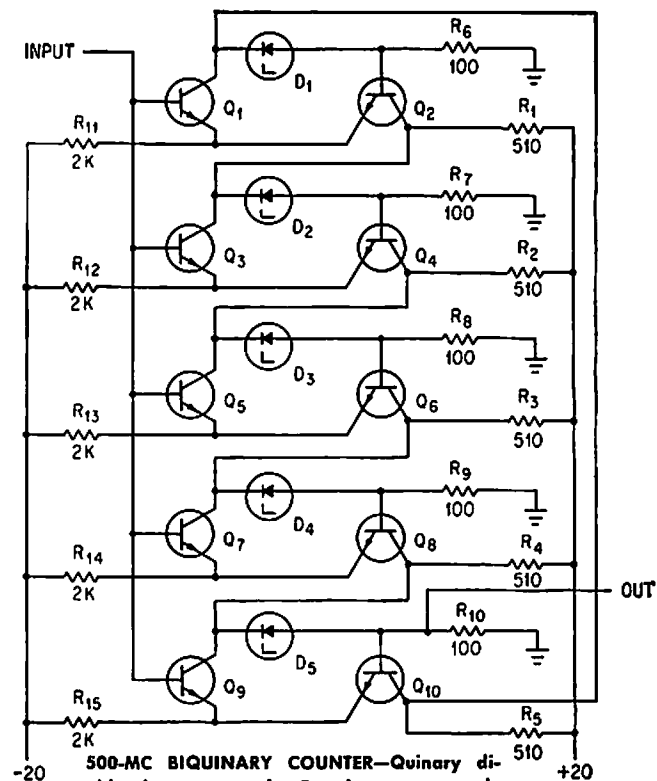


DECADE SCALER—For accurate timing applications up to 400 pps.—G. Jeynes, Using Cold-

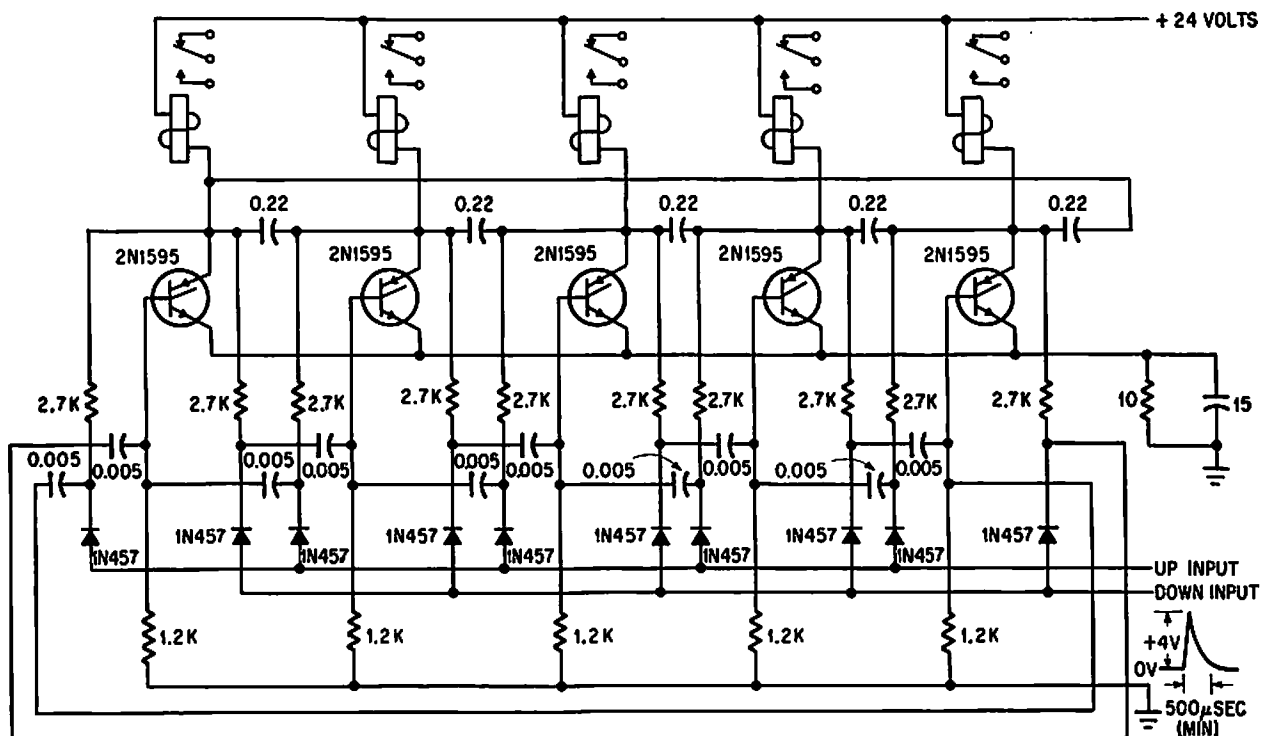
Cathode Tubes to Count and Store, *Electronics*, 38:8, p 80-89.



1-MC COMPLEMENTARY-TRANSISTOR COUNTER—Only one stage draws current from power supply, because on stage has both transistors conducting and off stages have both transistors cut off. Average power drawn is that of single conducting stage and is independent of number of stages. Circuit prefers off state at startup.—Counter Uses Complementary Transistors, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 129.



500-MC BIQUINARY COUNTER—Quinary divides input events by 5, using current-mode switches, and binary divides quinary output by 2 to give total division of 10. Chief drawback is difficulty of converting to bcd. All transistors are 2N2708.—R. Englemann, 8-Quinary Scaling: Accuracy and Simplicity at 500 Mc, *Electronics*, 36:46, p 34-36.



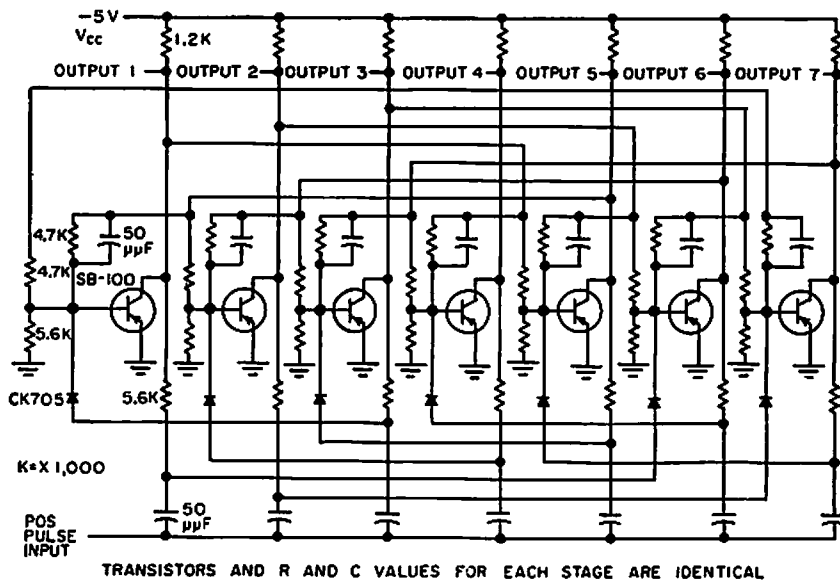
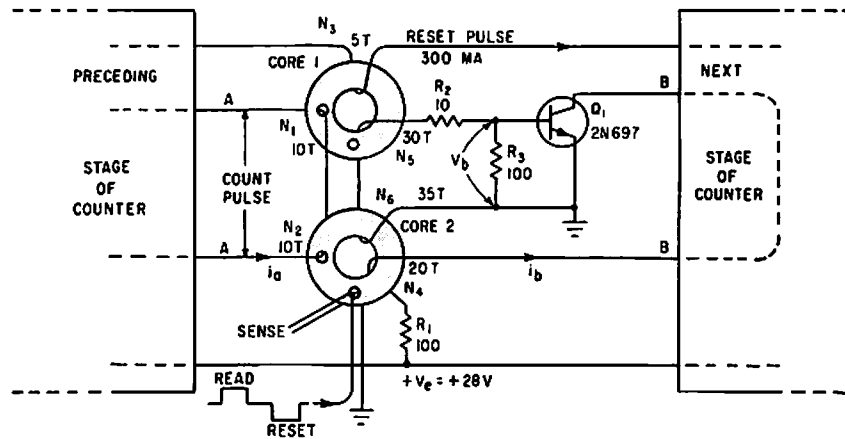
ALL CAPACITANCE VALUES ARE IN MICROFARADS ALL RELAYS ARE C.P. CLARE TYPE RP7641G2

UP OR DOWN—Scr ring counter shifts up or down in 3 millsec without missing count.

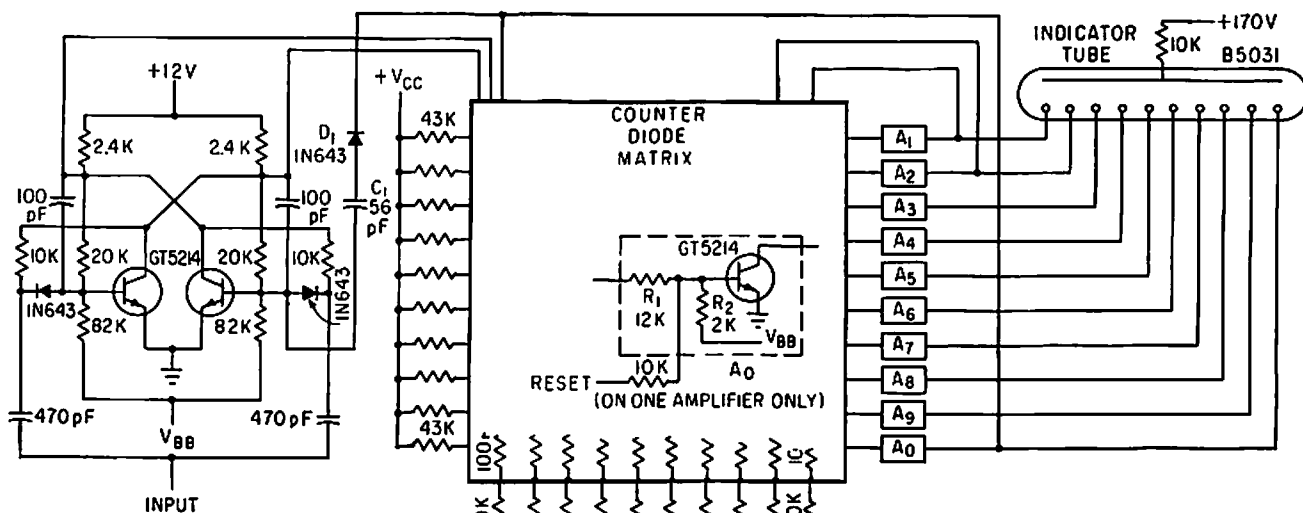
Only conducting stage draws power.—J. G. Peddie, SCR Ring Counter Switches Up or

Down, *Electronics*, 38:18, p 84-85.

COMMAND TIME COUNTER—Used with time-logic matrix to store command times for missile-launching and guidance-control systems.—W. R. Johnston, *Multiaperture-Core Counters Give Nondestructive Storage Readout*, *Electronics*, 34:24, p 62-64.



RING-OF-7 COUNTER—Uses surface-barrier transistors in arrangement wherein pattern of four on stages is stepped along ring, permitting maximum number of stages in ring to be much higher than in conventional rings.—W. Carlson, *Ring Counter Has Increased Count Capacity*, *Electronics*, 31:15, p 89-91.

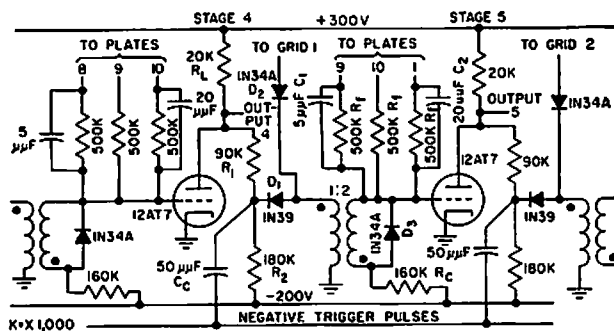
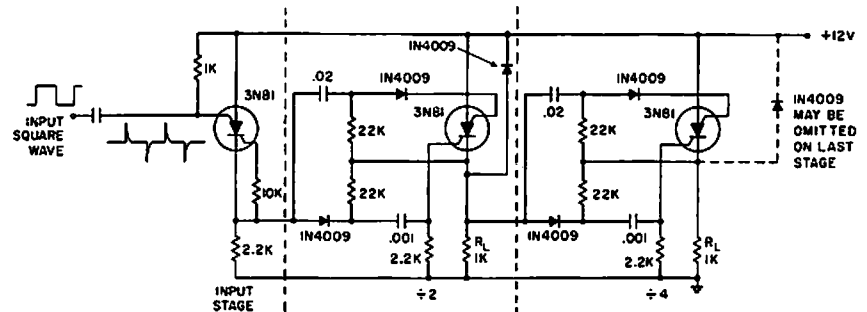


ZERO SET FOR 90-DIODE MATRIX—Each pulse input to flip-flop advances counter one position. Single-transistor amplifiers A1-A0 drive glow indicator tube serving as readout. For

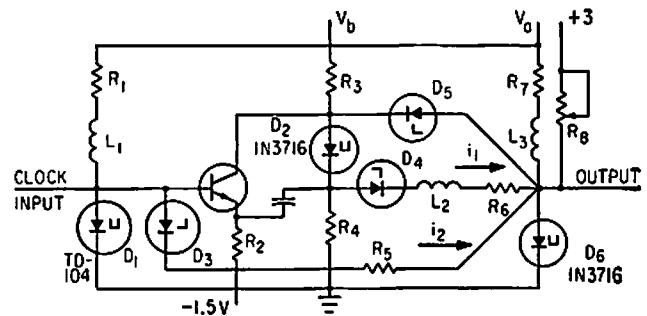
reset to zero, positive pulse is applied to base of transistor in zero amplifier, to turn its transistor on and turn all other amplifier

transistors off.—R. W. Wolfe, *Diode Matrix Shrinks Decimal Counter*, *Electronics*, 35:13, p 50-52.

SCS BINARY COUNTER—Stages are triggered by positive-going edge of input. Silicon controlled switch is turned on at cathode gate, and turned off at anode gate.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 429.



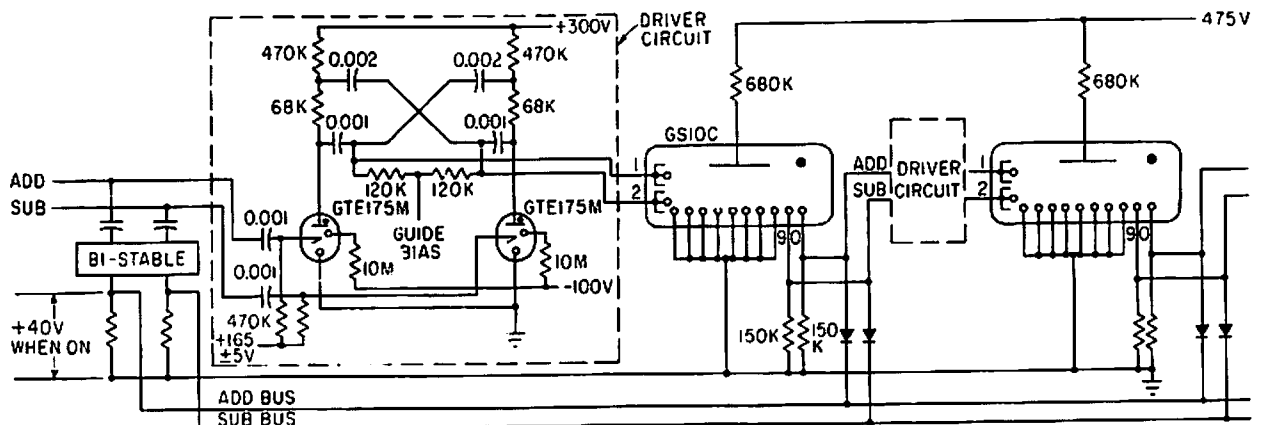
RING-OF-10 COUNTER—Based on stepping of recognizable sequence of on and off stages along ring at each count pulse, in contrast to conventional ring counters having only one on stage to hold off all other stages. Circuit gives partial diagram, and article gives wiring table for remaining stages. Four on stages are stepped along ring. Time constants of gating circuits limit count rate to 240 kc, but components with faster time constants permit operation up to 1 Mc. Ring of 13 is upper limit.—W. Carlson, Ring Counter has Increased Count Capacity, *Electronics*, 31:15, p 89-91.



$R_1 = 18 \text{ ohms}$
 $R_2 = 1.1 \text{ K}$
 $R_3 = 68 \text{ ohms}$
 $R_4 = 68 \text{ ohms}$
 $R_5 = 470 \text{ ohms}$
 $R_6 = 130 \text{ ohms}$
 $R_7 = 51 \text{ ohms}$
 $R_8 = 10 \text{ K}$

$D_3, D_4, D_5 = BD - 4 (GE)$
 $L_1 = 0.68 \mu\text{H}$
 $L_2 = 62 \mu\text{H}$
 $L_3 = 56 \mu\text{H}$
 $V_a = 200 \text{ mV} \pm 10 \text{ percent}$
 $V_b = 500 \text{ mV} \pm 20 \text{ percent}$
 $C = 82 \text{ pF}$

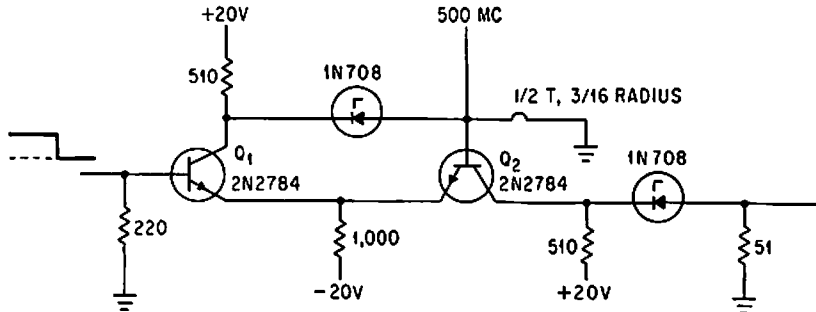
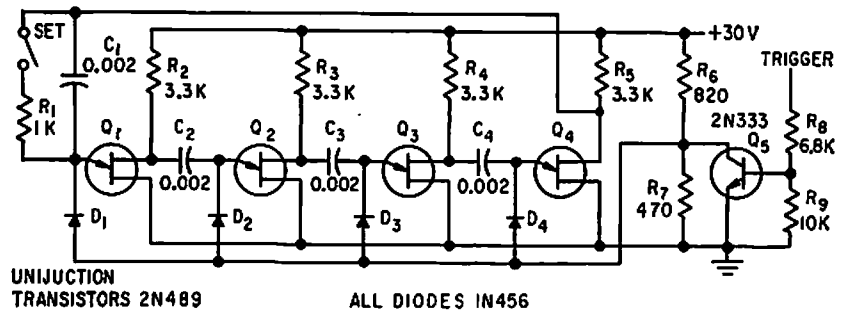
MULTI-SCALE COUNTER—Changing value of L_2 changes scale factor in range of 2 to 8. Circuit operates to 10 Mc at scale of 5.—C. A. Budde, One-Stage Scaler Needs No Complex Feedback, *Electronics*, 36:39, p 32-33.



BIDIRECTIONAL MULTIDECADE COUNTER—Single sign-determining circuit ahead of input to tens stage provides gating signals for every decade, to handle rapid reversal of

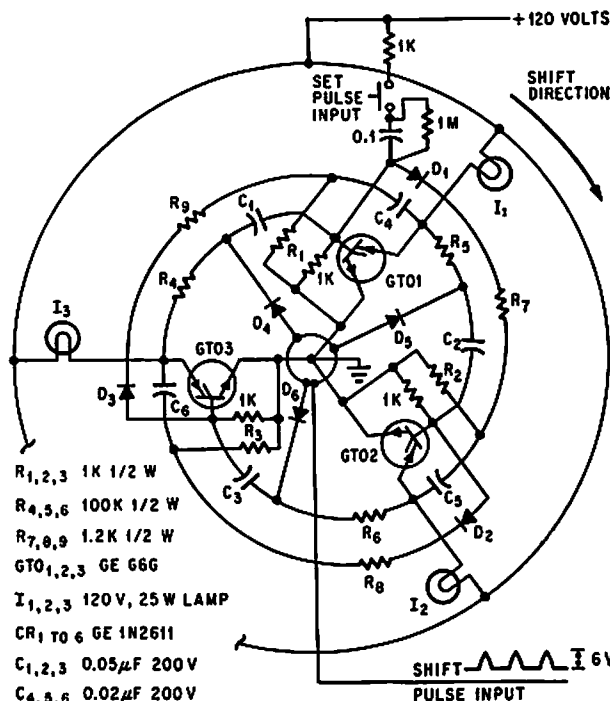
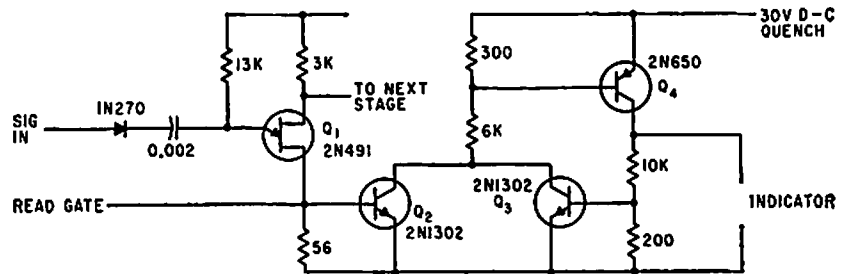
count direction.—L. C. Burnett, Reversible Decade Counter, *Electronics*, 35:9, p 46.

UJT BISTABLE RING COUNTER—Operates up to 40,000 cps, with trigger pulse widths between 6 and 9 microsec.—T. P. Sylvan, *Bistable Circuits Using Unijunction Transistors*, *Electronics*, 37:51, p 89-91.



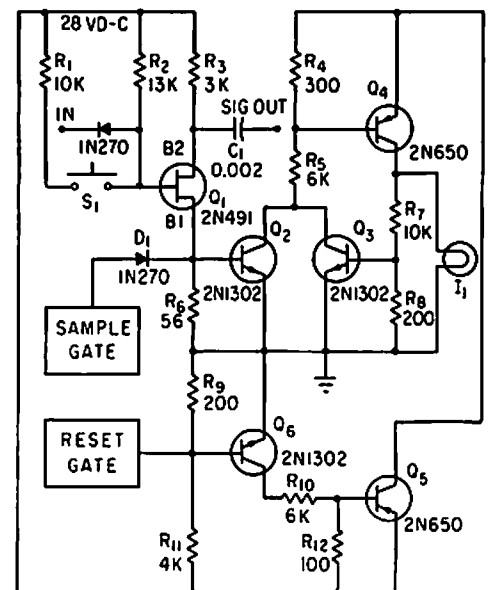
SYNC CIRCUIT FOR QUINARY SCALER—Time-base signal at 500 Mc turns Q1 on and Q2 off at timing rate. Negative 1-v start signal applied to base of Q1 makes 500-Mc signal appear at collector of Q2 to serve as output for one of quinary scalars. Two scalars in parallel can measure time accurately to within 1 nsec.—R. Englemann, *Quinary Scalers: Measure Time Intervals Digitally*, *Electronics*, 37:5, p 34-36.

PULSE COUNTER—Unijunction transistor Q1 serves as counter, with other counter stages being identical. Q3 and Q4 energize and lock readout circuit until quench pulse is applied after next counting cycle.—F. W. Kear, *Unijunction Transistor Pulse-Circuit Design*, *Electronics*, 35:21, p 58-60.

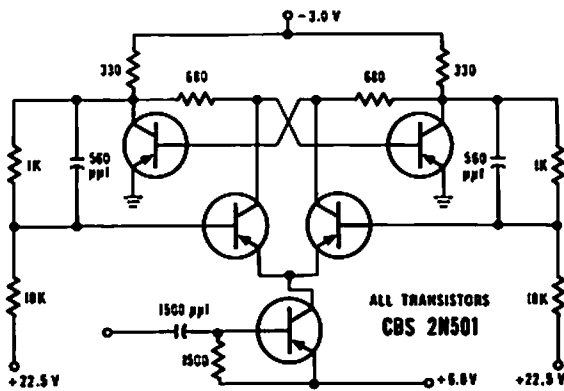


RING COUNTER USES GATE TURNS—Closing of set pulse input switch turns on first gto, applying voltage to its lamp load. Each shift pulse input then transfers conduction to

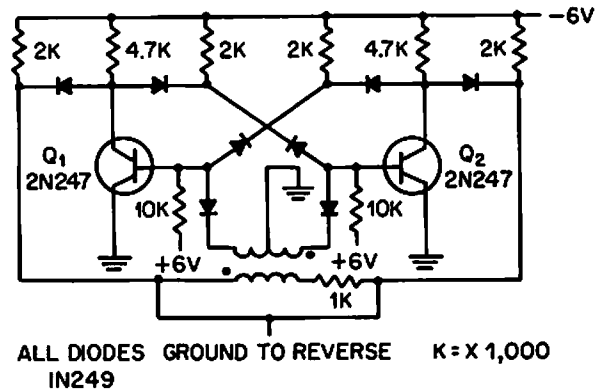
next gto and lamp.—D. R. Grafham, *Now the Gate Turnoff Switch Speeds Up D-C Switching*, *Electronics*, 37:12, p 64-71.



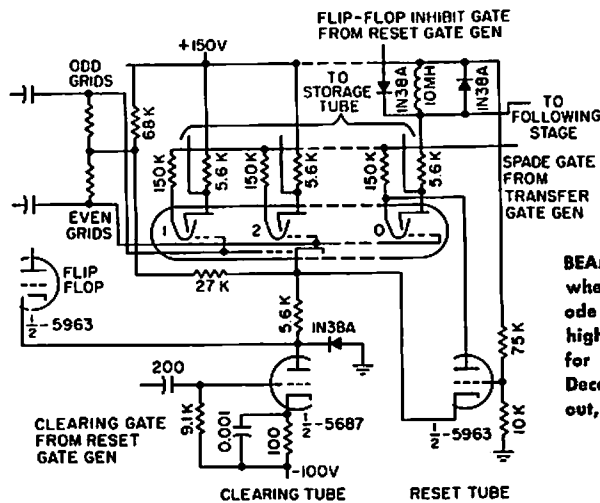
UNIUNCTION RING COUNTER—Provides switching for readout and control applications, including data display for airborne digital instrumentation. Q5-Q6 provide resetting.—F. W. Kear, *Digital Control Uses Unijunction Transistors*, *Electronics*, 34:18, p 79-80.



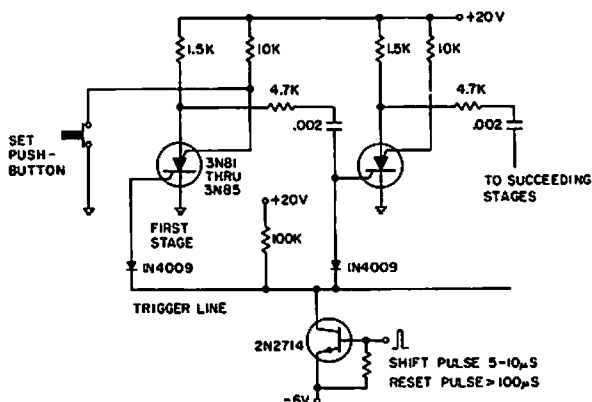
BASE-GATED BINARY—Input counting rate is up to 70 Mc. Saturating transistor gate minimizes turnoff and turnon delay. Flip-flop transition is completed in less than 16 millimicrosec.—High-Speed Switching Transistors (CBS Electronics ad), *Electronics*, 33:39, p 45.



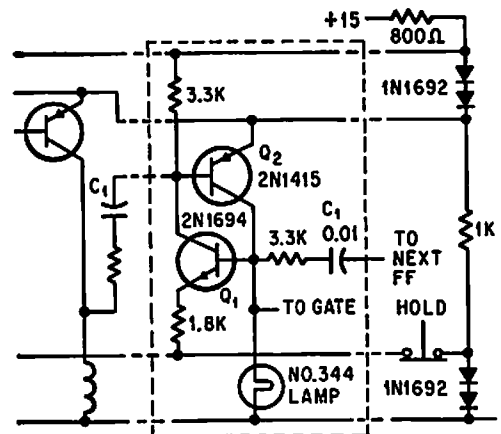
TRANSFORMER-CONTROLLED COUNTER—Uses conventional linear transformer in conventional bistable flip-flop to store information.—W. M. Carey, Using Inductive Control in Computer Circuits, *Electronics*, 32:38, p 31-33.



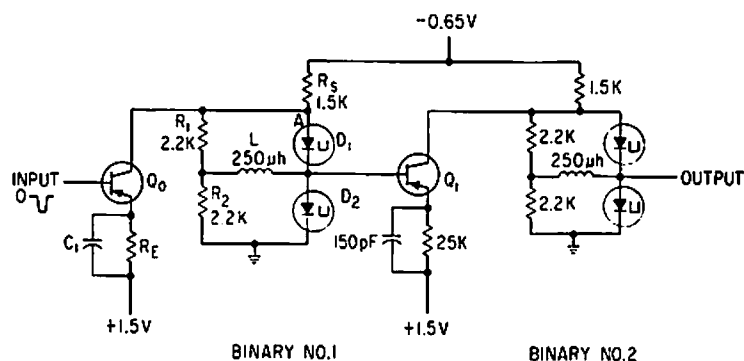
BEAM-SWITCHING DECADE—Counter is cleared when reset tube cuts off series triode in cathode of beam-switching tube. Circuit presents high impedance to initiating gate, as required for resetting several decades.—R. W. Wolfe, Decade Decimal Counter Speeds Printed Readout, *Electronics*, 31:3, p 88-90.



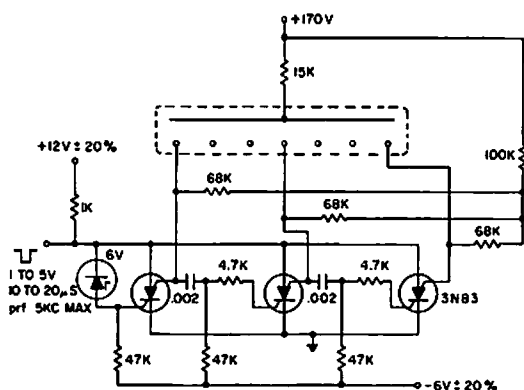
20-KC RING COUNTER—Shift pulses turns off conducting silicon controlled switch by reverse-biasing cathode gate. Charge stored on coupling capacitor then triggers next gate.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 431.



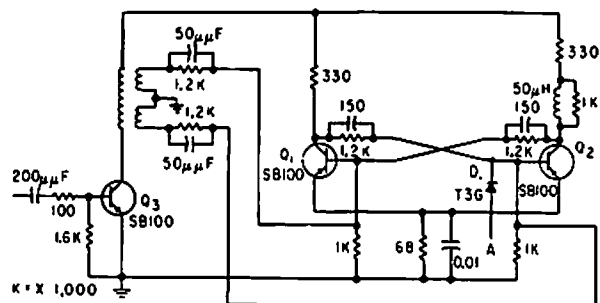
FLIP-FLIP RING COUNTER—Complementary mvbr, in which Q1 and Q2 are either both on or both off, gives low power drain. Strong negative pulse applied to base of Q2 of first stage gives reset.—J. E. Russell, Ten Signals at a Glance, *Electronics*, 37:19, p 54-57.



LOW-LEVEL 5-MC TUNNEL-DIODE—With 1N2933 germanium tunnel diode, power consumption is only 525 microwatts per transistor and binary stage. Circuit voltage and resistances are such that only one tunnel diode is in high-voltage state at a time. Silicon transistors, for coupling, can be pnp or npn.—E. Gottlieb and J. Giorgis, *Tunnel-Diode Switching Circuits*, *Electronics*, 36:27, p 26-31.



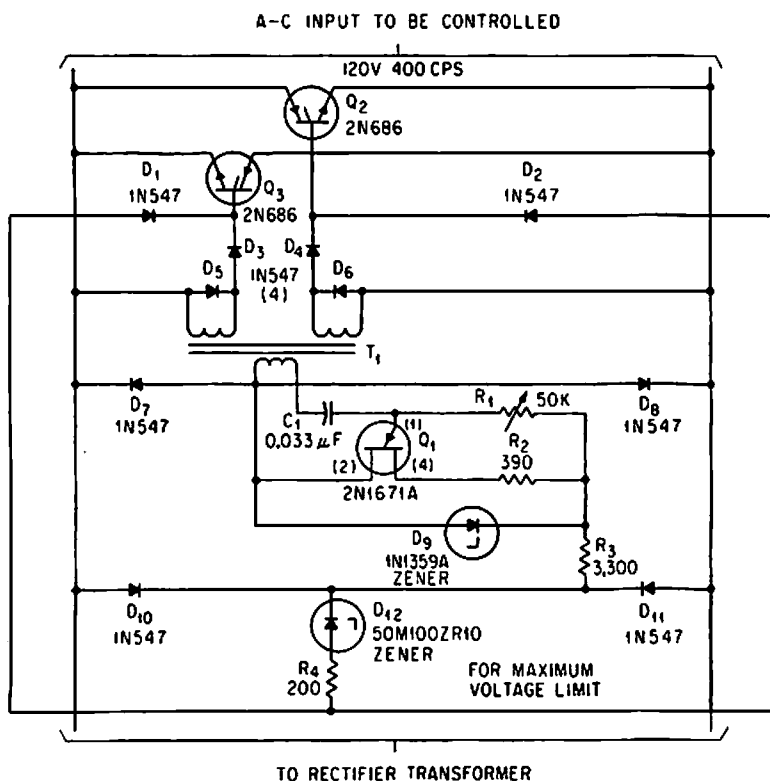
NIXIE-TUBE RING COUNTER—Uses silicon controlled switches.—"Transistor Manual," General Electric Co., 1964, p 430.



7-DIGIT BINARY COUNTER—Stores pulses received from oscillator gate. 128th pulse resets counter to zero. Complete binary counter consists of seven cascaded bistable multivibrators, transformer-triggered.—W. W. Grannemann et al, *Pulse-Height-to-Digital Signal Converter*, *Electronics*, 33:2, p 58-60.

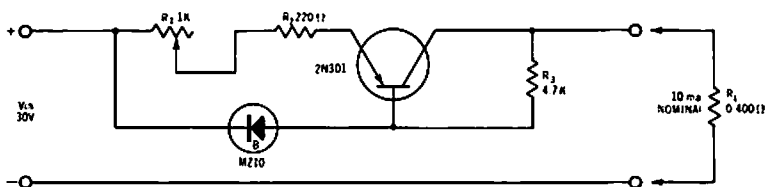
CHAPTER 22

Current Control Circuits



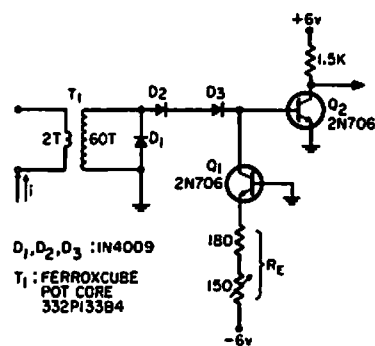
LASER MODULATOR CURRENT CONTROL—When modulator or pumping current for laser is lost, output voltage of pulse transformer T1 will rise to limit set by zener D12, which then conducts to make Q2 and Q3

absorb current not required by energy storage capacitors.—S. J. Grabowski, Pulse Power Supply Design for Laser Pumping, *Electronics*, 36:51, p 33-35.

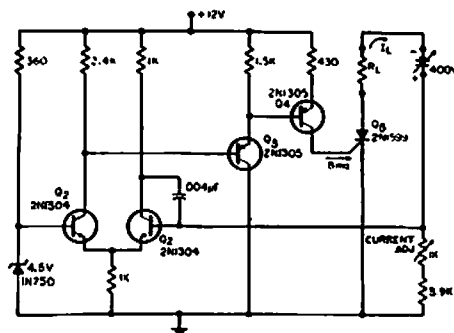


CONSTANT-CURRENT REGULATOR—Uses transistor as variable series resistor. Current will remain within 10% of 10 ma from short-circuit

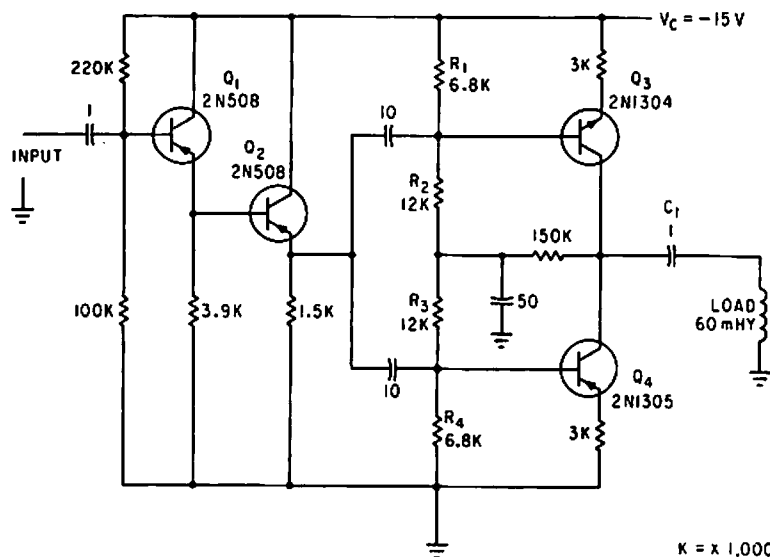
up to maximum load of 400 ohms. — "Zener Diode Handbook," International Rectifier Corp., 1960, p 59.



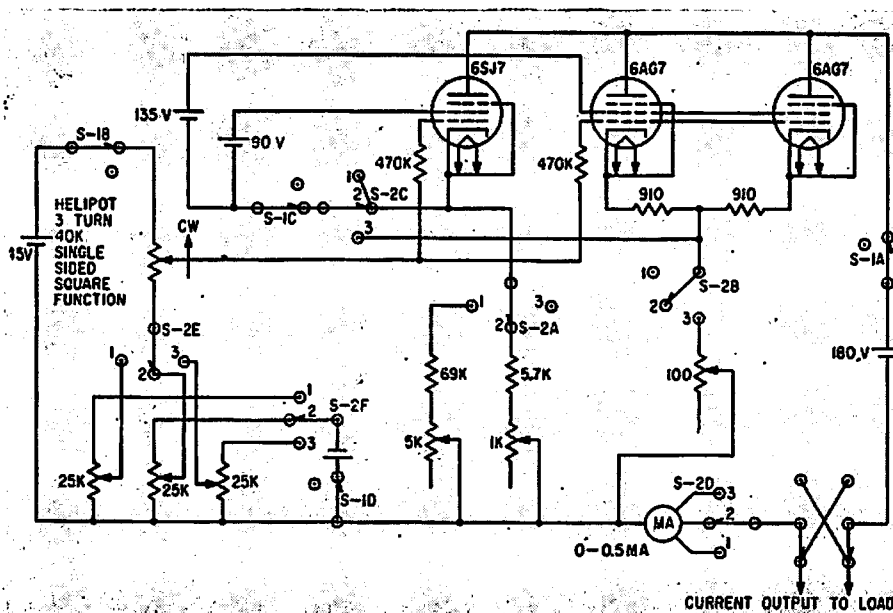
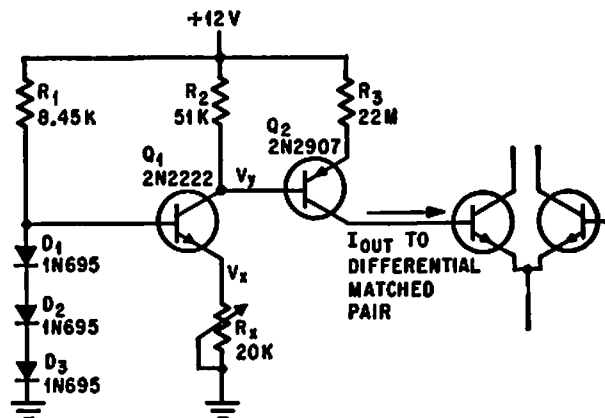
CURRENT AMPLITUDE DETECTOR—Used to indicate when pulsed drive currents for memory array exceed tolerance limits. Can detect current pulse deviation of 10 ma from 1.2-amp current level. RE is adjusted to vary current clamping level.—H. M. Winters and J. P. Shuba, *Current Amplitude Detector, EEE*, 12:11, p 68-70.



1-MA CONSTANT-CURRENT SCR SOURCE—Use of high-breakdown-voltage 2N1599 scr gives 0.25% regulation at 1 ma for input voltages of 10 to 400 v. Output current can be adjusted up to 10%. Differential amplifier Q1-Q2 compares sampled output current with voltage across reference zener.—R. H. Crawford, 400-Volt SCR Constant-Current Source, *EEE*, 12:3, p 74.

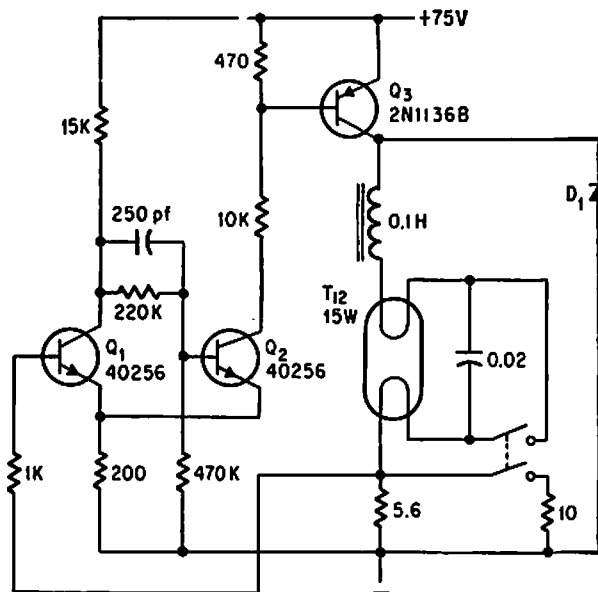
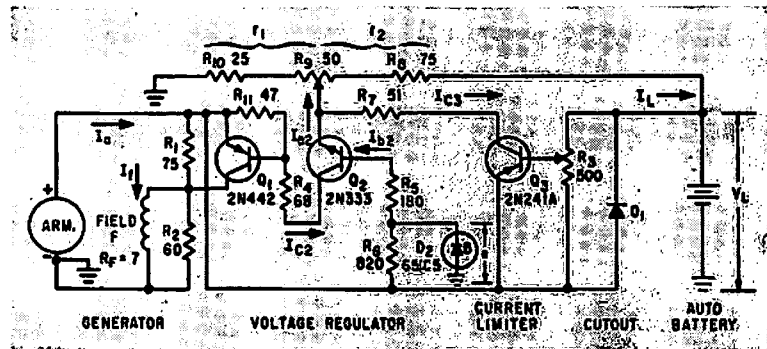


TEMPERATURE-COMPENSATED CURRENT SOURCE—Presents 1,000 meg of output impedance while supplying up to 200 na of temperature-compensated current. Germanium diodes serve as compensating network drawing 1.3 ma. Based on fact that matched transistor pairs have base-current temperature coefficients that are predictable as function of operating current.—C. C. Hanson, Low-Drift Current Generator Compensates for Temperature, *Electronics*, 39:12, p 108-109.



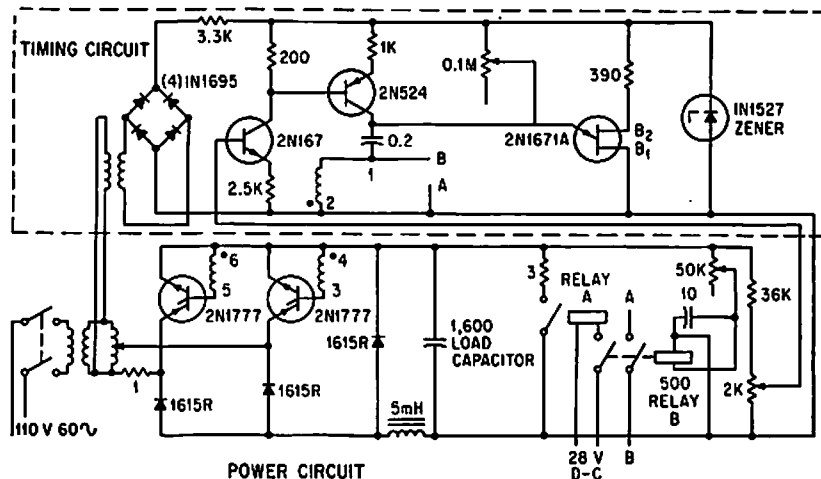
CONSTANT-CURRENT SUPPLY—Used to measure resistivity of semiconductors rapidly and accurately. Switch gives choice of 0.5, 5, and 50 ma. Values are read from dial settings rather than meters, to increase accuracy.—P. J. Olshefski, Constant-Current Generator Measures Semiconductor Resistance, *Electronics*, 34:47, p 63.

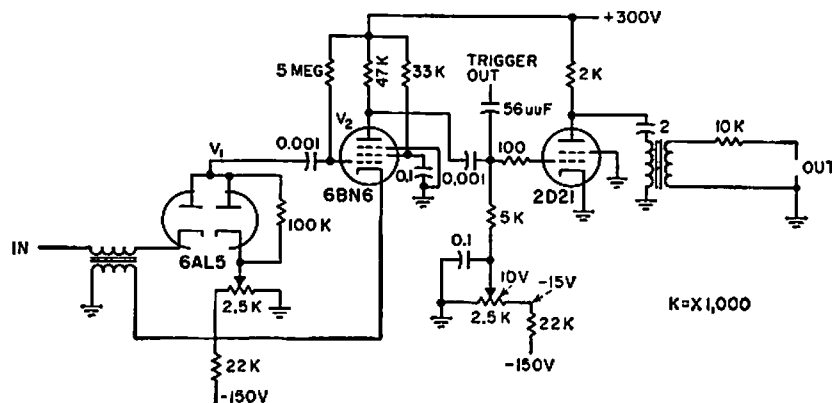
AUTO GENERATOR REGULATOR—Limits maximum generator current to safe value, prevents current flow from battery through generator when generator voltage falls below battery voltage, and regulates voltage.—I. D. Clements, *Solid-State Generator Regulator for Autos*, *Electronics*, 33:8, p 52-54.



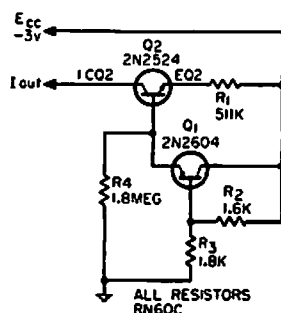
D-C SUPPLY FOR FLUORESCENT LAMPS—Lamp operates directly from d-c supply, without a-c conversion. Transistors form constant-current source that controls lamp current. Q3 is controlled by Schmitt trigger Q1-Q2. When lamp current exceeds preset value, voltage drop across 5.6-ohm resistor turns on Q1, thereby turning off Q2 and Q3. When lamp current falls, Q3 comes on again. To start, pushbutton closes circuit through lamp heaters and shunts 5.6-ohm resistor to give faster heating. When button is released, voltage surge caused by series inductor ignites lamp. Control circuit then varies lamp current 25% above and below its average value at 1-kc rate. Regulator losses are only 3 w.—D. B. Hoisington, *Direct Current Regulator Drives Fluorescent Lamps*, *Electronics*, 39:17, p 94-95.

CONSTANT-CURRENT CAPACITOR CHARGER—Firing angle of a-c supply voltage is decreased in steps after each supply cycle, to match charging rate of 360-mfd capacitor for 30-kw plasma pinch space engine, so current pulses have identical average peak of 8.6 amp.—F. Ellern, *Capacitance Chargers for Space Employ Controlled Rectifiers*, *Electronics*, 36:41, p 32-33.

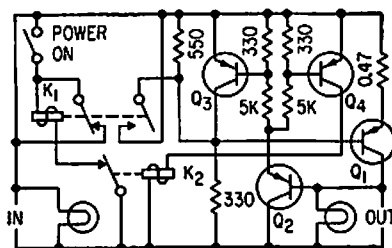


**50,000-AMP SINGLE-PULSE CURRENT SWITCH**

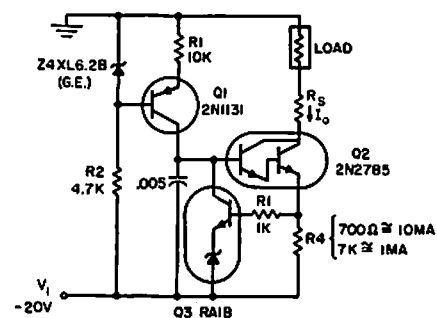
—Simple triggered-gap switch operates at voltages down to 1 kv to control switching with time jitter of only 0.1 microsec between successive pulses. Can be used for magnetron testing, surge-current generator, and flash-lamp source. Output of trigger generator is damped sine wave having sufficient amplitude to break down gap in switch and initiate current pulse. V1 and V2 serve as sharpener for triggering pulse.—E. H. Cullington, W. G. Chace, and R. L. Morgan, *Low-Voltage Trigger Controls High Currents*, *Electronics*, 31:15, p 86-88.



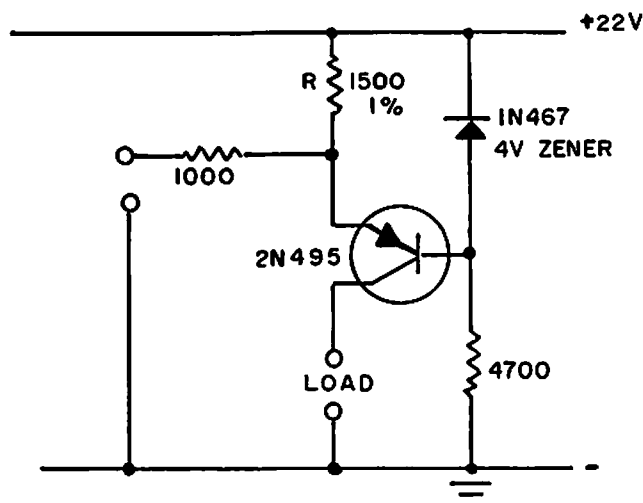
CONSTANT-CURRENT GENERATOR—Provides 2.8 microamp, regulated within 0.75% over range of -20 to $+60^{\circ}\text{C}$, to feed emitters of low-level differential amplifier. Circuit compensates for base-emitter voltage change with temperature.—M. Wolpert and D. Spooner, *Temperature-Compensated Constant-Current Generator*, *EEE*, 12:12, p 58.



TRANSISTOR OVERLOAD PROTECTION—Current greater than 3 amp flowing through 0.47-ohm resistor in emitter of current-switching transistor Q1 drops voltage on base of Q2, causing Q2, Q3, and Q4 to saturate. Q3 opens circuit immediately and keeps it open for duration of overload. For complete short-circuits, Q4 latches K2 to provide positive protection.—F. W. Kear, *Fast-Response Overload Protection*, *Electronics*, 33:7, p 125.



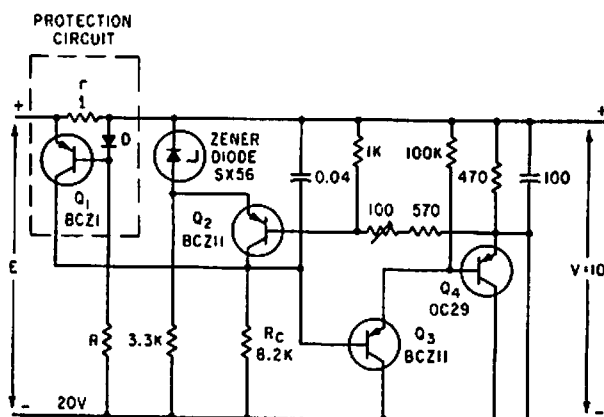
PRECISION CONSTANT-CURRENT SUPPLY—Reference amplifier, consisting of integrated zener diode and npn transistor, acts with Q2 to maintain constant reference voltage across R4. Current through R4 equals load current except for relatively small base currents of Q2 and Q3. Current drift over 15 hours is less than 0.01%.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 233.



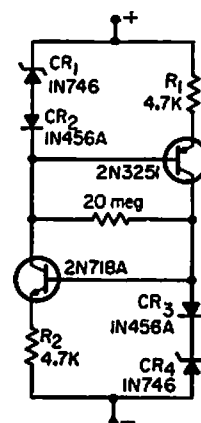
TEMPERATURE-COMPENSATED CONSTANT-CURRENT GENERATOR—Reverse voltage characteristic of zener, in conjunction with base-

emitter characteristic of transistor, stabilizes collector current by maintaining constant voltage across R from -55 to $+25^{\circ}\text{C}$.—Tem-

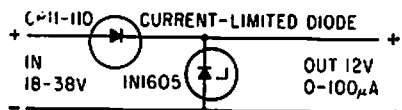
perature-Compensated Constant Current Generator, "Electronic Circuit Design Handbook," Macfater Pub. Corp., N.Y., 1965, p 169.



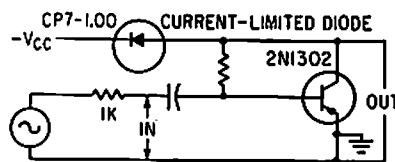
OVERLOAD PROTECTION—Protection circuit detects excessive load current and reduces output voltage proportionately. Values shown limit output current to 530 ma for short-circuit, while holding output voltage at 10 v during normal operation.—C. Yarker, Overload Protection Circuit Uses Low-Power Transistor, *Electronics*, 35:13, p 60.



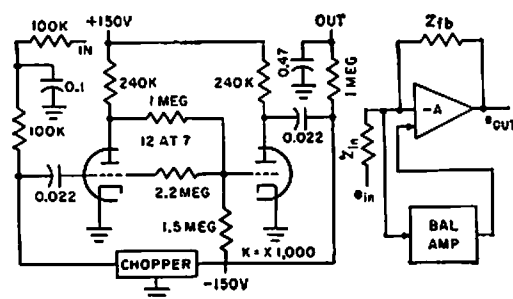
CONSTANT-CURRENT CONTROL—Uses pnp and npn current sources connected to regulate each other's reference. Values shown are for 1 ma, but R1 and R2 can be changed to give other constant values of current. Applied voltage must be at least 8 v.—F. C. Allen, Two-Terminal Constant-Current Device, *EEE*, 13:10, p 71-72.



CURRENT REGULATOR FOR 0-100 MA—Constant-current Currentor diode and shunting zener diode together maintain constant current over extremes of input voltage for both normal and shorted loads.—N. Welsh, How Diodes Keep Current to Constant Value, *Electronics*, 36:4, p 74-78.

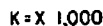


CONSTANT-CURRENT DIODE AS COLLECTOR LOAD—Current-limited Currentor diode isolates transistor amplifier output from changes in supply voltage and serves also as collector load impedance. Gain is over 60 db at 50 to 100 kc.—N. Welsh, How Diodes Keep Current to Constant Value, *Electronics*, 36:4, p 74-78.



offset adjustment period.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 114.

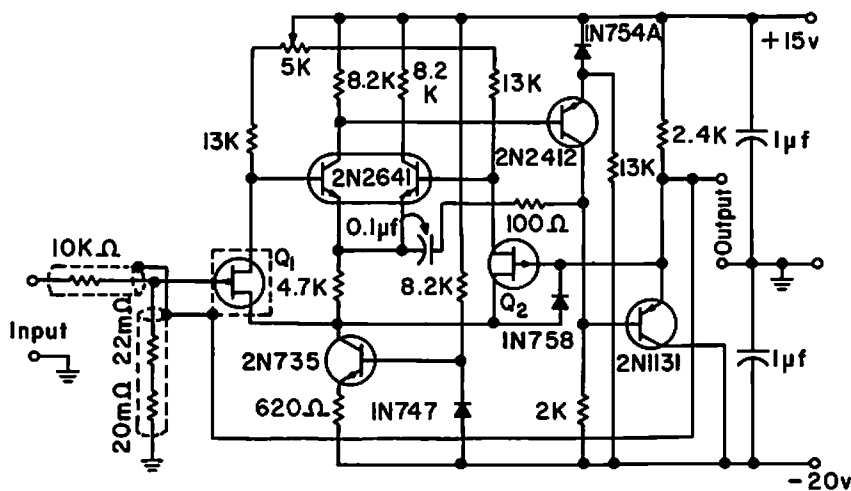
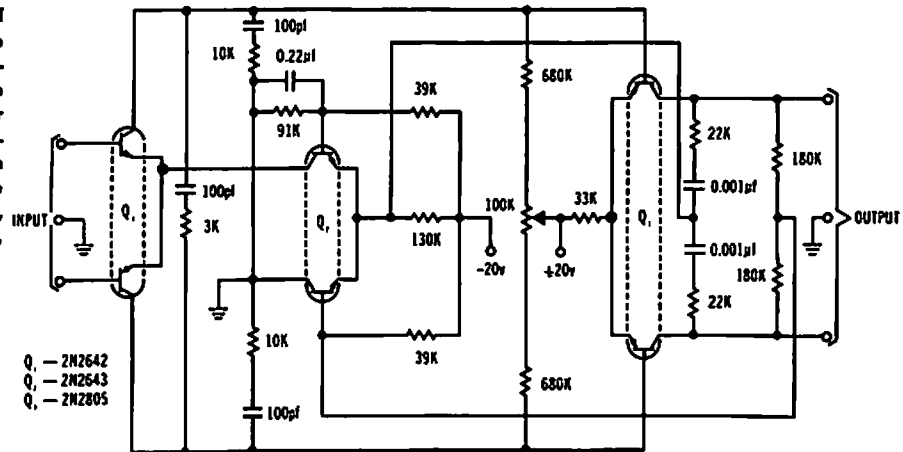
BALANCING D-C AMPLIFIER—Gain is 300. Detects slow changes at summing point of operational amplifier, amplifies drift voltage, and provides opposite-polarity output signal for second input grid of operational amplifier, much as in null-seeking servomechanism. —L. S. Klivans, D-C Amplifiers for Control Systems, *Electronics*, 31:47, p 96-100.



within 0.01% for long-term operation (1,000 hours) from 15 to 35° C. Linearity is equally precise for normal output range of plus or

minus 10 v.—F. Offner, Transistorized Data Amplifier Has High Gain-Stability, *Electronics*, 33:27, p 55-57.

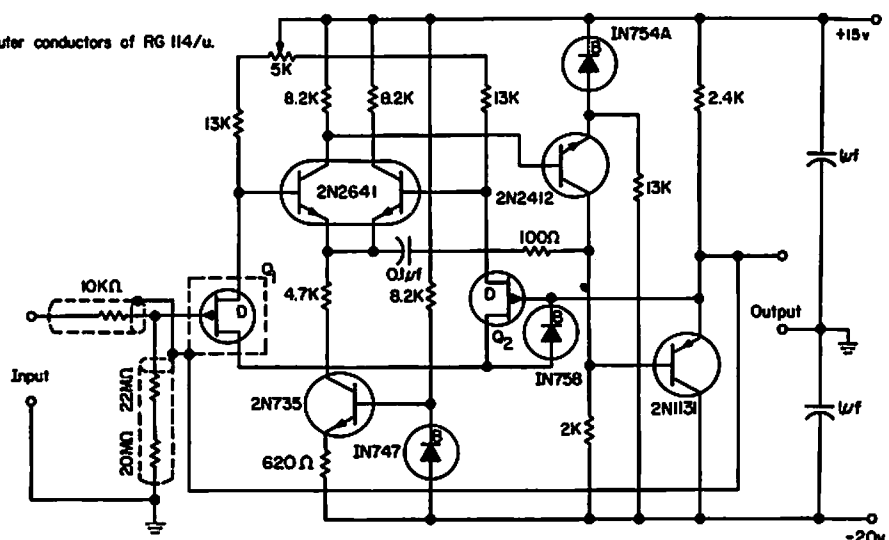
COMPLEMENTARY-PAIR LOW-LEVEL—Dual transistors provide extremely high gain, to give greater stability with fewer stages. Circuit has low drift and high common-mode rejection (120 db) for either differential or single-ended outputs. Differential input impedance is 500K minimum, gain-bandwidth product is 5 Mc, and low-frequency voltage gain is 68 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 290.

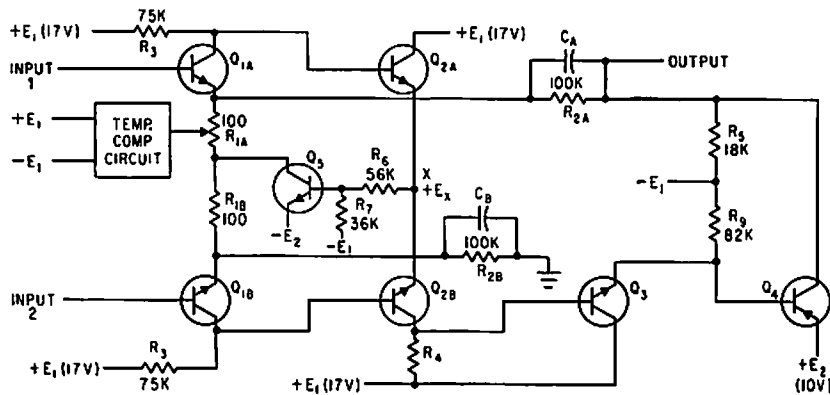


UNITY-GAIN TEMPERATURE-STABLE D-C AMP-LIFIER—Two bootstrapped cathode followers are combined to form differential input stage, where one gate serves as feedback input and other as signal input. Field effect transistors Q1 and Q2 are matched.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 139.

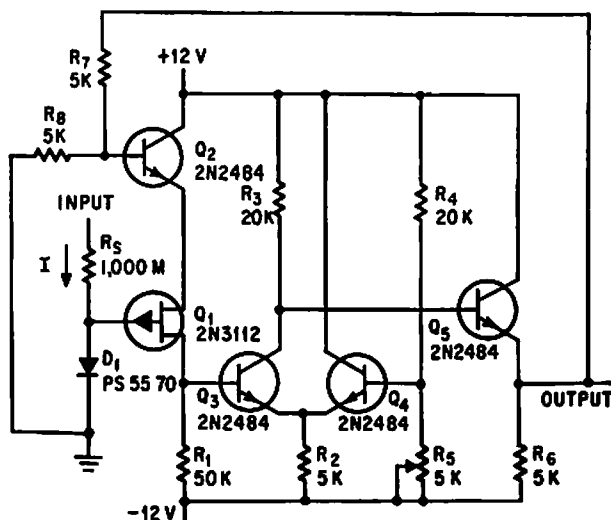
Notes: (1) Input shields are the outer conductors of RG 114/u.
(2) Q₁ and Q₂ are field-effect transistors matched for I_{DSS} between 2 and 6 ma, V_p < 5v, and I_{GSS} < 10na, all within 10%.

FET UNITY-GAIN D-C AMPLIFIER—Each base of 2N2641 dual transistor is driven by source-follower fet's Q1 and Q2. Q1 performs impedance transformation, while Q2 closes feedback loop and tends to cancel changes in parameters due to temperature variations.—L. J. Savin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 102.

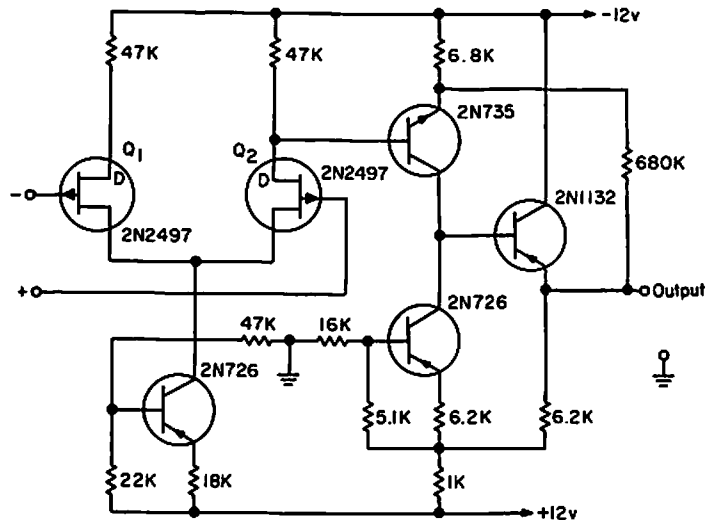




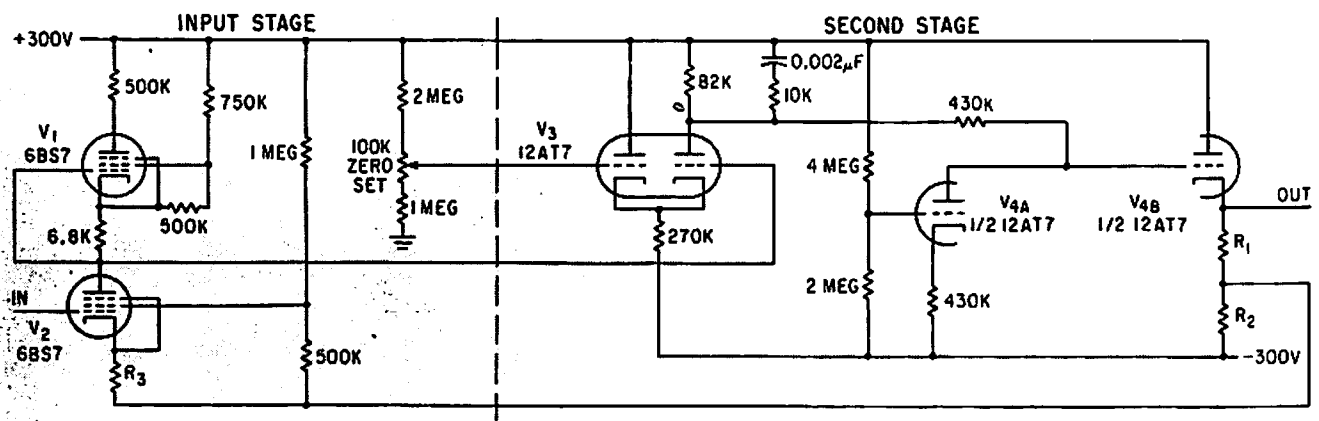
LOW DRIFT WITHOUT CHOPPER—Developed for airborne or missile telemetry. Gain is adjustable. Has wide frequency response, high common-mode rejection factor, and high input impedance, along with gain stability of 0.75% over wide temperature range.—R. D. Middlebrook and A. D. Taylor, *Differential Amplifier with Regulator Achieves High Stability, Low Drift*, *Electronics*, 34:30, p 56-59.



LOGARITHMIC INPUT CURRENT COMPRESSOR—Simple d-c logarithmic amplifier compresses positive input current into logarithmically related input voltage.—G. W. Condel, *D-c Logarithmic Amplifier Compresses Input Current*, *Electronics*, 39:10, p 91-92.



FET OPERATIONAL AMPLIFIER—Open-loop voltage gain at direct current is above 100,000. When operated open-loop, makes excellent voltage comparator having high resolution.—L. J. Sevin, Jr., *"Field-Effect Transistors,"* McGraw-Hill, N.Y., 1965, p 107.

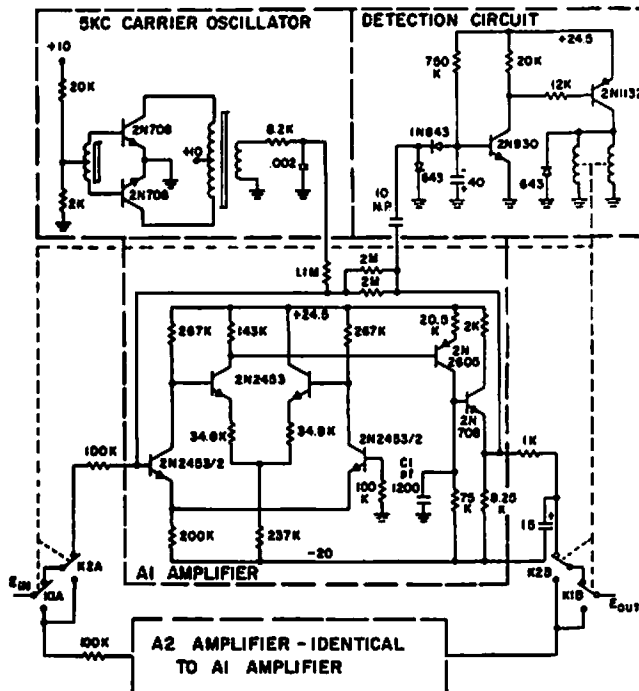
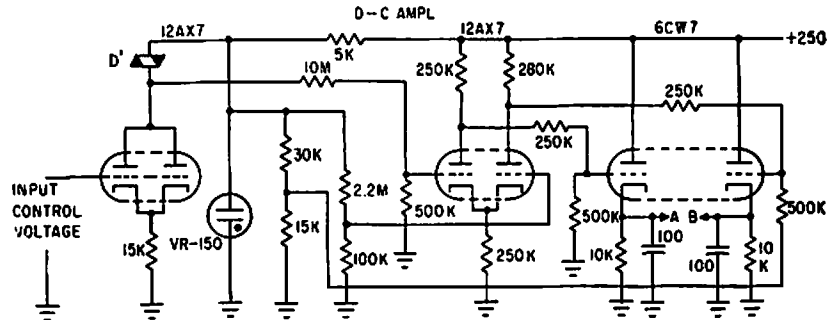


ULTRAHIGH-IMPEDANCE AMPLIFIER—Bridge-balanced series-compensated d-c amplifier using ordinary vacuum tubes gives input im-

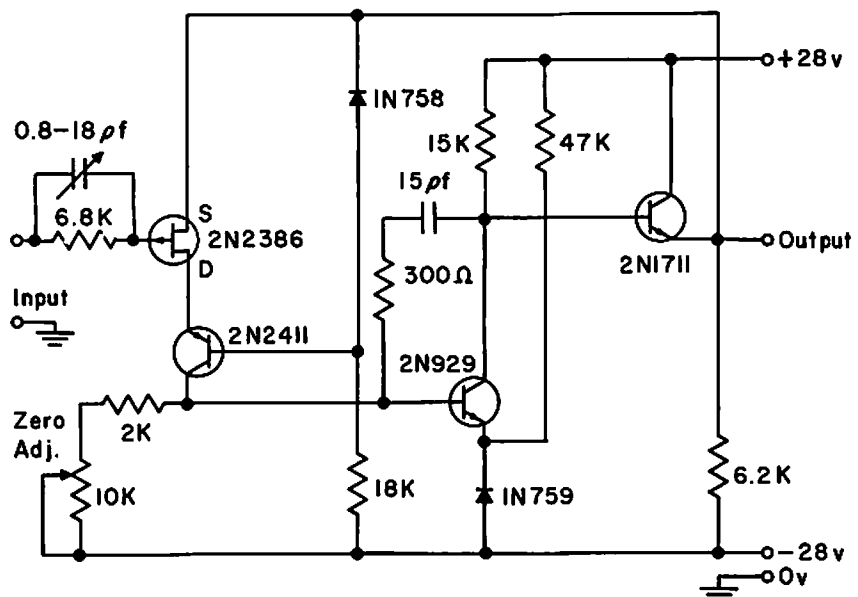
pedance of 250,000 meg, for precise voltage measurements without loading high-impedance circuits.—J. Morrison, *For Precise Meas-*

urements An Ultrahigh Impedance Amplifier, *Electronics*, 35:40, p 49.

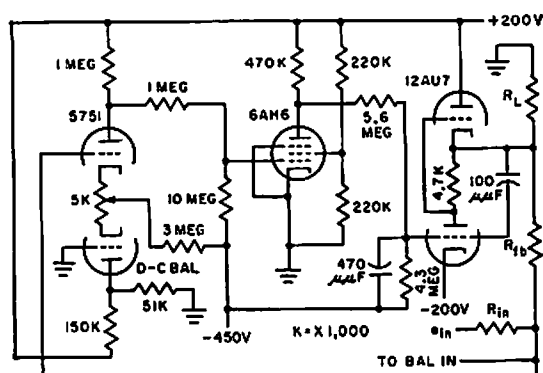
—Input d-c control voltage required by SIC varistors of voltage-tuned oscillator is boosted by d-c amplifier stages that produce two control voltages (at A and B) for SIC varistors of phase-shift oscillator circuit, changing their a-c resistance and thereby oscillator frequency.—M. Uno, Varistor Network Controls Voltage-Tuned Oscillator, *Electronics*, 34:30, p 44-47.



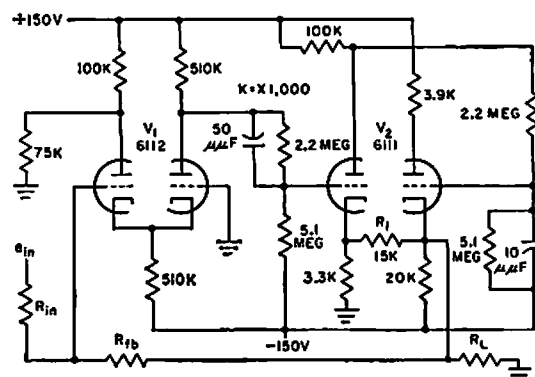
ACTIVE REDUNDANT D-C AMPLIFIER—Test carrier signal is added to input of normal amplifier A1. When failure occurs in A1, detector senses absence of test carrier and causes relay K to change over to amplifier A2. Both amplifiers are differential type, having open-loop gain of 1,000 and closed-loop gain of 10. Open-loop response is flat to about 2 kc, and closed-loop response extends beyond 5 kc.—T. B. Hooker, Designing Redundant Analog Amplifiers, *EEE*, 13:2, p 55-59.



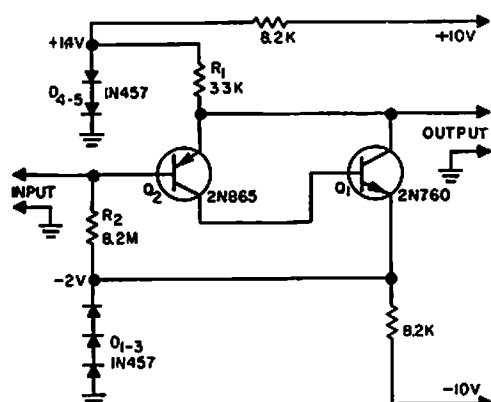
HIGH-INPUT-Z UNITY-GAIN—Source-output connection provides feedback. Cascade input stage has high gain, as required for good amplifier gain accuracy. Upper cutoff is 5,000 kc for 10,000-ohm generator resistance.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 141.



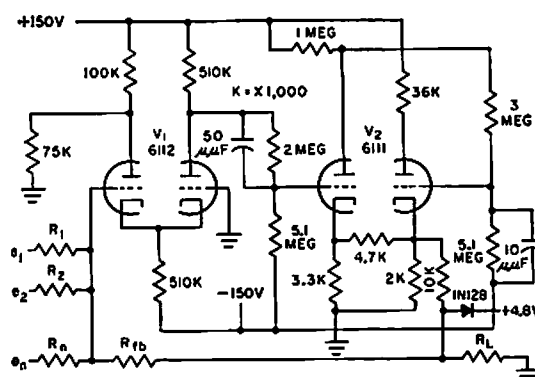
OPERATIONAL D-C AMPLIFIER—Ideal for control systems and analog computers because of broad passband and large control-system response characteristic. Open-loop gain above 15,000. Output swings 100 v into 20,000-ohm load. Can be used from d-c to 20 kc at unity closed-loop gain.—L. S. Klivans, *D-C Amplifiers for Control Systems, Electronics*, 31:47, p 96-100.



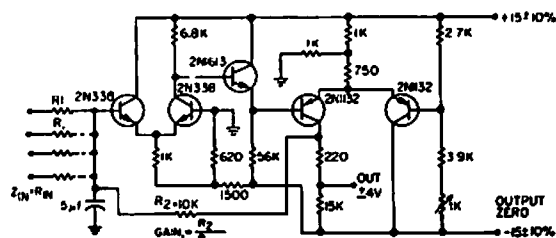
POWER-SUMMING D-C AMPLIFIER—Open-loop gain is 2,000 and maximum voltage swing is 10 v into 2,000-ohm load. Maximum closed-loop gain should be 50 for good stability. Used for straight resistance summing of several input signals.—L. S. Klivans, *D-C Amplifiers for Control Systems, Electronics*, 31:47, p 96-100.



STARVED D-C AMPLIFIER—Serves as wide-band input stage for chopper-stabilized amplifier that resolves 10 microvolts. Bandwidth is 100 kc, equivalent input noise is less than 10 microvolts rms, and input current is only 8 nanoamperes.—Starved DC Amplifier Has Low Noise, High Z, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 108.



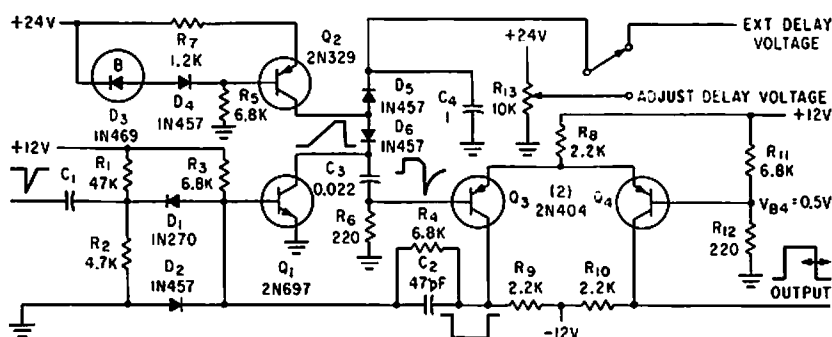
TELEMETRY SUMMING AMPLIFIER—Uses differential dual-triode first stage, voltage amplifier, and current and voltage-limited cathode follower to give output swing of 5 v above and below 0. Open-loop gain of 2,000 is obtained with positive feedback in last two stages. Used to isolate transducers and to amplify d-c or low-frequency signals in airborne or ground-based telemetry systems.—L. S. Klivans, *D-C Amplifiers for Control Systems, Electronics*, 31:47, p 96-100.



CURRENT-SUMMING OPERATIONAL AMPLIFIER—Uses current-summing to hold voltage at input node R1 at 0 v. With more than one input, there is exact summation of inputs, with no interaction. As sine-wave amplifier, gain is 1,000, and bandwidth for transistors shown is d-c to 20 kc.—C. J. Ulrich, *Minimum-Interaction Summing Amplifier, EEE*, 12:2, p 30.

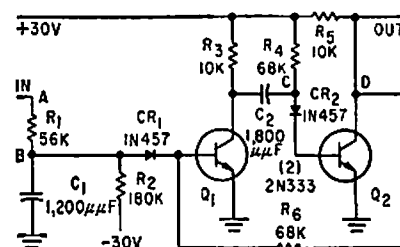
CHAPTER 24

Delay Circuits

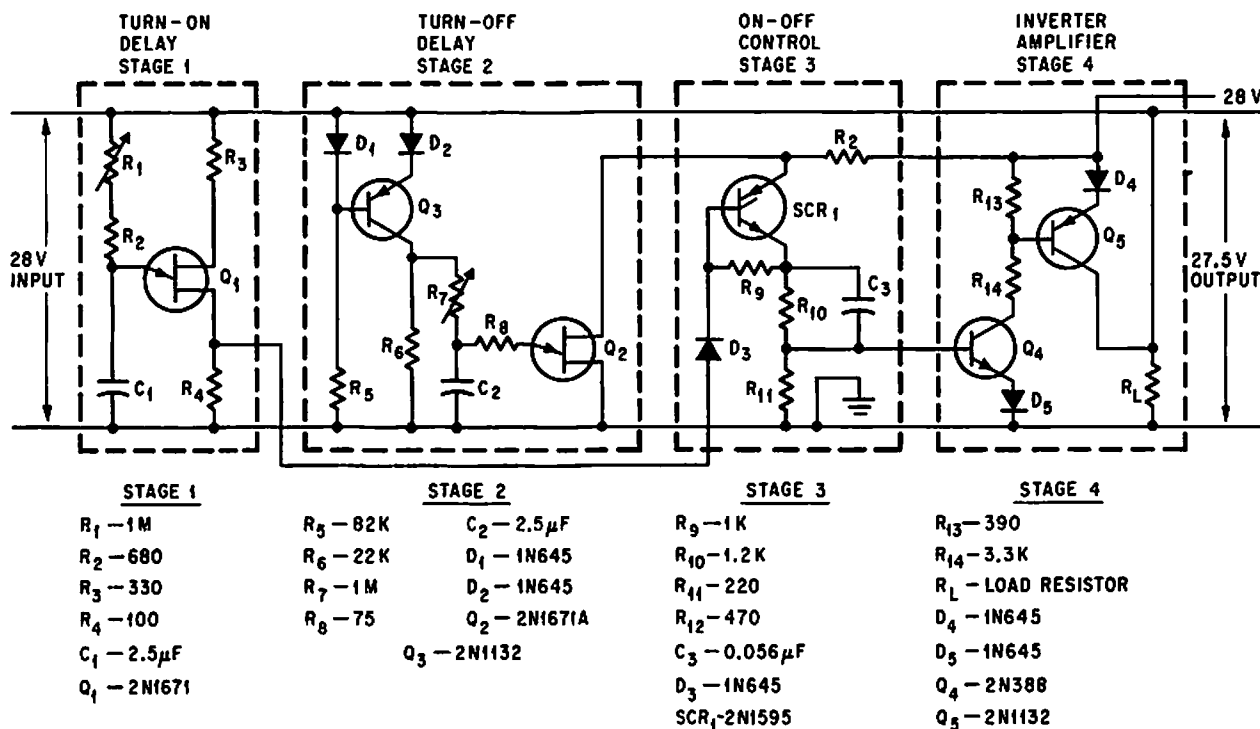


VOLTAGE-CONTROLLED DELAY GENERATOR
—Accuracy is 0.7%, with high stability. Used in radar range tracker, which requires accurate voltage analog of time between out-

going pulse and incoming echo.—C. K. Friend and S. Udalov, *Stabilized Delay Circuit Provides High Accuracy*, *Electronics*, 34:15, p 78-80.



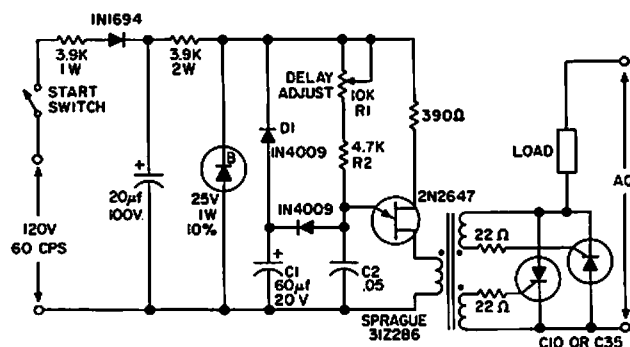
DUAL DELAY—Two-transistor circuit produces pulses of finite width that start finite time after reference pulse. Initial delay is determined by R1-R2-C1 and pulse width by C2-R4.—H. P. Brockman, *Circuit Provides Dual Delay*, *Electronics*, 32:18, p 62-65.



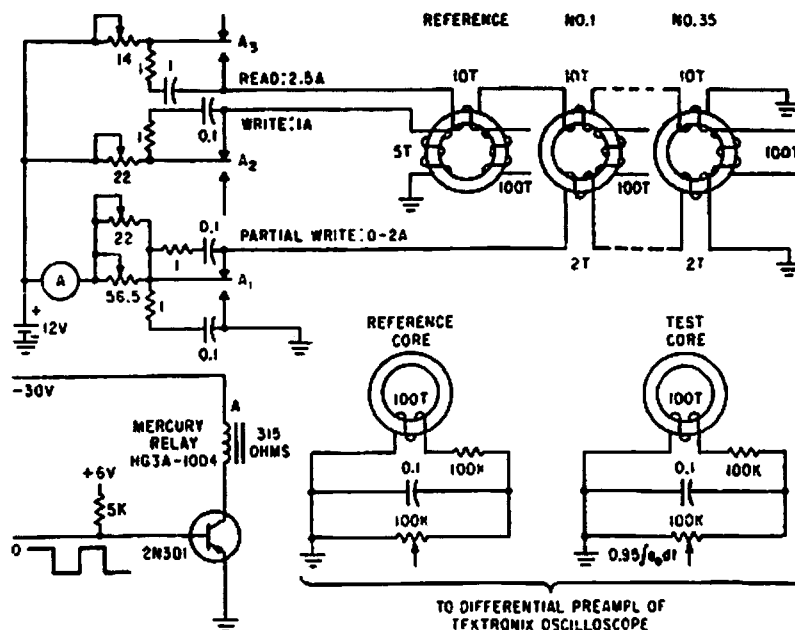
VARIABLE TURN-ON AND TURN-OFF—Both input and output delays are variable from

1.5 to 1,500 millise. Maximum turn-on delay cannot exceed duration of input pulse.

—C. R. Mora, *Delay Circuit Varies Turn-on, Turn-off*, *Electronics*, 39:7, p 92-93.

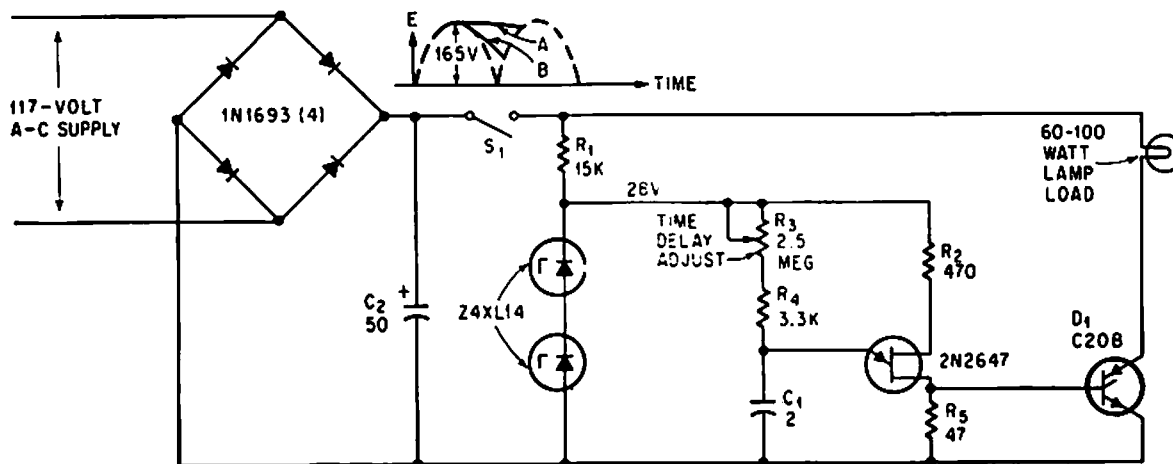


SOLID-STATE DELAY WITH A-C OUTPUT—Timing sequence is initiated by switch, which applies power to ujt circuit. When emitter voltage of ujt reaches peak point, C1 remains charged and ujt oscillates at high frequency. Resulting pulses turn on scr's through pulse transformer, applying voltage to load.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 322.



VARIABLE DELAY FOR ANALOG SIMULATION
—Uses thick-walled ferrite cores to store voltage levels as flux levels.—W. C. Till and

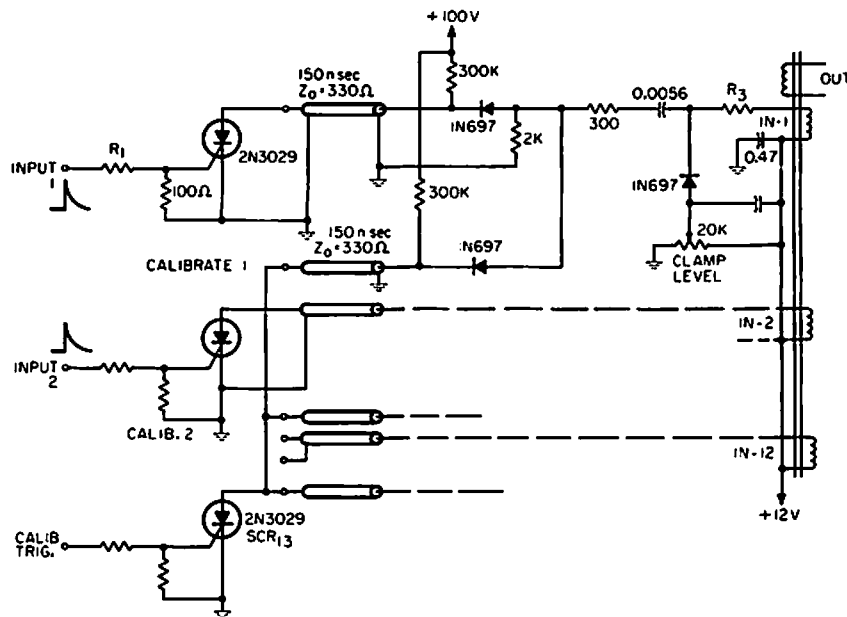
**W. H. Ko, Versatile Analog Storage uses Fer-
rite Cores, *Electronics*, 35:39, p 60-63.**



LINE-OPERATED DELAY—Operates directly from 117 v a-c line to provide time delays

adjustable from 8 millisec to 5 sec for lamp loads up to 100 w.—D. V. Jones, Quick-On-

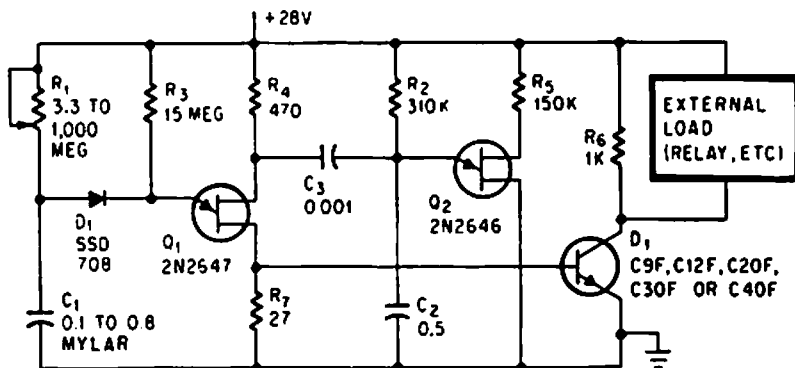
The-Trigger Design, *Electronics*, 38:12, p 105-110.



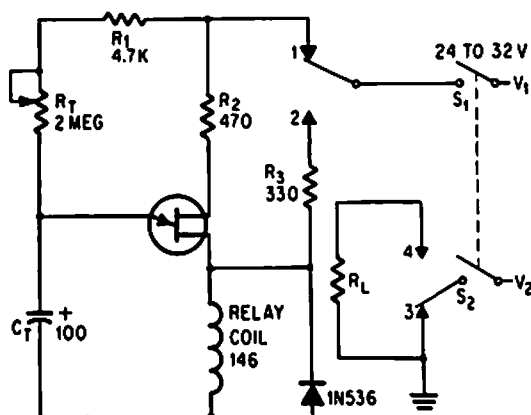
MEASURING NEARLY SIMULTANEOUS EVENTS
—Used for measuring 12 events that can be as close together as 20 nsec and as far

apart as 200 nsec. Twelve identical circuits, one for each trigger, drive magnetostrictive delay line for serializing events.—R. P. Rufre,

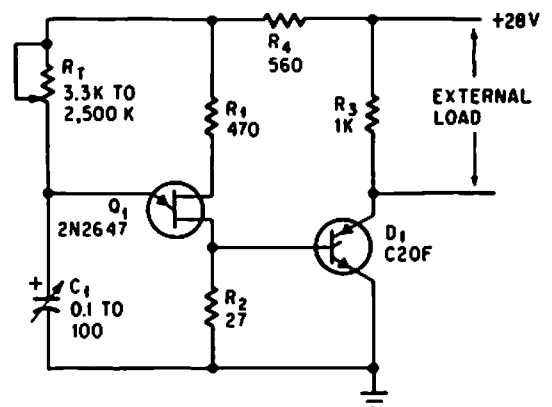
How to Measure Simultaneous Events with Magnetostrictive Delay Lines, *EEE*, 14:5, p 44-49.



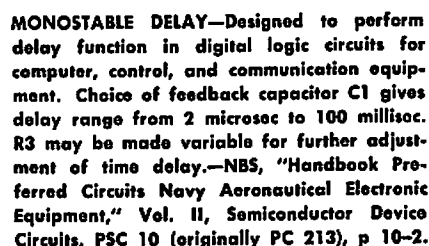
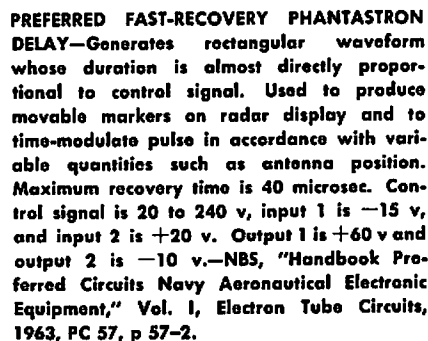
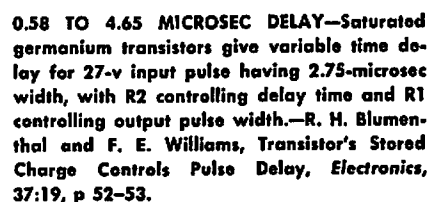
LONG DELAYS—Delays up to 2 hours are obtained, using unijunction transistor Q1 as trigger for scr and Q2 as free-running oscillator. Only 2 na through timing resistor R1 will provide triggering.—D. V. Jones, Quick-On-The-Trigger Design, *Electronics*, 38:12, p 105-110.

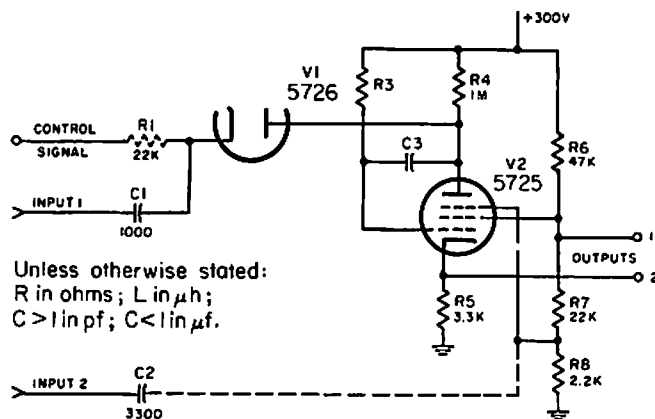


RELAY DELAY—Unijunction transistor is used to delay operation of relay from 0.5 sec to 3 minutes. CT-RT determine delay interval.—D. V. Jones, Quick-On-The-Trigger Design, *Electronics*, 38:12, p 105-110.



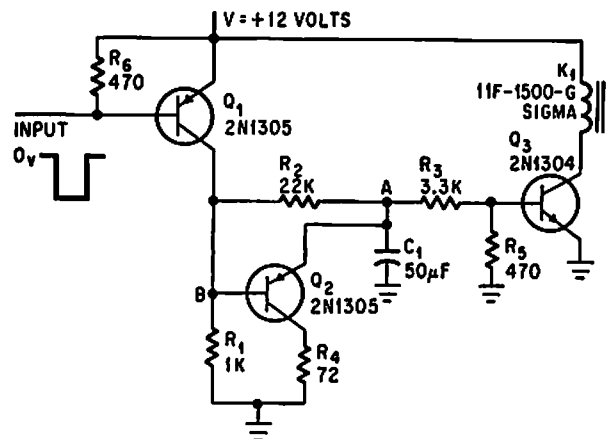
SCR TIME DELAY—Unijunction transistor Q1 and low-cost scr D1 give time delay of 0.4 millisecc to 4 minutes, adjustable by CT-RT.—D. V. Jones, Quick-On-The-Trigger Design, *Electronics*, 38:12, p 105-110.





Unless otherwise stated:
R in ohms; L in μ h;
C > 1 in pf; C < 1 in μ f.

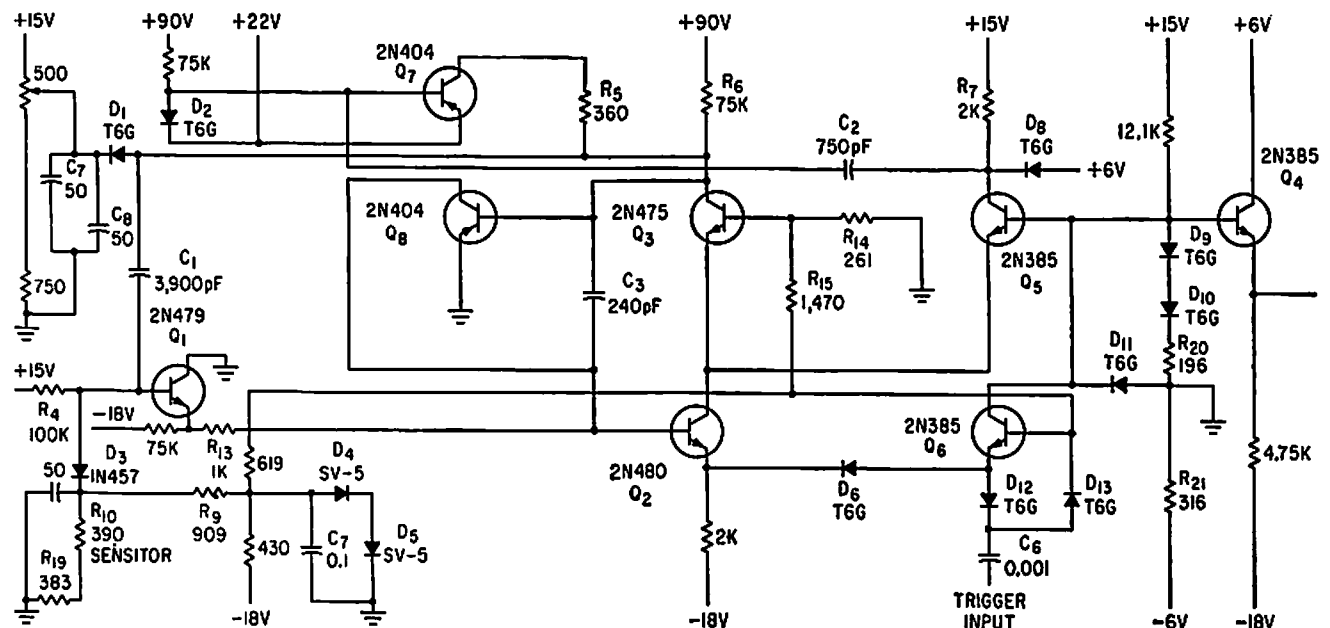
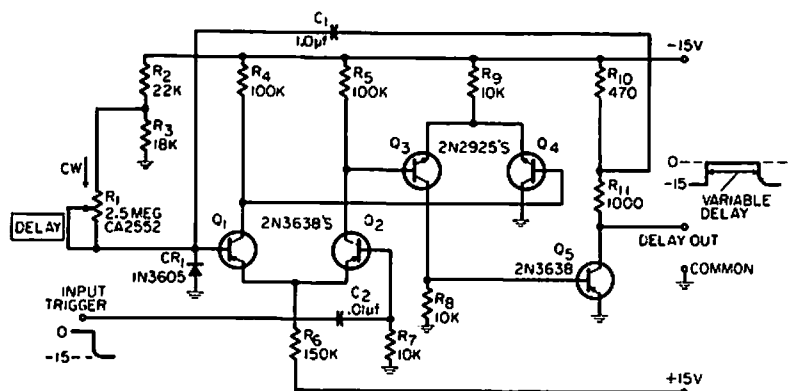
PREFERRED PHANTASTRON DELAY—Accepts 20 to 280 v control signal, 15-v negative pulse at input 1, and 20-v positive pulse at input 2. Output is +60 v, and output is -10 v. R3 can be 1 to 3.3 meg and C3 100 to 1,200 pf, depending on minimum and maximum duration desired.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 56, p 56-2.



SLOW-MAKE, FAST-BREAK ($\tau = 0.1$ SEC)

SLOW-MAKE RELAY—Circuit energizes relay with delay controlled by R2-C1 after driving pulse is applied. Relay de-energizes the instant driving pulse is removed.—P. Haas, Timing Circuits Control Relays, *Electronics*, 38:6, p 85.

6 MSEC TO 1.5 SEC—Provides continuously variable delay having good settability over entire range due to use of single log-tapered pot R1.—S. G. Freshour, Wide-Range Variable Delay Circuit, *EEE*, 14:3, p 62.

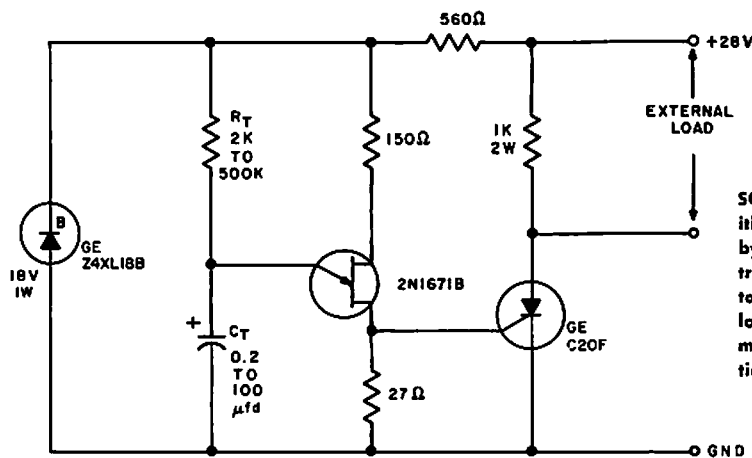
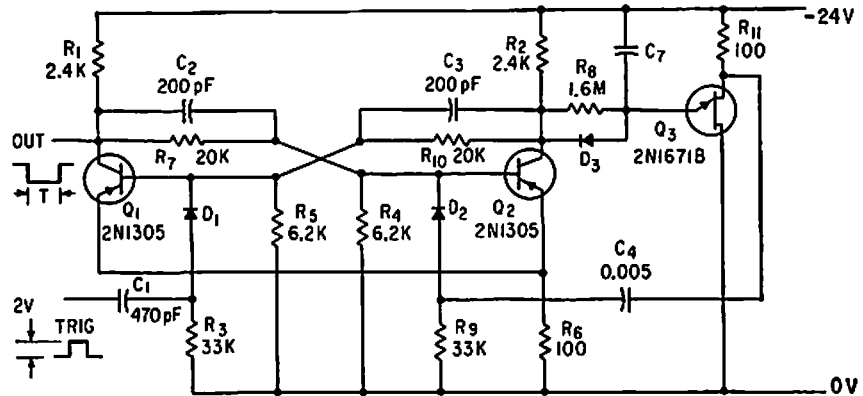


220-MICROSEC PHANTASTRON DELAY—Timing accuracy of 1% is achieved by adding

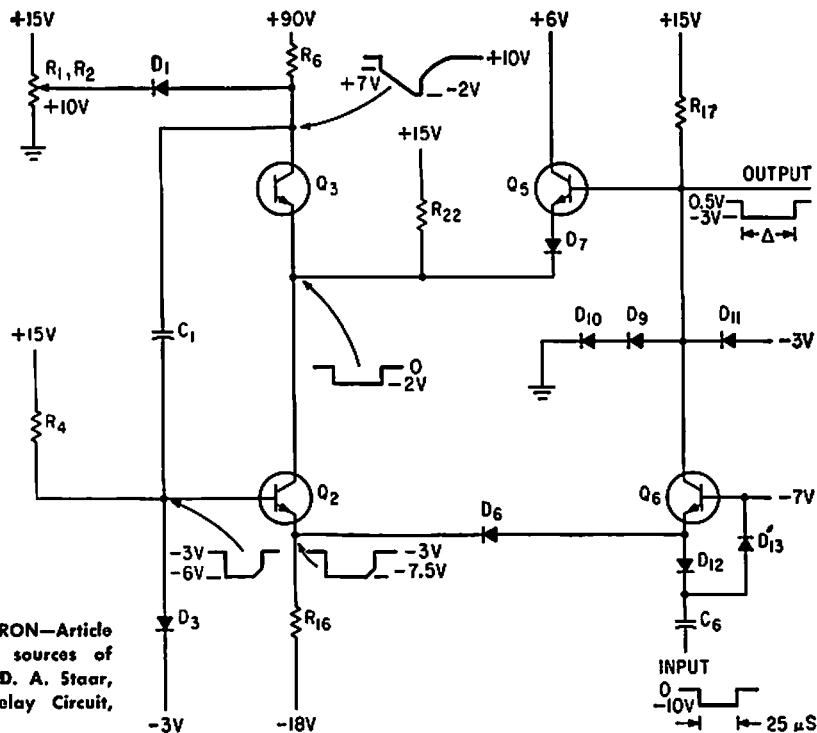
temperature-correcting features to basic solid-state circuit.—S. R. Parris and D. A. Starr,

Highly Accurate Phantatron Delay Circuit, *Electronics*, 33:43, p 72-74.

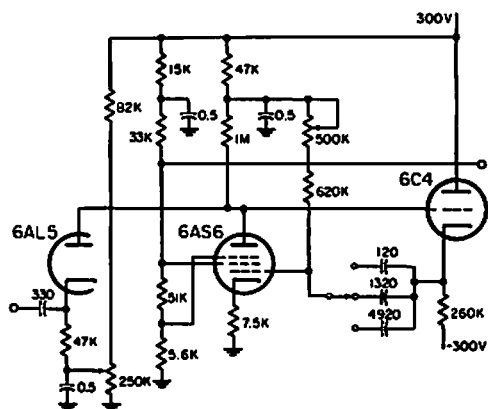
DELAY MULTIPLIER—Addition of silicon unijunction transistor Q3 to conventional monostable mvbr expands time delay two orders of magnitude.—M. P. Humblet, Unijunction Transistor Multiplies Monostable's Pulsewidth, *Electronics*, 35:26, p 74-75.



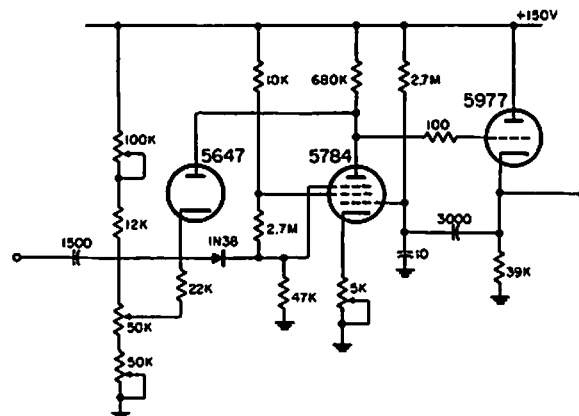
SCR-UJT TIME DELAY—Timing interval is initiated by applying power, and is determined by $R_T C_T$. At end of interval, unijunction transistor triggers silicon controlled rectifier, to apply essentially full supply voltage to load. Delay range is from 0.4 millisecc to 1 minute.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 321.



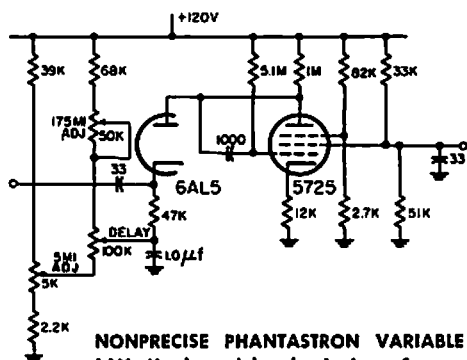
BASIC 4-TRANSISTOR PHANTASTRON—Article traces operation and analyzes sources of timing errors.—S. R. Parris and D. A. Staar, Highly Accurate Phantastron Delay Circuit, *Electronics*, 33:43, p 72-74.



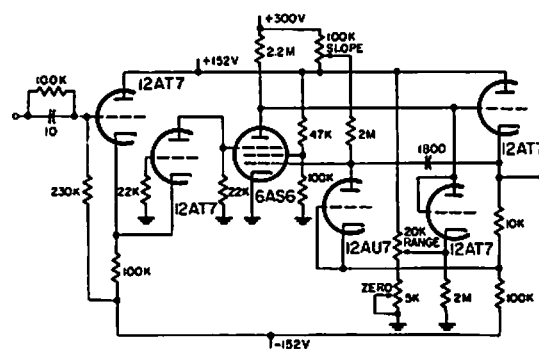
NONPRECISE PHANTASTRON VARIABLE DELAY—Used to delay beginning of crt sweep for expanded display. Provides three ranges: 0 to 5, 60, and 200 miles. Circuit is basically cathode-coupled phantatron, with additional coupling by returning suppressor and screen to same divider. Cathode follower reduces recovery time and provides low-impedance point for range switching.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N9-3.



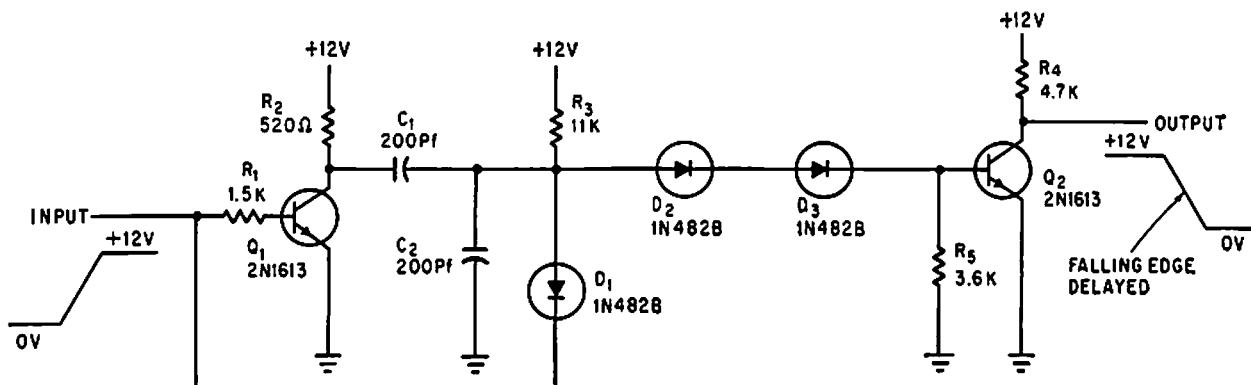
NONPRECISE PHANTASTRON VARIABLE DELAY—Is triggered by positive pulse on suppressor instead of negative pulse on plate. Crystal diode prevents interruption of phantatron operation by trailing edge of trigger. Used to delay beginning of crt sweep for expanded display. Delay range is 0 to 190 miles.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N9-3.



NONPRECISE PHANTASTRON VARIABLE DELAY—Used to delay beginning of crt sweep for expanded display. Range is 5 to 175 miles. Trailing edge of screen waveform is differentiated and used to trigger blocking oscillator through trigger amplifier.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N9-3.



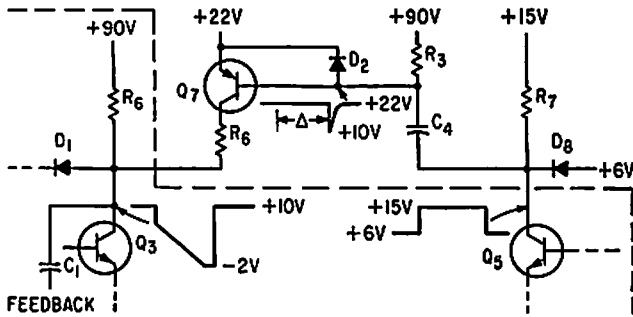
0 to 2,440-MICROSEC PHANTASTRON DELAY—Is triggered by gating mvbr. Receives control voltage from ten-turn potentiometer calibrated in distance units. Output drives blocking oscillator through transformer. Accuracy is about 1% of delay setting.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N9-2.



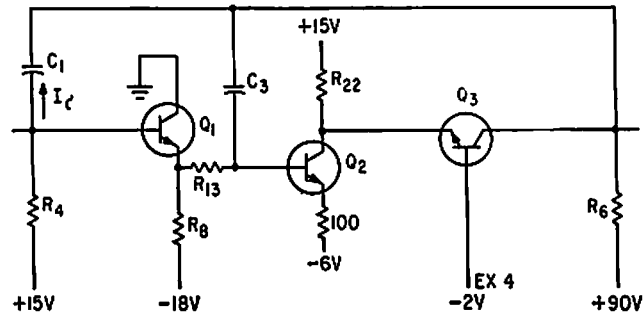
FAST RECOVERY—Addition of C2 to basic circuit reduced recovery time from 2 microsec

to 0.5 microsec. Used in computers.—L. C. Radzik and J. J. Curtis, Adding A Com-

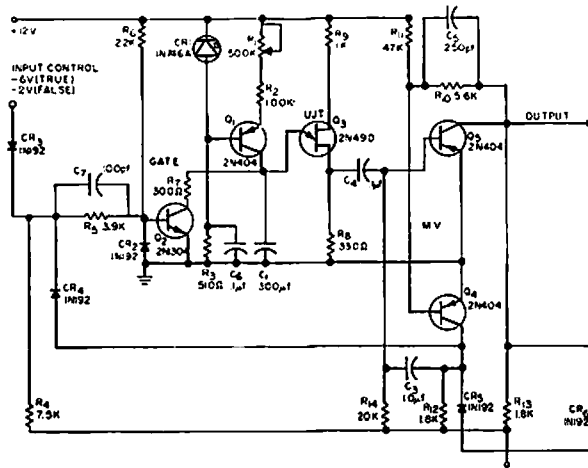
ponent Reduces Recovery Time, *Electronics*, 38:2, p 78-79.



FAST-RECHARGING PHANTASTRON—Recharging of solid-state phantatron delay capacitor is reduced to 5 microsec by addition of Q7. Circuit of Q7 is inoperative during all other parts of cycle, including quiescent state.—S. R. Parris and D. A. Staar, Highly Accurate Phantatron Delay Circuit, *Electronics*, 33:43, p 72-74.

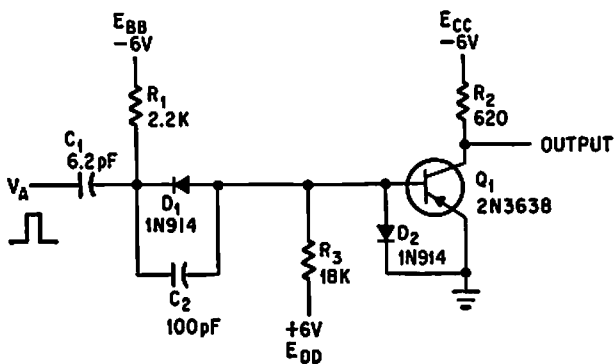


MODIFIED PHANTASTRON INTEGRATOR—Addition of input emitter-follower Q1 increases open-loop current gain of integrator and provides increased accuracy and linearity.—S. R. Parris and D. A. Staar, Highly Accurate Phantatron Delay Circuit, *Electronics*, 33:43, p 72-74.

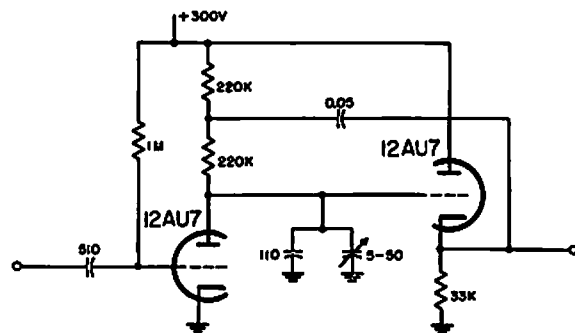


FOUR-MINUTE DELAY—Ujt switch gives accuracy of 1% for time delays in range of 1 to 4 minutes, over range of 10°C above and

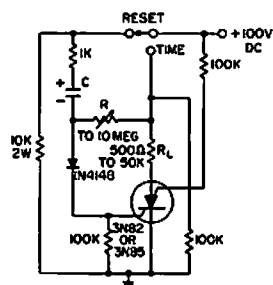
below 25°C. R1 controls amount of delay.—E. G. McCoy, Accurate Time Delays up to Four Minutes, *EEE*, 11:10, p 31.



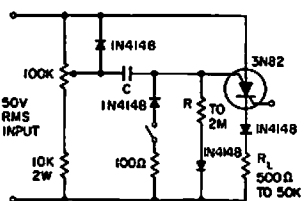
HIGH-VOLTAGE PULSE DELAY—Diode input network isolates base of Q1 from input when input pulse voltages exceed maximum rated emitter-to-base reverse voltage of input-inverting digital pulse delay circuit.—R. A. Karlin, One-Transistor Multi Delays Digital Pulses, *Electronics*, 38:17, p 85-86.



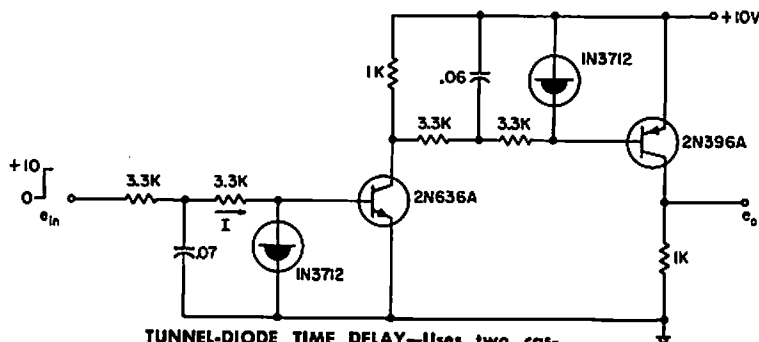
FIXED BOOTSTRAP DELAY—Used to provide buffer interval between sync and video information in radar relay transmitter. Requires gate at least as long as 30-microsec delay. Accuracy is only 10%.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N9-1.



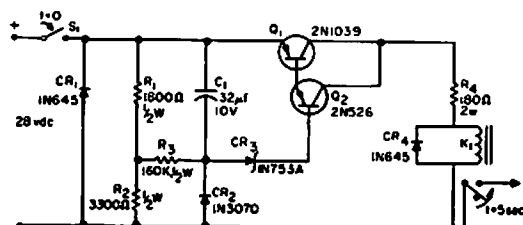
LOAD CURRENT DELAY—Silicon controlled switch circuit delays start of load current for interval of $0.5 RC$ after switch is thrown.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 435.



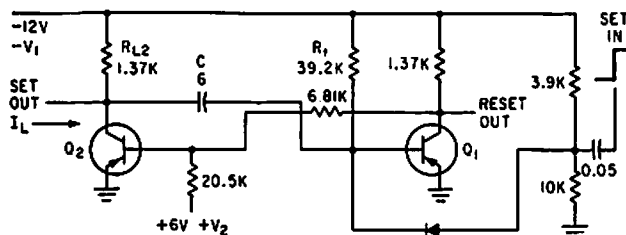
A-C OPERATED TIME DELAY—Switch is normally closed, charging C and blocking scs . Delay is initiated by opening switch. After delay interval, determined by R , C , and potentiometer, silicon controlled switch conducts on alternate half-cycles.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 435.



TUNNEL-DIODE TIME DELAY—Uses two cascaded complementary stages in which tunnel diodes are used with transistors. Takes $+10 v$ input pulse.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 369.

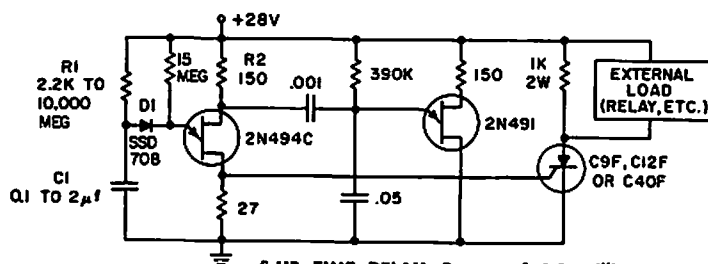


FAST-RECYCLING TIME-DELAY RELAY—Uses 28-v relay to give delay of 5 sec, with recycle time of 200 millise. —R. W. Eubank, Fast Cycling Time-Delay Relay, EEE, 12:3, p 71.

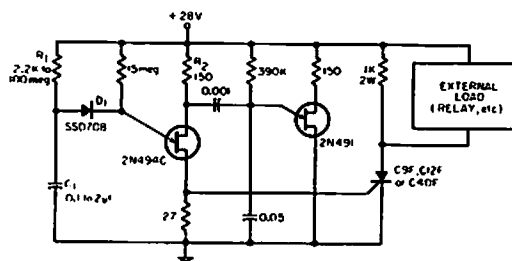


NOM. TIME = 158mSEC
TRANSISTOR $\beta \geq 80$
25°C $I_{CO} \leq 2.5 \mu A$

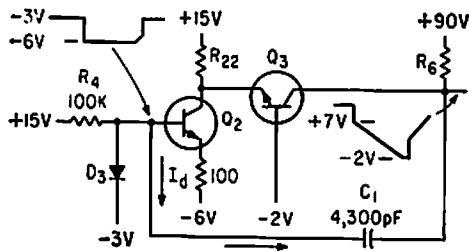
CONVENTIONAL MONO MVBR—Requires bulky capacitors and large timing resistors to get accurate delay times of 1 to 300 millise. Ideal for laboratory use, but gives problems with mass production.—D. E. Haselwood, Monostable Multivibrators with Stable Delay Times, Electronics, 34:49, p 64-65.



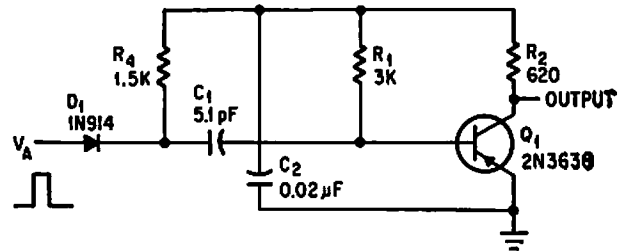
5-HR TIME DELAY—Range of 0.3 millise to 5 hours is obtained without using electrolytic capacitor. At end of timing interval, initiated by applying power and determined by $R1-C1$, 2N494C triggers controlled rectifier.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 322.



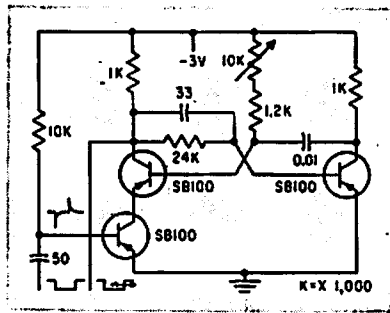
PRECISION SCR DELAY—Gives time delays over 3 minutes without need for tantalum or electrolytic capacitor. Timing capacitor $C1$ can be mylar unit. Application of power initiates timing of power, which is determined by $R1-C1$. At end of interval, 2N494C fires other scr, to place supply voltage across load.—D. V. Jones, Precision Solid-State Delay Circuit, EEE, 11:12, p 26-27.



INTEGRATOR OF SOLID-STATE PHANTASTRON—Q2 and Q3 provide open-loop gain, while R4 and C1 are feedback elements.—S. R. Parris and D. A. Staar, *Highly Accurate Phantatron Delay Circuit*, *Electronics*, 33:43, p 72-74.

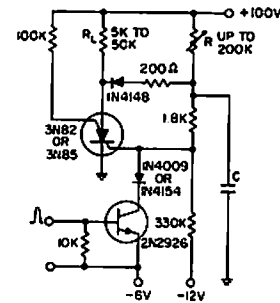


DECOUPLING FOR PULSE DELAY—Coupling circuit ahead of input-inverting digital pulse delay prevents C1 from loading driving collector and decreases noise sensitivity.—R. A. Karlin, *One-Transistor Multi Delays Digital Pulses*, *Electronics*, 38:17, p 85-86.



MONOSTABLE MVBR DELAY—Prevents certain circuits from operating until proper time and generates and shapes required output pulses. Is triggered by positive pulse produced by input differentiating circuit. Circuits are cas-

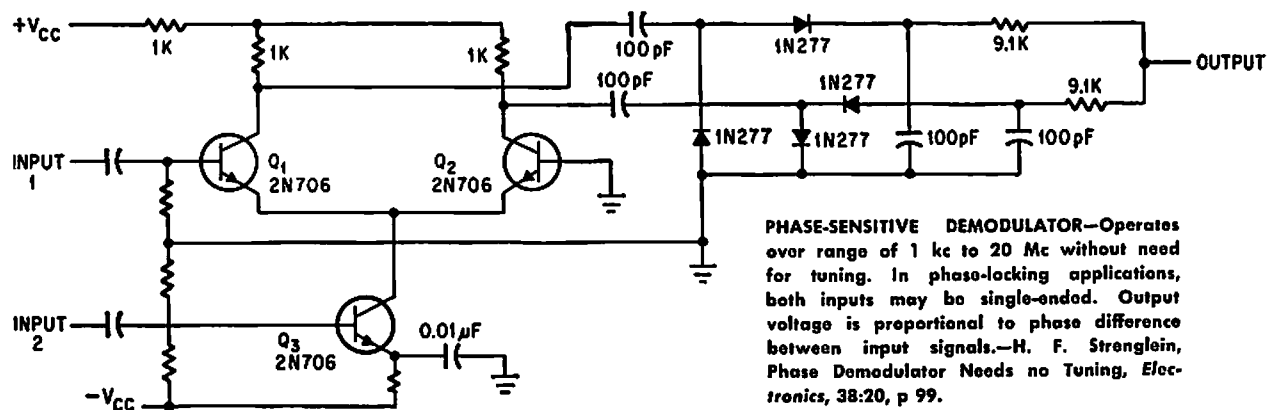
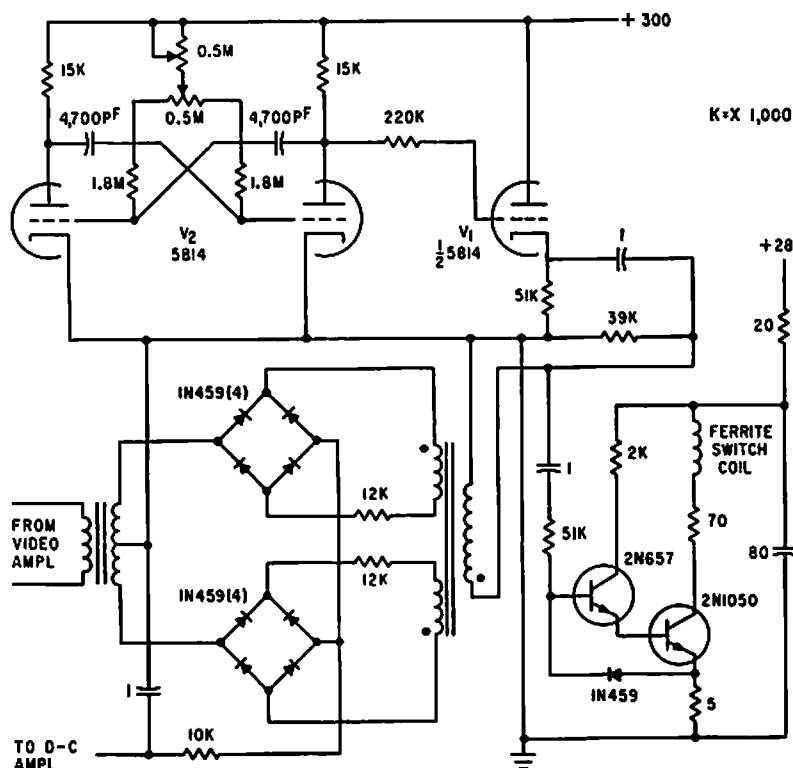
caded, and second stage starts its delay coincident with trailing edge of first delay output pulse.—W. W. Grannemann et al, *Pulse-Height-to-Digital Signal Converter*, *Electronics*, 33:2, p 58-60.

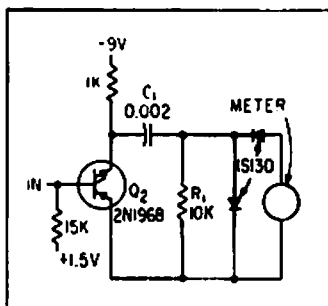


LOAD CURRENT TURNOFF DELAY—Input pulse turns off silicon controlled switch, which triggers after delay of approximately RC.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 435.

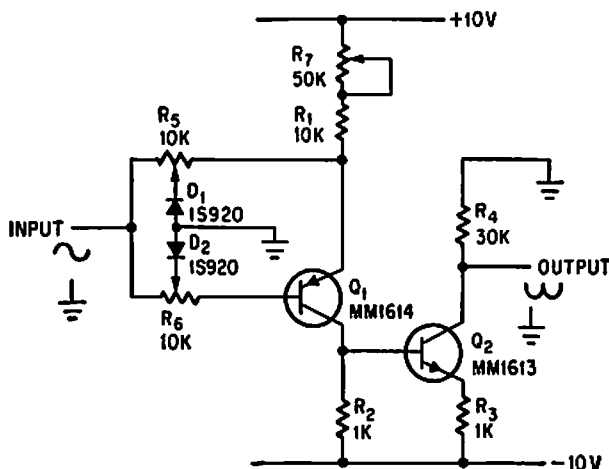
CHAPTER 25

Demodulator Circuits





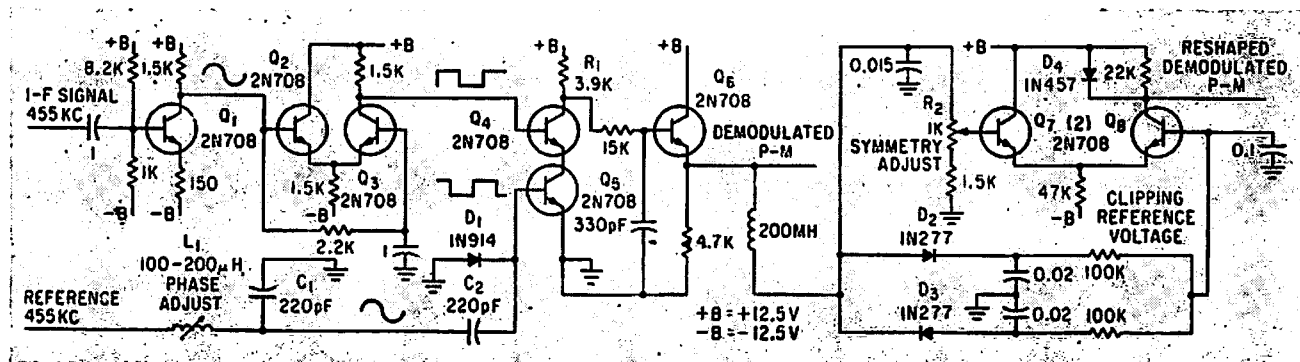
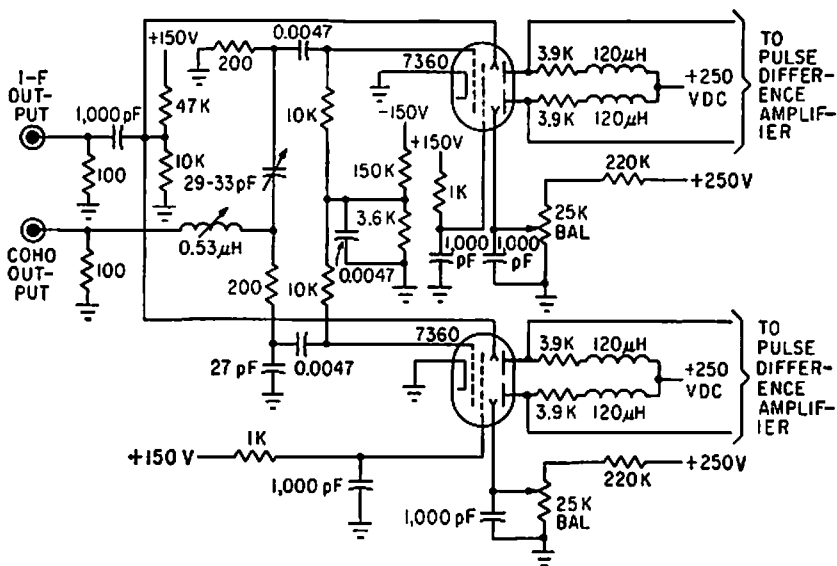
D-C FROM F-M—Mean d-c level, directly proportional to number of pulses per unit time, is read on meter for f-m data recorded on magnetic tape.—K. R. Whittington, Simple F-M Demodulator for Audio Frequencies, *Electronics*, 35:48, p 89.



TRANSFORMERLESS FULL-WAVE DETECTOR—Each half-cycle of input sine wave produces negative half-cycle at output. Both input and output are referenced to ground. Operat-

ing range is from d-c to 10 Mc.—C. Yarker, Full-Wave Detector Without Transformer, *Electronics*, 39:15, p. 100-101.

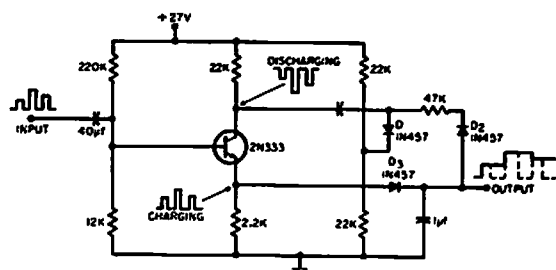
SYNCHRONOUS DEMODULATOR FOR COHERENT PULSE DOPPLER RADAR—C-w output of coherent oscillator is applied to control grid of one beam-deflection tube, and radar receiver i-f output is applied to control grid of other tube in push-pull, so i-f signal and modulation products are in push-pull at the two anodes while c-w signal components are in same phase and are hence cancelled in following pulse difference amplifiers.—J. B. Theiss, More Target Data with Sideband Coherent Radar, *Electronics*, 36:3, p 40-43.



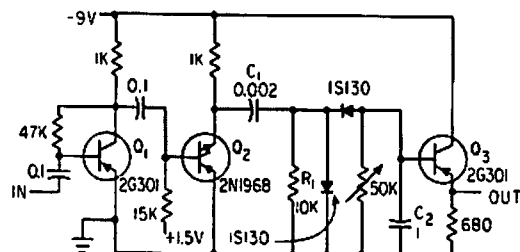
HIGH-LINEARITY PHASE DEMODULATOR—If signal is amplified by Q1, clipped by Q2-Q3, and resulting square wave phase-demodulated in coincidence circuit Q4-Q5 which also

receives similarly clipped 455-kc reference signal. Demodulated output of Q6 is reshaped by clipper Q7-Q8, to give symmetrical output with linearity for deviations up to

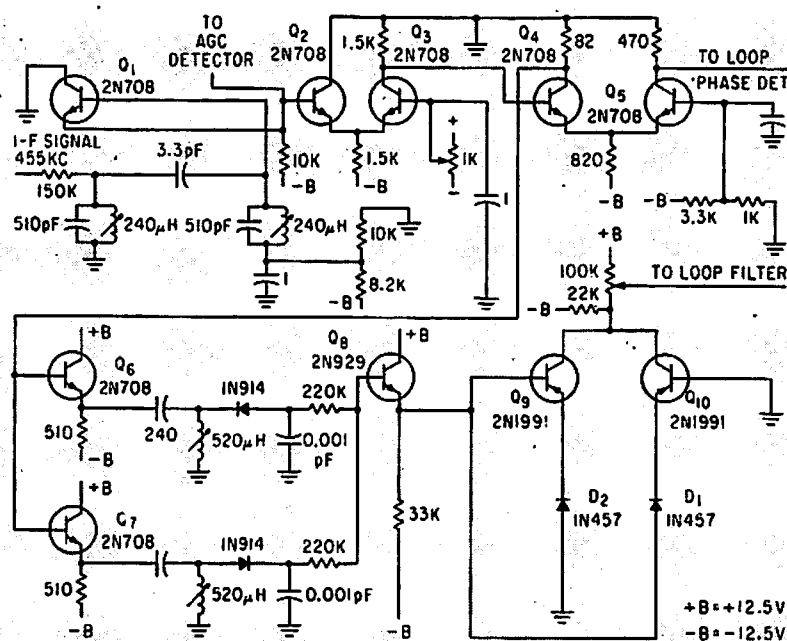
85 deg.—W. H. Casson and C. C. Hall, New Phase-Tracking Demodulator Will Not Lock on Sidebands, *Electronics*, 36:6, p 52-55.



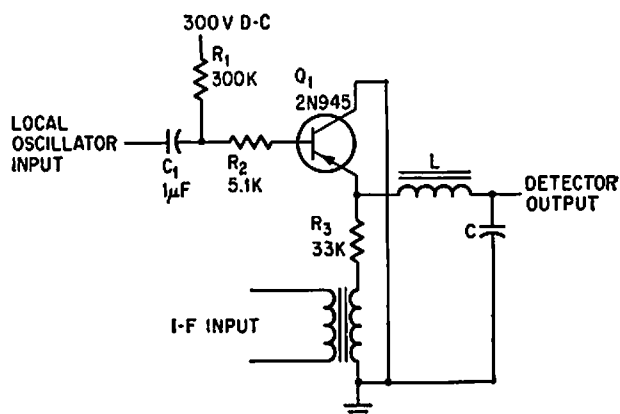
BOXCAR ENVELOPE DETECTOR—Gives accurate recovery of one-polarity modulation envelope by approximating envelope in level steps between successive peaks of wavetrain.—J. L. Markwiler, Boxcar Envelope Detector, *EEE*, 12:9, p 62-63.



SMOOTHED D-C FROM F-M—Mean d-c level, derived from f-m data on magnetic tape, undergoes R-C smoothing in three-transistor pulse-counting demodulator circuit so output can be fed to cro.—K. R. Whittington, Simple F-M Demodulator for Audio Frequencies, *Electronics*, 35:48, p 89.

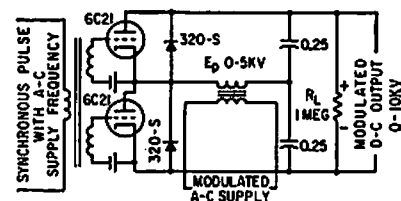


DUAL DETECTORS PREVENT LOCKING ON SIDEBANDS—Antisideband circuit rejects sideband locking while telemetry tracking loop is automatically searching for signals around i-f value. Circuit also provides both p-m and a-m demodulation. Emitter-follower Q1, receiving i-f signal, feeds discriminator Q6-Q7 through limiters Q2-Q3 and Q4-Q5. For 455-kc input, d-c outputs of diode detectors cancel at base of Q8. For lower or higher frequencies, difference voltage serves to apply antisideband error signal to loop filter through Q9 or Q10.—W. H. Casson and C. C. Hall, New Phase-Tracking Demodulator Will Not Lock on Sidebands, *Electronics*, 36:6, p 52-55.



SYNCHRONOUS DETECTOR—Linear detection permits variation of bandwidth after detection in pcm receivers. Also used as a-m detector and for measuring phase and ampli-

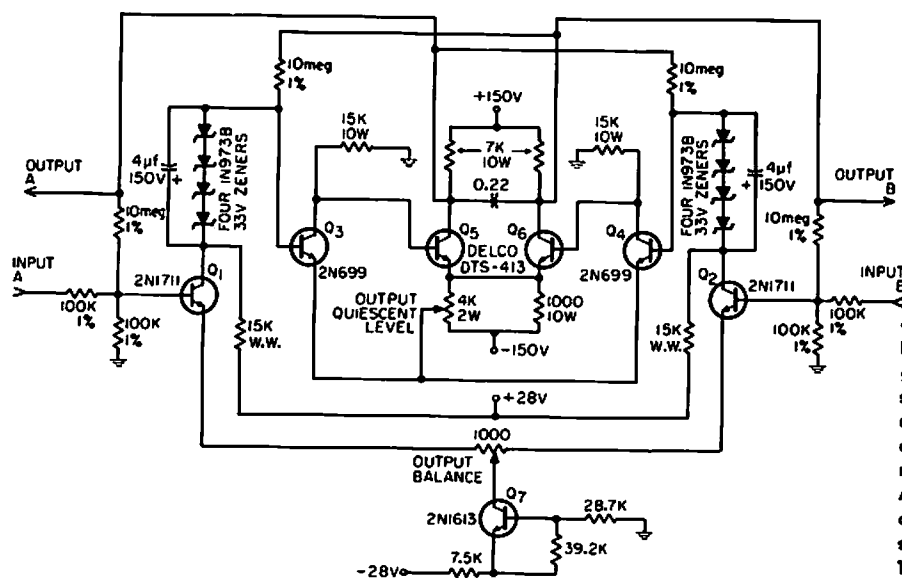
tude of unmodulated signals.—G. S. Parks, Detector Circuit Measures Phase and Amplitude, *Electronics*, 38:12, p 84-85.



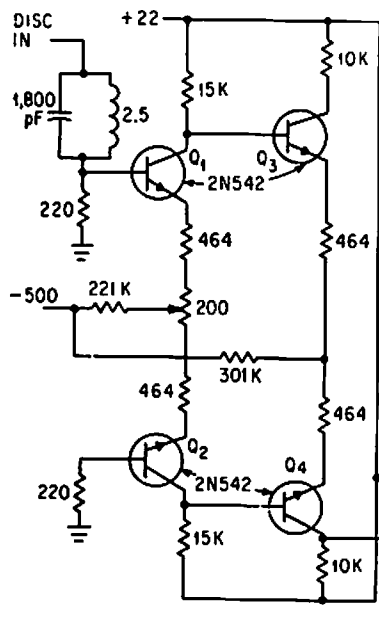
PULSED DEMODULATOR—Used to provide voltage to modulating anode of klystron power amplifier at control rates above 10 cps, in system that controls output power of uhf tropospheric communications links in accordance with received signal at opposite end of link, to compensate for fading.—L. P. Yeh, Loop Controls Scatter Power to Offset Fading, *Electronics*, 32:5, p 60-62.

CHAPTER 26

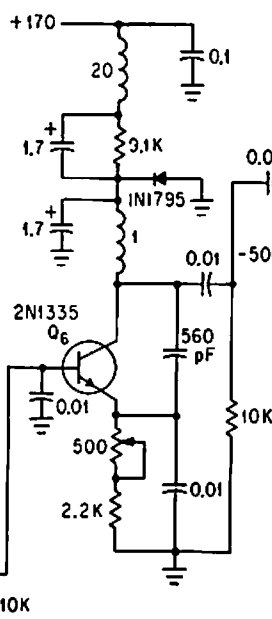
Differential Amplifier Circuits



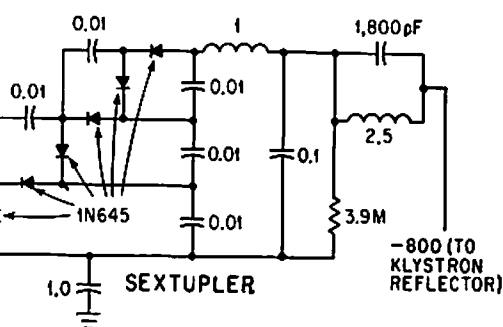
400-V OUTPUT SWING WITH TRANSISTORS—Direct-coupled differential voltage amplifier gives gain of 100 as low-frequency oscilloscope amplifier. Drift is less than 1 v over normal room temperature range. Output quiescent level is 0 v to ground, peak noise level is 3 v, and bandwidth is 5 kc. Amplifier is not damaged by shorted output, overdrive, or supply voltages applied in any sequence.—C. L. Benson, 400-Volt Output Transistor Amplifier, *EEE*, 14:8, p 168.



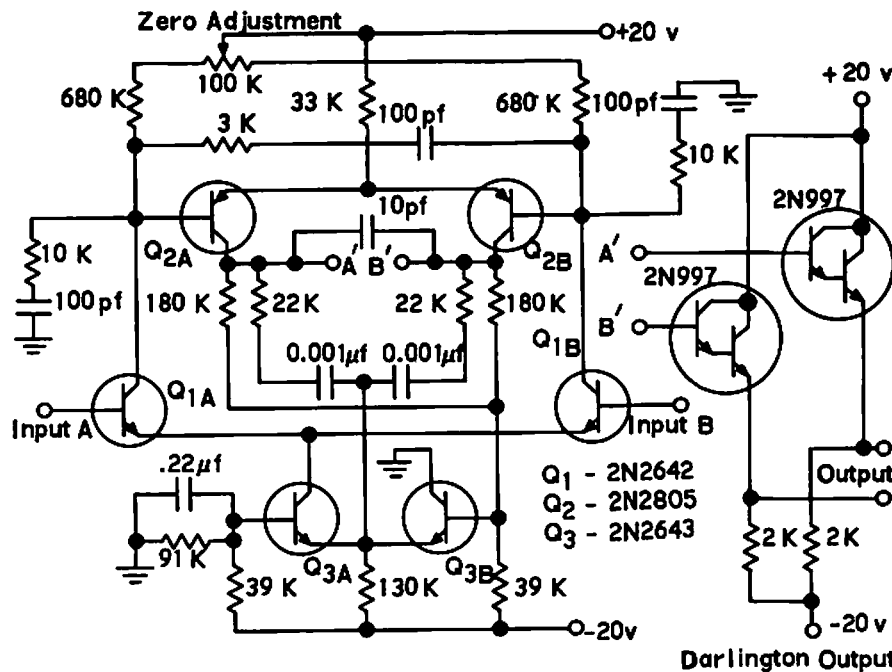
D-C AMPL



230 KC OSC



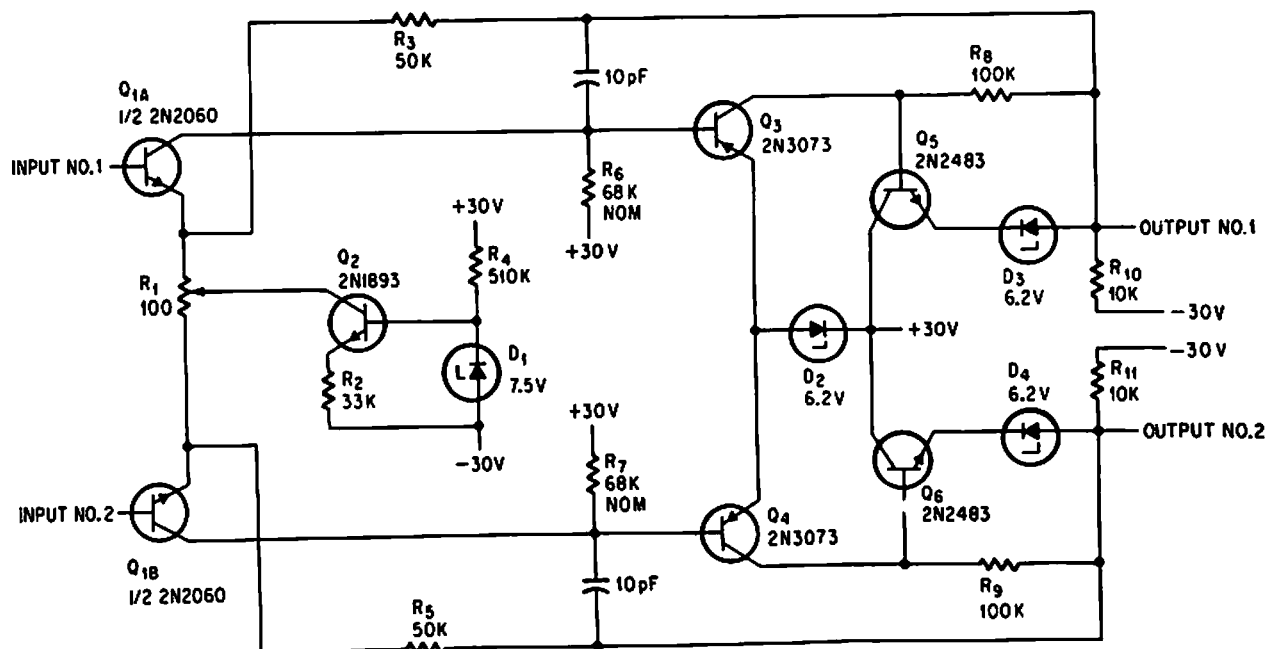
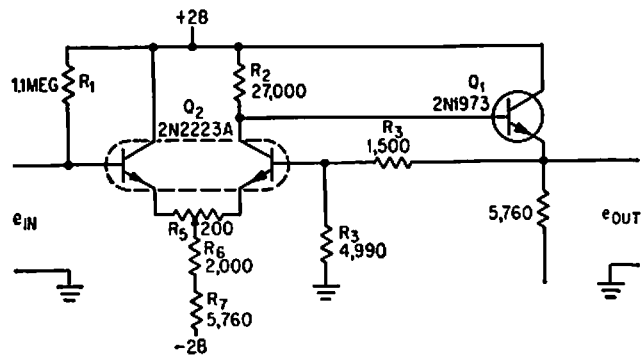
DIFFERENTIAL D-C AMPLIFIER CONTROLS 230-KC C-W RADAR OSCILLATOR—Combined output of two detectors in dual-mode cavity, having typical discriminator S curve, is amplified by four transistors in differential d-c circuit and applied to oscillator through emitter-follower to make output voltage swing up to 20%. Voltage sextupler applies step-up voltage to reflector of klystron, to maintain klystron frequency constant within 0.2 Mc.—H. D. Raynes, C-W Radar Measures Artillery Ballistics, *Electronics*, 37:1, p 31-33.



DIRECT-COUPLED DIFFERENTIAL AMPLIFIER—Designed for general use as complete amplifier with Darlington output stage, or as first two stages of low-drift high-gain amplifier without amplifier stage. Provides both low and high common-mode rejection for either differential or single-ended outputs. High common-mode rejection is achieved by use of common-mode feedback loop. Low drift is achieved by using dual transistor Q3 as first stage of common-mode feedback loop.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 161.

Darlington Output Stage

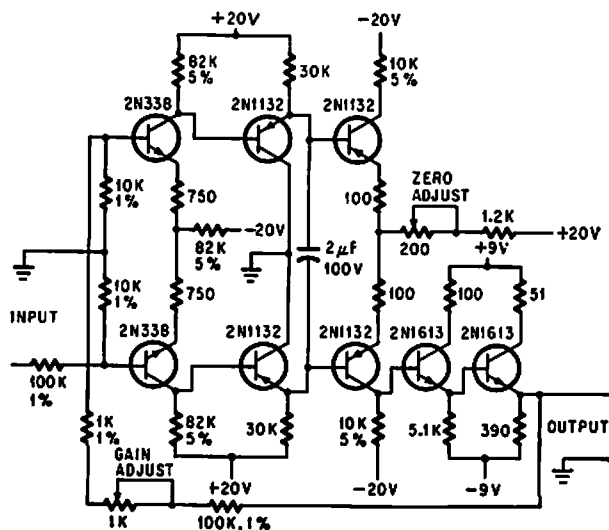
D-C AMPLIFIER SERVES AS VOLTAGE REGULATOR—Output voltage of series pass transistor Q1 is compared to input voltage serving as reference voltage by differential amplifier Q2 and variations are fed back to reduce difference. Feedback ratio of 0.67 gives overall gain of 1.3 and 10-ohm output impedance.—W. S. Zukowsky, Aligning Saturn Missile's Guidance System, *Electronics*, 37:8, p 26-27.



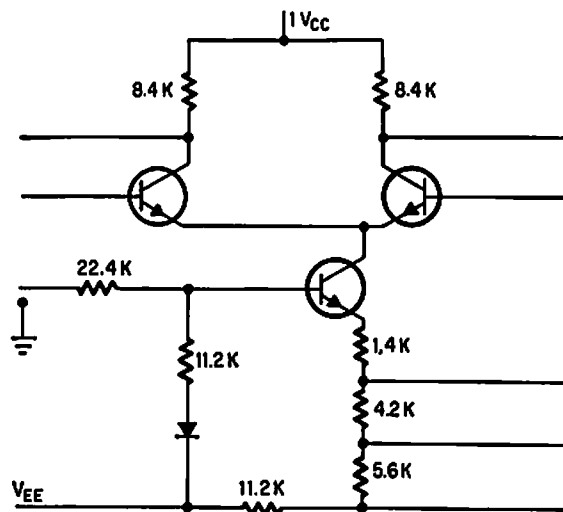
NO-CHOPPER DIFFERENTIAL AMPLIFIER—Stable voltage gain is 1,000. Current source Q2 provides bias for input stage. Amplification

is linear within 10 microvolts over 100°C range.—D. F. Hilbiber, Stable Differential Amplifier Designed Without Choppers, *Electronics*, 38:2, p 73-75.

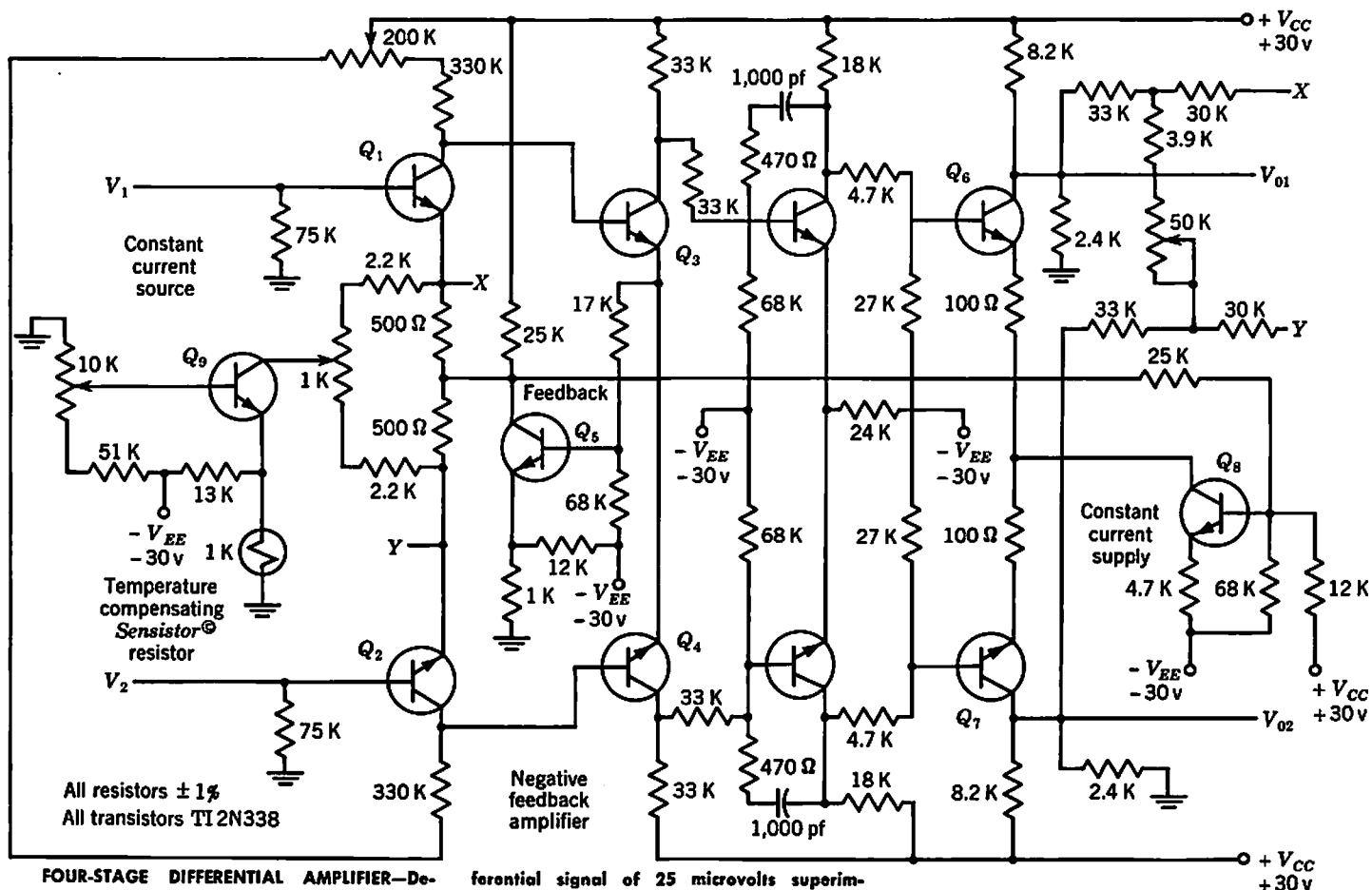
Electronics, 38:2, p 73-75.



UNITY-GAIN DIFFERENTIAL D-C AMPLIFIER—Negative feedback with differential input and single-ended output give gain stability of 1.0000 for output of 1.2 v across 100-ohm load, for use in battery-powered transistor leakage-current tester.—A. T. Ashby, T. R. Shaifer, and H. R. Hogner, *Testing Transistors In-Circuit*, *Electronics*, 37:17, p 53-56.



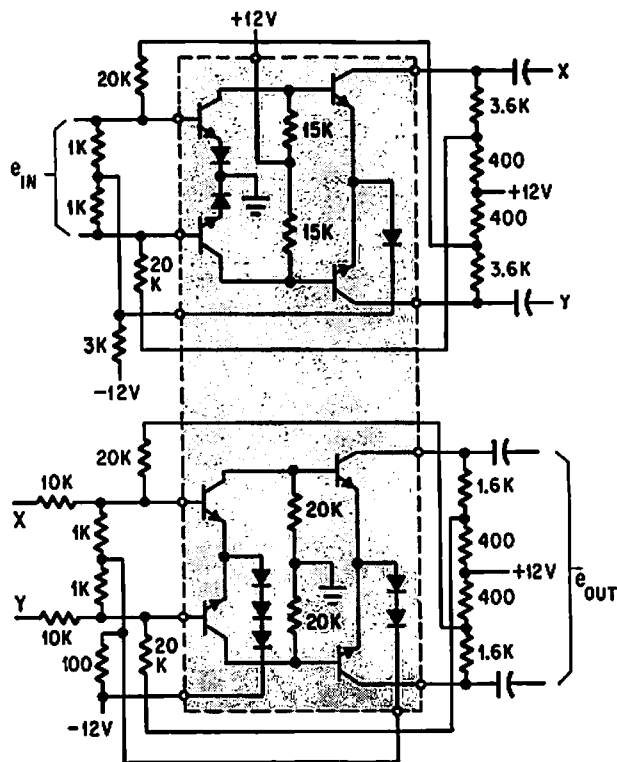
DIFFERENTIAL AMPLIFIER—Use of transistor in place of emitter resistor gives tenfold increase in impedance of emitter circuit, up to 200,000 ohms, while using only 1% of substrate area that would be needed by film resistor of this size.—R. Hirschfeld, *IC's Improve Differential Amplifiers—and Vice Versa*, *Electronics*, 38:16, p 75-79.



FOUR-STAGE DIFFERENTIAL AMPLIFIER—Designed for maximum open-loop amplification of differential signal. Series-shunt negative feedback provides high input impedance and low output impedance. Responds to dif-

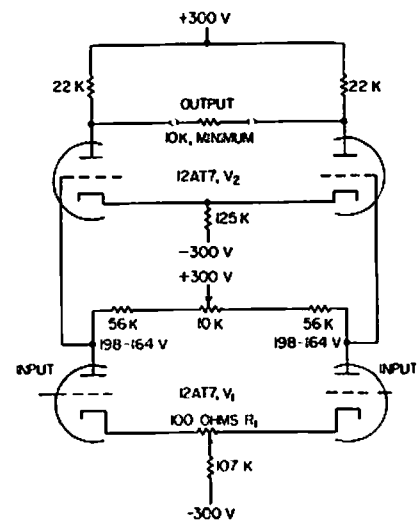
ferential signal of 25 microvolts superimposed on common level that varies from 0 to 5 v. Voltage gain is continuously variable from 100 to 500. Frequency response is flat within 1% from d-c to 1,000 cps. Ideal

for telemetering systems.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N. Y., 1963, p 138.

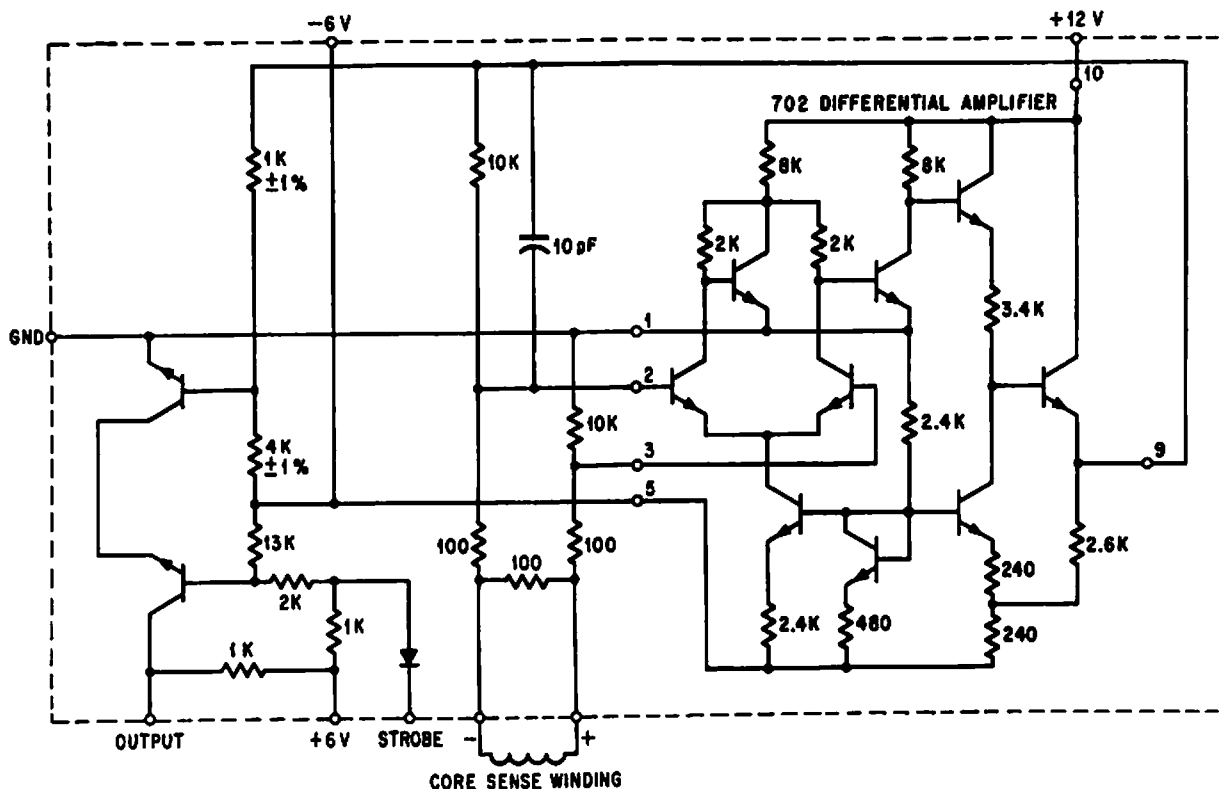


DIFFERENTIAL CURRENT AMPLIFIER—Uses eight npn transistors and eight diodes.—D. D. Robinson, *Linear Microcircuits Scarce? Now*

You Can Breadboard Your Own, *Electronics*, 37:27, p. 58-64.



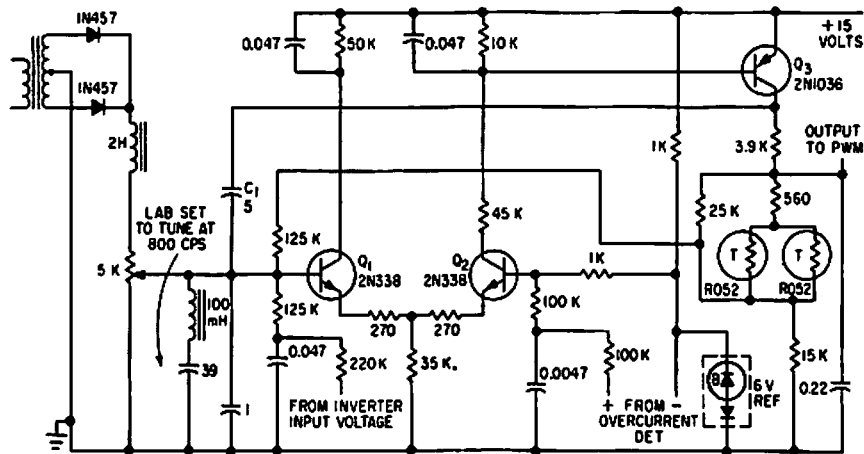
WIDE-DYNAMIC-RANGE DIFFERENTIAL AMP-LIFIER—Used in amplifying and measuring small differences between two large voltages, either of which may be up to 100 v above ground. Amplification of difference voltage is 250. Frequency response is within 3 db from d-c to 250 kc.—D. D. Davis, *High Dynamic Range Differential Amplifier*, *Electronics*, 31:5, p. 64-66.



SENSE AMPLIFIER—Uses two flatpacks (shaded areas) attached to thick-film passive network.

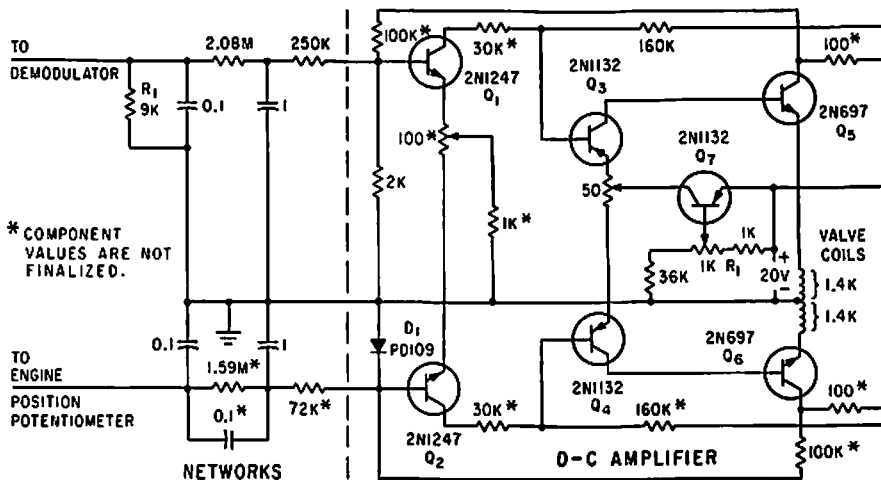
—F. A. Plemenos, *The Packaging Revolution*, Part VI: Converting to Microelectronics,

Electronics, 39:4, p. 103-109.

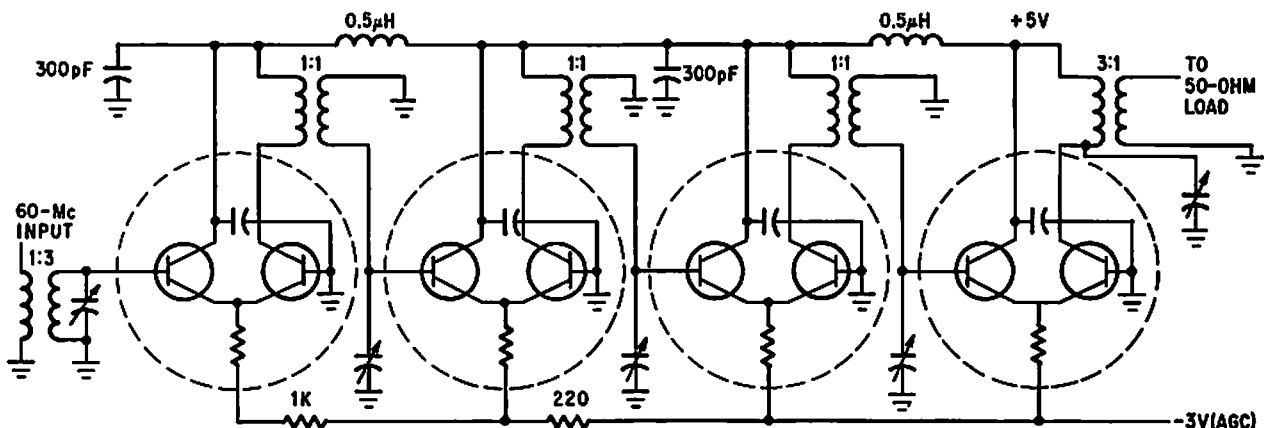


VOLTAGE REGULATOR—Portion of three-phase inverter output is filtered, rectified, and compared with temperature-compensated breakdown diode in differential amplifier Q1-Q2. Inverter load current and input bat-

tery voltage signals are also fed into differential amplifier, to further improve regulation.—R. J. Kearns and J. J. Rolfe, Three-Phase Static Inverters Power Space-Vehicle Equipment, *Electronics*, 34:18, p 70-73.



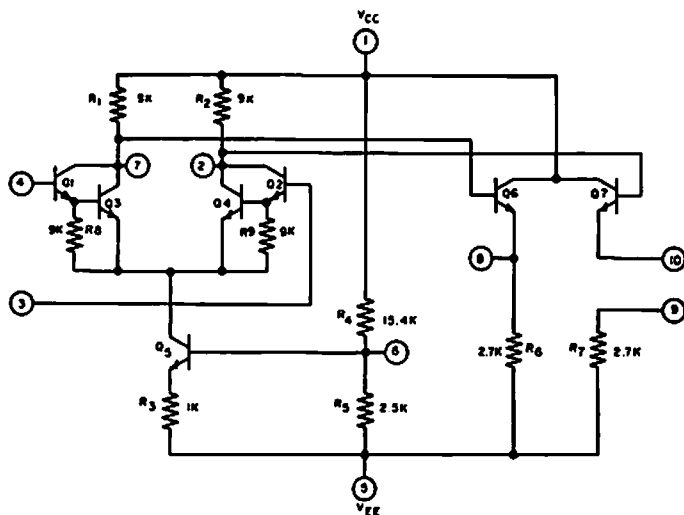
51-DB POWER GAIN FOR AUTOPILOT—Two differential stages, Q1-Q2 and Q3-Q4, drive two emitter-followers Q5 and Q6 which in turn drive valve coils in pitch and yaw channels of autopilot.—J. H. Porter, Miniaturized Autopilot System for Missiles, *Electronics*, 33:43, p 60-64.



F-M LIMITER—Four differential-amplifier integrated circuits serve as 60-Mc i-f f-m limiter

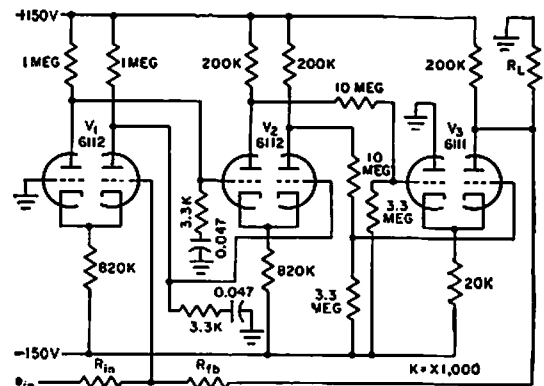
having 6-Mc bandwidth and 80 db power gain.—R. Hirschfeld, IC's Improve Differential

Amplifiers—and Vice Versa, *Electronics*, 38:16, p 75-79.

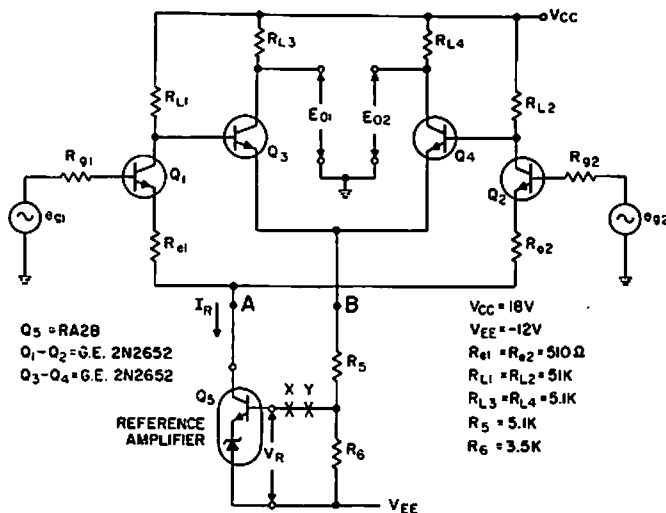


DIFFERENTIAL AMPLIFIER—Single-stage configuration for monolithic construction uses bleeder resistors with Darlington input transistors to increase bandwidth and gain. Current source is biased from separate bias resistor to increase output amplitude. Mini-

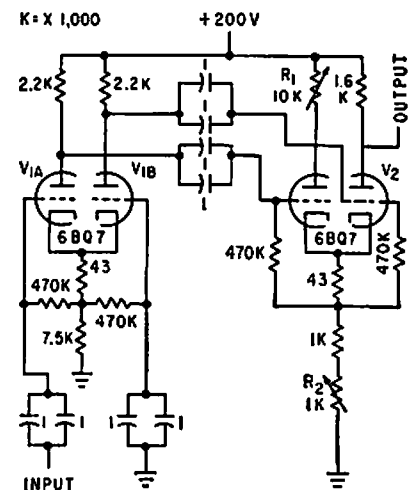
mum differential voltage gain of Micronet 203 version is 100 and minimum bandwidth is 500 kc.—C. L. Helzman and D. G. Paterson, Circuit Analysis: A Monolithic Integrated Operational Amplifier, *EEE*, 13:5, p 80-84.



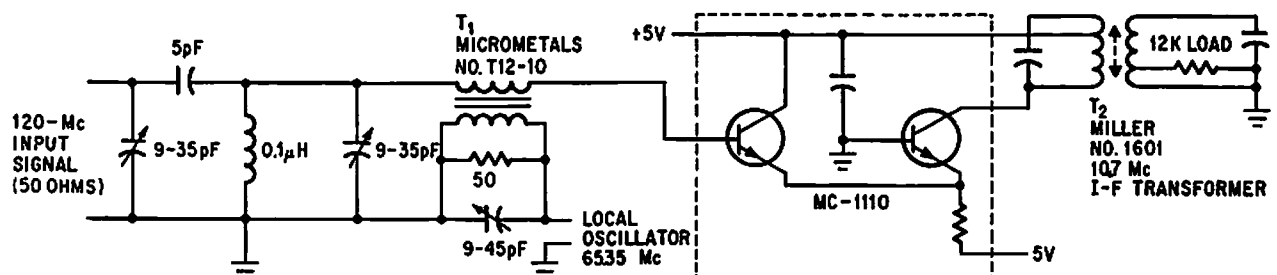
BALANCED DIFFERENTIAL OPERATIONAL AMPLIFIER—Open-loop gain is above 5,000 into 10,000-ohm load. Good stability and summing accuracy are obtained with closed-loop gains of 0.1 to 100. Provides 50-v output voltage swing for integrating or differentiating in control systems. Phase lag of 5° at 20 cps with closed-loop gain of 10 precludes use in high-frequency control systems.—L. S. Klivans, D-C Amplifiers for Control Systems, *Electronics*, 31:47, p 96-100.



TWO-STAGE DIFFERENTIAL AMPLIFIER WITH COMMON-MODE FEEDBACK—Feedback arrangement provides significant reduction in temperature drift of bias circuits. Voltage gains of several thousand are possible.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 119.



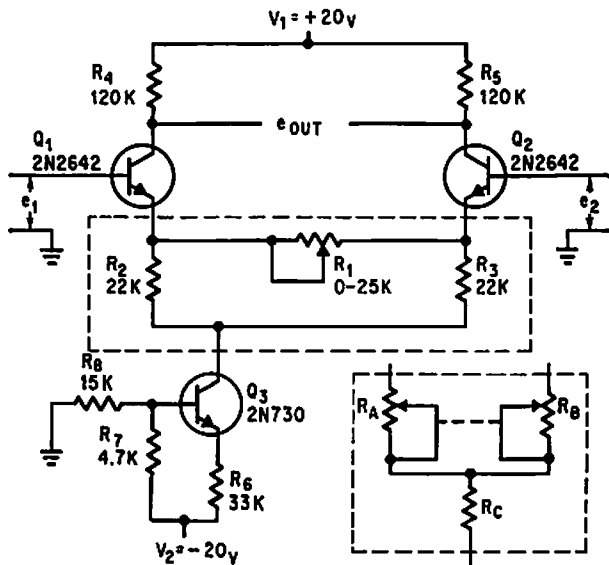
CANCELLING POWER SUPPLY VARIATIONS—Differential amplifiers in cascade cancel output error caused by supply fluctuations, to permit low-level signal amplification.—J. Holtzman, Reducing Errors Caused by Power-Supply Variations, *Electronics*, 32:29, p 54-55.



HARMONIC MIXER—MC-1110 differential-amplifier integrated circuit cancels odd-order harmonics while mixing. Local oscillator op-

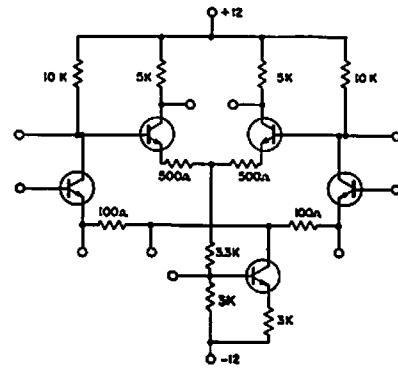
erates at half of mixing frequency. Conversion gain is 33 db from 120 Mc to 10.7 Mc.—R. Hirschfeld, IC's Improve Differential

Amplifiers—and Vice Versa, *Electronics*, 38:16, p 75-79.



VARIABLE-GAIN DIFFERENTIAL AMPLIFIER— R_1 controls gain. High dynamic impedance of constant-current source gives differential amplifier Q_1 - Q_2 high common-mode rejection

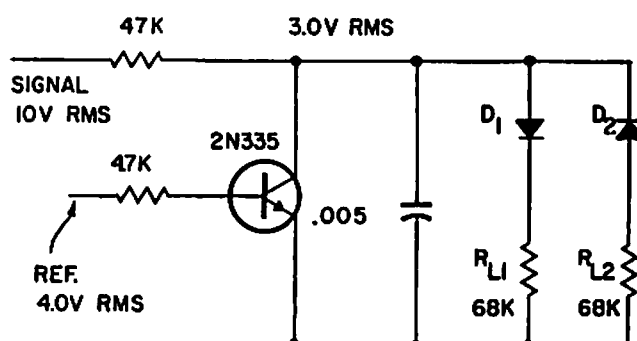
ratio.—G. Beene, Variable Resistor Controls Differential Amplifier Gain, *Electronics*, 37:29, p 74.



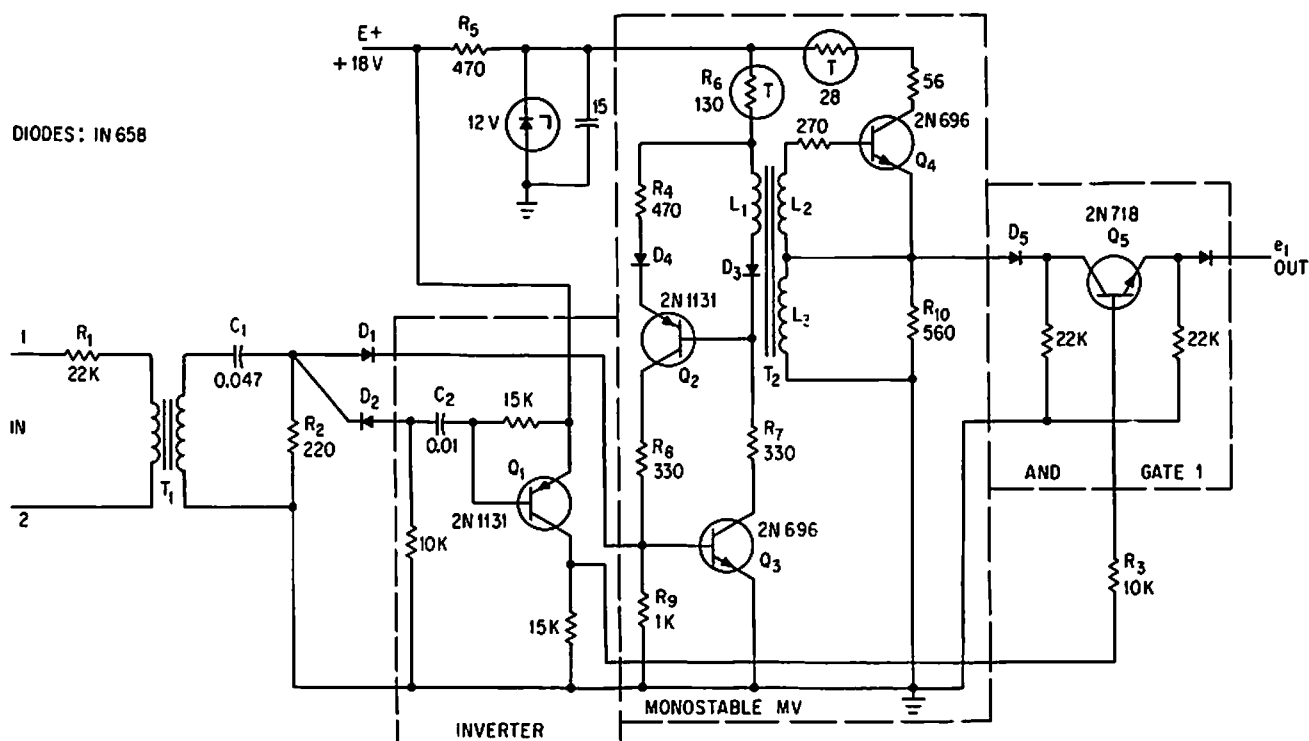
INTEGRATED-CIRCUIT DIFFERENTIAL AMPLIFIER—Common-mode output is 0.5 mv peak-to-peak, differential gain is 540, and common-mode rejection is 120 db at 60 cps in Amelco D13001 monolithic integrated circuit. —T. Presser, How to Measure Differential-Amplifier Common-Mode Rejection, *EEE*, 12:7, p 74-75.

CHAPTER 27

Discriminator Circuits



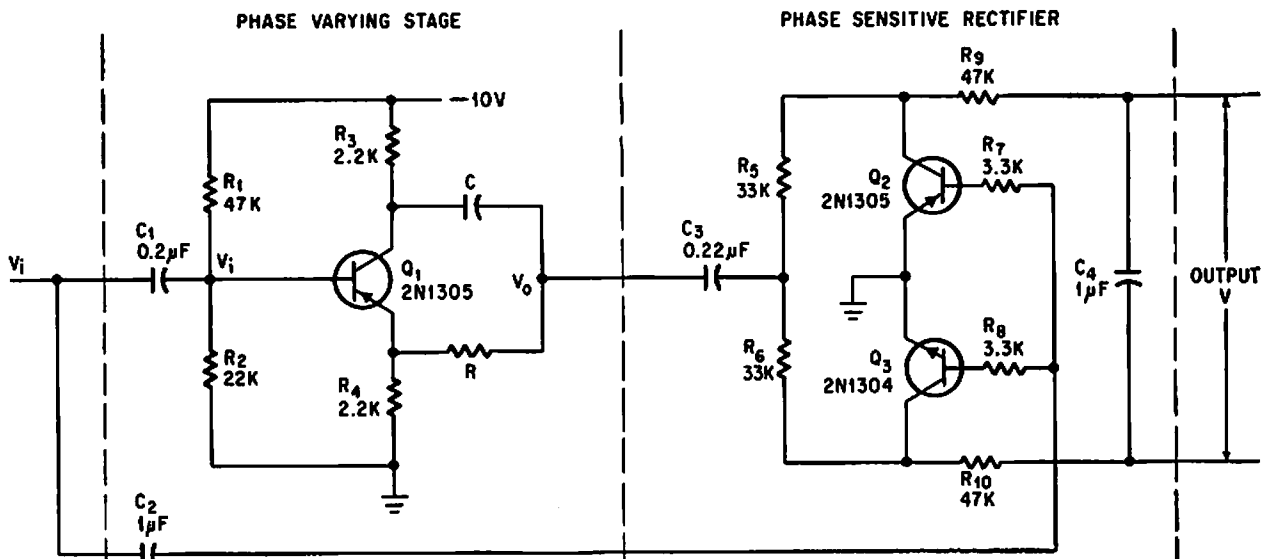
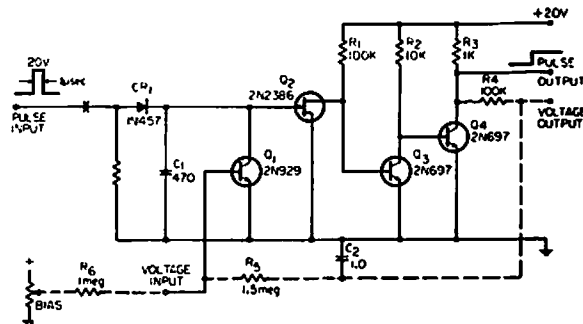
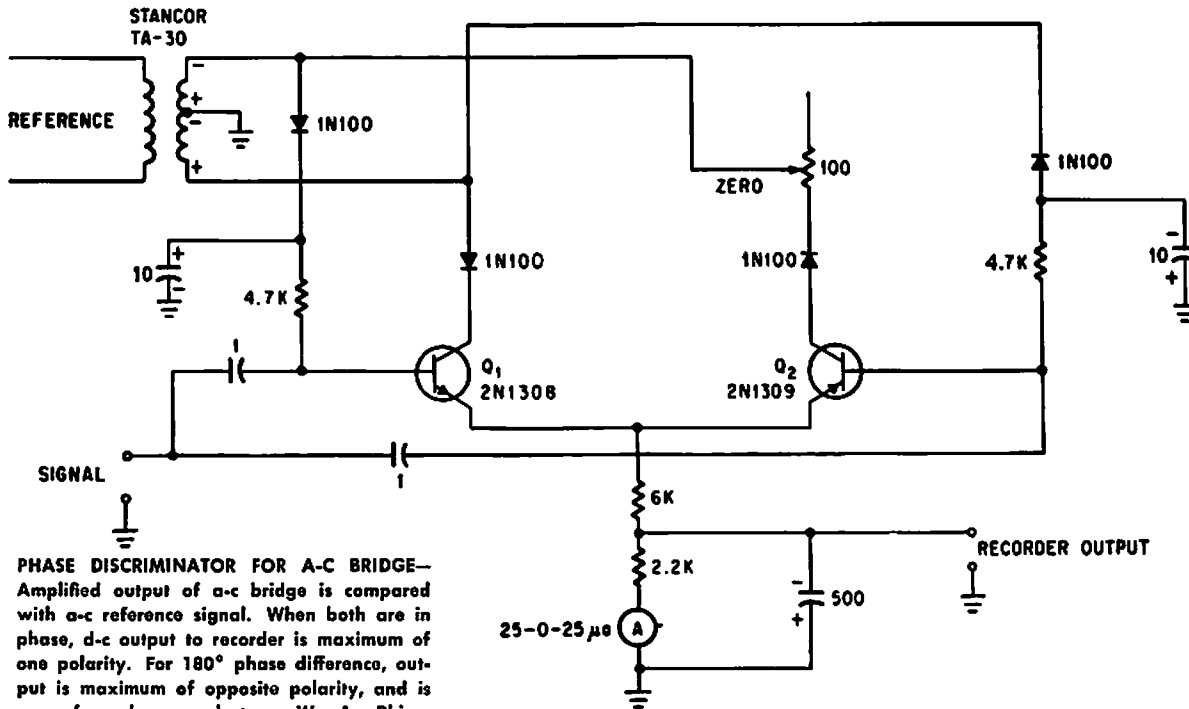
PHASE DISCRIMINATOR—Will deliver half-wave pulses to one of two loads, as determined by 0 or 180° difference between input signal and reference source. Useful where different devices, such as heating and cooling equipment, are to be actuated by change of signal phase.—A Phase Discriminator, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 88.

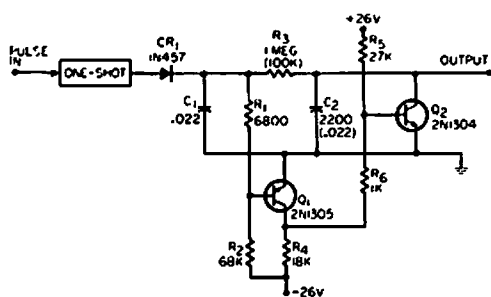


CHANNEL SELECTOR REPLACES TUNING DEVICES—By sensing leading edges of input signals, discriminator having two monostable

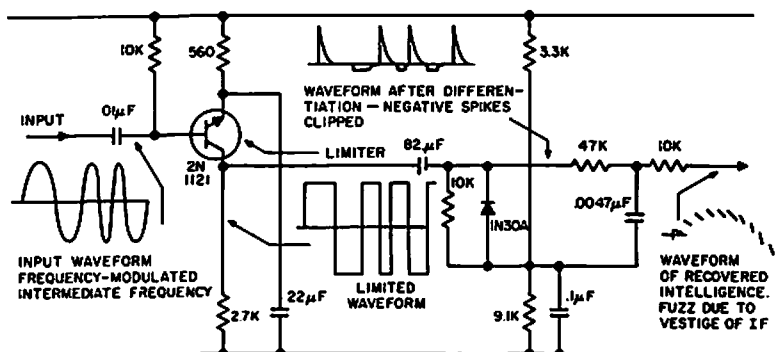
multivibrators, inverter, and two and gates provides output for desired channel frequency in radio, television, telemetry, and

digital control systems.—J. H. Firestone, *Gated Pulses Yield Selected Frequency Outputs*, *Electronics*, 36:51, p 38-40.

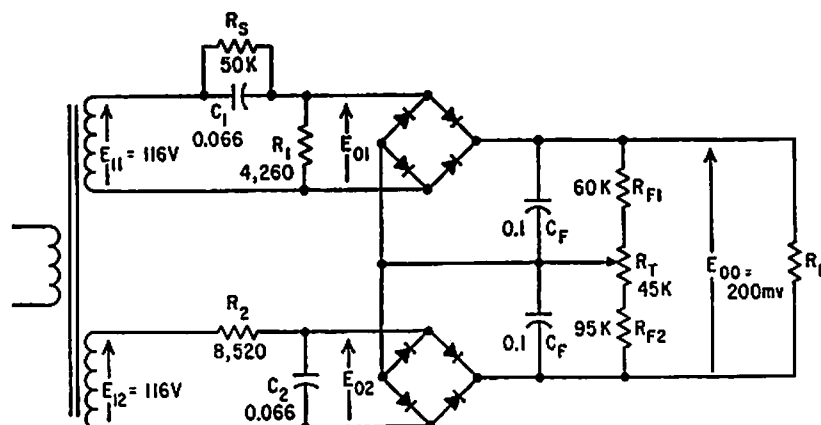




PRF DISCRIMINATOR—Requires pulse-train burst of only two successive pulses to determine prf above or below given limit. Two such circuits with nand gate can indicate presence of given prf within 0.1% or within 1 cps of 1 kc. Input pulses are first given standard width and amplitude by one-shot.—G. Richwell, PRF Discriminator, *EEE*, 13:7, p 41.

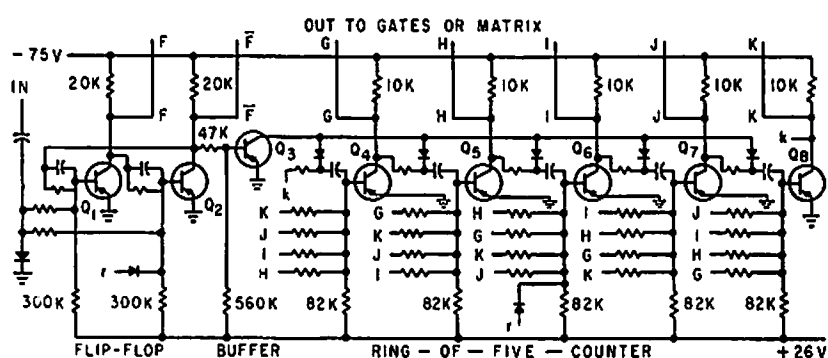


PULSE-COUNTING F-M DISCRIMINATOR—Based on inherent stability of tunnel diode as converter oscillator in f-m receiver for strong-signal locations. Uses 200-kc i-f center frequency as input.—D. Hubbard, Pulse-Counter FM Discriminator Design, *EEE*, 10:7, p 44-49.



CHAPTER 28

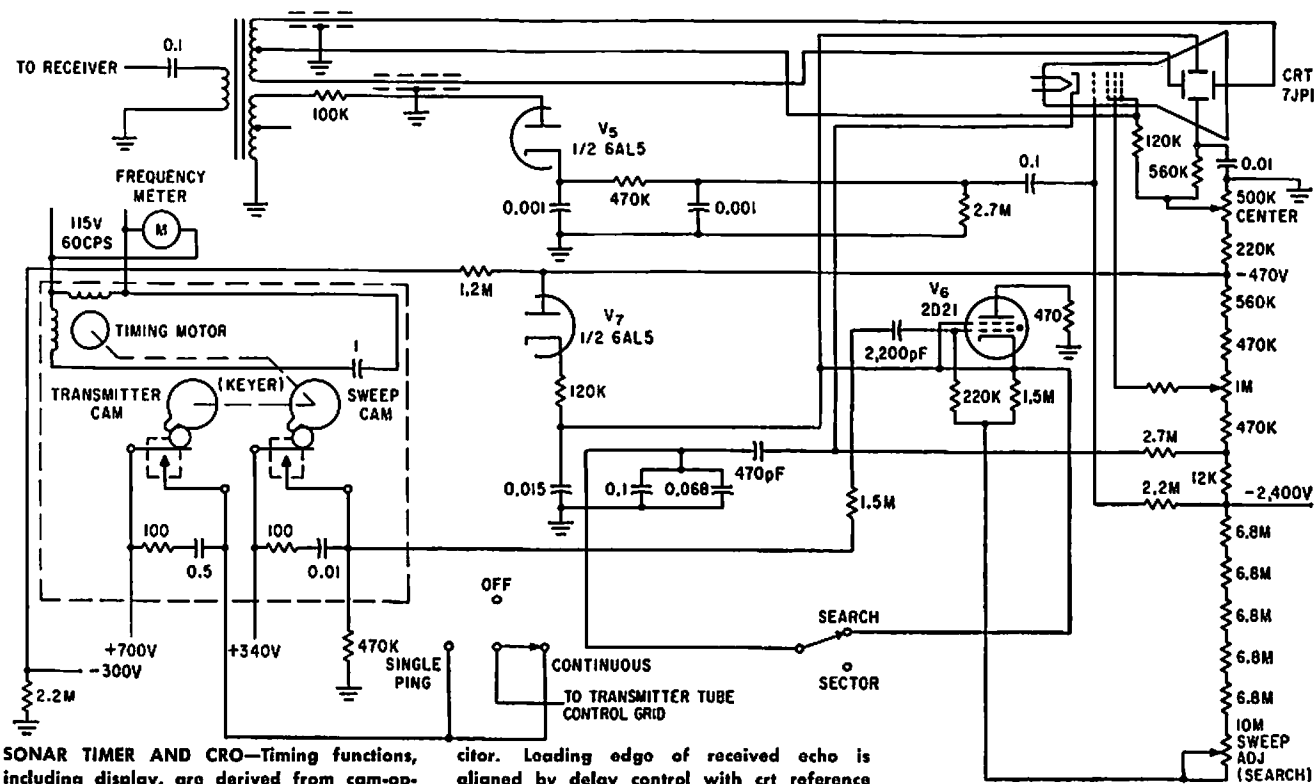
Display Circuits



ALL UNMARKED RESISTORS ARE 18K
ALL UNMARKED CAPACITORS ARE 0.001 μ F
POSITIVE RESET PULSE

ALL DIODES ARE TIG
ALL TRANSISTORS ARE 2N60
K $\times$ 1,000

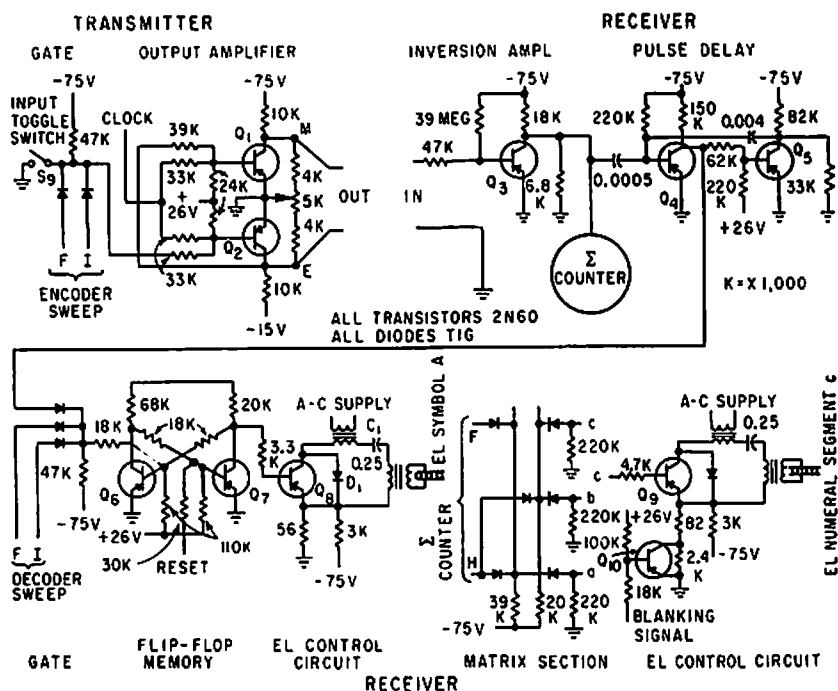
10-STATE RING COUNTER—Flip-flop drives ring-of-five stage that in turn drives diode matrix which translates each stored decimal number to electroluminescent display segment code.—R. C. Lyman and C. I. Jones, *Electroluminescent Panels for Automatic Displays*, *Electronics*, 32:28, p 44-47.



SONAR TIMER AND CRO—Timing functions, including display, are derived from cam-operated switches driven by synchronous motor. Thyatron V6 discharges sweep capacitor. Loading edge of received echo is

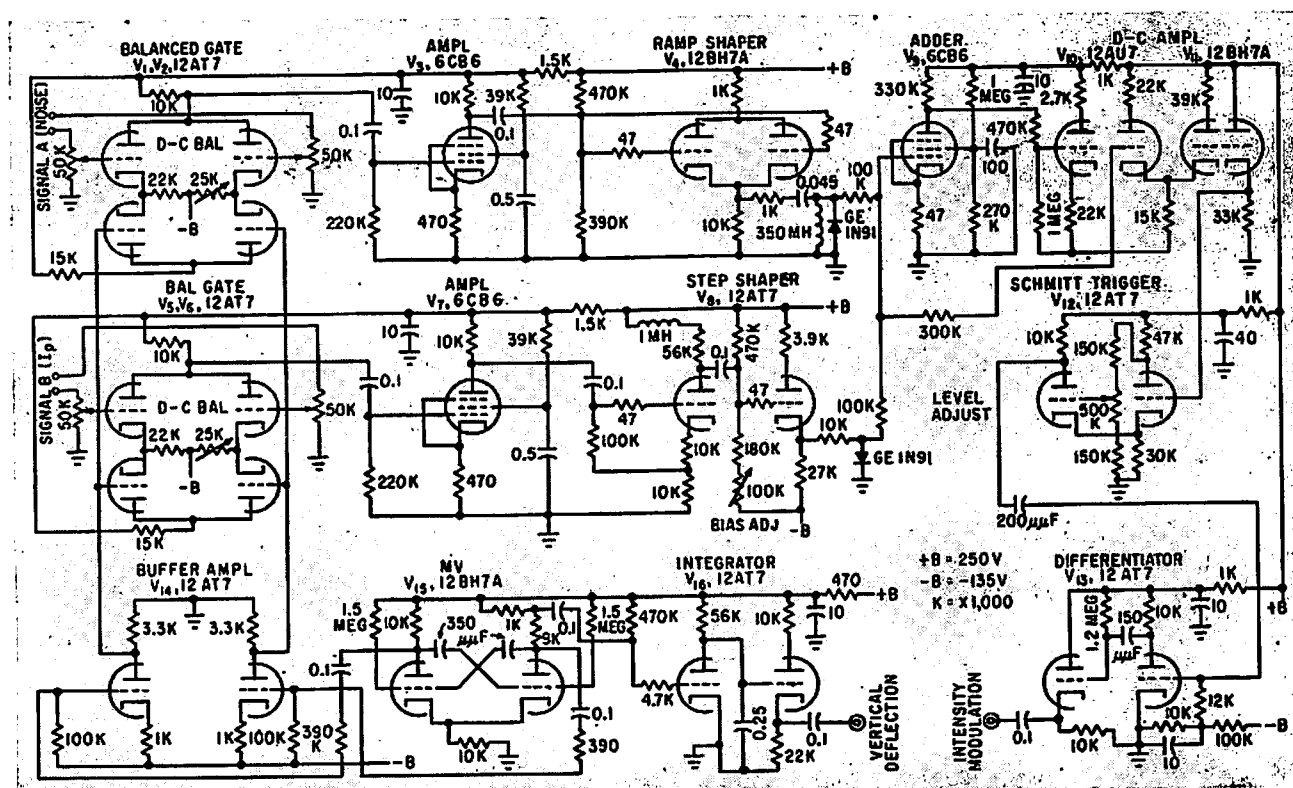
aligned by delay control with crt reference line, and delay time in millisec is read directly from dial.—L. H. Dulberger, *Sonar to*

Survey Arctic Ocean Shelf Transmits Through Ice and Water, *Electronics*, 34:31, p 44-45.



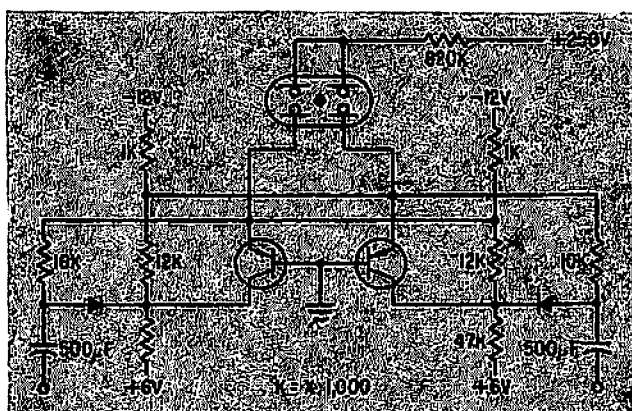
BINARY CHANNEL FOR EL DISPLAY—Information is transmitted to decoding unit and display board in series of pulse bursts, each containing entire information to be dis-

played, for rapid error correction if information is garbled during transmission. System can use pair of wires for transmission, having sufficient bandwidth to pass pulse burst.



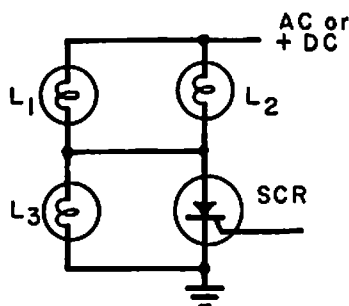
ANALOG-TYPE RATIO COMPUTER—Computes and automatically displays on oscilloscope the ratio of two time-varying quantities, such as noise suppression factor of tube

sampling signal.—J. Tamiya, Automatic Display of Noise Suppression Factor, *Electronics*, 33:6, p 55-57.



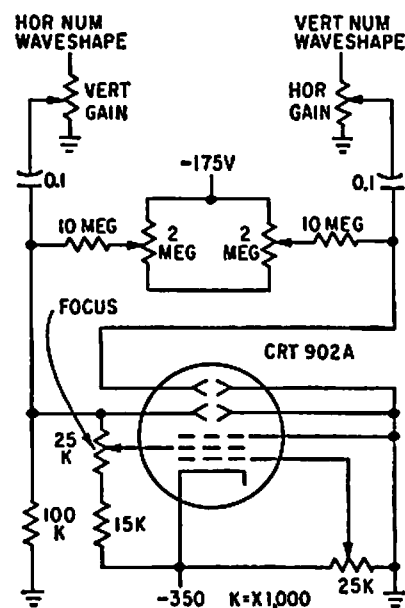
FOUR-ELECTRODE NEON—On-off indicator for transistorized flip-flop operates on voltage differential of 6 v.—A. Erikson, French

Components Getting Smaller, Electronics,
34:11, p 24-25.

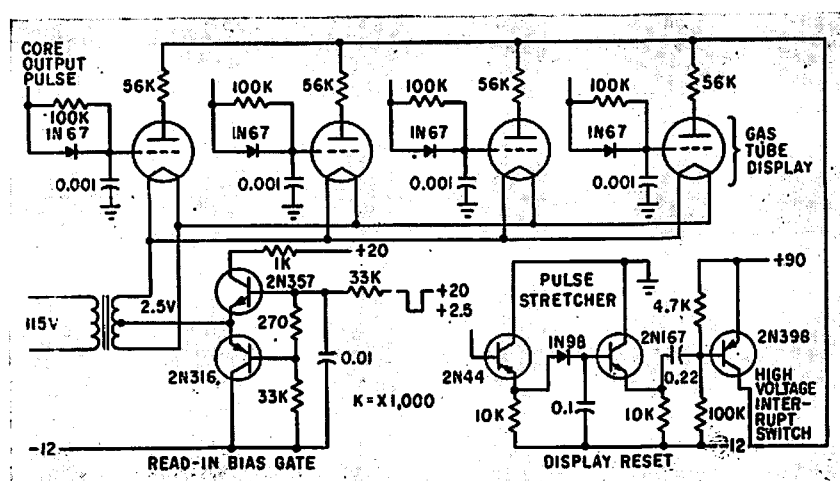


LAMP READOUT INVERSION—Used if lamp output is required with switch open, or if two lamp outputs are required (one lamp coming on for switch open and the other for switch closed). All lamps are type 39, rated 6.3 v at 0.36 amp. With scr off, voltage across L1 and L2 was 0.8 v with 6.3 v

across L3, with no visible light from L1 and L2. With scr on, there is about 6.3 v across L1 and L2, with no visible output from L3.—Inversion Technique for Incandescent Lamp Readouts, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 208.

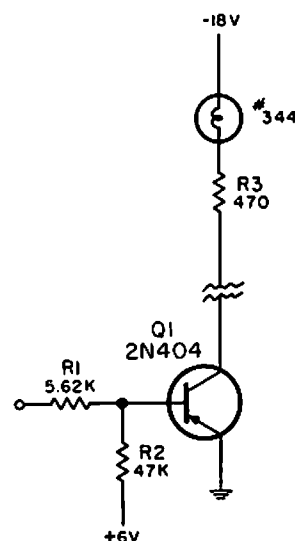


CRT CONTROL—Takes waveforms from gates and applies them to deflection electrodes of 2-inch crt to create numeral-forming Lissajous patterns.—R. L. White, *Forming Handwritten-Like Digits on CRT Display*, *Electronics*, 32:11, p 138-140.

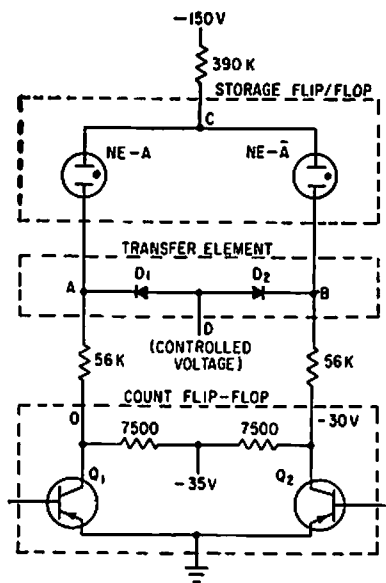


GAS-TUBE READOUT—Thyratron display tubes (Kip Memolites) remain on until next input sync pulse occurs. Static delay one-shot is then triggered, to extinguish display bulbs by dropping their plate voltage below ionization point. Bulbs are extinguished only

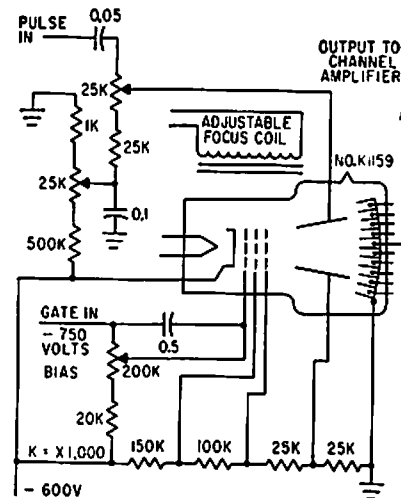
when new input information is to be received. Used in converting up to 13 bits from Gray code to straight binary.—R. Wasserman and W. Nutting, Solid-State Digital Code-to-Code Converter, *Electronics*, 32:50, p 60-63.



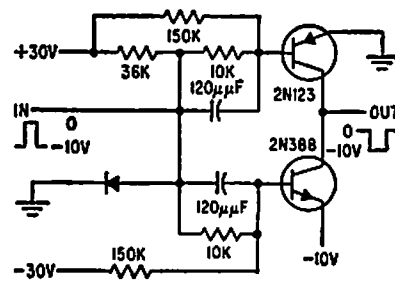
LAMP-TYPE INDICATOR—Used as indicator in digital logic circuits. Common-emitter amplifier drives type 344 lamp rated 18 ma at 12 v. Can also be used to drive electro-mechanical indicator having the same operating power requirements. Lamp may be remotely located.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 13 (originally PC 216), p 13-2.



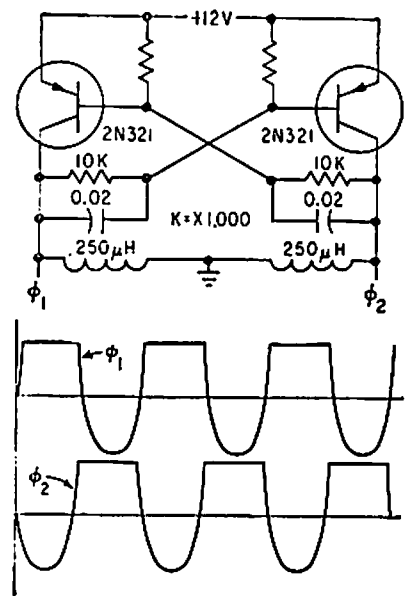
BINARY NEONS—Q1 and Q2 are active elements in transistor binaries, and lamps NE-A are pre-aged neons matched with respect to firing and running voltages. Voltage at D stores information in neon lamps.—B. H. Harrison, Photoconductive Matrix Simplifies Counter Display, *Electronics*, 34:51, p 28-30.



NANOSECOND PULSE DISPLAY—Magnetic-focus electrostatic-deflection beam-deflection tube permits pulse height analysis where pulse separation is of the order of microseconds.—J. Burns, Special Tubes for Nanosecond Display, *Electronics*, 33:49, p 82-85.



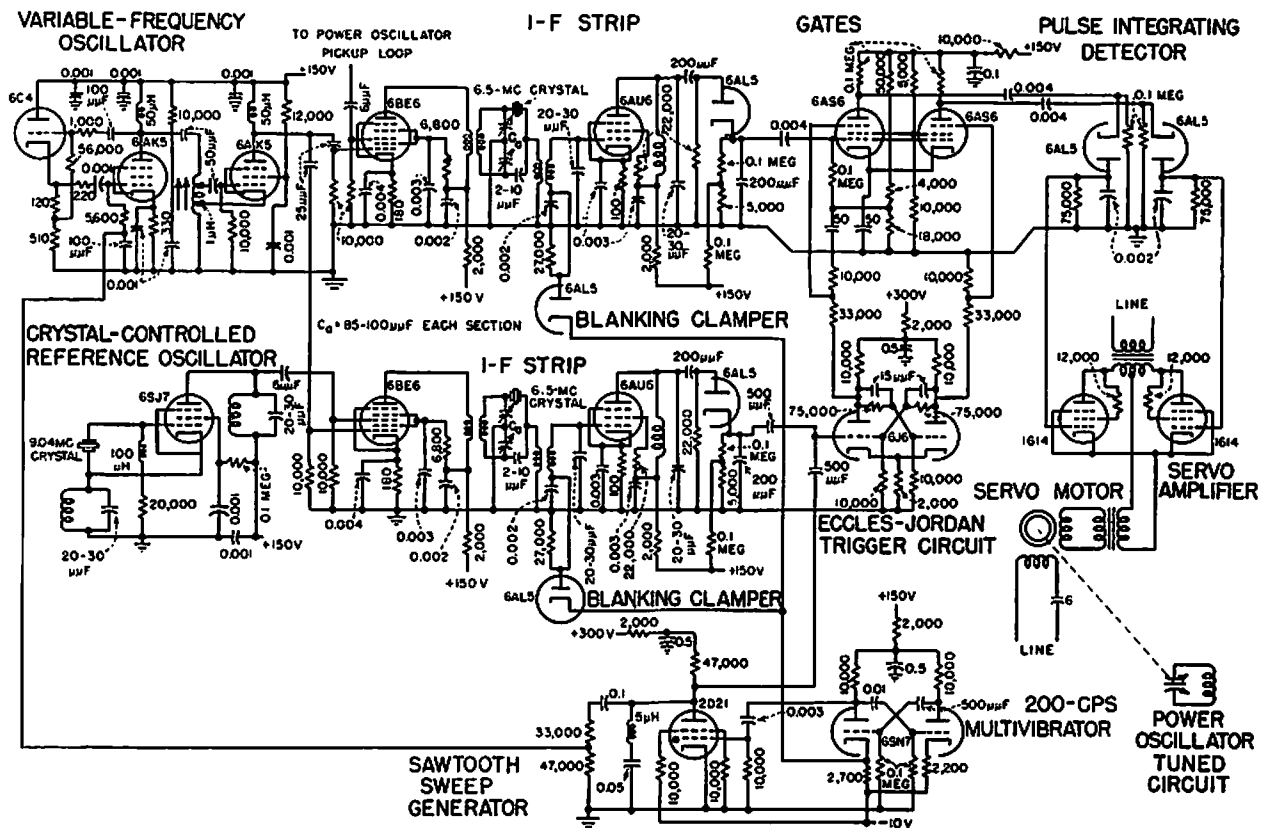
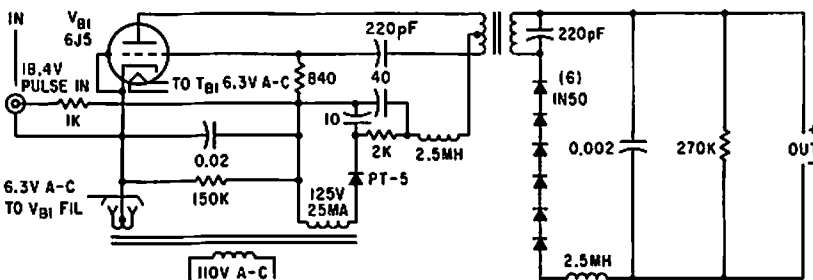
CHARACTER-FORMING DOT GENERATOR—Transistor switch, having drop of less than 50 mv when delivering 50 ma, is used in display that provides fast alphanumeric readout on crt by forming characters from series of overlapping dots.—S. C. Chao, Character Displays Using Analog Techniques, *Electronics*, 32:43, p 116-118.



GATE-OPENING 100-KC OSCILLATOR—Output voltages are taken across r-f chokes in collector circuits, for controlling number gates of crt display that creates handwritten numerals.—R. L. White, Forming Handwritten-Like Digits on CRT display, *Electronics*, 32:11, p 138-140.

Electronic Heating Circuits

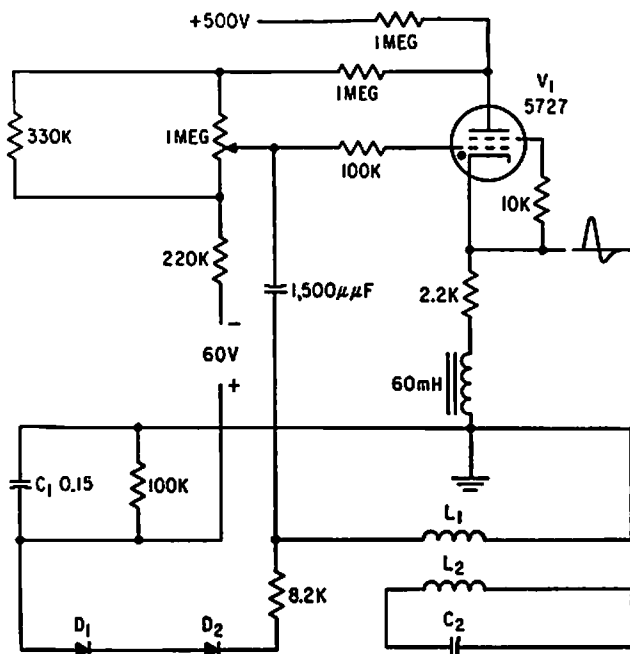
ISOLATION OSCILLATOR—Used to isolate duty-cycle generator of induction heater control system from pulser of power oscillator.—R. E. Mathews and F. R. Sias, Jr., *Testing Space Craft with Induction Heaters, Electronics*, 35:34, p 38-41.



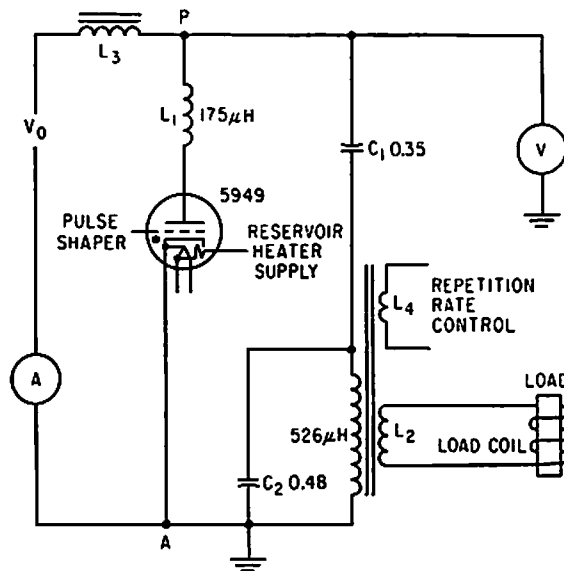
27.12-MC DIELECTRIC HEATER—Pulse-controlled frequency-stabilization servo mechanism retunes self-excited power oscillator con-

tinually, with 200-cps mvbr governing rate at which system compares oscillator frequency with that of crystal-controlled ref-

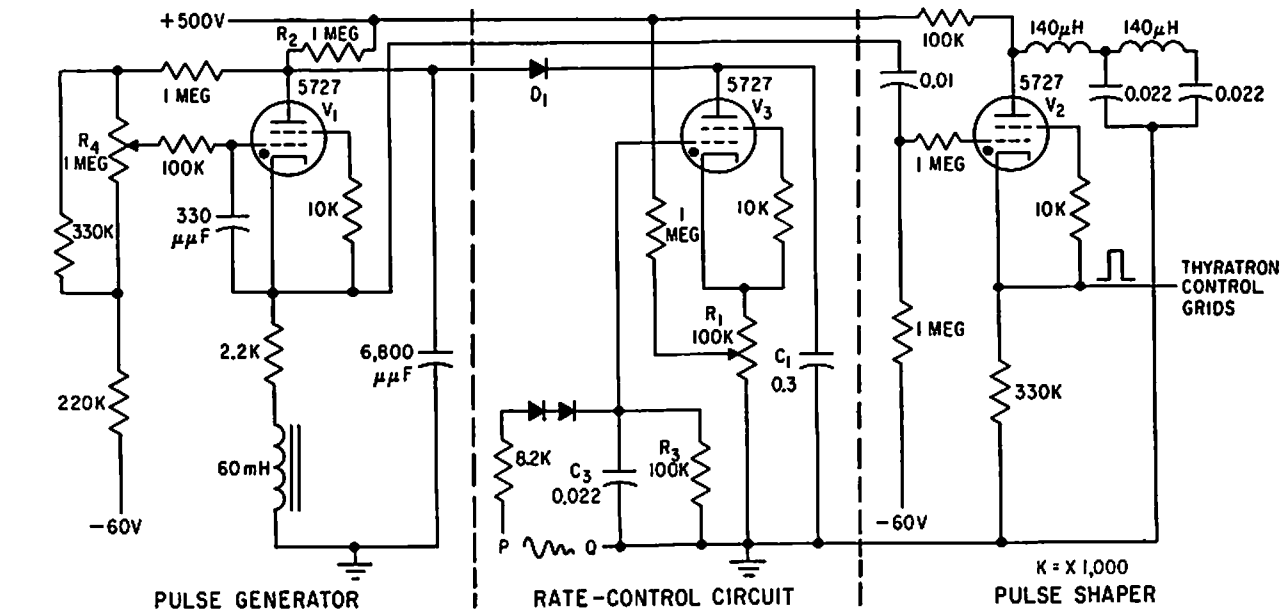
erence oscillator.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 173.



REPETITION RATE CONTROL—Compensates for fluctuations in repetition rate of hydrogen thyatron in induction heater. Ripple voltage induced in L1 acts on control grid of V1 to displace peaks at which ignition occurs in correct direction to maintain constant repetition rate in damped circuit.—H. L. Van Der Horst, *How Radar Techniques Improve Induction Heating*, *Electronics*, 32:7, p 51-55.



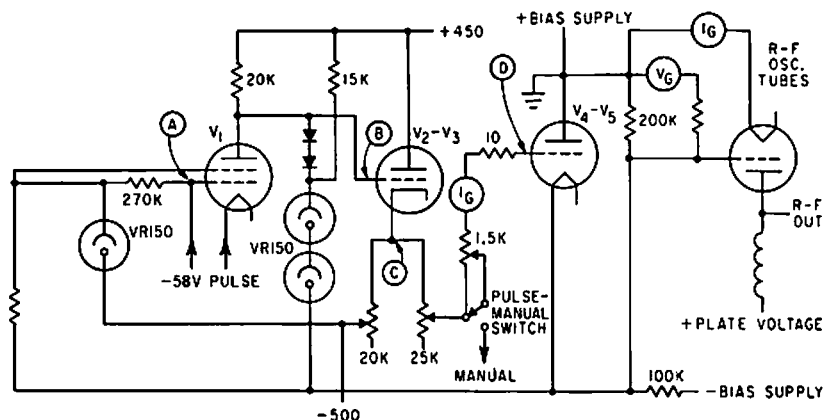
HYDROGEN-THYRATRON INDUCTION HEATER—Thyratron acts as high-speed switch, much like spark-gap oscillator, to produce damped oscillations in tank circuit L2-C2. Output frequency is 10 to 14 kc, depending on load. Peak thyatron current is about 340 amp when V0 is 10 kv. Repetition rate depends on maximum average current, and is 124 cps for 0.5 amp. L3 is 0.32 h.—H. L. Van Der Horst, *How Radar Techniques Improve Induction Heating*, *Electronics*, 32:7, p 51-55.



INDUCTION HEATER CONTROL—Thyratron pulse generator V1 produces voltage pulses of adjustable frequency for pulse shaper V2, which drives hydrogen thyatrons of high-

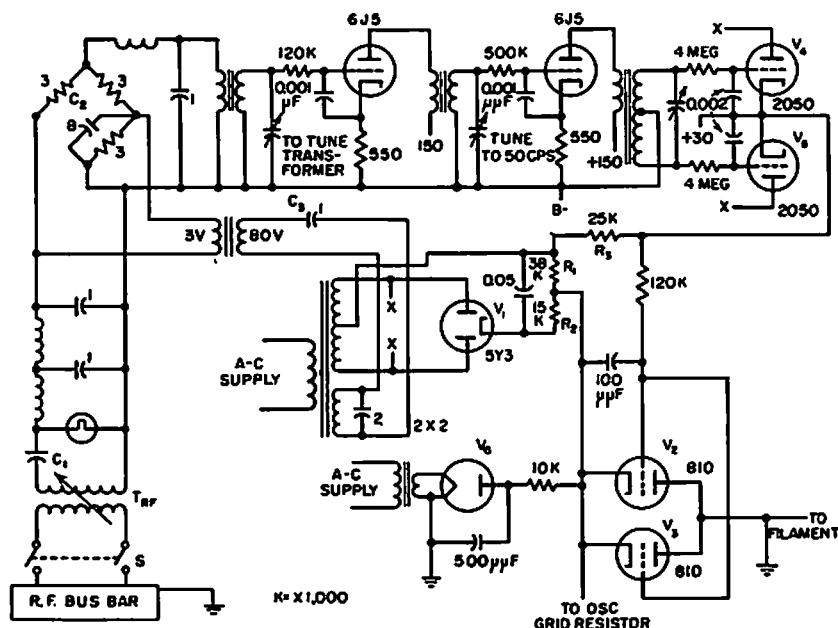
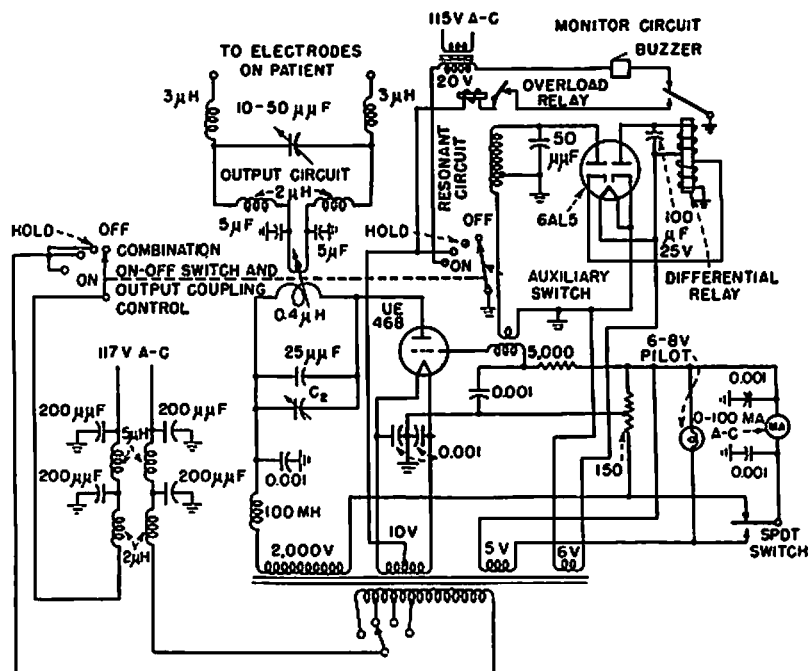
power induction heater. V3 regulates repetition rate of pulses by acting as switch that, when conducting, allows C1 to discharge rapidly through R1.—H. L. Van Der

Horst, *How Radar Techniques Improve Induction Heating*, *Electronics*, 32:7, p 51-55.



PULSER AND KEYER—Used to control power oscillator of induction heater at rates up to 800 pps.—R. E. Mathews and F. R. Sias, Jr., *Testing Space Craft with Induction Heaters*, *Electronics*, 35:34, p 38-41.

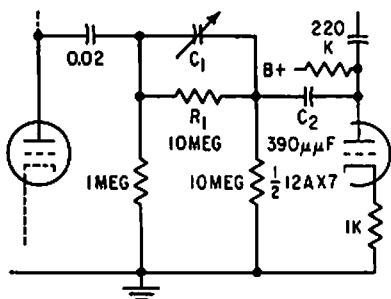
DIATHERMY FREQUENCY MONITOR AND CONTROL—Monitor circuit stops 27.12-Mc oscillator and sounds buzzer when frequency drifts beyond legal limits established by FCC.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 100.



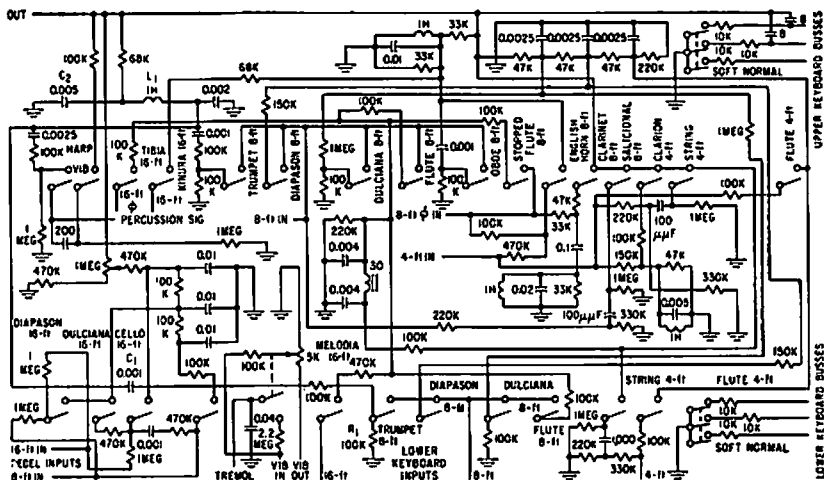
CLOSED-LOOP REGULATOR FOR INDUCTION HEATER—Switching action is performed by parallel triodes V2 and V3 that replace 5,000 ohms of oscillator grid resistance. Tungsten-lamp bridge serves as measuring circuit that produces phase-modulated supply-frequency error signal. Requires no components with heavy power rating because only low-power signal is required by switching triode.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, N.Y., 1959, p 178.

CHAPTER 30

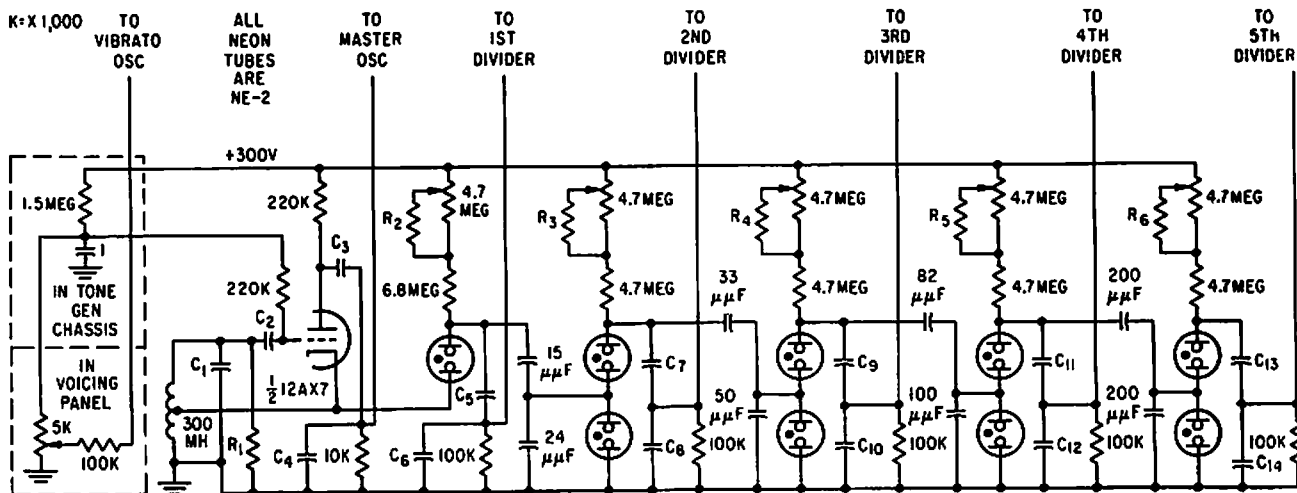
Electronic Music Circuits



ORGAN SWELL SHOE—Uses capacitive volume control C1 to replace expensive industrial-type potentiometer. Operation of swell shoe varies value of C2 which consists of two hinged metal plates. C1 is series leg of capacitive voltage divider, shunt leg of which is dynamic capacitance of about 0.02 mfd across tube grid due to capacitive feedback from plate through C2. Attenuation range is great, and noise and hum are negligible. —R. H. Dorf, *Electronic Organ Uses Neon Tone Generators, Electronics*, 31:35, p 36-41.



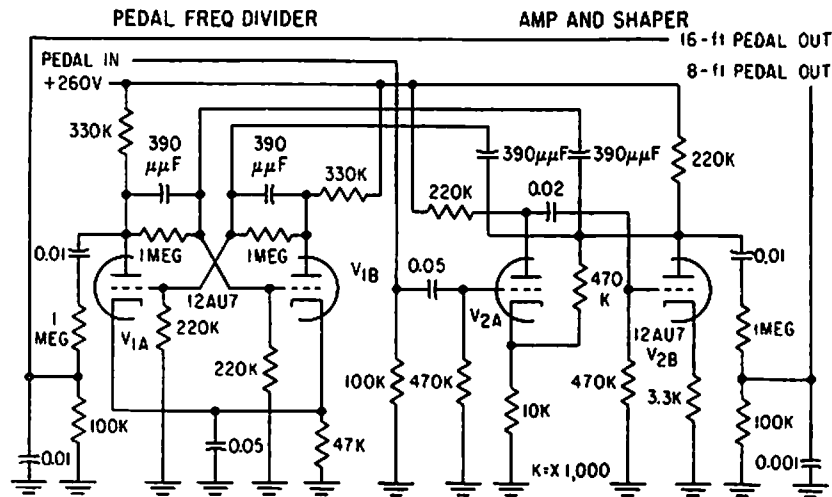
ORGAN VOICING PANEL—Contains formant filters that transform sawtooth generator signals into waveforms of various instruments. 19 different tone colors or timbres are available, ranging from sharp strings and reeds to bland flutes and pipelike diapasons. Filters are interlocked to produce composite effects.—R. H. Dorf, *Electronic Organ Uses Neon Tone Generators, Electronics*, 31:35, p 36-41.



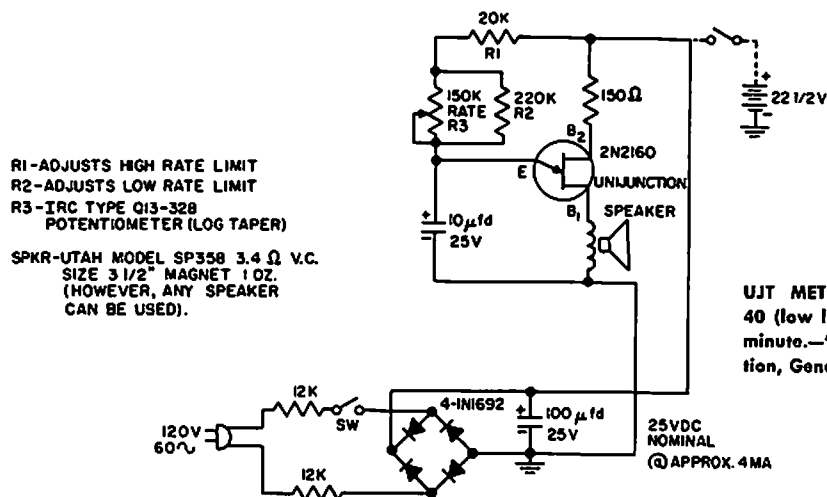
NEON TONE GENERATOR—Each of 12 organ tone generators has four pairs of neon tubes in series, each pair shunted by two series capacitors. With signal taken from common

point between capacitors, sufficient insulation exists to prevent feedback and spurious tones in output. Master oscillator provides initial sync for neon-lamp divider chain. Ar-

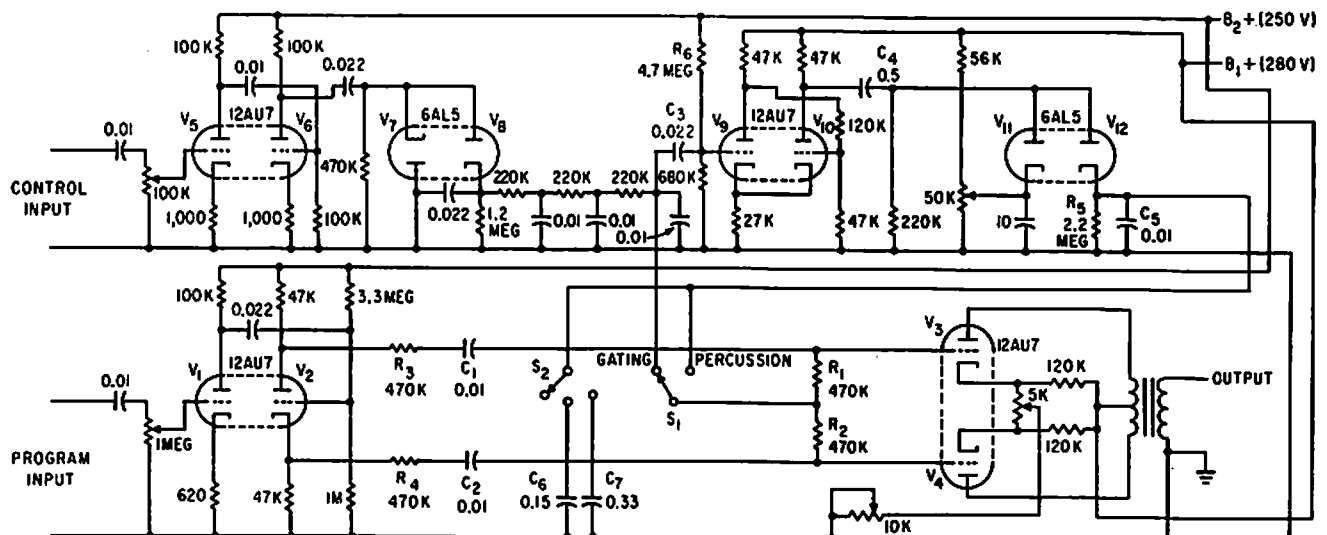
title gives capacitor values for each musical note.—R. H. Dorf, *Electronic Organ Uses Neon Tone Generators*, *Electronics*, 31:35, p 36-41.



ORGAN PEDAL GENERATOR—Shaper V2 yields sawtooth wave with steeper flyback than from neon oscillator. Output of V2 feeds bistable mvbr V1 which changes state with each input trigger. Bistable output at V1A is half the input frequency (one octave lower), so 16-ft pedal tones are produced without low-frequency divider stage for each tone generator.—R. H. Dorf, *Electronic Organ Uses Neon Tone Generators*, *Electronics*, 31:35, p 36-41.



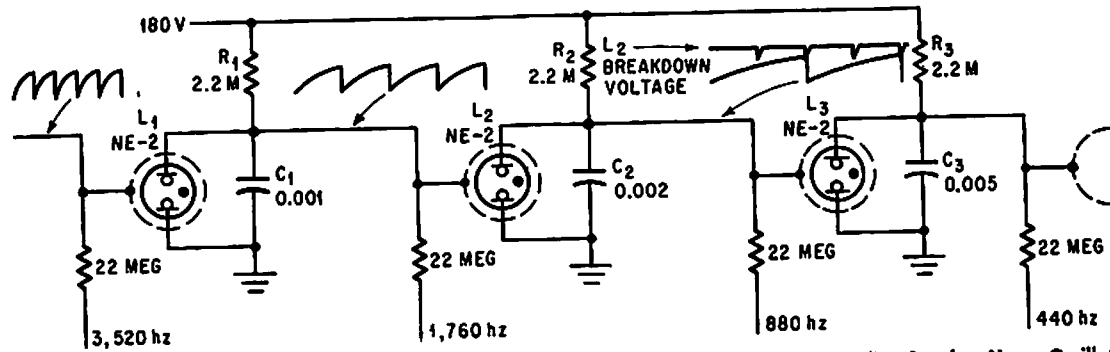
UJT METRONOME—Rate is adjustable from 40 (low largo) to 220 (high presto) beats per minute.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 379.



PERCUSSION OR ELECTRONIC MUSIC—Provides congruent envelope shaping and coincident percussive envelope shaping of syn-

thesized program material. One input accepts control signal, while other accepts material requiring envelope shaping.—H. Bode, *Sound*

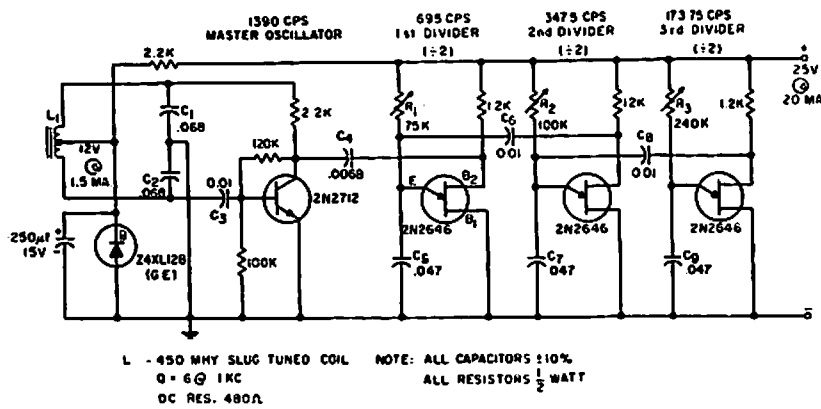
Synthesizer Creates New Musical Effects, *Electronics*, 34:48, p 33-37.



NEON-OSCILLATOR SYNCHRONIZER—Metal clips on neon lamps are used to synchronize successive stages of neon-lamp relaxa-

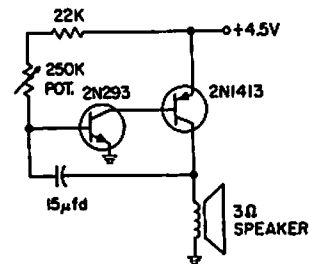
tion oscillators, overcoming their inherent instability. Used in frequency-division type of electronic organ tone generator.—R. F.

Woody, Jr., Clip Couples Neon Oscillators, *Electronics*, 39:9, p 77.

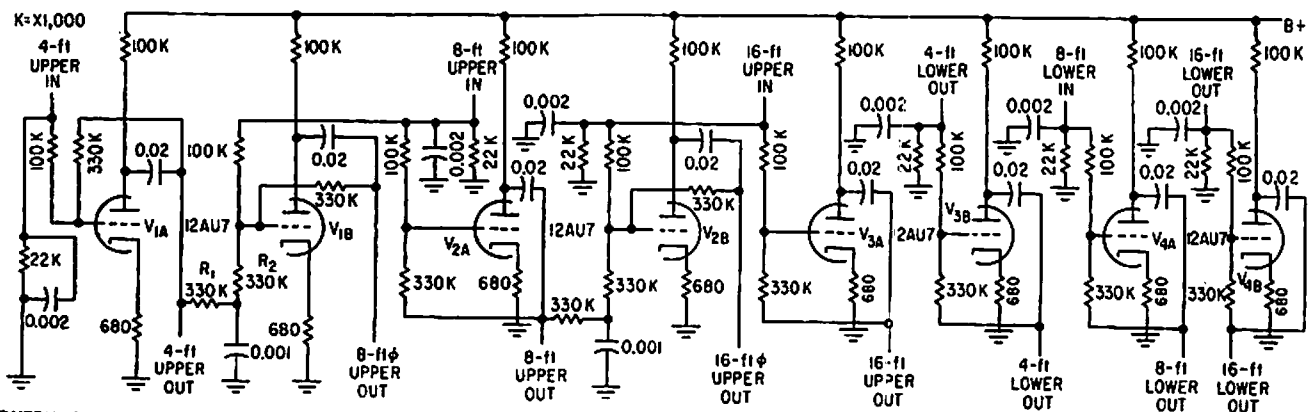


ELECTRONIC ORGAN FREQUENCY DIVIDER—UJT relaxation oscillator circuits reduce number of master oscillators needed and elimi-

nate large inductors.—J. F. Cleary and D. V. Jones, A Unijunction Frequency Divider, *EEE*, 12:5, p 52-53.



TWO-TRANSISTOR METRONOME—Rheostat provides rate adjustment.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 379.



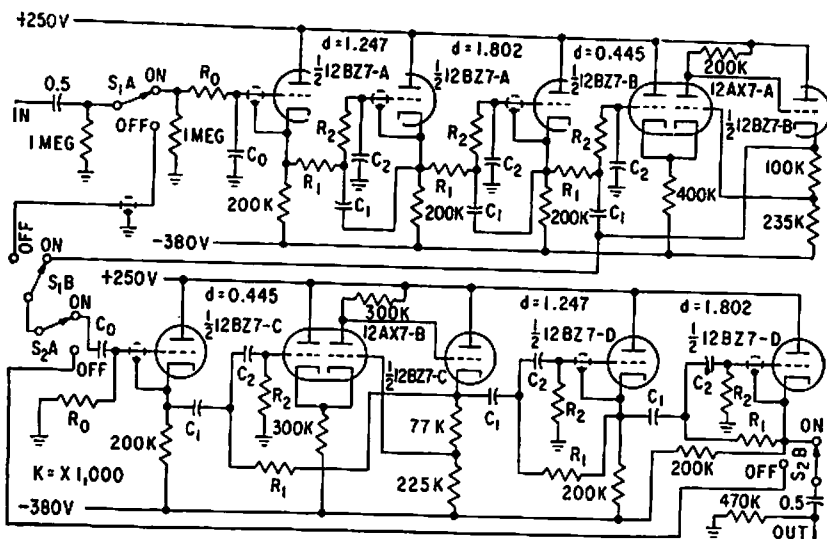
OUTPHASED TONE GENERATOR—Bus amplifiers located between keying-system outputs and formant filters provide outphased sig-

nals that lack even-harmonic content. Combinations of these produce organ tone colors called for by voicing panel.—R. H. Dorf,

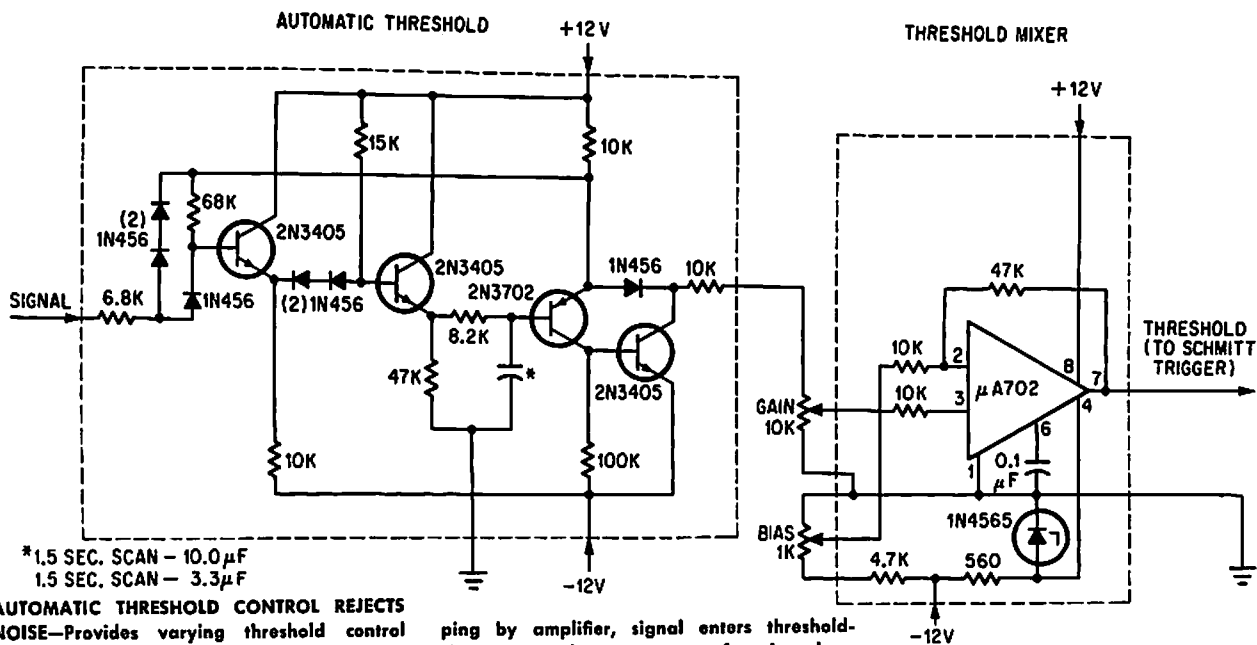
Electronic Organ Uses Neon Tone Generators, *Electronics*, 31:35, p 36-41.

CHAPTER 31

Filter Circuits



ACTIVE ADJUSTABLE-BANDPASS AUDIO FILTER—Has Butterworth attenuation characteristics and 42 db/octave cutoff slopes. Output is 50 v rms with low distortion, and dynamic range over 100 db. Second-order harmonic distortion is reduced by operating tube heaters at low voltage. Seven elements are varied simultaneously by switching different resistor and capacitor values to change cutoff frequencies. Article has three tables giving these values for high-pass cutoffs from 16 to 16,200 cps and low-pass cutoffs from 20 to 20,000 cps.—J. R. MacDonald, *Active Bandpass Filter* has Sharp Cutoff, *Electronics*, 31:33, p 84-87.

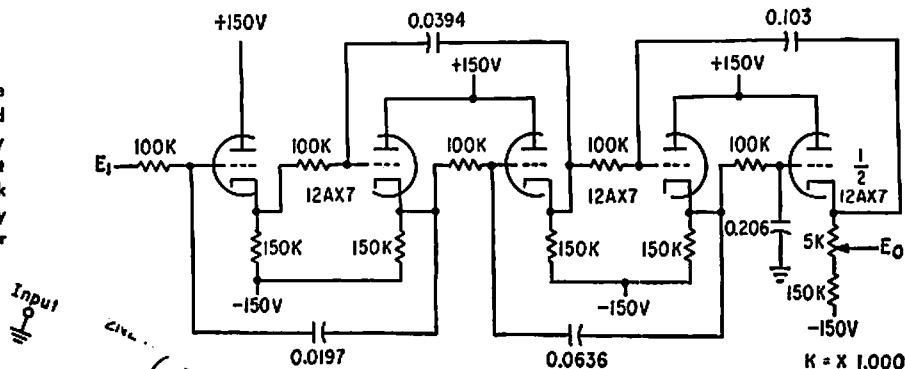


AUTOMATIC THRESHOLD CONTROL REJECTS NOISE—Provides varying threshold control voltage that causes detection threshold of celestial guidance system to operate at level slightly above background noise. After clip-

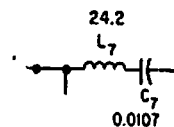
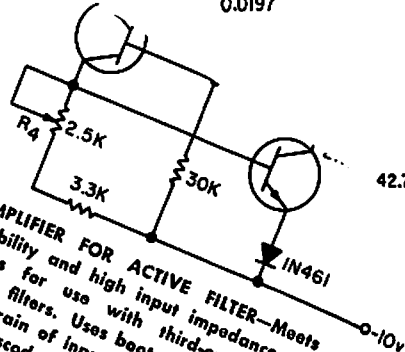
ping by amplifier, signal enters threshold-shaping unit that operates as fast-rise, slow-fall agc.—R. L. Lillestrand, J. E. Carroll, and J. S. Newcomb, Automatic Celestial Guidance,

Part 2: New Challenge to Designers' Ingenuity, Electronics, 39:7, p 94-105.

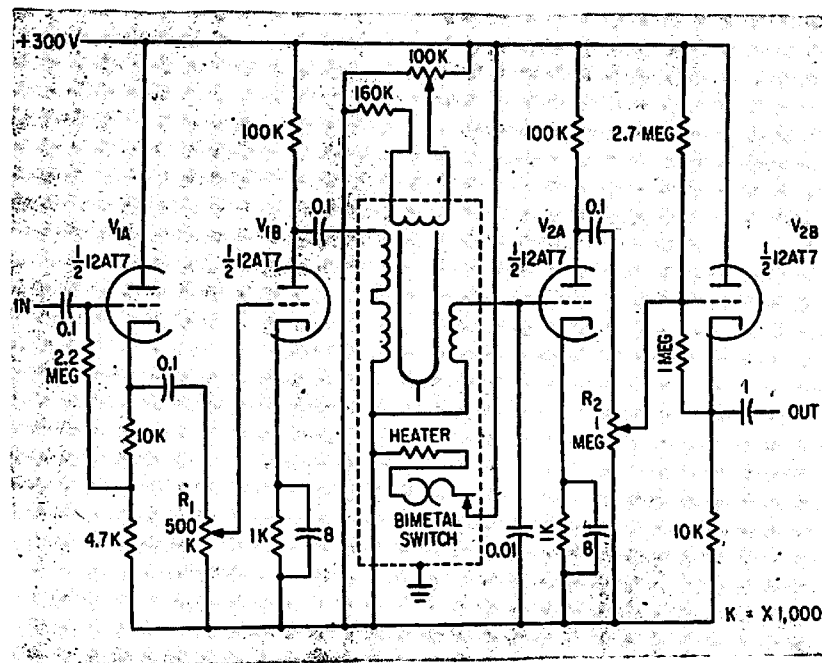
ACTIVE BUTTERWORTH R-C FILTER—Article gives design procedure for selecting R and C values for active filters characterized by zero output either at zero frequency or at infinite frequency. Symmetry of network transfer function allows choice of values by coefficient matching technique. For low-pass filter, down 50 db at shown.—R. E. Bach, Jr., Selection for Active Filters, Electronics



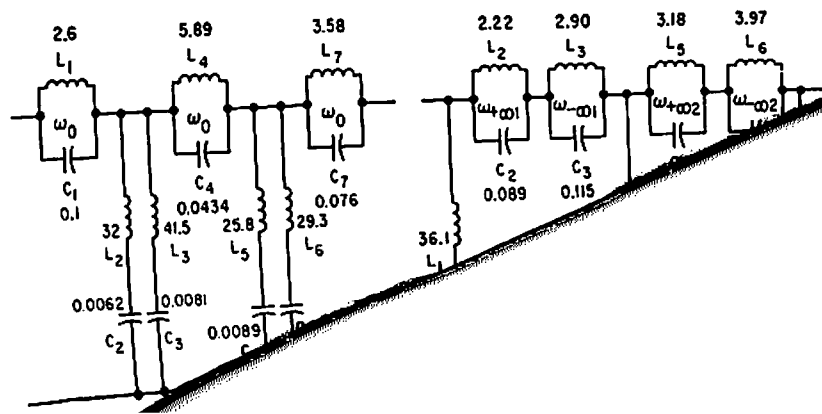
FET AMPLIFIER FOR ACTIVE FILTER—Meets gain stability and high input impedance requirements for use with third-order low-pass active filters. Drain of input fet drives pnp transistor in cascade to reduce input capacitance.—L. J. Savin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 105.



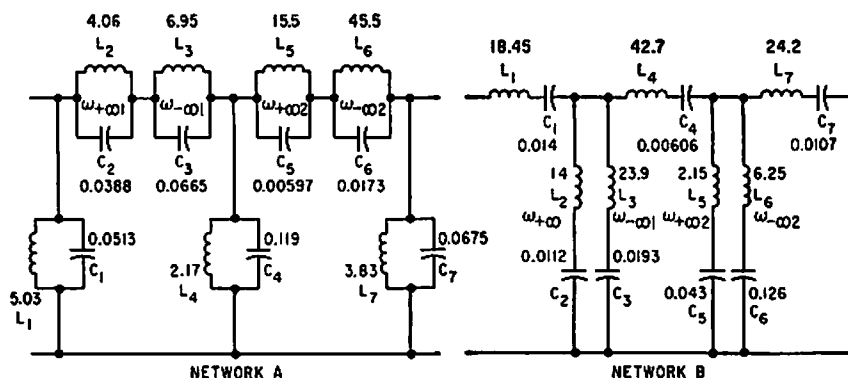
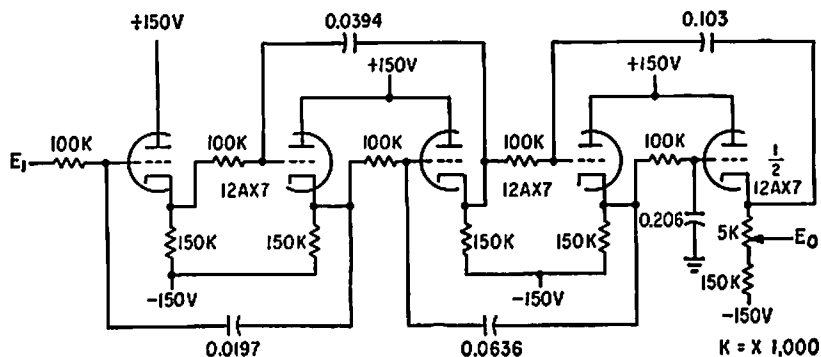
CRYSTAL RADIOTELEGRAPH I-F FILTER—Voltage-controlled varactor diode D1 permits remote location of potentiometer used for phasing adjustment. Circuit can be used for any i-f value from 100 kc to 1.6 Mc. H. Olson, Remotely Tuned Crystal Filters, Notes Tuned Transformer, Electronics, 173.



ADJUSTABLE 400-CPS TUNING-FORK FILTER—Tuning-fork frequency is adjusted by varying current in extra magnet coils facing ends of tines. Current change of 1 ma in frequency-adjust coils gives frequency change of 50 parts per million. Input and output cathode followers isolate filter from rest of circuit. Drive and pickup amplifiers cancel fork insertion loss.—J. J. O'Connor, Tuning-Fork Audio Filter Tunes Electrically, *Electronics*, 33:49, p 66-67.

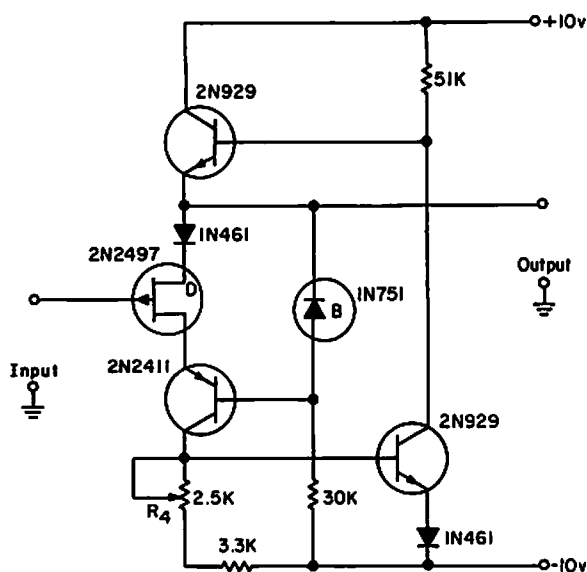


ACTIVE BUTTERWORTH R-C FILTER—Article gives design procedure for selecting R and C values for active filters characterized by zero output either at zero frequency or at infinite frequency. Symmetry of network transfer function allows choice of values by coefficient matching technique. Fifth-order low-pass filter, down 50 db at 70 cps, is shown.—R. E. Bach, Jr., *Selecting R-C Values for Active Filters*, *Electronics*, 33:20, p 82-85.

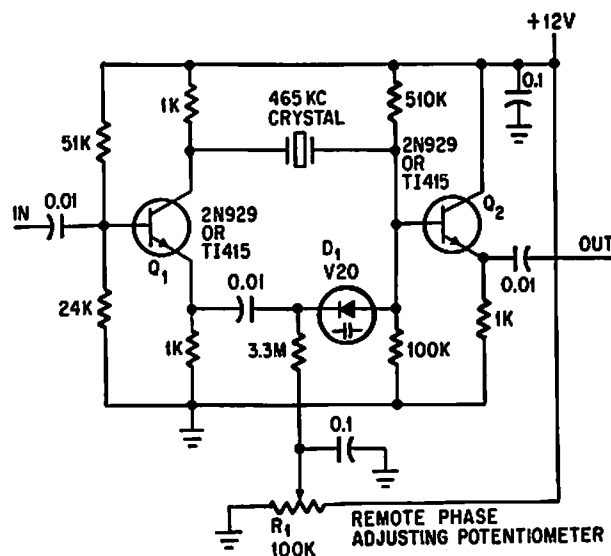


ZOBEL BAND-PASS FILTER—Both examples give at least 40 db attenuation below 7,500 cps and above 12,500 cps, for 600-ohm

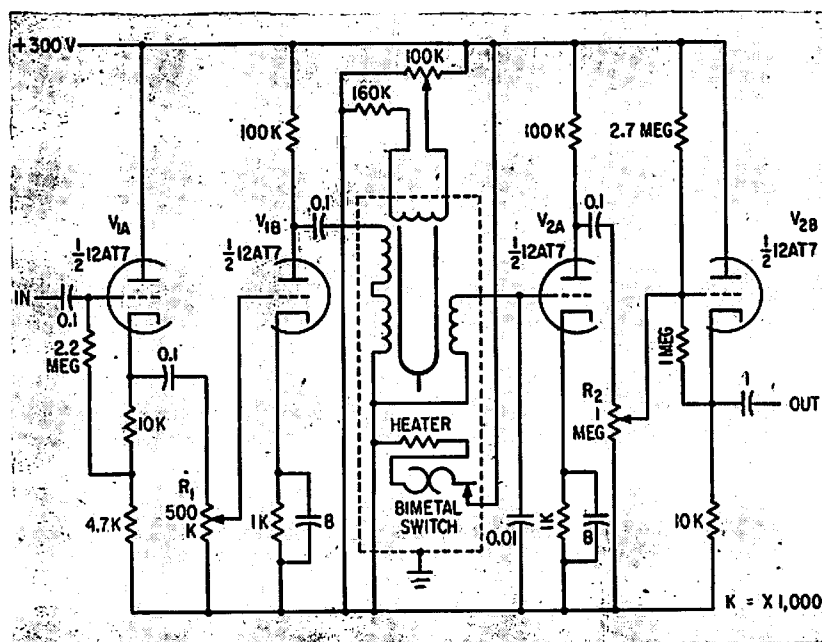
source and load resistances.—K. Lichtenfeld, *Method for Simplifying Filter Design, Electronics*, 33:21, p 96-99.



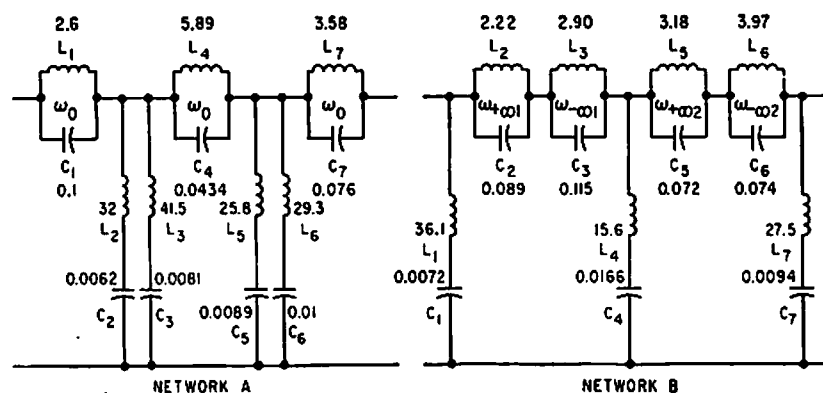
FET AMPLIFIER FOR ACTIVE FILTER—Meets gain stability and high input impedance requirements for use with third-order low-pass active filters. Uses bootstrapped source-follower. Drain of input fet drives pnp transistor in cascode to reduce input capacitance.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 105.



CRYSTAL RADIOTELEGRAPH I-F FILTER—Voltage-controlled varactor diode D1 permits remote location of potentiometer used for phasing adjustment. Circuit can be used for any i-f value from 100 kc to 1.6 Mc by selecting crystal with desired frequency.—H. Olson, Remotely Tuned Crystal Filter Eliminates Tuned Transformer, *Electronics*, 38:23, p 113.

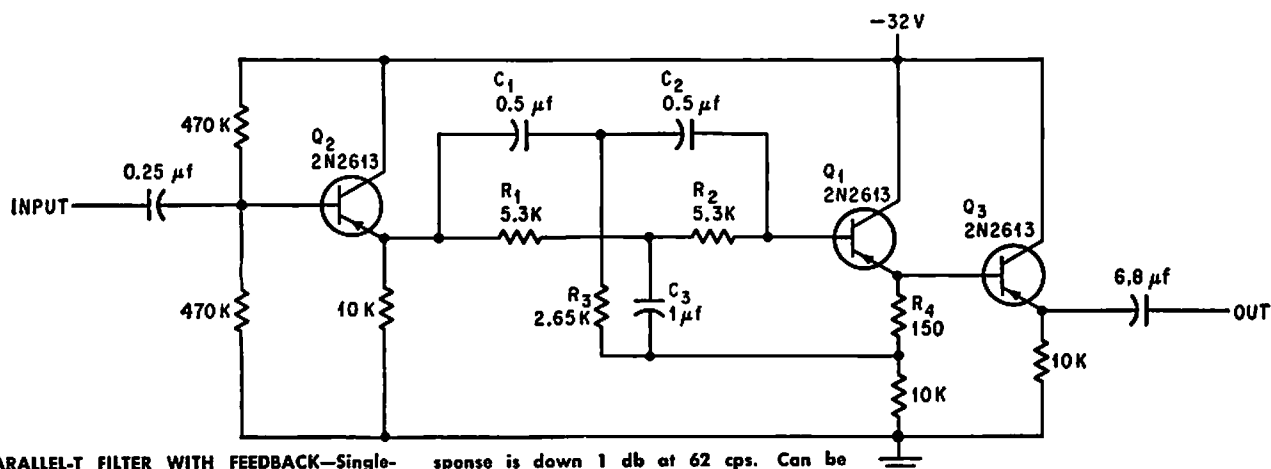


ADJUSTABLE 400-CPS TUNING-FORK FILTER—Tuning-fork frequency is adjusted by varying current in extra magnet coils facing ends of tines. Current change of 1 ma in frequency-adjust coils gives frequency change of 50 parts per million. Input and output cathode followers isolate filter from rest of circuit. Drive and pickup amplifiers cancel fork insertion loss.—J. J. O'Connor, Tuning-Fork Audio Filter Tunos Electrically, *Electronics*, 33:49, p 66-67.



ZOBEL BAND-ELIMINATION FILTER—Both examples give at least 40 db attenuation between 8,410 cps and 11,150 cps, for 600-

ohm source and load resistances.—K. Lichtenfeld, Method for Simplifying Filter Design, *Electronics*, 33:21, p 96-99.



PARALLEL-T FILTER WITH FEEDBACK—Single-transistor feedback circuit Q2 reduces high attenuation in passband that severely limits conventional 60-cps T-notch filter. Filter re-

sponse is down 1 db at 62 cps. Can be used in reproducing stereo tape, where it will salvage signals normally buried far below noise level of original tape recording.

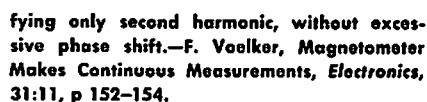
—J. Strattan, Feedback Improves Parallel-T Filter, *Electronics*, 39:18, p 99.



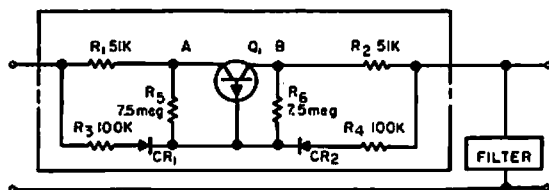
88-90.



p 96-99.

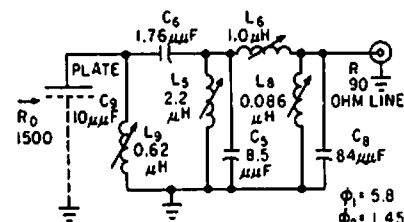


33:21, p 96-99.

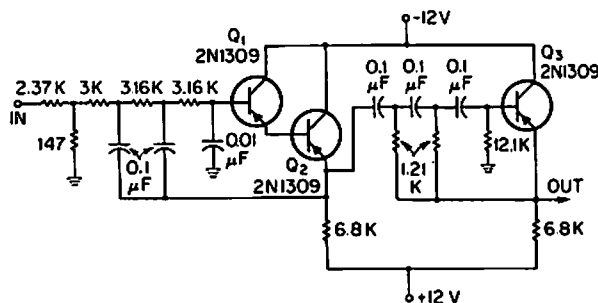


FET VOLTAGE-CONTROLLED RESISTOR—Field-effect transistor circuit (enclosed in dashed rectangle) serves as dropping resistor working into antiresonant a-f filter, to deliver

constant voltage to filter despite input voltage variations. Uses 2N2386 fet as Q1.—H. H. Nord, the FET as a Voltage-Controlled Resistor, *EEE*, 13:1, p 65.

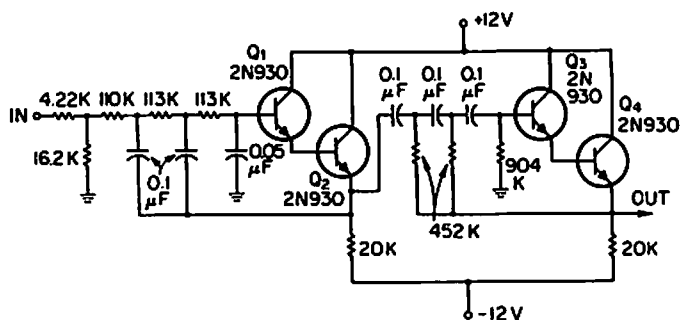


TRIPLE-TUNED 90-OHM OUTPUT—Used to provide bandpass between 55 and 65.5 Mc for signal from 10-mmfd plate capacitance.—R. B. Hirsch, How to Design Bandpass Triplax, *Electronics*, 32:34, p 41-44.

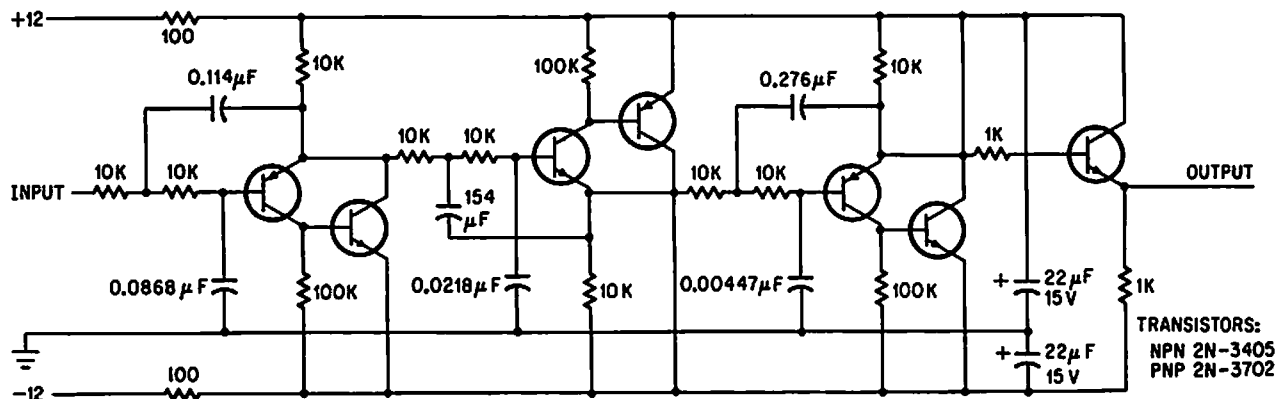


800-CPS ACTIVE BANDPASS—Provides bandwidth of 13 cps. Maximum gain is 24 db, and divider at input reduces this to 0 db.

Selectivity at 3-db points is 72 db/octave.—T. Mollinga, Active Bandpass Filters, *EEE*, 14:8, p 115-119.



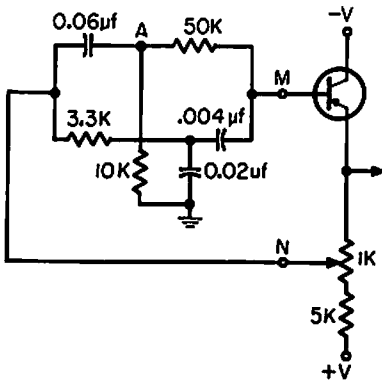
7-CPS ACTIVE BANDPASS—Band width is 1.6 cps for center frequency of 7 cps.—T. Mollinga, Active Bandpass Filters, *EEE*, 14:8, p 115-119.



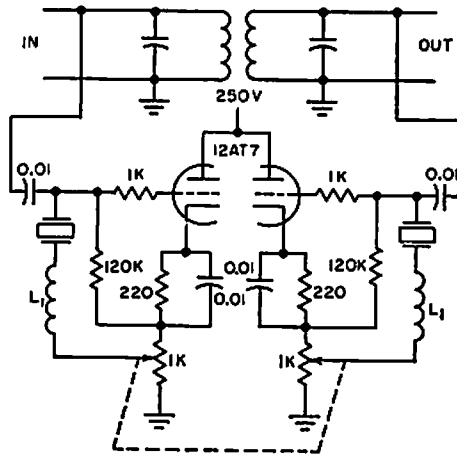
LOW-PASS FILTER—Unwanted short pulses from shot noise in celestial guidance photomultiplier are removed by active low-pass filter having constant phase shift over pass

band. Active filter avoids bulky inductors and impedance-matching problems. Filter is modified 6th-order Bessel type, called a Paynter filter.—R. L. Lillestrand, J. E. Carroll, and J. S.

Newcomb, Automatic Celestial Guidance, Part 2: New Challenge to Designers' Ingenuity, *Electronics*, 39:7, p 94-105.

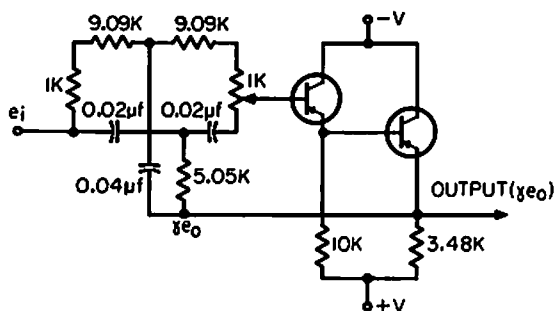


800-CP5 OSCILLATOR WITH PARALLEL-T FILTER—R-C network in feedback loop determines frequency of oscillation.—T. Mollinga, *Active Parallel-T Networks*, *EEE*, 14:4, p 93-98.

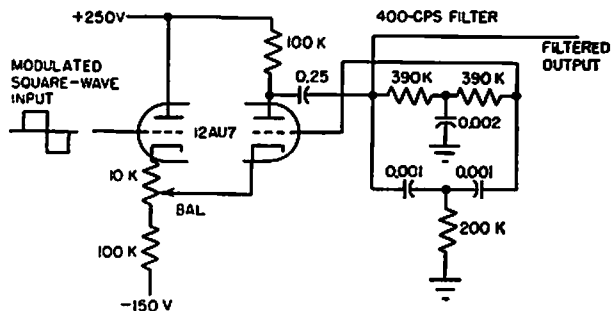


VARIABLE-BANDWIDTH 848-KC CRYSTAL FILTER—High-Q unbalanced crystal filter is easy to adjust over appreciable frequency range. Can be used in f-m oscillators, signal genera-

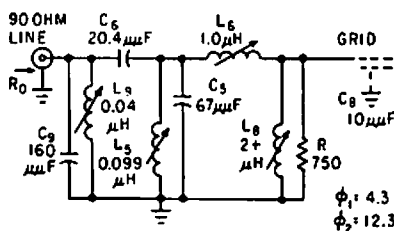
tors, and i-f amplifiers, as well as in variable-bandwidth filters.—J. C. Seddon, *Stable Crystal Filter is Parallel Resonant*, *Electronics*, 31:11, p 155-156.



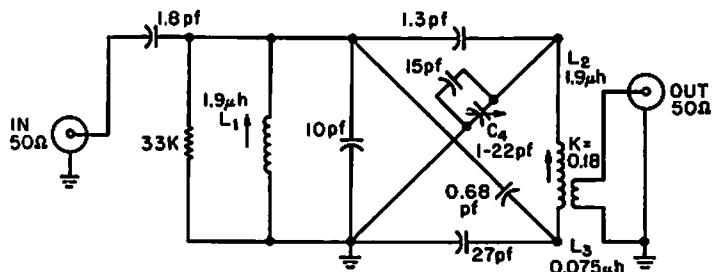
ACTIVE 800-CPS PARALLEL-T FILTER—Potentiometer adjusts amount of rejection to compensate for tolerances of components. Second emitter-follower provides lower output impedance so feedback to network is more effective in sharpening notch of filter characteristic and in decreasing phase shift around null frequency. Used in servo systems.—T. Mollinga, *Active Parallel-T Networks*, *EEF*, 14:4, p 93-98.



TWIN-T 400-CPS FILTER—Used with modulators to increase signal-noise ratio. Filter is tuned to 400 cps, and eliminates other frequencies by feeding them back. Q of filter is 6. Output is low-distortion sine wave in phase with input. Frequency regulation of carrier signal should be better than 1% or filter will introduce phase shift.—L. S. Kilvans, *Modulators for Automatic Control Systems, Electronics*, 31:1, p 82-84.

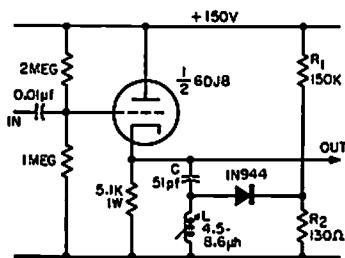


TRIPLE-TUNED 90-OHM INPUT—Article gives design procedure. Example shown passes signals between 55 and 65.5 Mc.—R. B. Hirsch, *How to Design Bandpass Triples*, *Electronics*, 32:34, p 41-44.

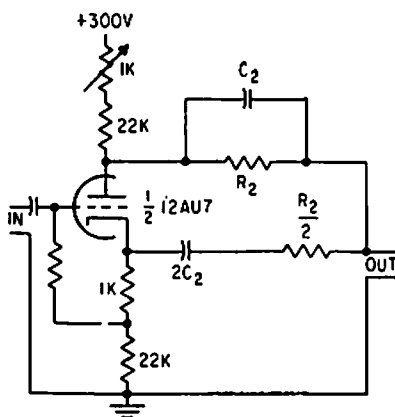


LATTICE COUPLING OF DOUBLE-TUNED FILTER—Permits adjusting coupling between input and output resonant circuits to compensate for stray reactances and variations in component values. Used in 30-Mc i-f

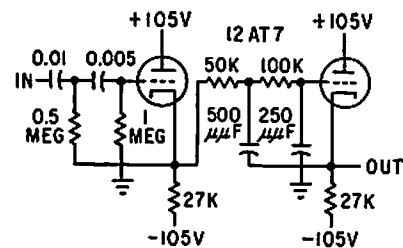
amplifier requiring 1-Mc bandwidth. One side of variable capacitor is grounded, permitting convenient mechanical design.—J. R. Grindon, Lattice Coupling of Resonant Circuits, *EEE*, 13:6, p 53-55.



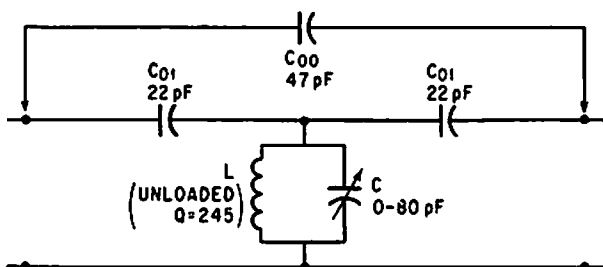
DYNAMIC NOTCH FILTER—Will trap out 10-Mc noise while passing 10-Mc signal in heterodyne frequency converter used to extend measurement range of 10-Mc counter. Operation is based on difference in level of noise and desired signal. Dynamic action of filter nulls out low-level noise, but filter disappears in presence of desired high-level signal.—H. T. McAleer, *Dynamic Notch Filter*, *EEE*, 10:9, p 90-91.



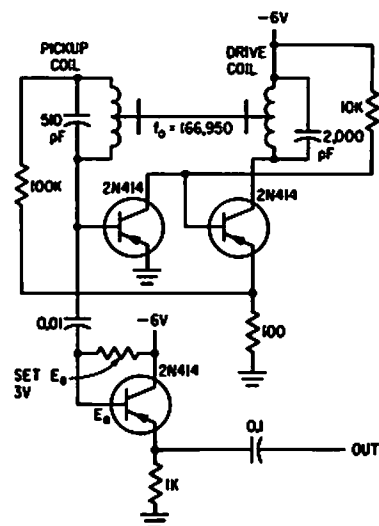
WIEN-BRIDGE FILTER—Does not have high Q, but provides good rejection (40 db attenuation with 1% tolerance components and 60 db with 0.1% tolerance components).—J. K. Goodwin, *Wien Bridge Forms Rejection Filter*, *Electronics*, 32:1, p 58-59.



CASCADED HIGH AND LOW-PASS A-F—Slope can be any desired multiple of 12 db per octave, with insertion loss less than 2 db. Corner frequencies are 200 radians per sec (32 cps) and 40,000 radians per sec (6,370 cps).—W. D. Fryer, *How to Design Low Cost Audio Filters*, *Electronics*, 32:15, p 68-70.

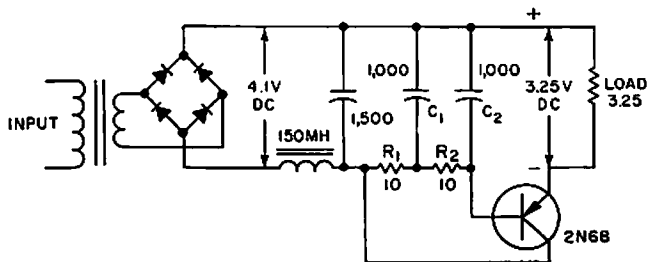


GENERAL FILTER—Bridging conventional bandpass filter with single capacitor C00 converts to general filter having both sharp pass and reject behavior at adjacent frequencies. For values shown, bandpass occurs at 20 Mc and peak rejection frequency is 19.15 Mc.—R. Kurzkro, *Single Component Changes Bandpass into General Filter*, *Electronics*, 39:8, p 95-96.



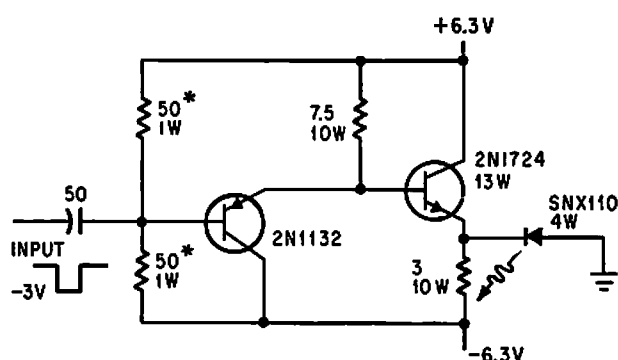
TRANSISTORIZED MAGNETOSTRICTION BAND-PASS FILTER—Three transistors and filter give stable fixed-frequency oscillator, with overall gain of 20 db and maximum linear output of 1 v rms.—E. J. Neville, Jr., *Designing Magnetostriction Filters*, *Electronics*, 33:51, p 88-89.

TRANSISTOR AS SMOOTHING FILTER—Single junction transistor in filter network of low-voltage power supply permits use of smaller filter capacitors and chokes. Used in calibrating d-c meters up to 1 amp, at which residual peak-to-peak ripple values are 0.0015 amp and 0.005 v.—F. Oakes and E. W. Lawson, *Transistor Filters Ripple*, *Electronics*, 31:15, p 95.

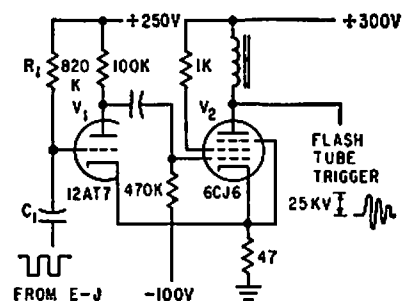


CHAPTER 32

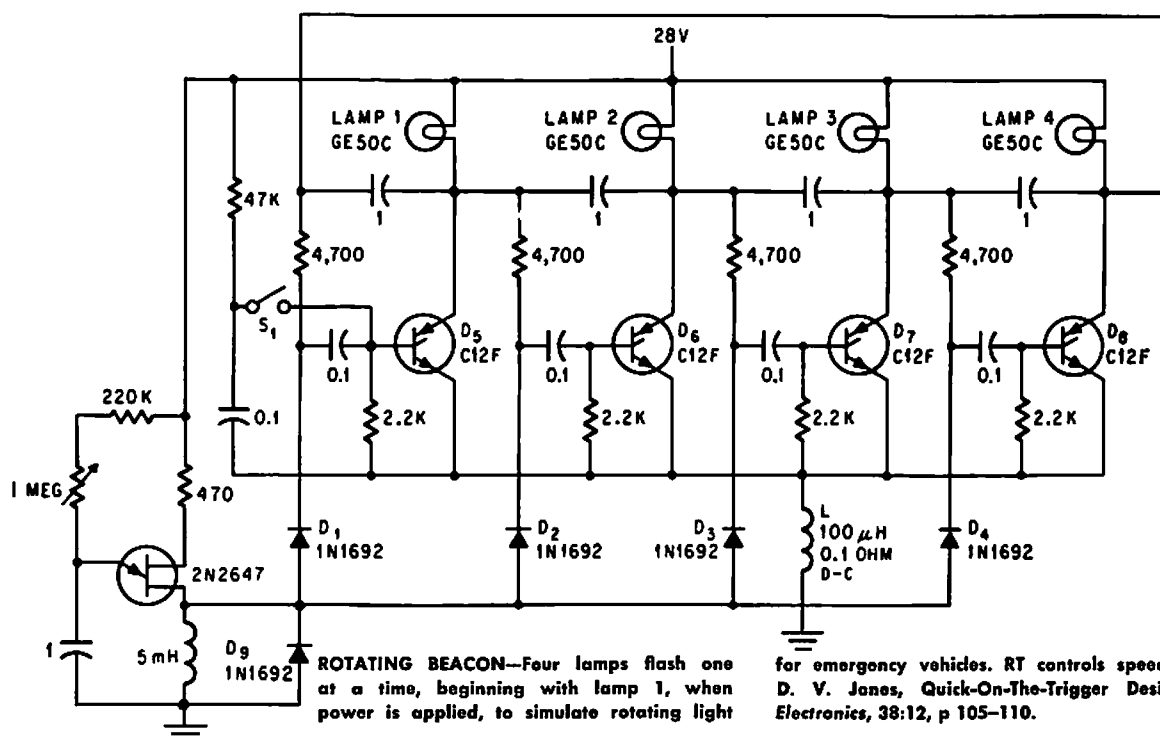
Flash Circuits



TWO-TRANSISTOR CURRENT-MODE SWITCH
—With two separate voltage supplies, input pulse triggers transistors to give 2-amp pulses for driving light-emitting diode.—E. L. Bonin, *Drivers for Optical Diodes*, *Electronics*, 37:22, p 77-82.

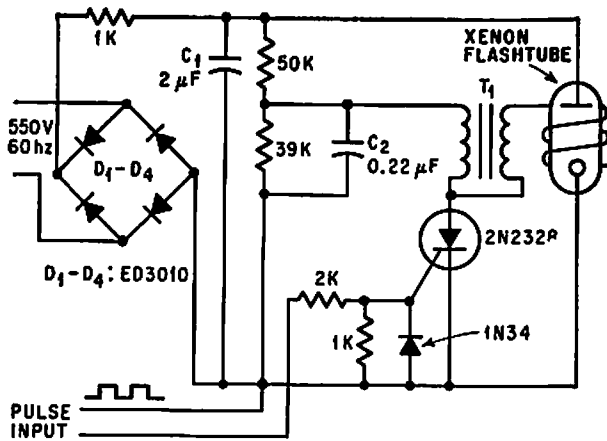


HIGH-VOLTAGE FLASH PULSER—Two identical pulse generators are used to fire two flash tubes alternately in high-speed strobe. One unit is coupled to each plate of an Eccles-Jordan trigger, to produce required alternating trigger sequence.—L. H. Barrett, *New Circuit Improves Stroboscope Versatility*, *Electronics*, 32:32, p 116-118.



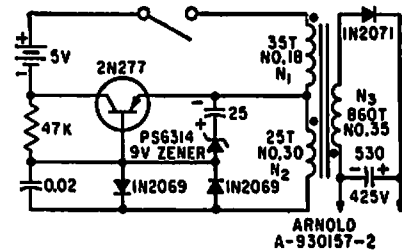
ROTATING BEACON—Four lamps flash one at a time, beginning with lamp 1, when power is applied, to simulate rotating light

for emergency vehicles. RT controls speed.—D. V. Jones, *Quick-On-The-Trigger Design*, *Electronics*, 38:12, p 105-110.



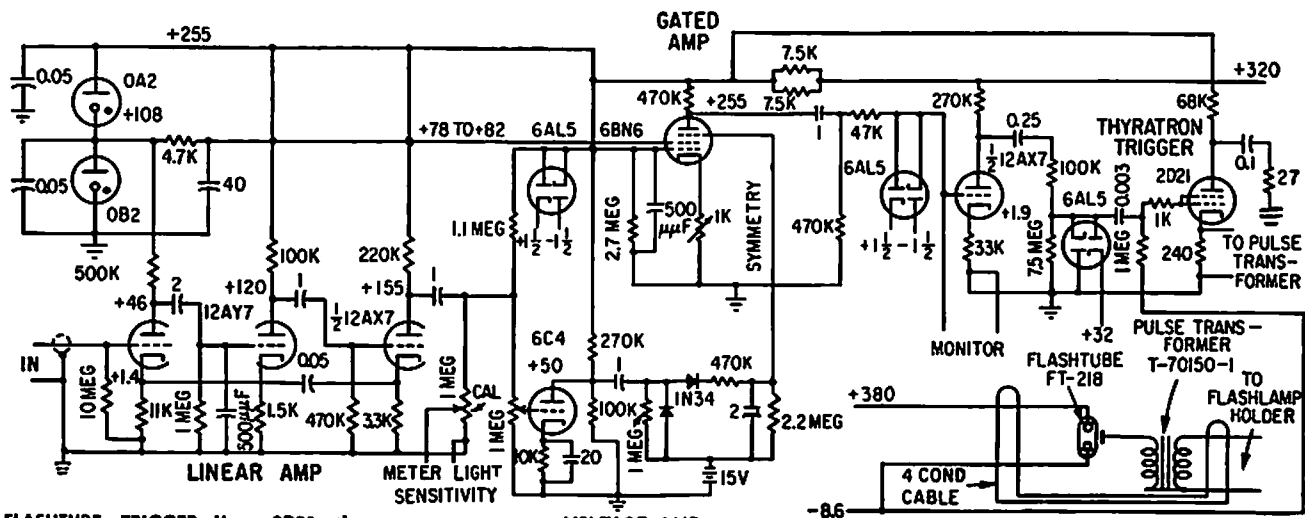
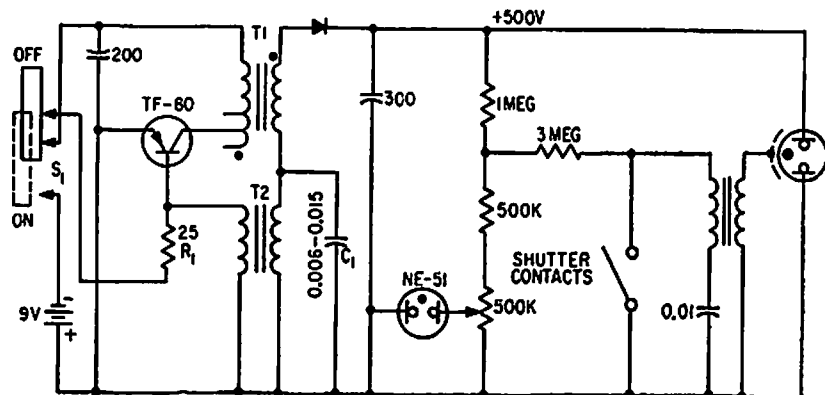
LOW-COST STROBE—Addition of silicon controlled rectifier in triggering circuit of \$20 automobile engine-timing strobe gives low-cost general-purpose stroboscope. Modification is completed by wrapping ten turns of No. 18 bare copper wire around flashtube and connecting one end to photoflash trans-

former T1, with other end unconnected. Can be triggered by 10-microsec 3-v pulse at 5 ma, up to 25 times per second. Flash duration is 100 microsec.—A. C. Eberle, Low-Cost Strobe Built with SCR in Trigger, *Electronics*, 39:14, p 80-81.



PHOTOFLASH SUPPLY—Flyback or ringing-choke oscillator is free-running when voltage on regulator capacitor C2 is less than zener voltage for reference diode D2, but converter action is halted when desired output voltage is reached. Circuit then periodically replaces charge lost by capacitor leakage. Energy conversion efficiency can therefore exceed 50% theoretical upper limit of most conventional photoflash circuits.—R. J. Sherin, Efficient Photoflash Power Converter, *Electronics*, 33:4, p 57.

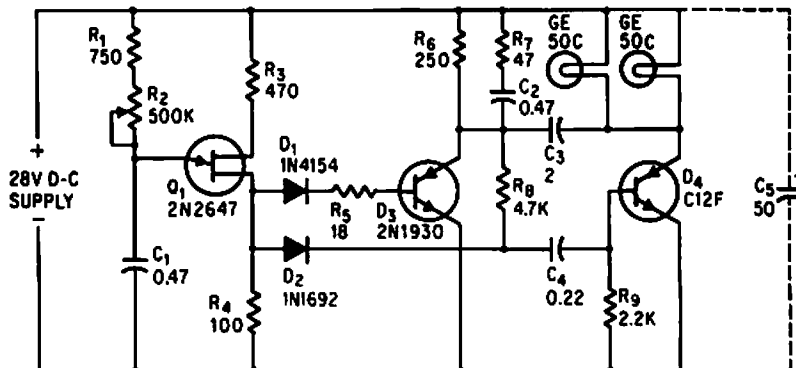
BLOCKING-OSCILLATOR SUPPLY—Uses modified blocking oscillator to obtain square-wave switching at 4,200 cps. Charges 300-mfd capacitor to 500 v in 5 to 10 sec from 9-v dry cell that can deliver up to 700 flashes. To start oscillator, S1 momentarily connects R1 to negative side of battery.—H. A. Manoogian, Transistor Photoflash Power Converters, *Electronics*, 31:35, p 29-31.



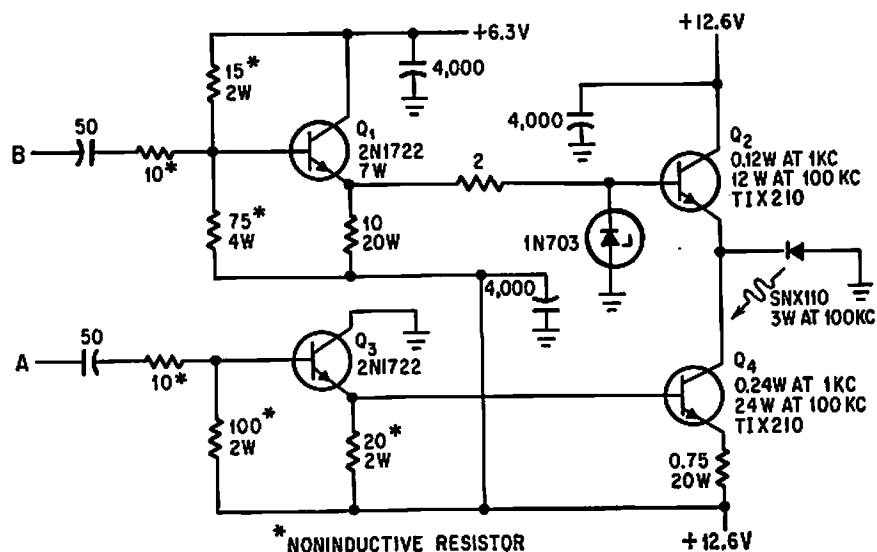
FLASHTUBE TRIGGER—Uses 2D21 thyatron switch to discharge capacitor across primary of high-voltage pulse transformer whenever thyatron is fired. Resulting pulse is applied to trigger electrode of flashtube. Fires re-

liably up to 60 times per second. Used to illuminate number on aluminum indexing wheel on spinning shaft of magnetic gage used to locate exact position of ferromagnetic

barrier encapsulated in shaped-charge container.—P. Seward, Magnetic Gage Locates Encased Metal Parts, *Electronics*, 31:33, p 65-67.

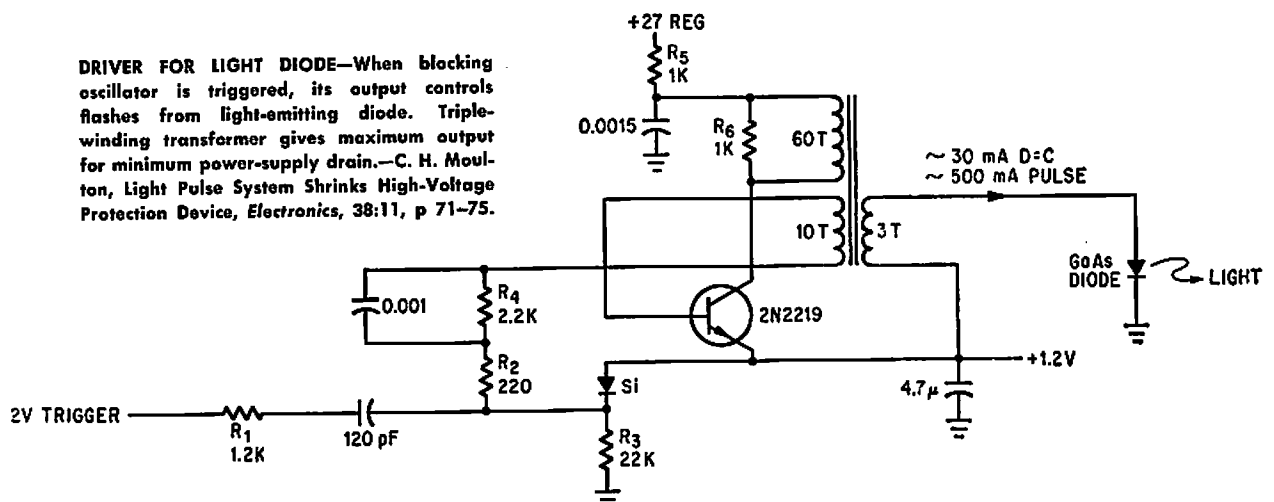


50% DUTY CYCLE—Provides 80 flashes per minute. Scr's conduct alternately in parallel inverter with capacitor commutation, and are triggered by free-running relaxation oscillator Q1. Flashing rate is determined by R1-R2-C1.—D. V. Jones, Quick-On-The-Trig-ger Design, *Electronics*, 38:12, p 105-110.

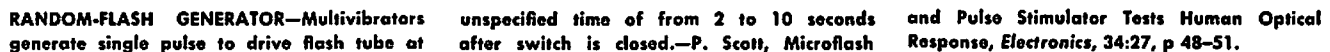
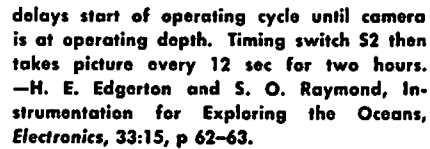


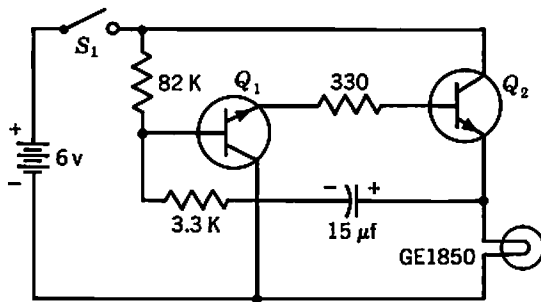
* NONINDUCTIVE RESISTOR
TRANSISTORS SWITCH 10-AMP PULSES FOR LIGHT-EMITTING DIODE—Input A must precede and follow B by 1 microsec to give 1-microsec width for 10-amp pulses driving

light-emitting diodes at repetition rates up to 100 kc.—E. L. Bonin, Drivers for Optical Diodes, *Electronics*, 37:22, p 77-82.



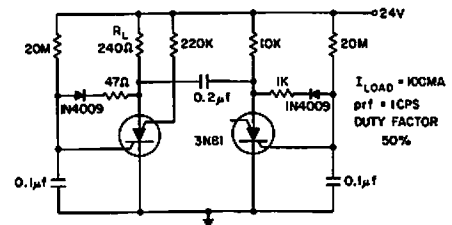
DRIVER FOR LIGHT DIODE—When blocking oscillator is triggered, its output controls flashes from light-emitting diode. Triple-winding transformer gives maximum output for minimum power-supply drain.—C. H. Moulton, Light Pulse System Shrinks High-Voltage Protection Device, *Electronics*, 38:11, p 71-75.



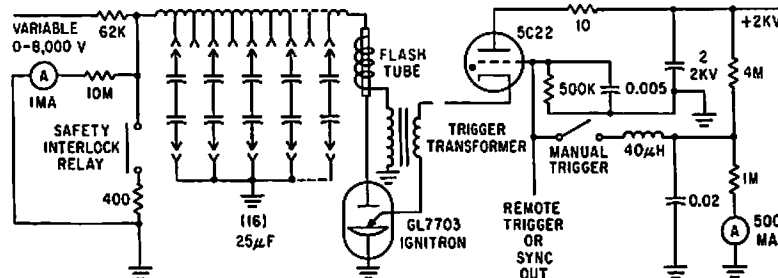


LOW-CURRENT FLASHER—Q1 is operated in inverted configuration for lower leakage current. Typical on time is 0.2 sec and off time 0.8 sec. Q1 is 2N1302 and Q2 is 2N1374. Can be used as construction barricade flasher, flashing single lamp at 1 cps

for up to 60 days on single battery. Use of solar-cell switch for S1, to turn off flasher automatically in daytime, will roughly double battery life in unattended locations.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 425.

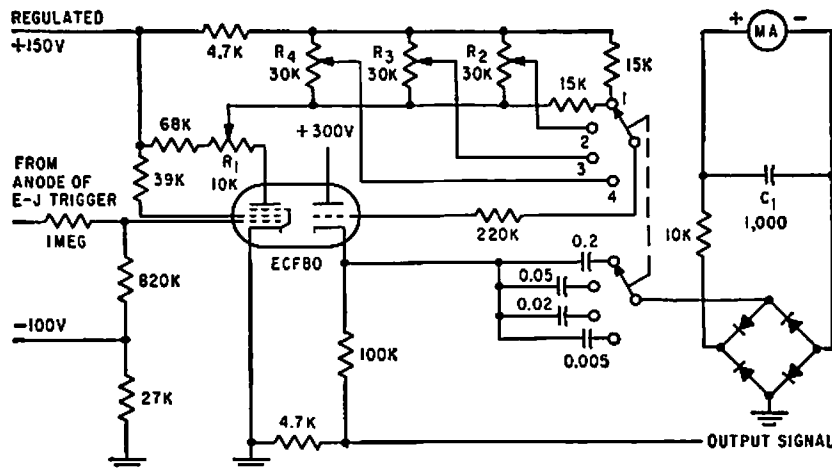


1-CPS FLASHER—When one scs triggers on, 0.2-mfd commutating capacitor turns off other scs and charges its gate capacitor to negative potential. At point in charging determined by 20-meg resistor, scs is retriggered. Battery power is delivered to load with 88% efficiency.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 434.



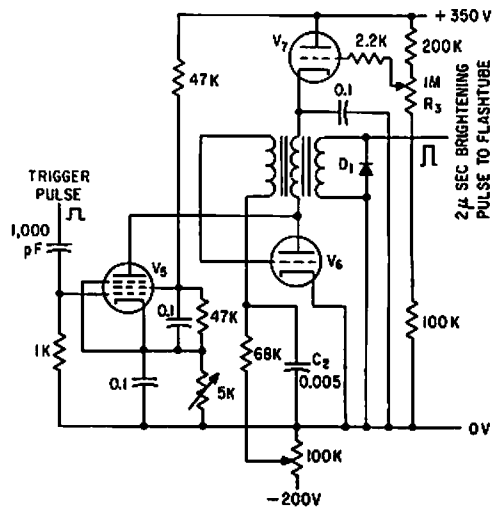
LASER FLASH TUBE SUPPLY—Variable pulse-forming network sends rectangular pulses of current through flash tube when network is discharged by ignitron used as switch. Resulting pump action on ruby crystal then

produces laser beam for optical ranging up to 3 miles.—M. L. Stitch, E. J. Woodbury and J. H. Morse, Optical Ranging System Uses Laser Transmitter, *Electronics*, 34:16, p 51-53.



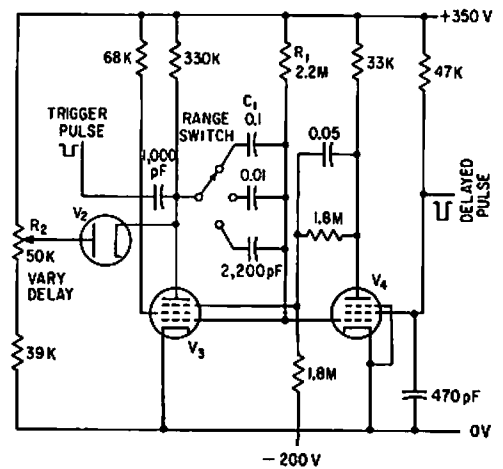
STROBE RATEMETER—Flash rate is metered by measuring mean charging current through capacitor supplied with constant-amplitude

pulse voltage.—L. H. Barrett, New Circuit Improves Stroboscope Versatility, *Electronics*, 32:32, p 116-118.

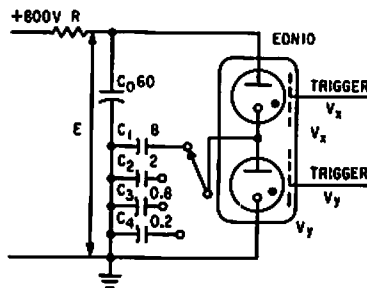


FLASH TUBE DRIVE—Generates low-impedance positive pulses having adjustable amplitude but constant duration, for driving flash tube to give same intensity-time characteristic

as other flash tubes having different colors. —P. Scott, *Microflash and Pulse Stimulator Tests Human Optical Response*, *Electronics*, 34:27, p 48-51.

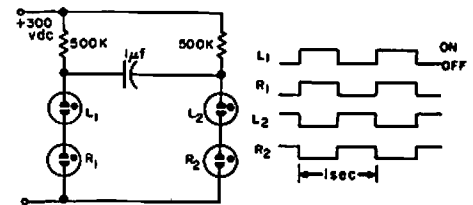


FLASH DELAY—Suppressor-gated sanatron pentode provides adjustable delay for changing spacing of flashes. —P. Scott, *Microflash and Pulse Stimulator Tests Human Optical Response*, *Electronics*, 34:27, p 48-51.



TWO-LAMP STROBE BOOSTS FLASH RATE—Two discharge tubes provide shared cycle of operation, to boost stroboscope firing rate to 1,000 flashes per second. Lamp circuit,

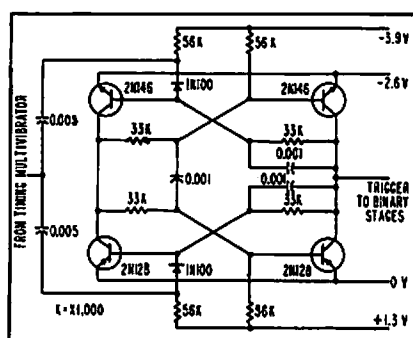
with range-switched discharge capacitors, receives triggers in alternation. —L. H. Barrett, *New Circuit Improves Stroboscope Versatility*, *Electronics*, 32:32, p 116-118.



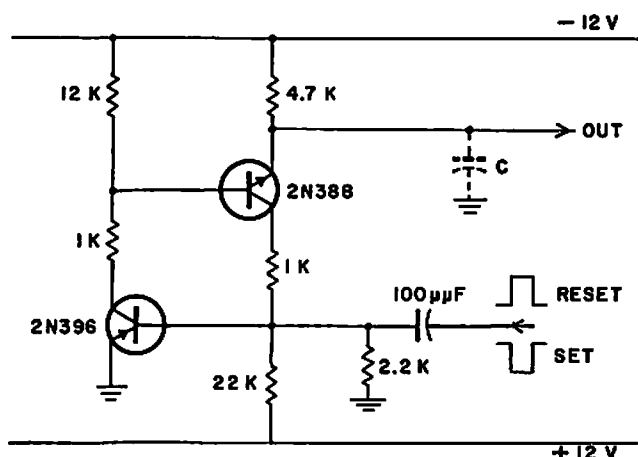
HALLOWEEN PUMPKIN BLINKER—Neon lamps blink alternately in eyes of pumpkin for 0.5-sec duration, with 0.5 sec between blinks. Will also serve as roadside blinker. Although NE2's can be used, LNE17's can be brighter and more effective. —More Glow-Lamp Circuits, *EEE*, 12:2, p 106-108.

CHAPTER 33

Flip-Flop Circuits

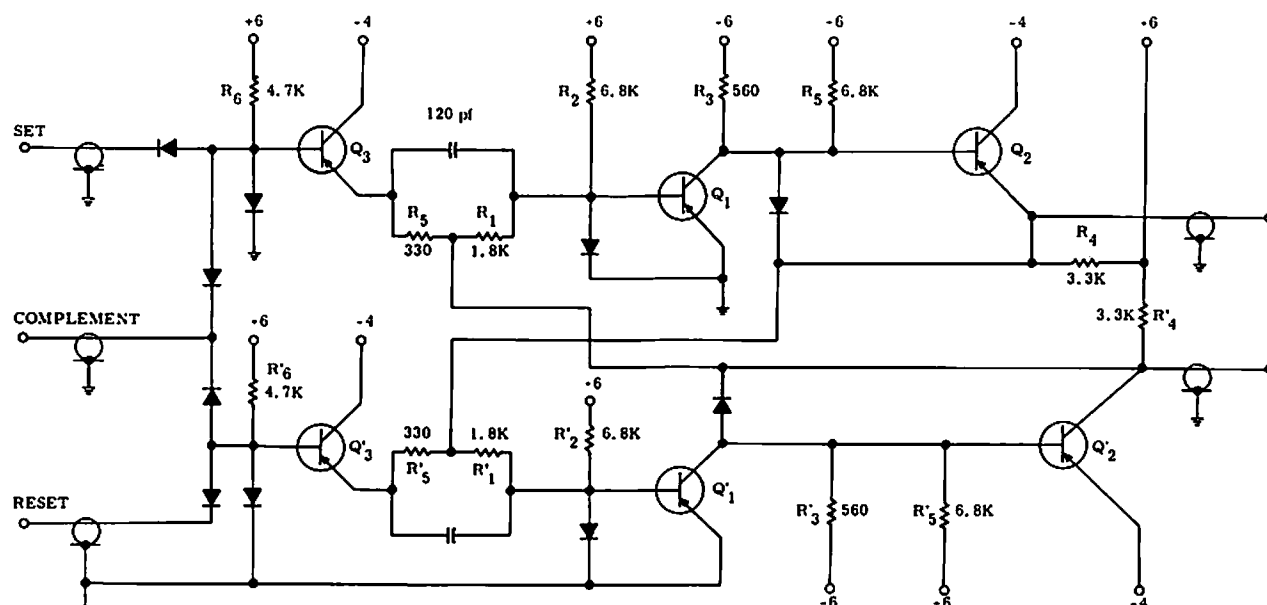


FLIP-FLOP FOLLOWER FOR COUNTER—Used to count down cycles of timing mvbr. Output of flip-flop follower is used in and circuit with matrix gates to turn on tone-burst oscillators or multivibrators during positive half-cycles only.—R. W. Rochelle, *Cyclops Cores Simplify Earth-Satellite Circuits, Electronics*, 31:9, 56–63.



NONSTALLING FLIP-FLOP FOR CAPACITIVE LOAD—Used for transferring data into storage having heavy capacitive load, such as long connecting wires. Complementary configuration, with load in emitter circuit of

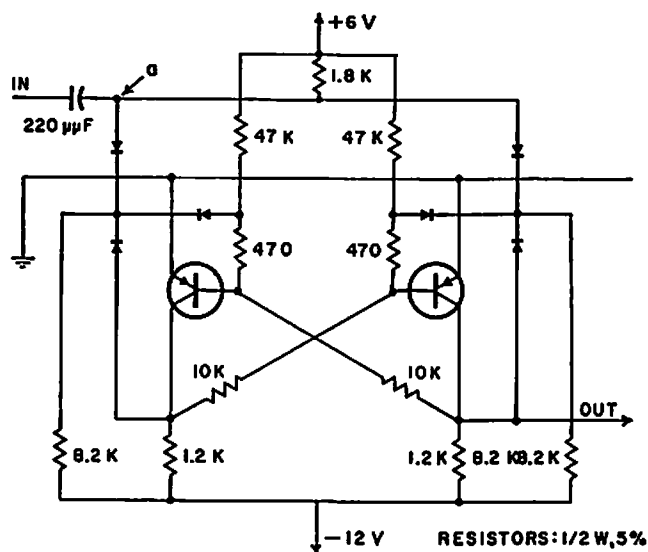
one transistor, makes stage trigger reliably in fraction of microsecond.—Non-Stalling Flip-Flop for Capacitive Load, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 213.



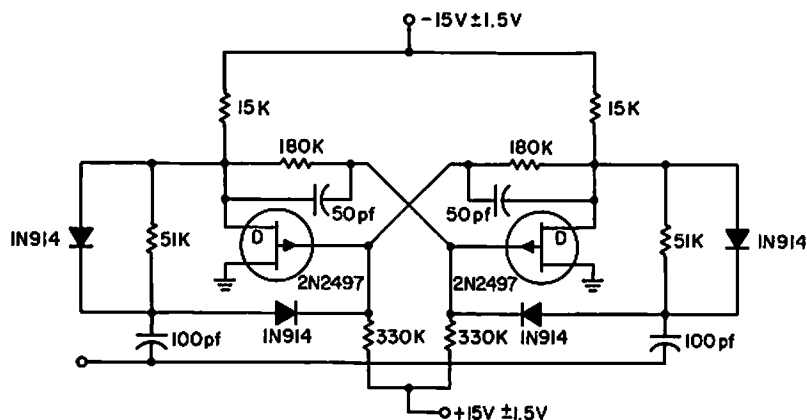
PAIRED INVERTERS—Cross-coupling of two basic inverters gives low-cost flip-flop using 2N711 germanium pnp mesa switching trans-

sistors. Flip-flop can be set and then reset, or run as counter using combined input. Close regulation is required for -4 v supply.

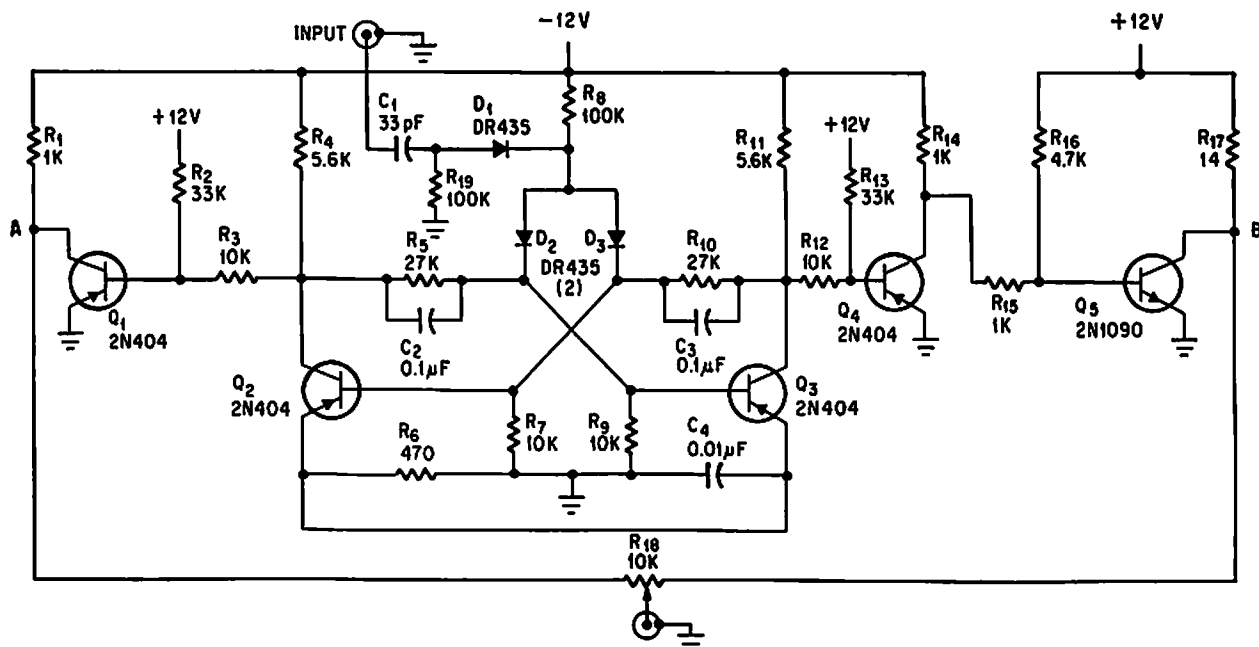
—P. A. McInnis, "Low-Cost Computer Circuits," Motorola Application Note AN-130, Nov. 1965.



BINARY FLIP-FLOP TURNS ON—Triggering is accomplished by turning transistors on, whereas in most similar circuits the transistors are turned off. Trigger pulse merely has to lower point a below ground for fraction of microsecond. Almost any diode and transistor can be used. Speed can be up to 10 Mc with high-speed transistors. Output fall time is fast.—Binary Flip-Flop Turns On, "Electronic Circuit Design Handbook," Mac-teller Pub. Corp., N.Y., 1965, p 214.



BASIC FET FLIP-FLOP—Connected as scale-of-two binary frequency divider, complete with steering diodes, speedup capacitors, and coupling capacitors. Used where speed is not primary consideration.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 89.

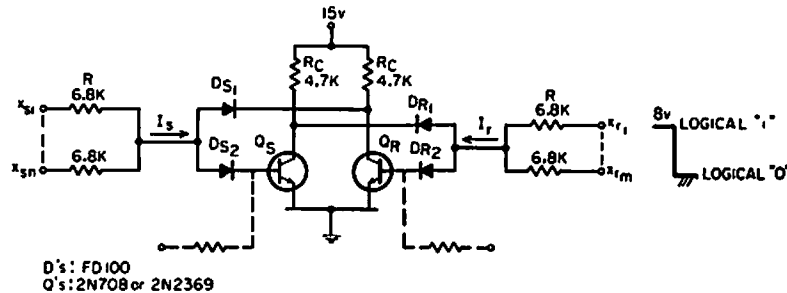
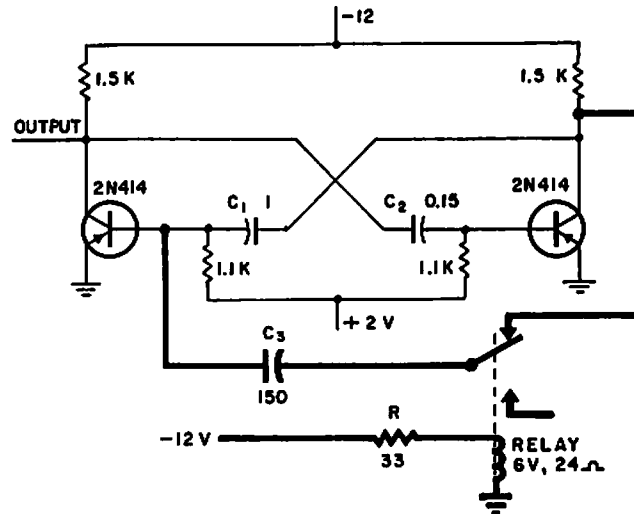


BIPOLAR OUTPUT—Output is clean square wave whose symmetry with respect to ground can be balanced by potentiometer

R18. When input is fed by pulse generator, output can be used to test frequency response of transistor circuits.—F. C. Ruegg,

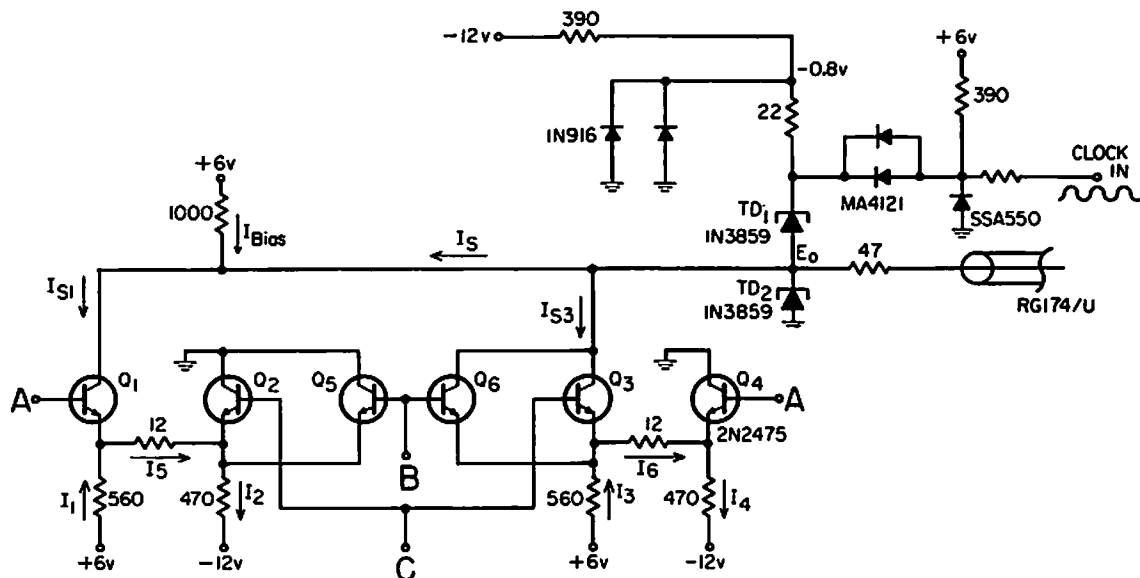
Multivibrator Provides Bidirectional Output Pulses, *Electronics*, 38:17, p 87.

STARTER FOR FLIP-FLOP—Free-running flip-flop (thin lines) has several desired features but will not start oscillation by itself. Output impedance is less than 1,800 ohms, period is 1 millise, and circuit is quite stable once in operation. Heavy lines show additions required for starting flip-flop when it is stalled. C3 (150 mfd) is in circuit for time period in which power supply voltage increases from 0 to 9 v after switch is closed. Relay pulls in at 9 v, to remove C3 from circuit after it has served its starting function.—Starter Circuit for Flip-Flop, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 229.



NOR-FUNCTION RESET—Modified flip-flop is set or reset when all input signals are low, corresponding to *nor*-function of input signals

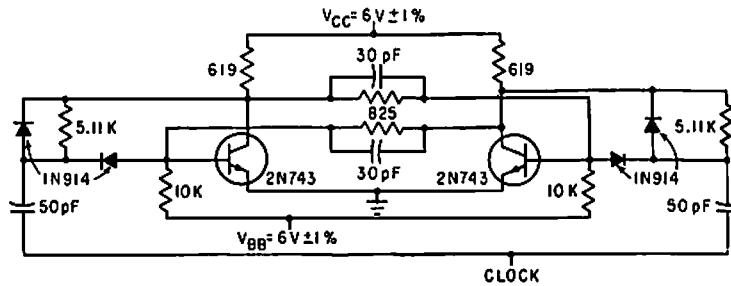
rather than the usual *or* function.—L. Mercurio, Flip-flop Operated by Input Signal NOR, *EEE*, 13:12, p 65.



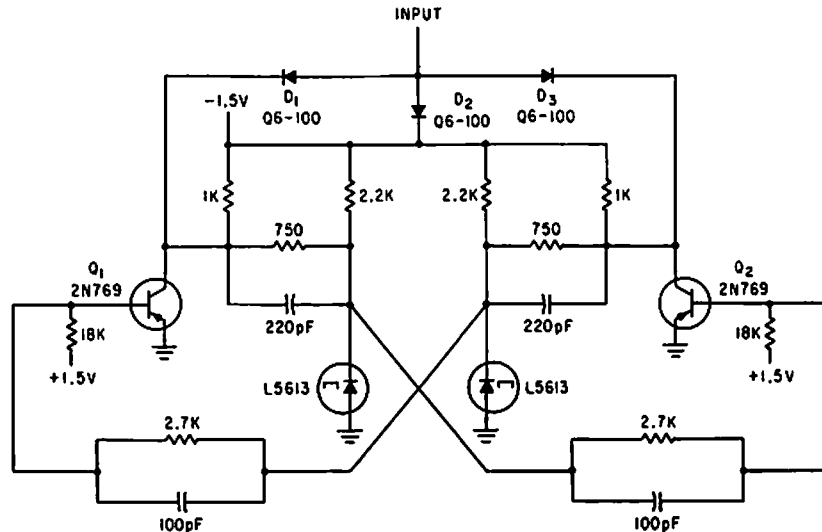
CURRENT MODE LOGIC FOR 500-MC GATED FLIP-FLOP—Uses 2N2475 transistors and 1N3859 tunnel diodes in current mode logic circuit having four inputs that can be ener-

gized to provide variety of desired logic functions. Supply of 0.8 v is obtained by passing current through two forward silicon diodes. Narrow 1-nsec clock pulse is gen-

erated by snap diode as close as possible to tunnel diode.—R. Glasgal, 500 MHz Transistor-TD Gated Flip Flop, *EEE*, 14:1, p 98-101.

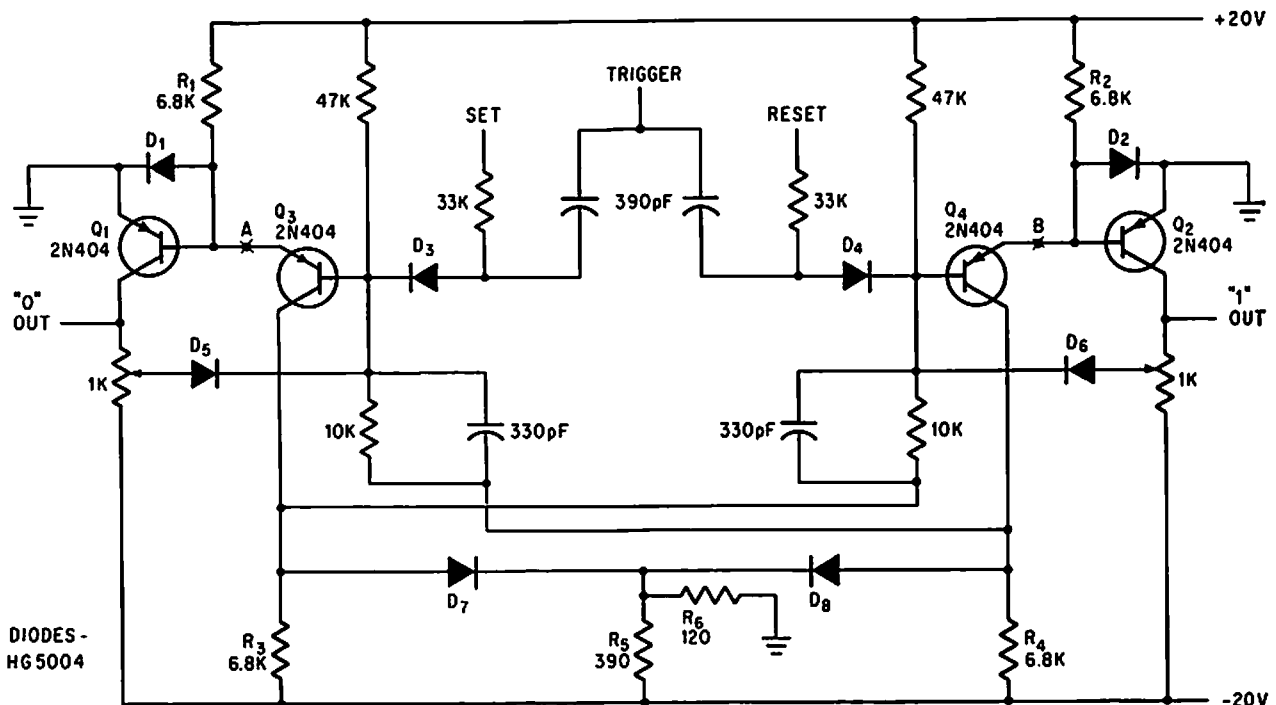


WIDE TEMPERATURE RANGE—Gives 10-Mc operation from -55°C to $+125^{\circ}\text{C}$, with 3-v clock trigger. Addition of emitter-follower will improve wave form and extend operation to $+150^{\circ}\text{C}$.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.



TUNNEL DIODE-COUPLED MICROENERGY FLIP-FLOP—Fan-in and fan-out capabilities per unit dissipation are improved up to ten times over existing types of logic. Use of

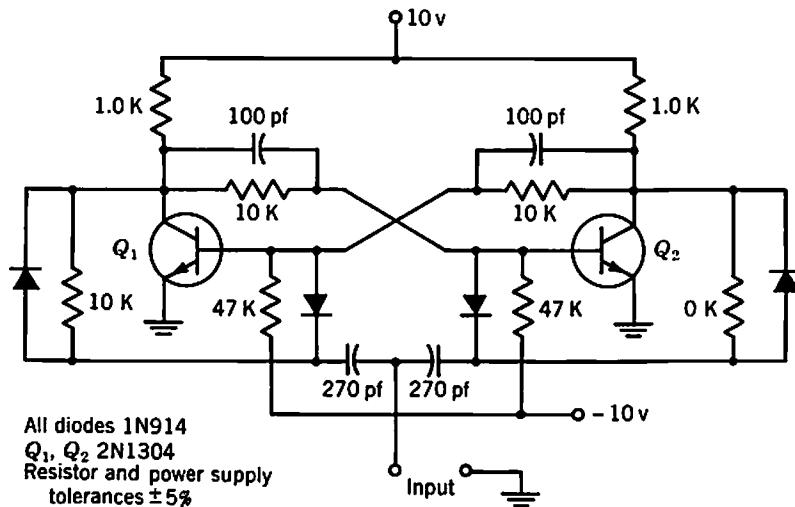
tunnel diodes allows low supply voltages, resulting in low circuit dissipation.—T. Maguire, Electro-Optical Developments Highlight NEREM, *Electronics*, 34:45, p 73-77.



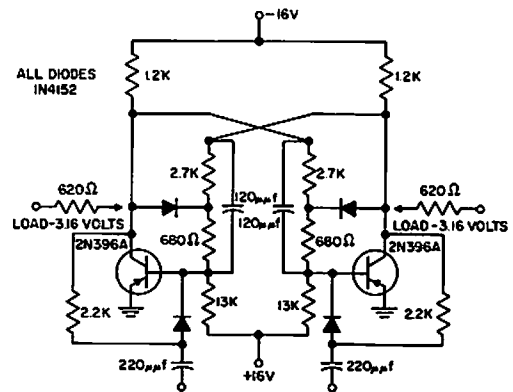
FAN-OUT TO 60—Fan-out of conventional flip-flop is quadrupled by driving bases of

amplifiers Q1 and Q2 directly from emitters of Q3 and Q4.—D. J. Grover, Modified Flip-

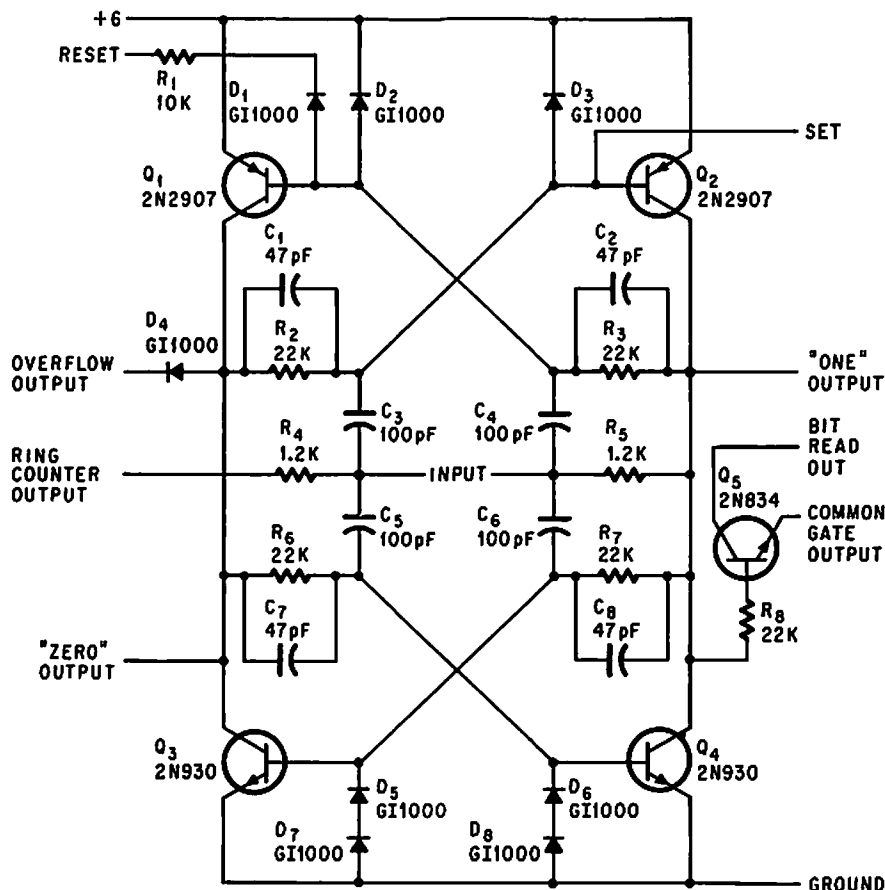
Flop Quadruples Fan-Out, *Electronics*, 38:26, p 67-68.



250-KC FLIP-FLOP—Basic design procedure is given. Circuit shown operates over temperature range of -55 to $+55^\circ\text{C}$ with input frequency of 250 kc.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 377.



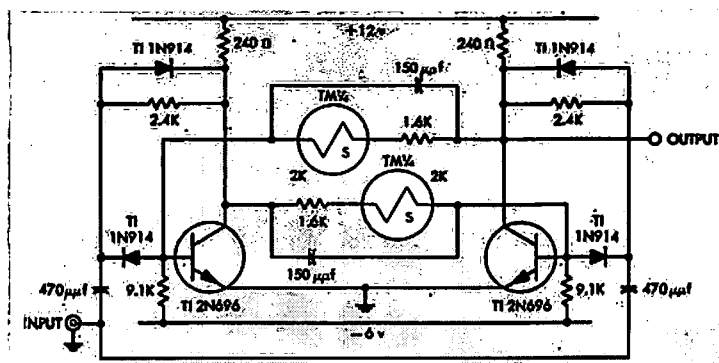
NONSATURATED FLIP-FLOP—Design procedure is given in 52 steps for manufacturability and long-term reliability, making full allowance for component tolerances, voltage fluctuation, and collector output loading.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 189.



MULTICHIP COMPLEMENTARY FLIP-FLOP—Circuit for microelectronic application allows

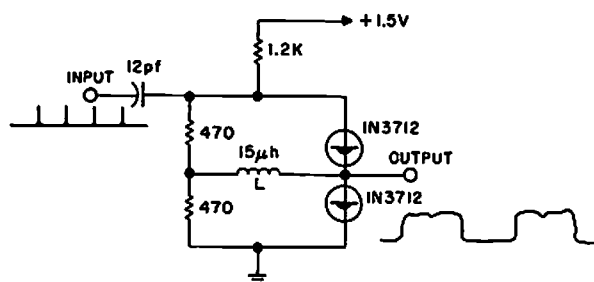
use of most suitable substrate for each component.—J. Eimbinder, Multichip Circuits

Get Off The Ground, *Electronics*, 37:25, p 105-107.

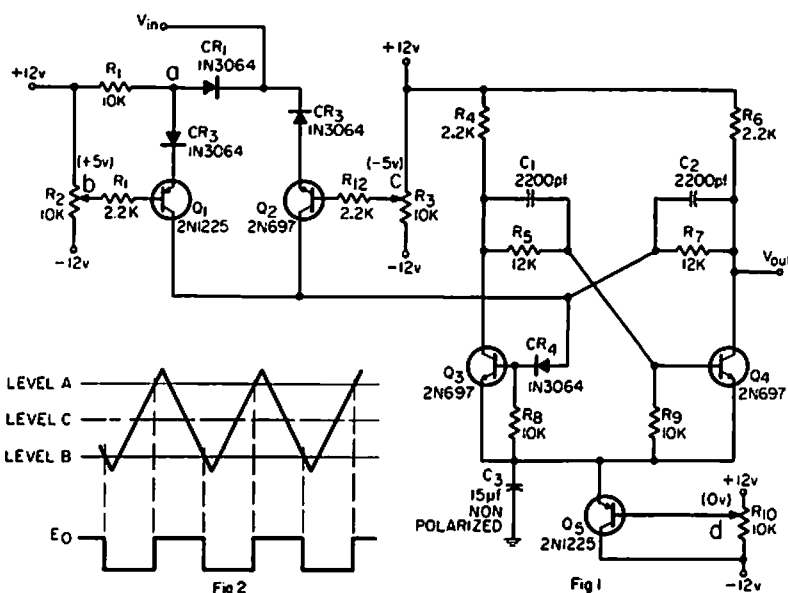


TEMPERATURE COMPENSATION—Sensistor silicon resistors in cross-coupling network compensate for temperature changes. Circuit operates at resolution rate above 5 Mc if input pulse is above 10 v when pulse width is

decreased from 100 millimicrosec.—How to Compensate for Temperature Variation in a Transistorized Flip-flop (Texas Instruments ad), *Electronics*, 33:37, p 97.

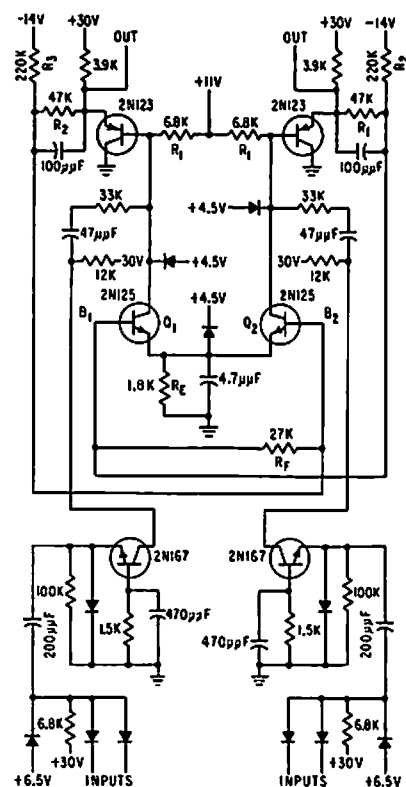


TUNNEL-DIODE FLIP-FLOP—Power consumption is very low. Takes advantage of fast switching speed (27 picosec) of tunnel diode. —"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 367.

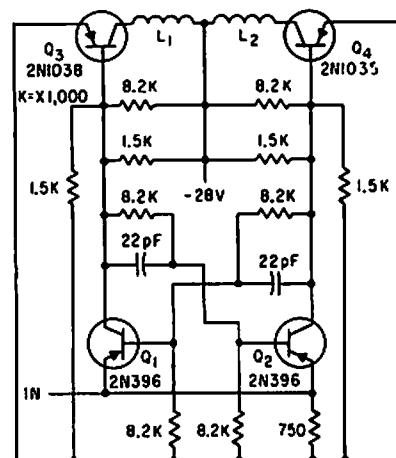


LEVEL DETECTOR—Used to provide switching function at two preset levels. R2 and Q1 determine highest level, while R3 and Q2 determine lowest level. Range of level adjust-

ment is -10 to +8 v.—H. Anway, Level Detecting Flip Flop With Adjustable Hysteresis, *EEE*, 14:1, p 63-64.

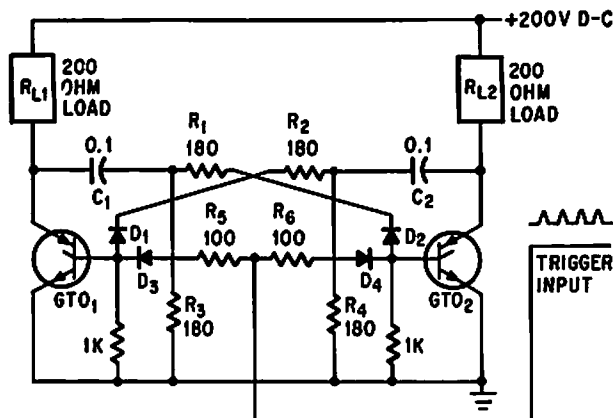
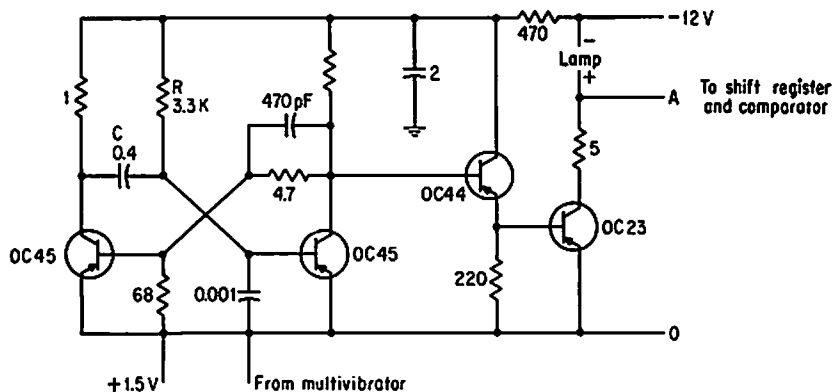


FEEDBACK PROVIDES STABILIZATION—Resistor Rf, connected between bases of Q1 and Q2, provides negative feedback to make flip-flop less sensitive to voltage variations and transistor unbalance. Will operate on 3-v pulses having 0.5-microsec fall time. Without feedback, higher voltage would be required for triggering.—P. Cheilik, Feedback Stabilizes Flip-Flop, *Electronics*, 31:19, p 92-96.

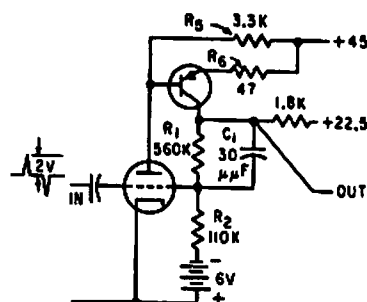


FLIP-FLOP SPEEDS MAGNETIC-DETENT STEPPING RELAY—Input signal goes to emitter of saturating flip-flop Q1-Q2, whose condition is sensed by driving transistors Q3-Q4. Stepping coils L1 and L2 are energized according to condition of flip-flop.—F. W. Kear, Coils Operate Stepping Relay at Higher Speed, *Electronics*, 35:6, p 60-63.

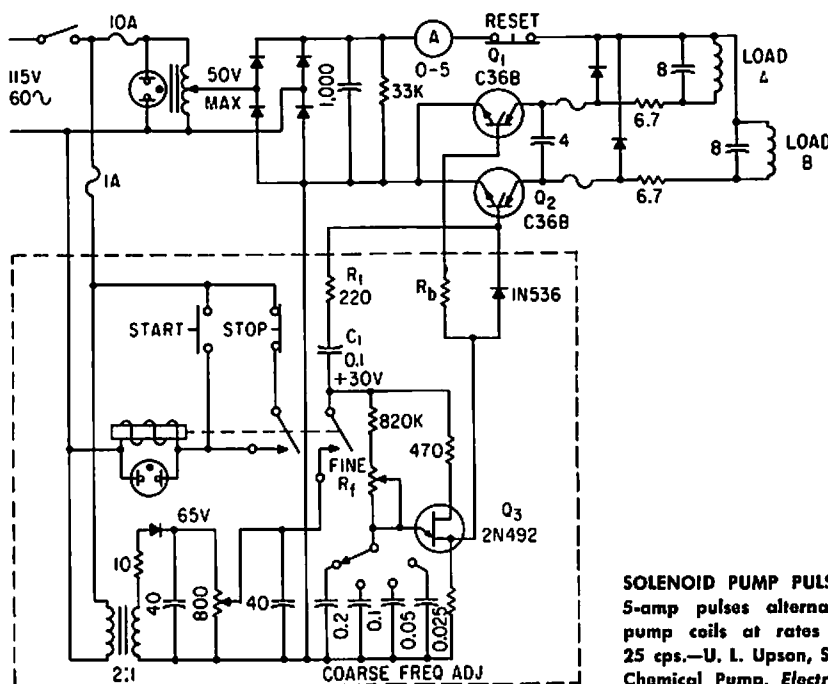
FLIP-FLOP DRIVES GALLIUM ARSENIDE LAMP
—Pulses from mvbr (not shown) trigger flip-flop that feeds 1-amp current pulses to GaAs lamp through emitter-follower and power transistor. Used in high-speed punched tape reader.—R. F. Broom and C. Hilsum, Diode Lamp Makes Tape Readers Faster, *Electronics*, 36:20, p 44-45.



GATE-TURNOFF SCR FLIP-FLOP—Capacitor-commutated flip-flop transfers current from one load to other each time positive trigger pulse is applied to common input line, at rates up to 10 kc.—D. R. Grafham, Now the Gate Turnoff Switch Speeds Up D-C Switching, *Electronics*, 37:12, p 64-71.

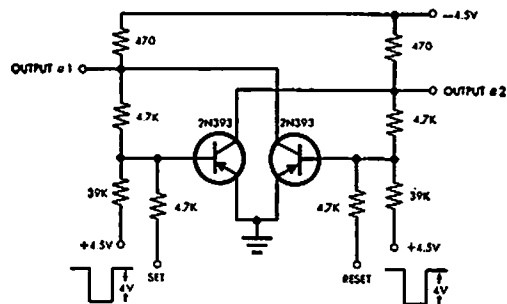


SELF-INDICATING FLIP-FLOP—Uses Amperex 6679 triode having fluorescent anode, to eliminate need for neons as indicators. Incorporates collector protection and stabilization against beta variation of transistor.—H. Rodrigues deMiranda and I. Rudich, Indicator Triode for Direct Data Readout, *Electronics*, 33:6, p 52-54.

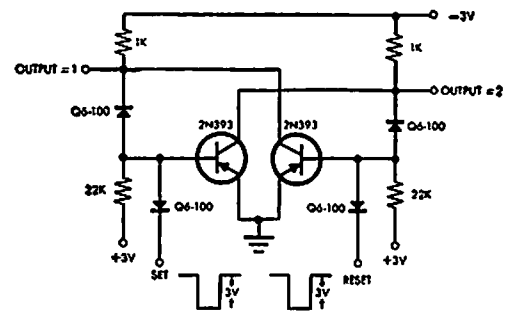


SOLENOID PUMP PULSER—Scr flip-flop feeds 5-amp pulses alternately to two solenoid pump coils at rates varying from 1.5 to 25 cps.—U. L. Upson, Solid-State Pulsar Drives Chemical Pump, *Electronics*, 33:49, p 74-76.

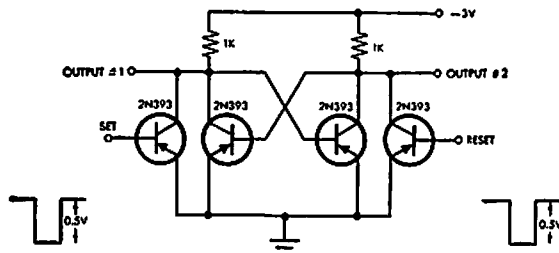
$R_f = 0$ TO 1M,
VARIABLE
 $R_b = 0$ TO 25 OHMS,
SELECTED



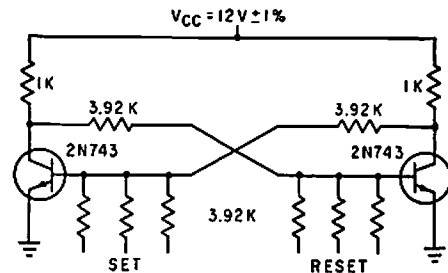
RESISTOR-COUPLED FLIP-FLOP—Typical switching times are 40 millimicrosec for t_r and 110 millimicrosec for t_f .—Philco MAT Transistors for Logic Circuits up to 5 Mc (Philco ad), *Electronics*, 33:17, p 50.



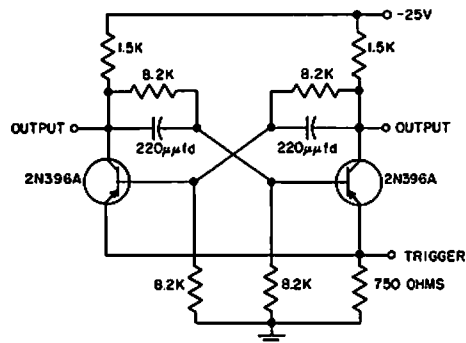
DIODE-COUPLED FLIP-FLOP—Typical switching times are 20 millimicrosec for t_r and 60 millimicrosec for t_f .—Philco MAT Transistors for Logic Circuits up to 5 Mc (Philco ad), *Electronics*, 33:17, p 50.



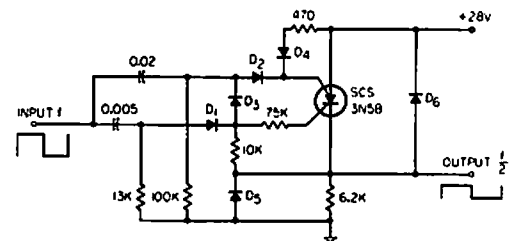
DIRECT-COUPLED FLIP-FLOP—Typical switching times are 12 millimicrosec for t_r and 15 millimicrosec for t_f .—Philco MAT Transistors for Logic Circuits up to 5 Mc (Philco ad), *Electronics*, 33:17, p 50.



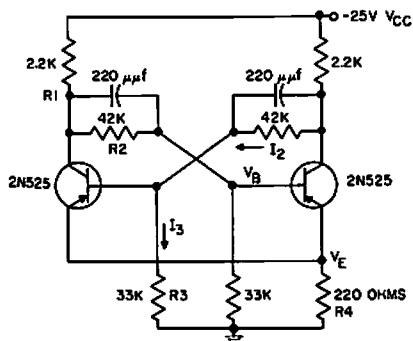
DIRECT-COUPLED NOR GATES—Consists of two epitaxial-transistor nor gates.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.



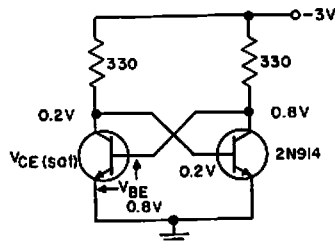
SATURATED FLIP-FLOP FOR 100°C—Increased temperature range is obtained at penalty of smaller voltage change at collector, more battery power consumed, and more trigger power required. Capacitor values depend on trigger characteristics and maximum trigger repetition rate.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 186.



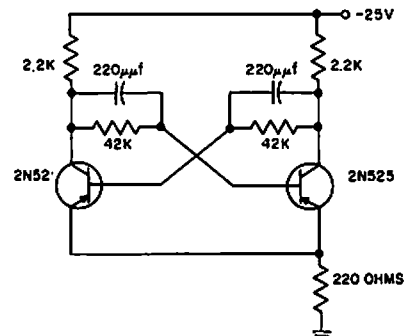
SINGLE-SCS FLIP-FLOP—Uses only one silicon controlled switch to perform flip-flop function over wide temperature range. Differentiated positive pulses are applied to cathode gate and anode gate alternately to turn scs on and off. If gate leads are brought out separately, circuit can be used as set-reset flip-flop.—E. Koda, Single-SCS Flip-Flop, *EEE*, 13:2, p 63.



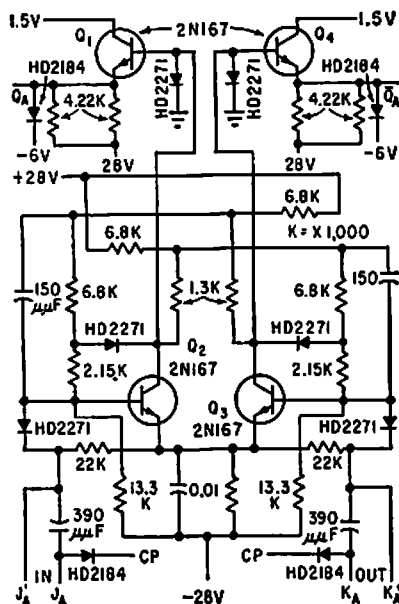
SATURATED FLIP-FLOP FOR 50°C—Addition of two 33,000-ohm resistors to basic saturated flip-flop boosts temperature range for stable operation above 50°C.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 186.



DCTL FLIP-FLOP—Direct-coupled transistor logic flip-flop utilizes saturation in circuit with extreme simplicity. With silicon transistors, operation up to 150°C is feasible. Saturation causes storage time delay that limits circuit speed. With germanium transistors, stray voltage signals of about 0.3 v can cause faulty performance.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 204.



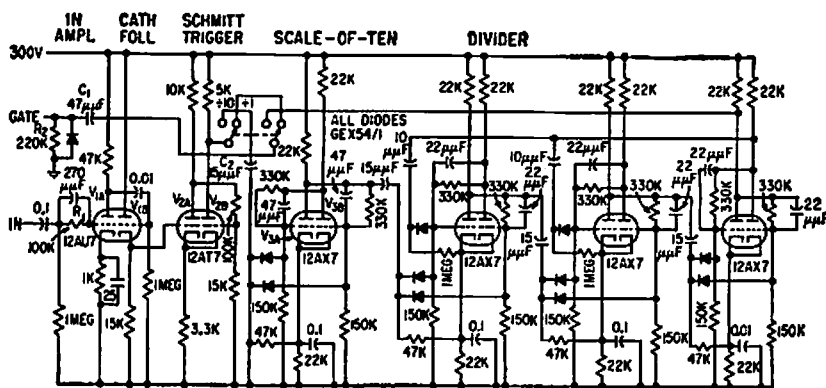
SATURATED FLIP-FLOP—Simple circuit shown is preferable at moderate operating temperatures. If emitter triggering is not used, 220-ohm emitter resistor can be removed.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 186.



JK FLIP-FLOP—Consists of transistorized Eccles-Jordan switch, with collectors clamped with diodes to stabilize operating points. Used to provide current for driving gating

circuits of voltage amplifiers for magnetic memory drum.—A. J. Strassman and R. E. Keeter, Clock Track Recorder For Memory Drum, *Electronics*, 32:41, p 74-76.

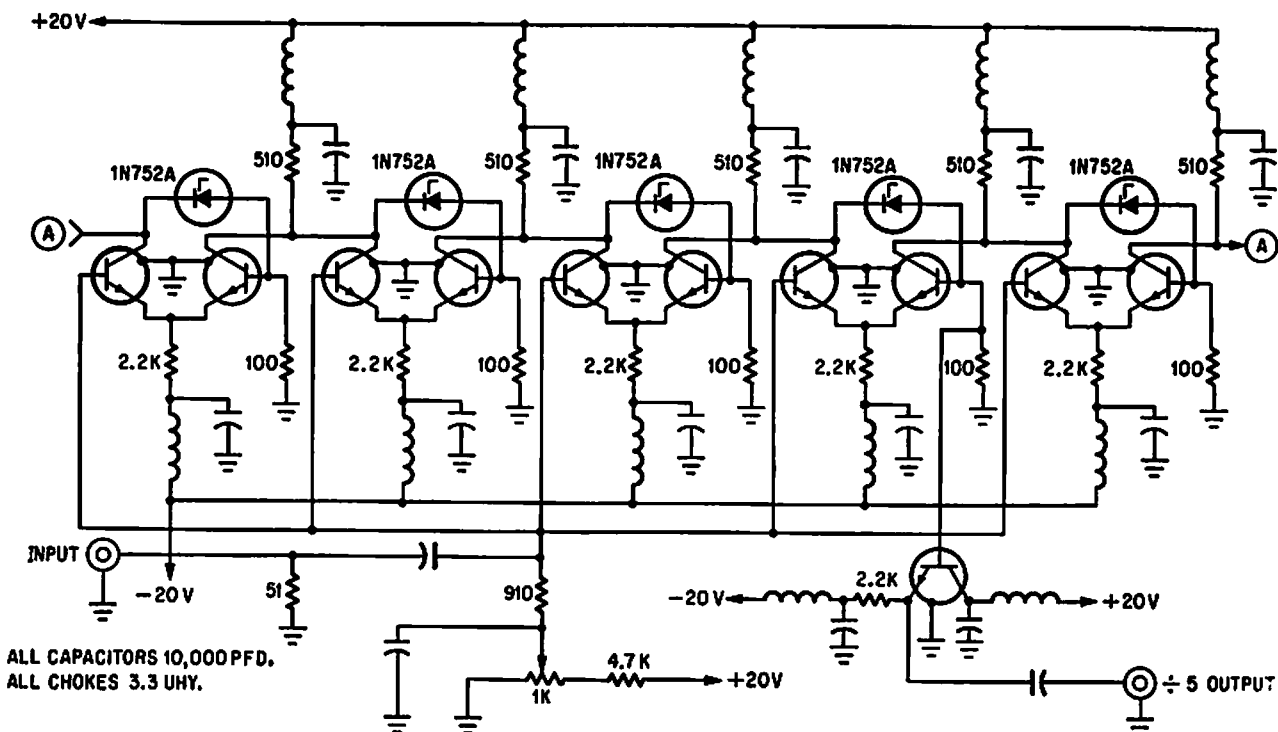
Frequency Divider Circuits



SCALE-OF-TEN DIVIDER—Consists of four cascaded Eccles-Jordan binary dividers with

feedback loops, to recycle at 10. Operates up to 500 kc.—J. K. Goodwin, Digital Tachometer.

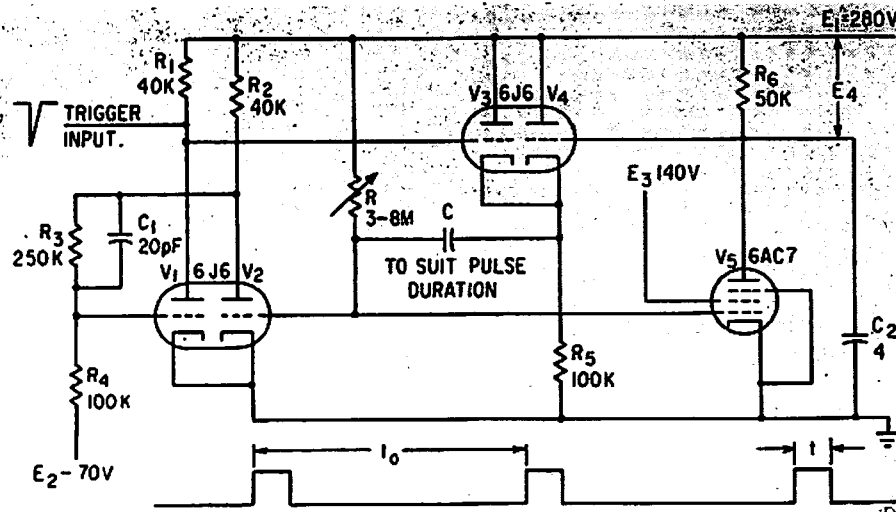
meter Aids in Turbine Design, Electronics,
32:15, p 58-61.



DIVIDE-BY-5 RING COUNTER—Each stage acts as nonsaturating current-mode switch. Two identical counters are cascaded in uhf

prescaler of frequency synthesizer for military uhf transceiver.—L. F. Blachowicz, Dial

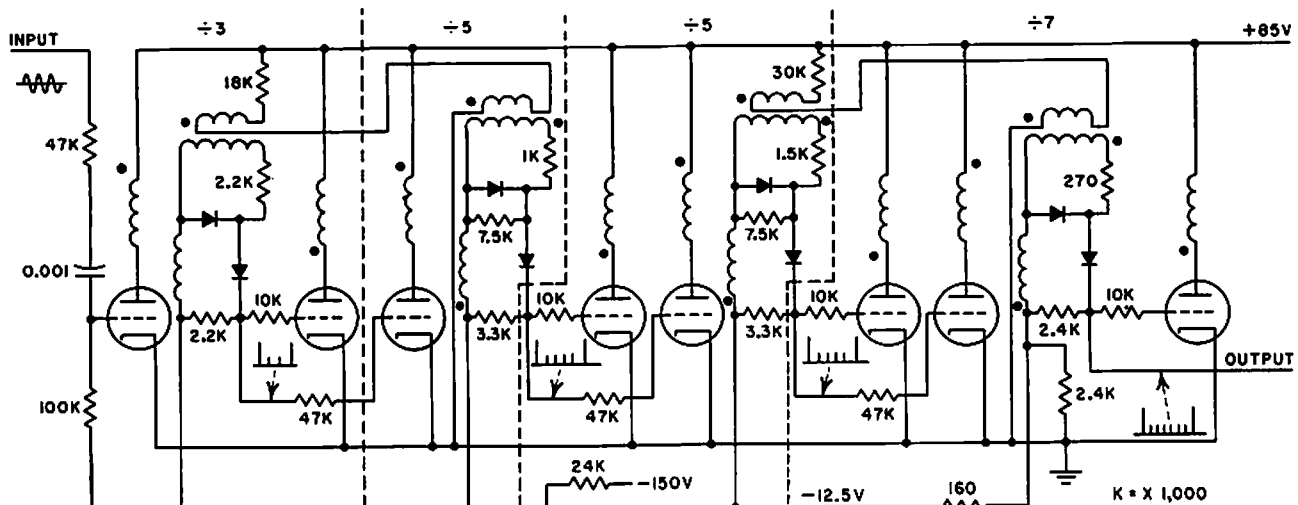
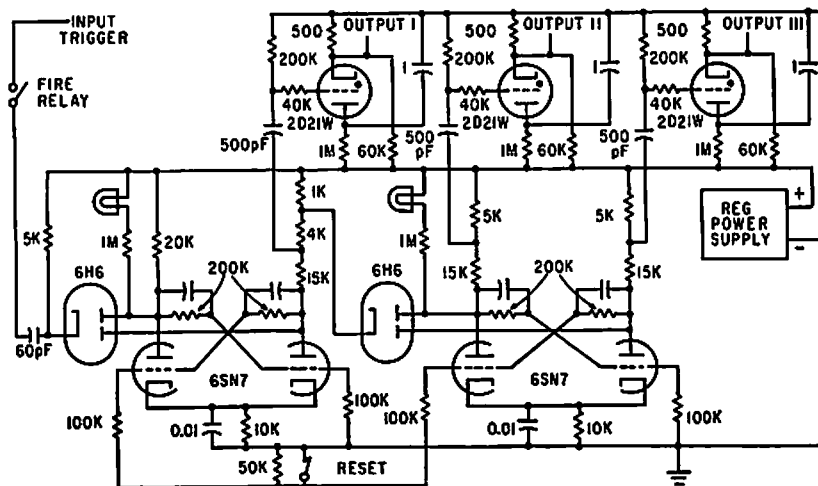
any Channel to 500 Mhz, *Electronics*, 39:9, p 60-69.



STABILIZED MONO—Operates as 10:1 divider at 1,000 pps. Division ratio of mvbr remains constant for supply variations of from 40 to 400 v. Average plate voltage of V5 is ad-

justed automatically to maintain optimum working conditions throughout operating range.—T. Hornak, *Stabilizing Monostable Multivibrators*, *Electronics*, 33:45, p 76.

CAPACITOR-BANK TRIGGER—Uses two bi-stable flip-flops in series as frequency divider for controlling firing of huge capacitor bank. Three outputs deliver pulses with times related to input frequencies.—R. Buser and P. Wolfert, *Experimental 100,000 Joule Capacitor Bank for Plasma Research*, *Electronics*, 33:32, p 58-61.

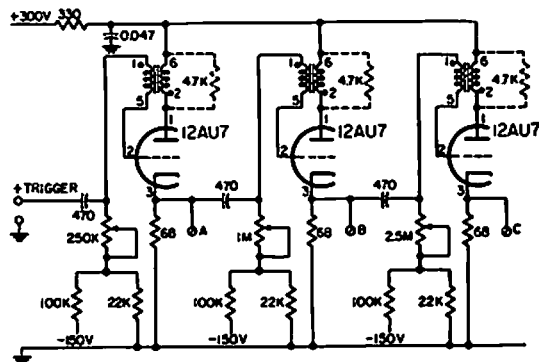


ITV DIVIDER CHAIN—Magnetic-core frequency divider counts 31.5-kc input signal down by 525 to produce 40-v 10-microsec output at

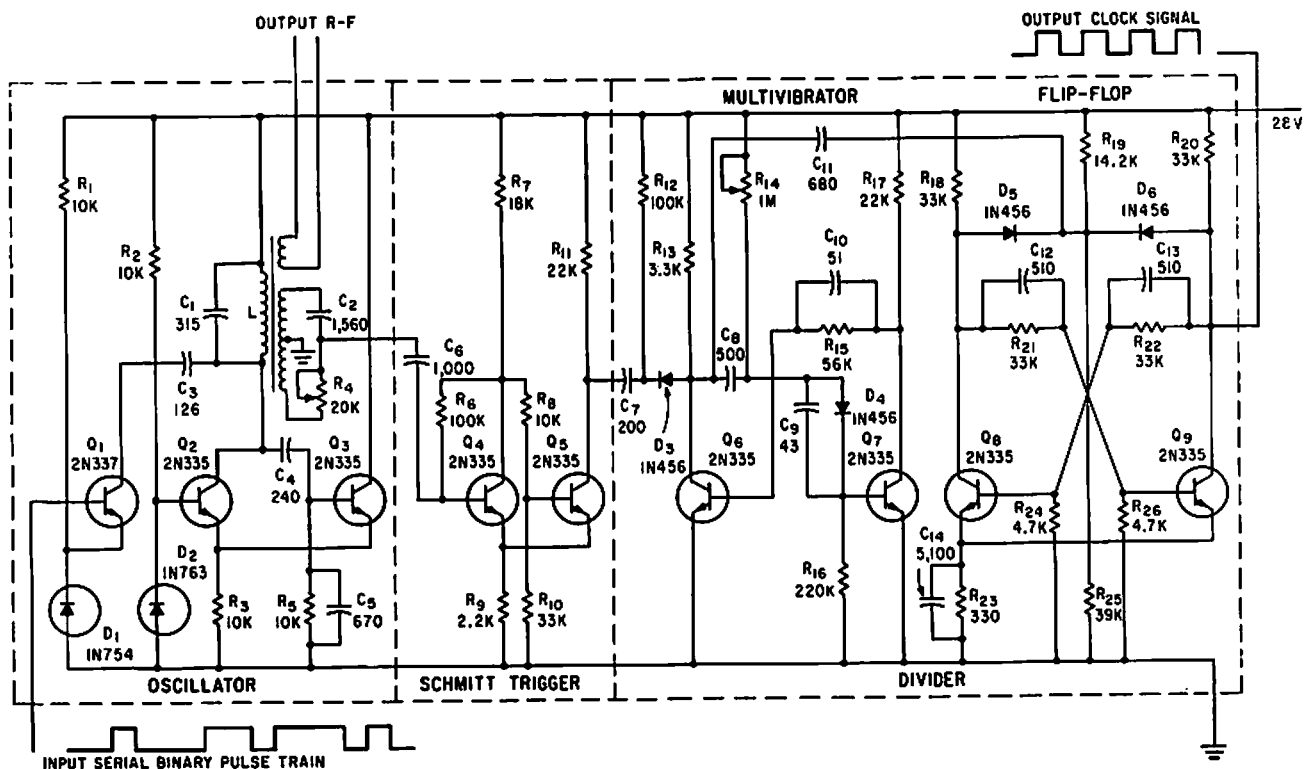
60 pps. Bias windings are series-connected in pairs to simplify circuit. Gives high accuracy and stability.—A. Rose, *Magnetic-Core*

Divider for ITV Sync Generators, *Electronics*, 31:15, p 76-77.

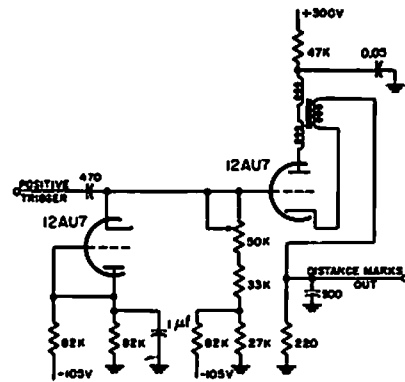
The diagram shows a complex electronic circuit for measuring magnetic flux density. It features a +150V power supply connected to a network of resistors and tubes. A 30K resistor is connected to the supply. A 1/2-62II tube is connected to the supply and a 0.001 47K resistor. A 100K resistor is connected to ground. A 33K resistor is connected to the supply. A 0.001 resistor is connected to the supply. A 2.2K resistor is connected to ground. A 10K resistor is connected to the supply. A 100K resistor is connected to ground. A 1/2-62II tube is connected to the supply and a 0.001 resistor. The circuit is labeled with 'LADLE', 'BUCKET', 'Np', 'Ns', 'Ls', 'Rl', and 'K = X1,000'. An 'OUTPUT' waveform is shown on the right.



CASCADED DISTANCE-MARK DIVIDER—With 1-mile markers used as input trigger, outputs A, B, and C give 2 to 5, 10 to 25, and 20 to 50-mile distance marks, respectively. Grid potentiometers control exact mile mark obtained at each output.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N7-5.

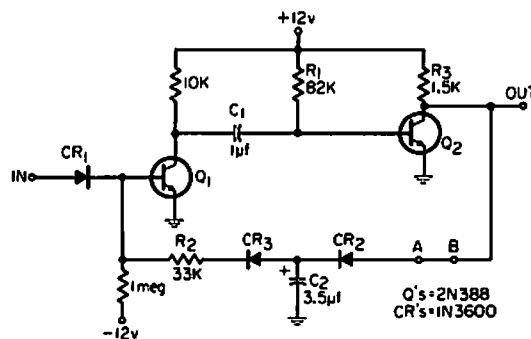


f-m/f-m telemetry system.—R. C. Onstad, New Coherent Koyer Simplifies Pulse-Code Telemetry, *Electronics*, 35:26, p 71-73.

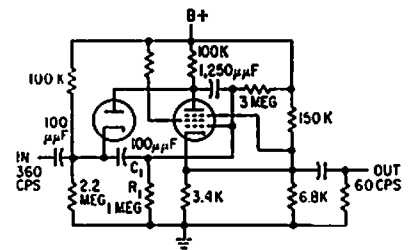


PULSE-FREQUENCY DIVIDER—Plate-to-cathode coupled blocking oscillator is used to divide from high to low pulse frequency, as required in radar distance-mark generator. Circuit is highly stable with respect to heater

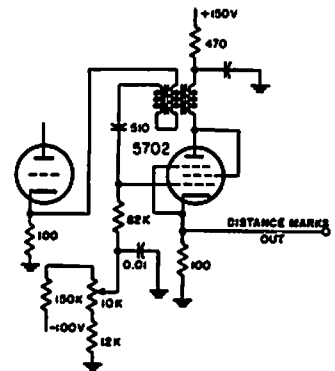
voltage variations.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N7-1.



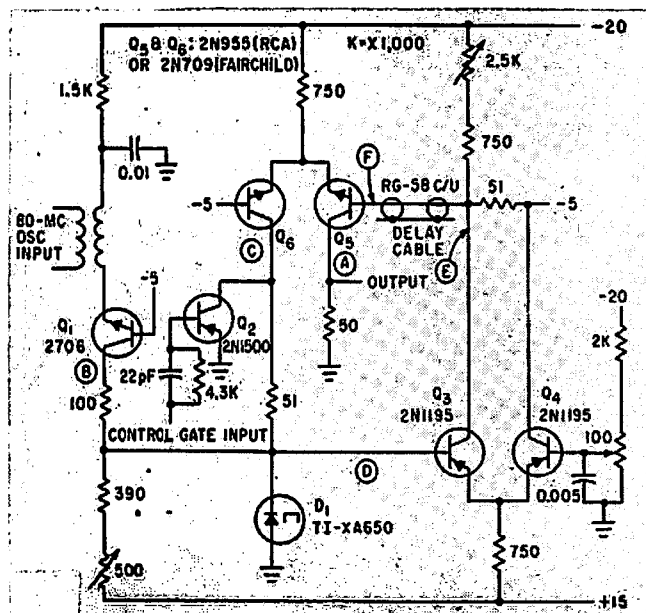
INDEPENDENT PULSE-WIDTH CONTROL—With components shown, circuit will divide 50-millisecond pulses by 5 without changing pulse width. Other components give different widths along with division.—J. McGruder, Frequency Divider With Independent Pulse-Width Control, *EEE*, 14:2, p 69.



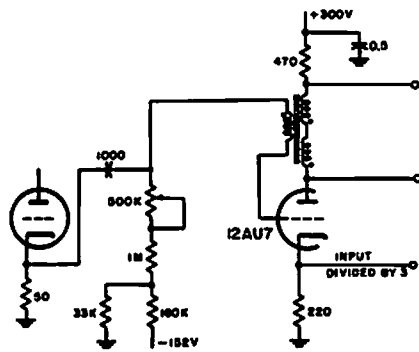
PHANTASTRON FREQUENCY DIVIDER—Will divide 360 cps to give 60 cps. Is triggered by pulse train with both positive and negative pulses.—K. M. Chen, Trigger Stabilizes Frequency Divider, *Electronics*, 31:47, p 104-107.



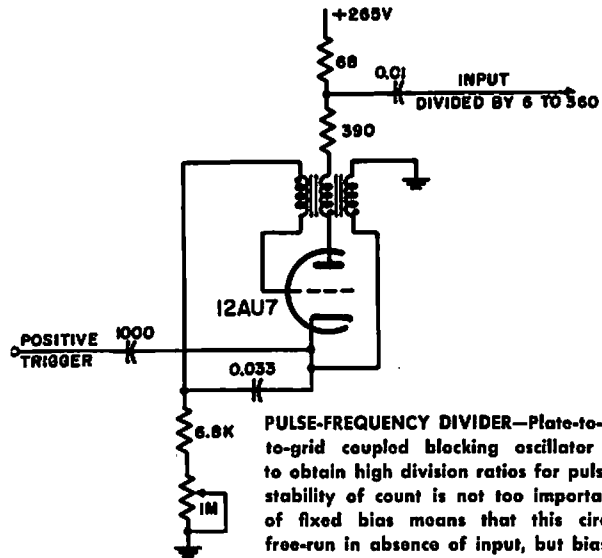
PULSE-FREQUENCY DIVIDER—Plate-to-grid coupled blocking oscillator, with voltage step-up to grid, is used to divide from high to low pulse frequency.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N7-1.



DIVIDER ELIMINATES COUNT-STARTING JITTER—Output of 80-Mc free-running crystal oscillator is divided by 8 to give 10-Mc time base that is almost perfect square wave.—W.O. LeCroy, Jr., Eliminating One-Count Uncertainty in Cycle-Counting Interval Timers, *Electronics*, 35:29, p 46-47.



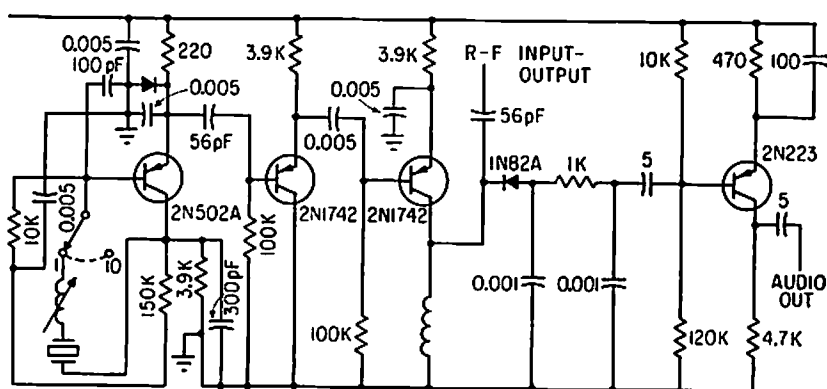
PULSE-FREQUENCY DIVIDER—Plate-to-grid coupled blocking oscillator, with voltage step-down to grid, is used to divide from high to low pulse frequency. Stepdown yields maximum peak pulse voltage at plate and permits maximum pulse duration from given transformer.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N7-1.



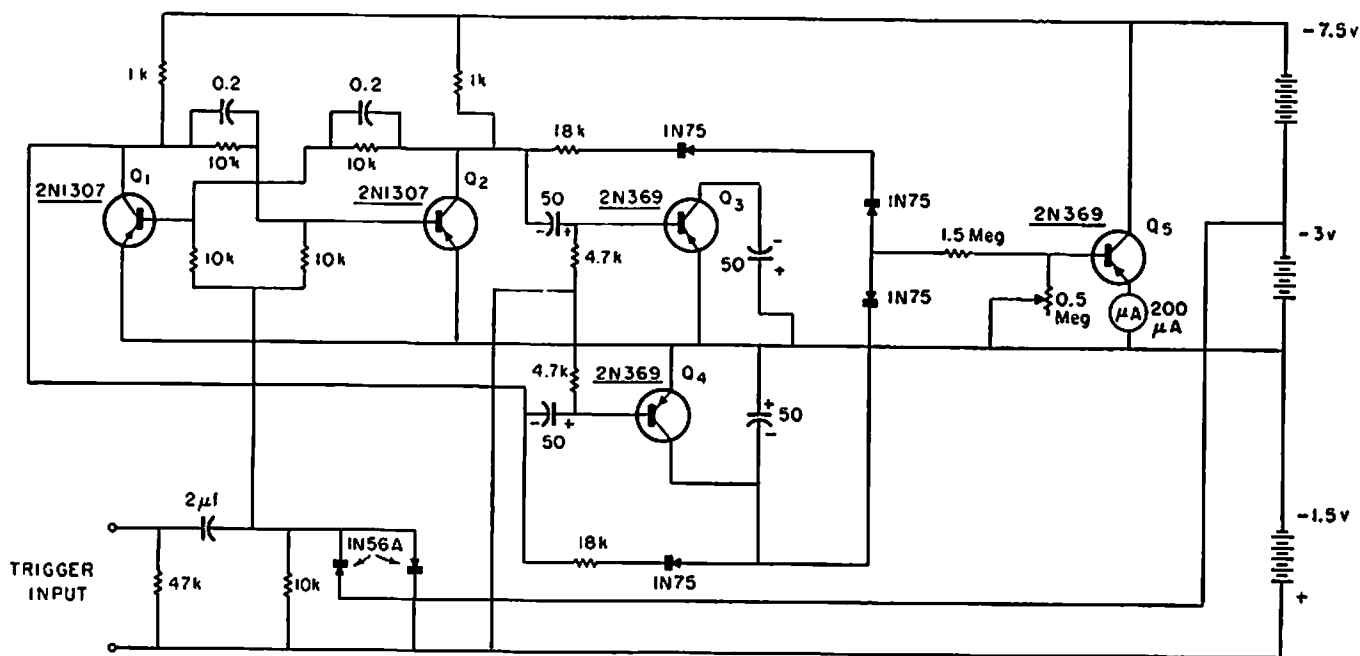
PULSE-FREQUENCY DIVIDER—Plate-to-cathode-to-grid coupled blocking oscillator is used to obtain high division ratios for pulses when stability of count is not too important. Lack of fixed bias means that this circuit will free-run in absence of input, but bias can be added if needed.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N7-1.

CHAPTER 35

Frequency Measuring Circuits



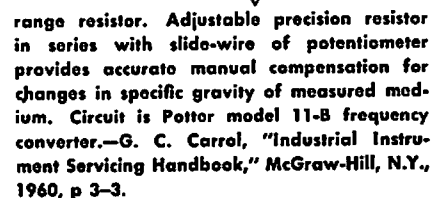
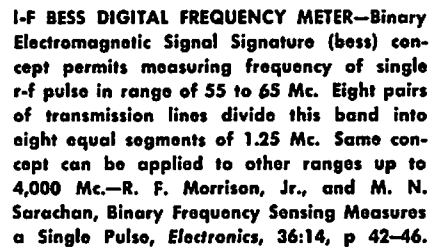
TEN-FREQUENCY STANDARD—Stable crystal switching oscillator, isolation amplifier, multiplier, mixer, and audio amplifier give choice of ten fundamental frequencies, between 10 and 20 Mc, with harmonic output from 20 to 480 Mc, for zero-beating with unknown input frequency being measured.—*Portable Frequency Standard Between 10 and 480 Mc, Electronics, 35:18, p 64.*



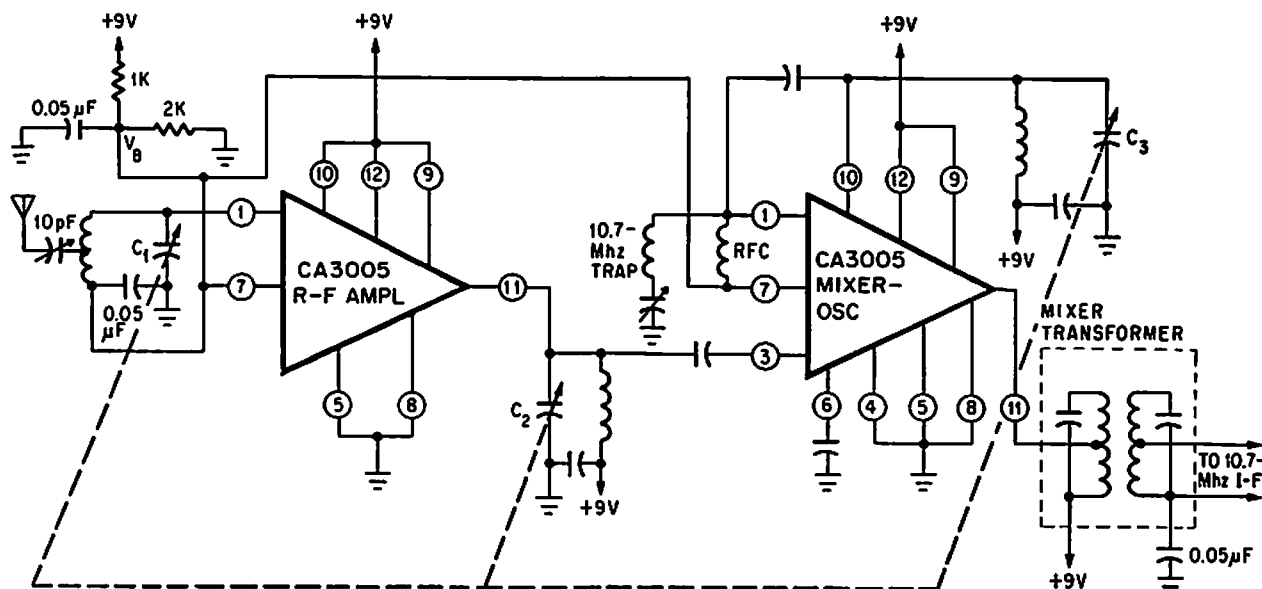
SUBAUDIO FREQUENCY METER—Provides motor indication of frequencies from 0.2 to 10 cps. Responds rapidly to changes in input frequency. Input signal is converted to square wave by limiting and differentiating

network and bistable flip-flop, with period of square wave indicated on meter calibrated in pulses per minute. By alternately charging each one of pair of capacitors, steady voltage is maintained on capacitor connected to

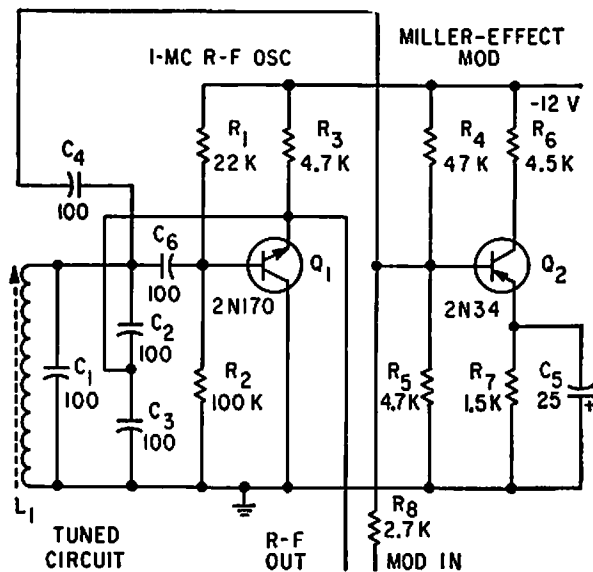
meter while other is charging.—Fast Acting Subaudio Frequency Meter, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 150.



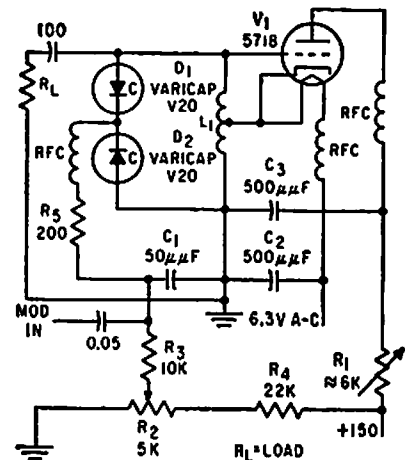
FLOWMETER FREQUENCY CONVERTER—Provides output signal that is directly proportional to frequency of input from turbine

[illegible]

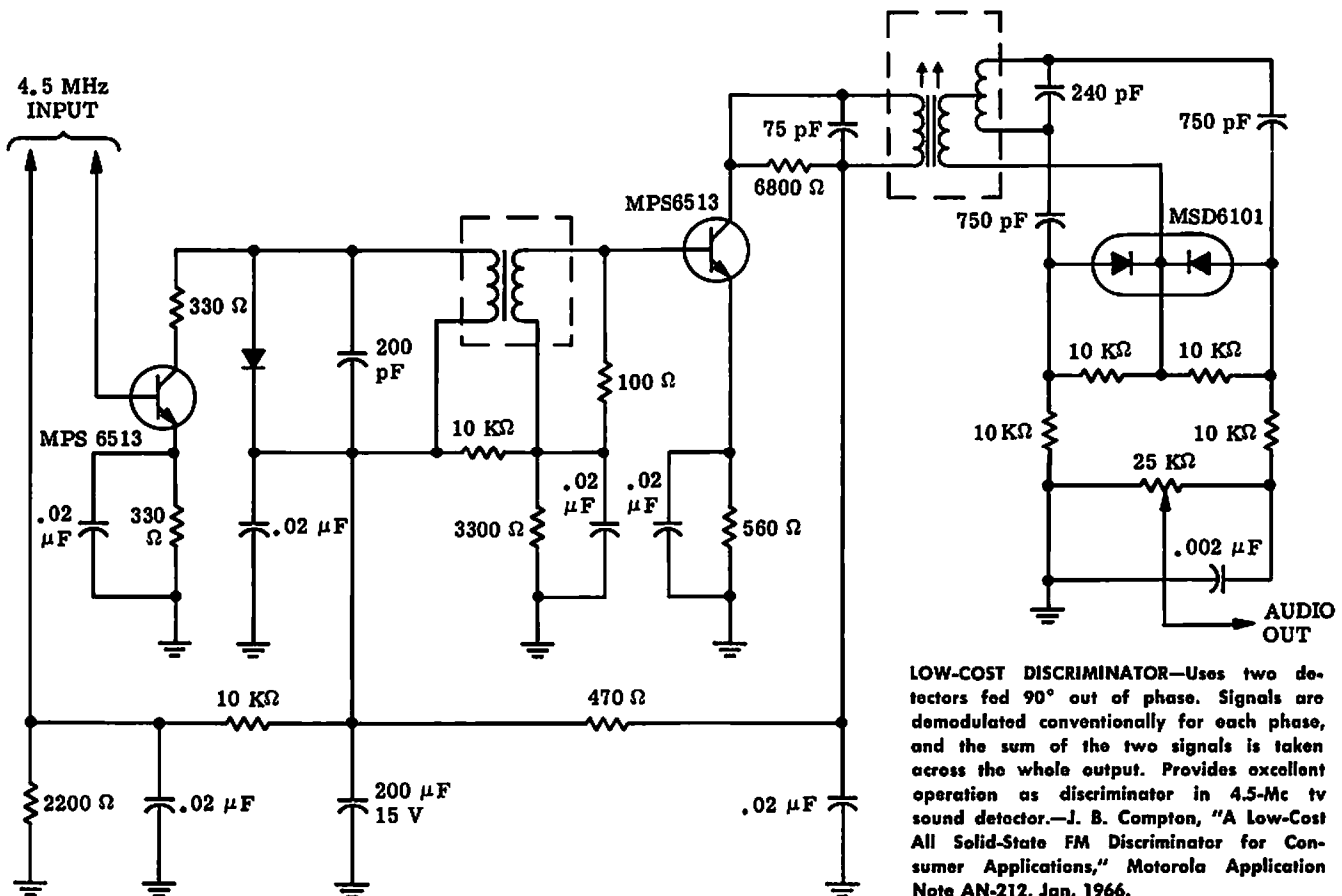
Integrated Circuits Make A Low-Cost F-M Receiver, *Electronics*, 39:16, p 133-138.



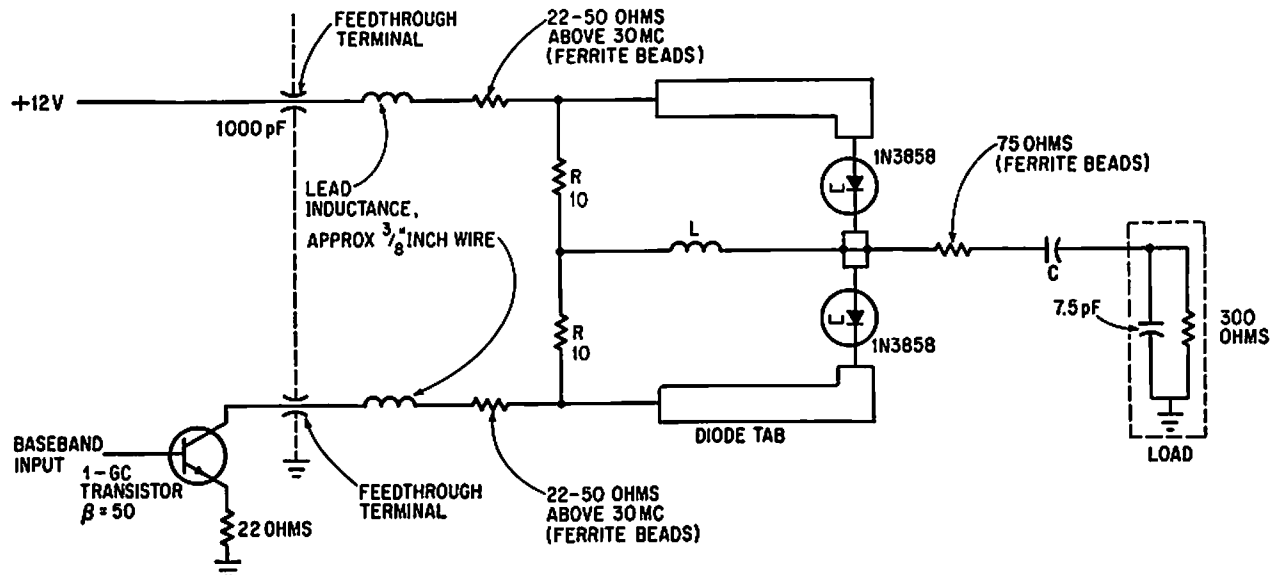
1-MC F-M OSCILLATOR—Combines Q multiplier with Miller effect to produce simple and stable f-m oscillator and modulator.—P. W. Wood, *Transistorized F-M Oscillator*, *Electronics*, 32:5, p 64.



100-MC VARICAP OSCILLATOR—Modulator consists of two variable-capacitance diodes in series to r-f and in parallel to audio modulating signals and d-c bias. Frequency deviation is 28 Mc peak-to-peak with modulating signals less than 28 v and negligible modulating power.—C. Arsem, *Wideband F-M with Capacitance Diodes*, *Electronics*, 32:49, p 112-113.



LOW-COST DISCRIMINATOR—Uses two detectors fed 90° out of phase. Signals are demodulated conventionally for each phase, and the sum of the two signals is taken across the whole output. Provides excellent operation as discriminator in 4.5-Mc tv sound detector.—J. B. Compton, "A Low-Cost All Solid-State FM Discriminator for Consumer Applications," *Motorola Application Note AN-212*, Jan. 1966.

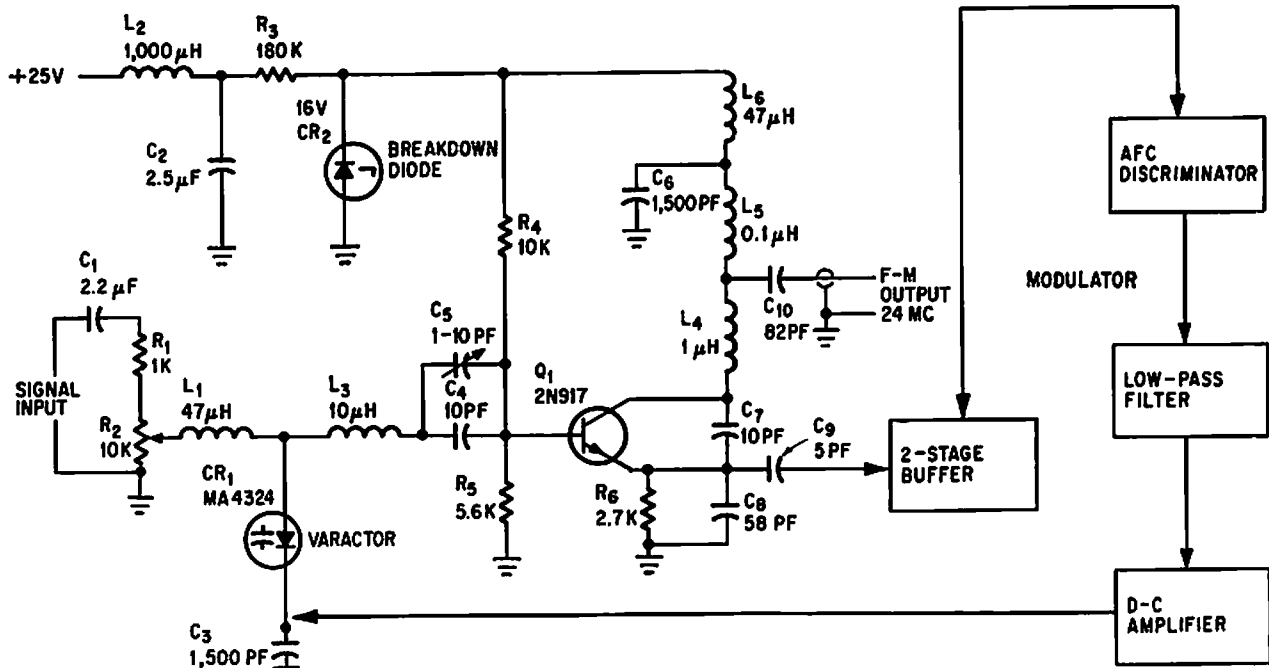
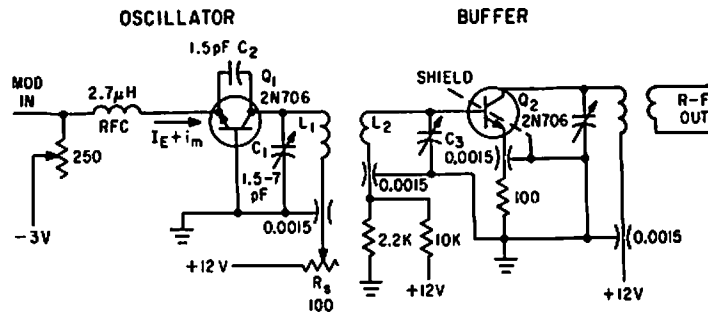


200-MC VOLTAGE-CONTROLLED OSCILLATOR
—Uses two tunnel-diodes in astable mvbr to give symmetrical square-wave output. Used

to produce wide frequency swing with respect to center frequency, linearly, when small control voltage is applied.—F. H. Lofrak,

Tunnel-Diode Oscillator Expands F-M System's Channel Capacity, Electronics, 39:1, p 105-109.

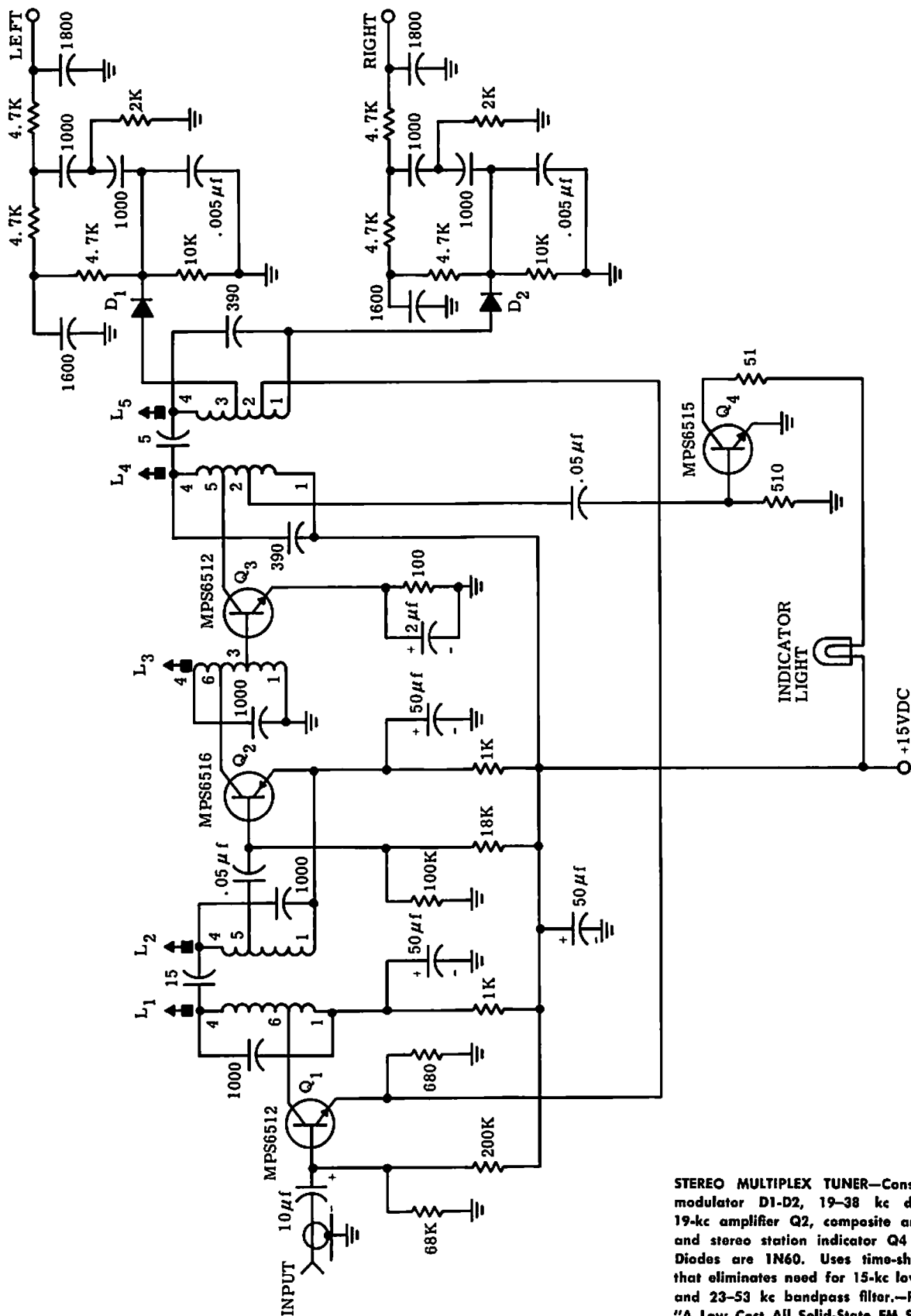
SELF-REACTANCE MODULATION—Modulation current injected at emitter changes collector-base voltage, thus varying output capacitance, tank resonant frequency, and oscillator frequency for 230-Mc pam/f-m telemetry beacon.—T. M. Conrad, *Self-Reactance Modulation in Telemetry Oscillators, Electronics, 35:9, p 35-37.*



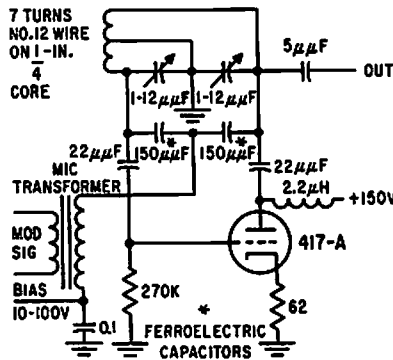
VARACTOR MODULATES 24-MC F-M OSCILLATOR—Modulating signal is applied to varactor diode in frequency-determining circuit

of telemetry oscillator. Linearity is 2% for deviation of 60 kc.—N. Downs and B. van Sutphin, *Solid-State Transmitter Ready for*

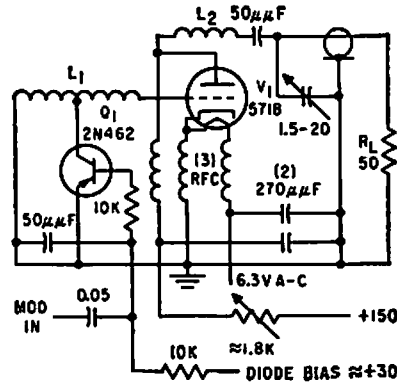
UHF Telemetry, Electronics, 37:17, p 76-80.



STEREO MULTIPLEX TUNER—Consists of demodulator D1-D2, 19–38 kc doubler Q3, 19-kc amplifier Q2, composite amplifier Q1, and stereo station indicator Q4 with lamp. Diodes are 1N60. Uses time-share method that eliminates need for 15-kc low-pass filter and 23–53 kc bandpass filter.—R. Brubaker, "A Low Cost All Solid-State FM Stereo Multiplex System," Motorola Application Note AN-207, Mar. 1966.

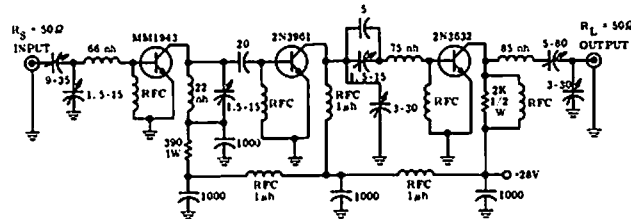


ELECTRIC TUNING FOR F-M OSCILLATOR—Voltage-tunable ferroelectric capacitors are used for tuning as well as for modulating.—T. W. Butler, Jr., *Ferroelectrics Tune Electronic Circuits*, *Electronics*, 32:3, p 52-55.



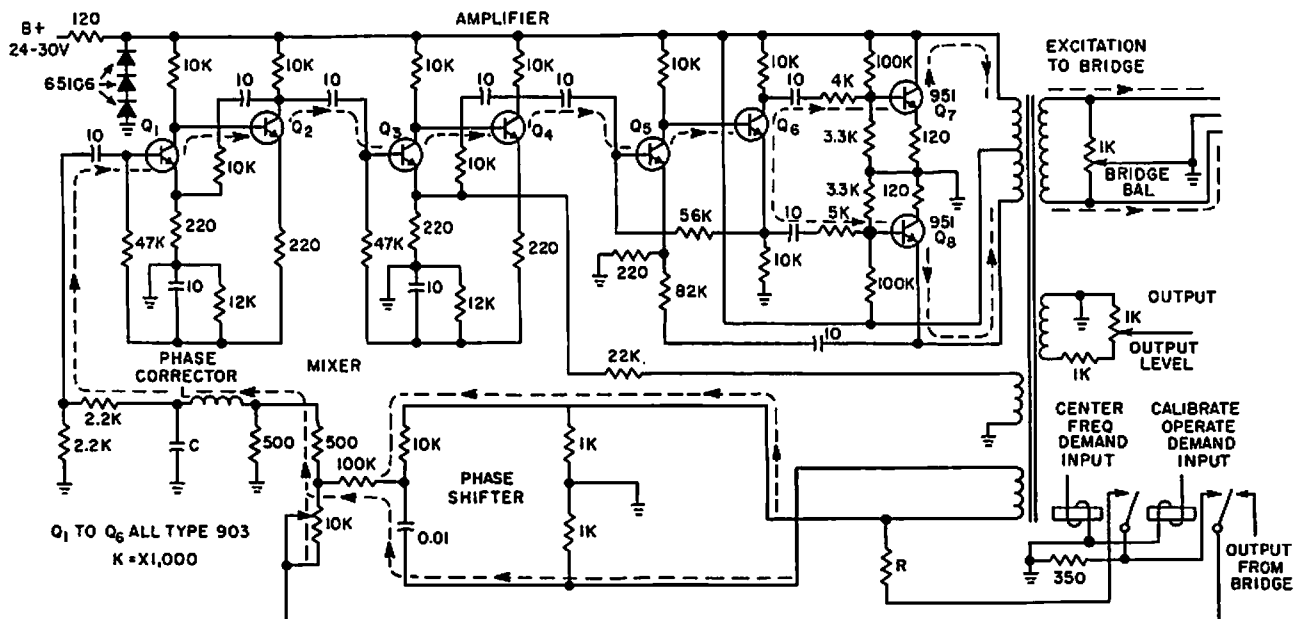
400-MC VARICAP OSCILLATOR—Wideband frequency modulation of 400-Mc distributed-parameter Colpitts oscillator is obtained with symmetrical transistor in modulator. Q1 is equivalent to two reverse-biased diodes in

series for r-f and in parallel with respect to modulating signals and d-c bias.—C. Arsem, *Wideband F-M with Capacitance Diodes*, *Electronics*, 32:49, p 112-113.



160-MC 15-W POWER AMPLIFIER—Performance of each of three stages is optimized by using input-output admittance data in network design, to give 30.5 db power gain from class-C operation. Circuit can be expanded to give complete f-m or c-w trans-

mitter by adding appropriate oscillator-buffer-multiplier stages.—R. Hojhall, "A 160 MHz 15-Watt Solid-State Power Amplifier," *Motorola Application Note AN-214*, Nov. 1965.



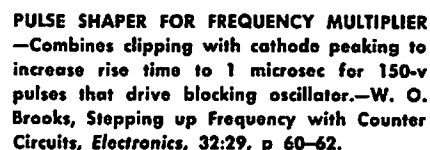
STRAIN-GAGE OSCILLATOR—Produces f-m signal output that is directly proportional to applied force, such as stress or pressure, on

resistive-type gage. Operating and band-edge frequencies of oscillator are determined by values of R, L, and C.—W. H. Foster,

Strain Gage Oscillator for Flight Testing, *Electronics*, 31:5, p 40-42.

CHAPTER 37

Frequency Multiplier Circuits



50 OHMS
150 MC

BUTTERFLY
0-12 pF

3-1/2 TURNS
1/2" I.D.
1/8" SPACING
0.059 DIA.
WIRE

0.75 pF

220K

1N4387

2 TURNS
3/8" I.D.
1/8" SPACING
100" DIA.
WIRE

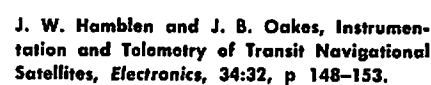
0.7-30 pF
PISTON

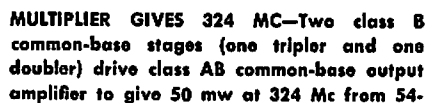
COAXIAL CAVITY

LOOP COUPLED
2" LONG
0.062"
DIA.
WIRE

1-1/4" LONG
LOOP COUPLED
0.032 DIA.
WIRE

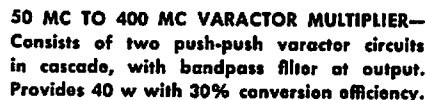
50 OHMS
450 MC



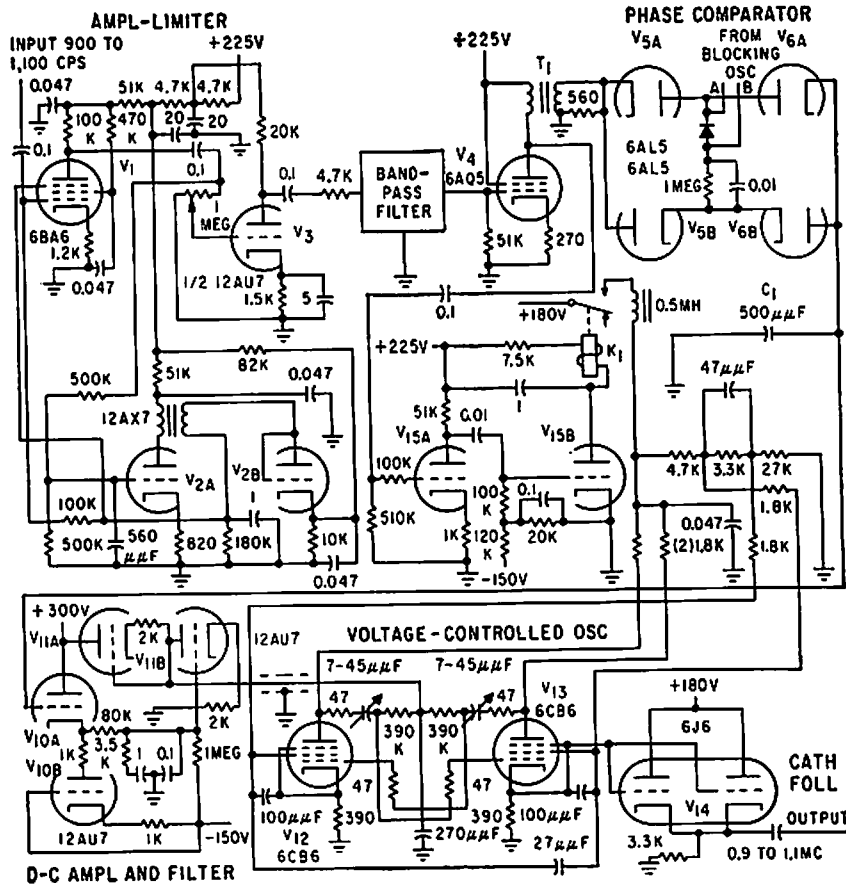


Mc input. Upper section, a 54-Mc transmitter, is simply a single class AB common emitter that gives 200 mw output from 25-mw input.—J. W. Hamblen and J. B. Oakes.

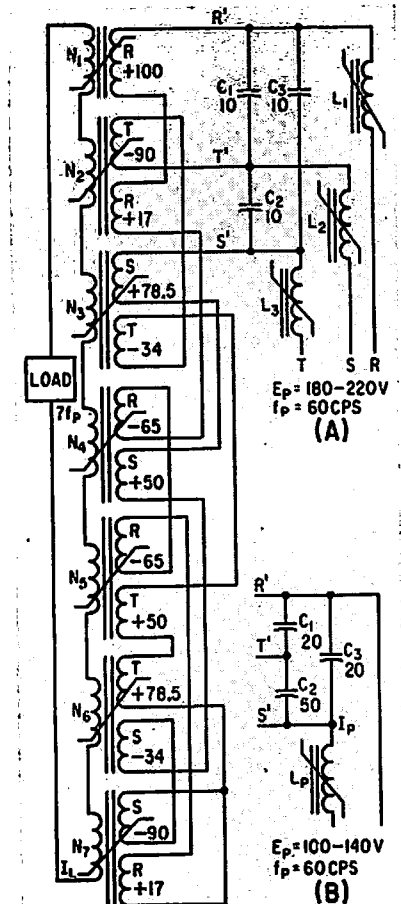
Instrumentation and Telemetry of Transit Navigational Satellites, Electronics, 34:32, p 148-153.



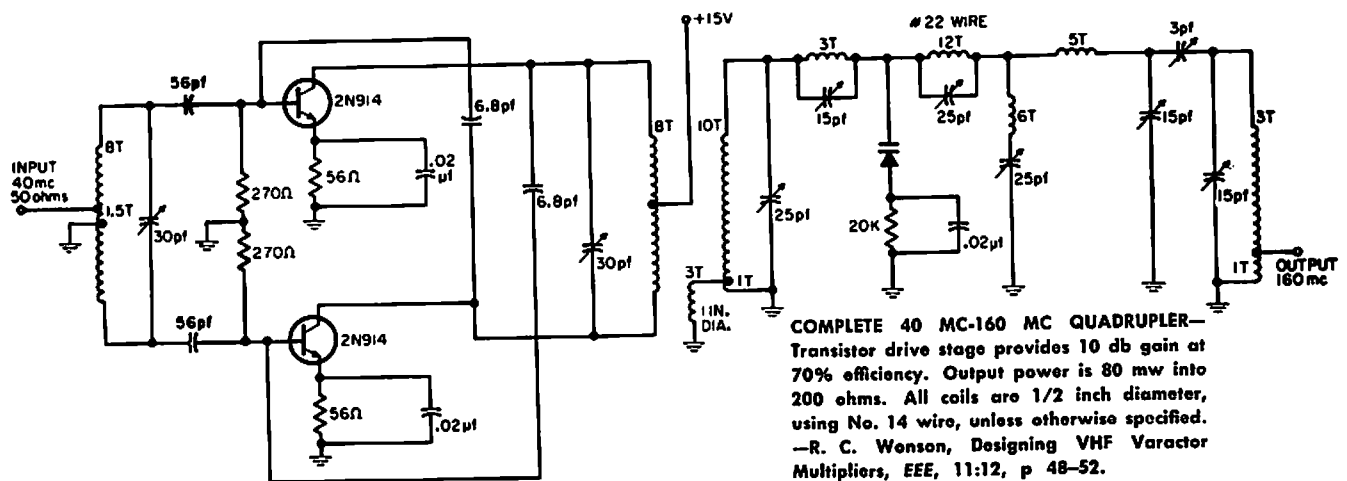
For pulse-modulated drive signals, will give 100 w peak pulse power at 0.0088 duty cycle. Uses Motorola 1N4386 varactors.—J. Cochran, "Two-State Varactor Multiplier Pro-



FREQUENCY MULTIPLIER—Phase comparator is combined with divider and digital feedback loop to give highly accurate frequency multiplication over wide band of inputs. Multiplying factor is 1,000, but technique is adaptable to other values. Amplified output of phase comparator is used to regulate voltage-controlled oscillator.—W. O. Brooks, *Stepping up Frequency with Counter Circuits*, *Electronics*, 32:29, p 60-62.

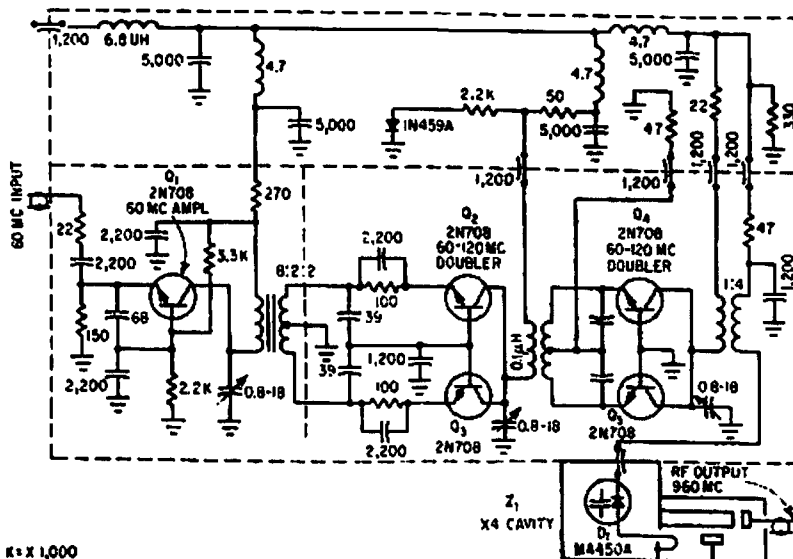
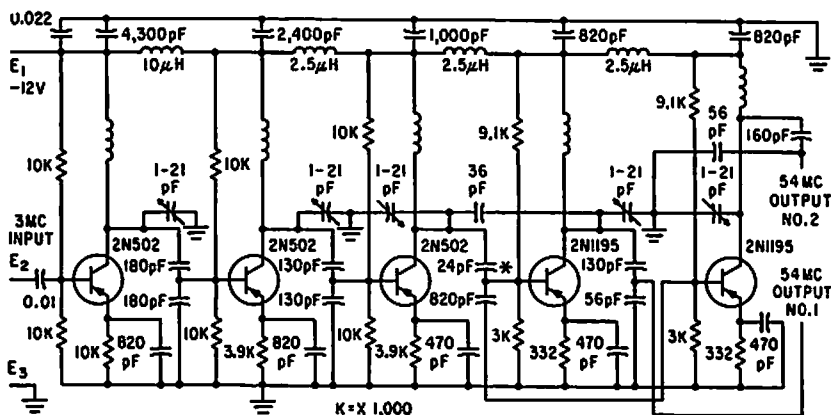


60-420 CPS FREQUENCY SEPTUPLER—Seven saturable-core transformers, with series-star connected multiple primary windings and series-aiding secondaries, serve as static frequency multipliers delivering 40 w at 420 cps. Input may be three-phase (A) or single phase (B).—W. A. Geyger, *Frequency Septupler Provides Stable 420-Cps Voltage*, *Electronics*, 36:18, p 58-61.

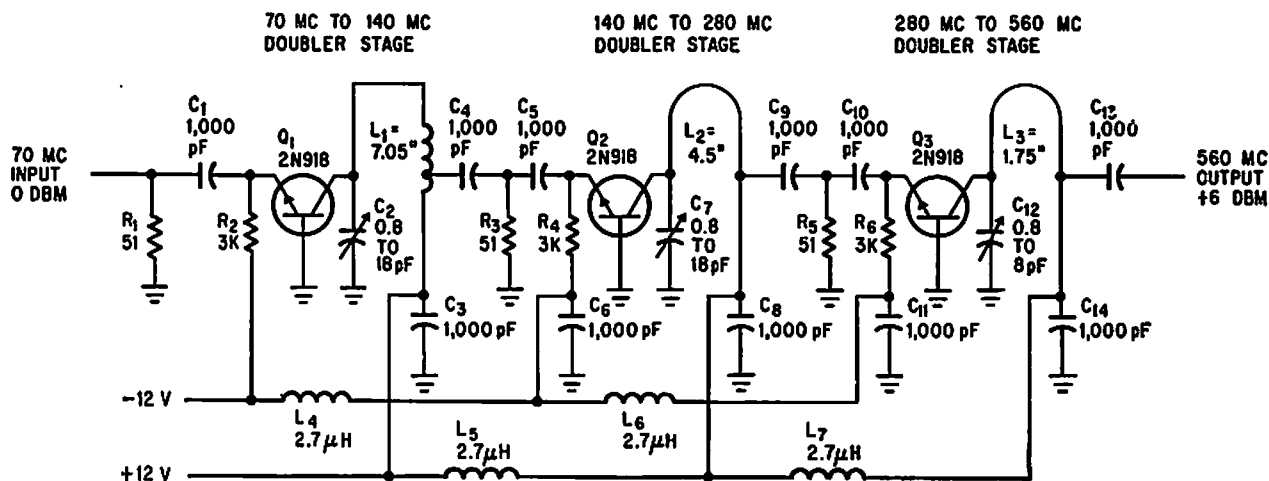


COMPLETE 40 MC-160 MC QUADRUPLER—Transistor drive stage provides 10 db gain at 70% efficiency. Output power is 80 mw into 200 ohms. All coils are 1/2 inch diameter, using No. 14 wire, unless otherwise specified.—R. C. Wenson, *Designing VHF Varactor Multipliers*, *EEE*, 11:12, p 48-52.

TWO 54-MC OUTPUTS FROM 3 MC—Uses three class AB common-emitter amplifiers. First two are triplers that multiply 3-Mc input to 27 Mc. Third doubles this to 54 Mc, and drives two parallel 54-Mc class AB output amplifiers providing 25 mw each.—J. W. Hamblen and J. B. Oakes, *Instrumentation and Telemetry of Transit Navigational Satellites*, *Electronics*, 34:32, p 148-153.



TWO DOUBLERS AND QUADRUPLER GIVE 960 MC—First two doublers are push-push class C with unity gain. Varactor D1 in final multiplier, feeding 960-Mc quarter-wave series-resonance coaxial cavity, acts as quadrupler.—W. E. Dahl, *Communicating with Future Deep-Space Probes*, *Electronics*, 36:22, p 28-32.

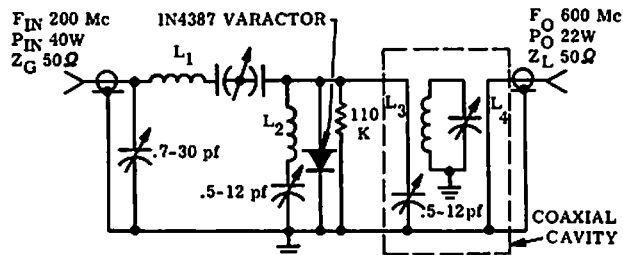
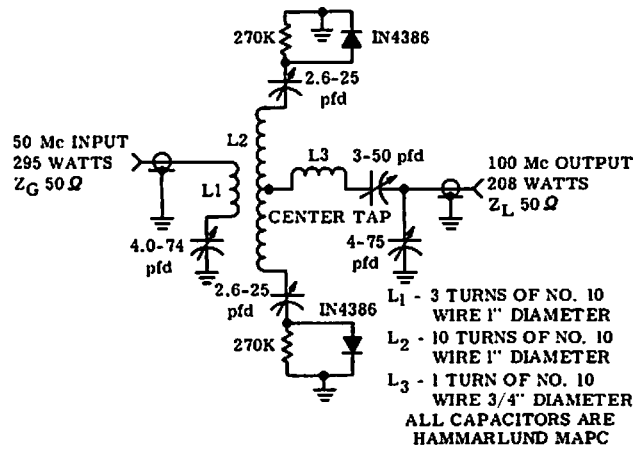


CASCADED UHF DOUBLERS—Common-base amplifier with tank circuit tuned to twice the input frequency is cascaded to give frequency

multiplication in 40 to 1,000 Mc range, with power output per stage ranging from 50% of stage d-c supply at low frequencies to 25% at

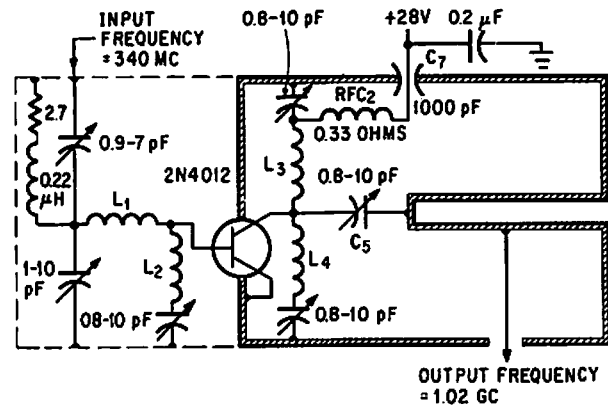
high frequencies.—A. E. Munich, *Basic UHF Circuit Forms Amplifiers and Multipliers*, *Electronics*, 37:20, p 59-60.

50 MC TO 100 MC PUSH-PUSH DOUBLER—
Two 1N4386 varactors are connected in phase opposition to input signal and parallel to common load at even harmonic signal, to give action comparable to push-push circuit. Power-handling capacity is twice that of single varactor, with added benefit of odd-harmonic suppression.—G. Schaffner and J. Cochran, "Varactor Diodes and Circuits for High Power Output and Linear Response," Motorola Application Note AN-191, Aug. 1965.

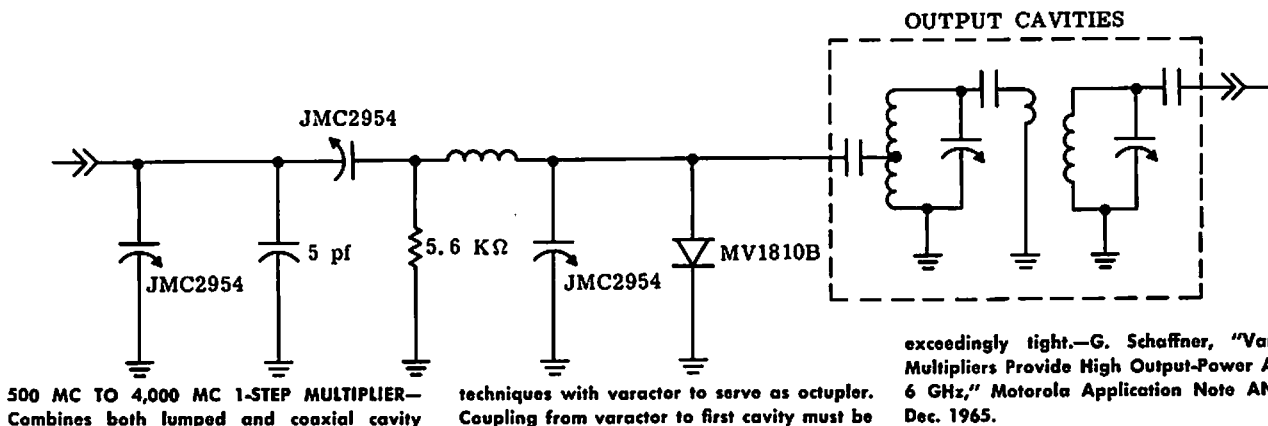


- L₁ - 3 TURNS OF 1/16" WIRE 1/2" DIA. x 1" LONG.
- L₂ - 1 TURN 1/8" TUBING 3/8" O.D.
- L₃ - STRAIGHT COUPLING LOOP 1/8" TUBING 2" LONG SPACED APPROX. 1/8" FROM CENTER CONDUCTOR.
- L₄ - STRAIGHT COUPLING LOOP 1/16" WIRE 1-1/2" LONG, SPACED APPROX. 1/16" FROM CENTER CONDUCTOR.

200 MC TO 600 MC HARMONIC TRIPLER—
Uses single varactor to give 20 w output from 40 w input.—G. Schaffner and J. Cochran, "Varactor Diodes and Circuits for High Power Output and Linear Response," Motorola Application Note AN-191, Aug. 1965.



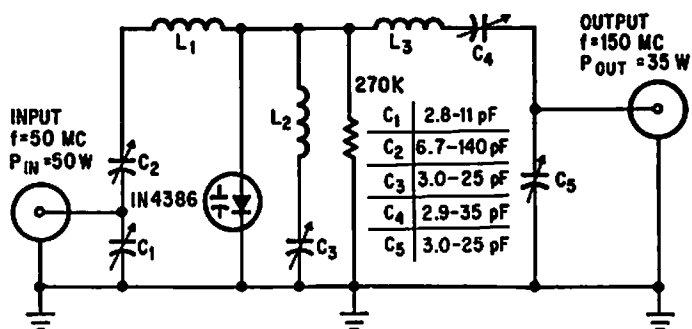
TRIPLER WITH OVERLAY TRANSISTOR GIVES 1.02 GC—Single overlay transistor eliminates conventional transistor amplifier and chain of varactor frequency multipliers. Output power is 3.5 w.—H. C. Lee and G. J. Gilbert, Overlay Transistors Move into Microwave Region, *Electronics*, 39:6, p 93-95.



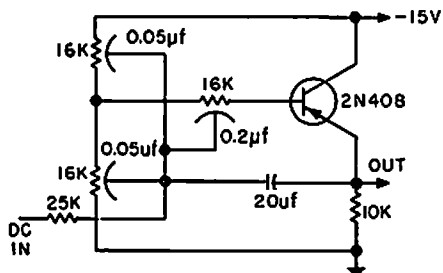
500 MC TO 4,000 MC 1-STEP MULTIPLIER—
Combines both lumped and coaxial cavity

techniques with varactor to serve as octupler. Coupling from varactor to first cavity must be

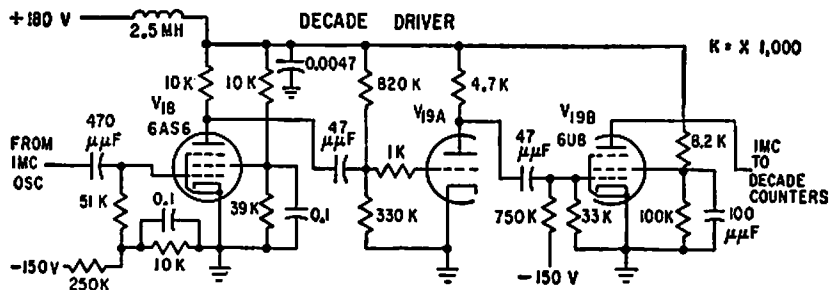
exceedingly tight.—G. Schaffner, "Varactor Multipliers Provide High Output-Power Above 6 GHz," Motorola Application Note AN-213, Dec. 1965.



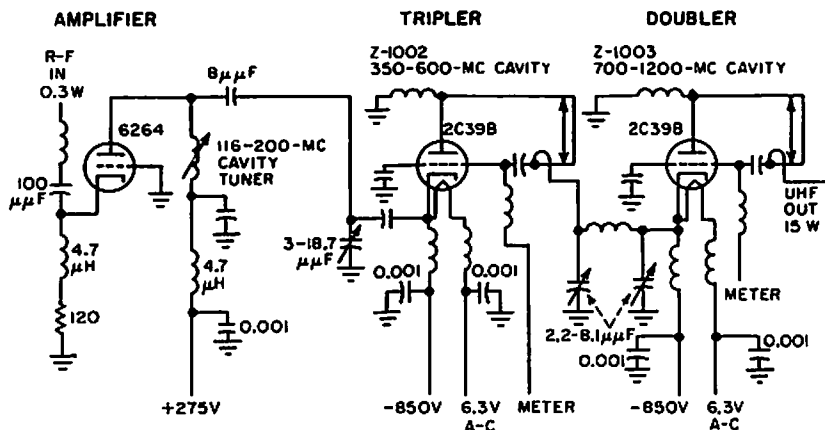
50-150 MC TRIPLER—Charge-storage 1N4386 varactor triples frequency with power efficiency of 70% for input of 50 watts.—G. Schaffner, Charge Storage Varactors Boost Harmonic Power, *Electronics*, 37:20, p 42-47.



SIMPLE DOUBLER—Uses distributed R-C networks consisting of resistive and conductive layers on dielectric substrate, with d-c applied between electrodes at 65 v for doubling frequency of ceramic-dielectric 400-cps oscillator.—M. M. Perugini, Race to Reduce Capacitor Size, *EEE*, 10:7, p 61-64.

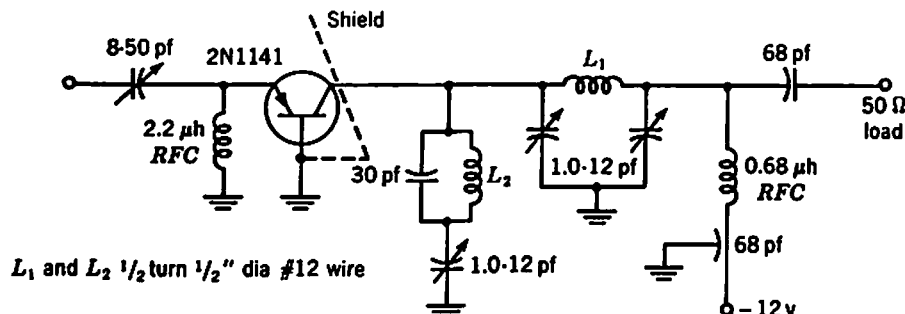


DECADE DRIVER FOR FREQUENCY MULTIPLIER—Input signal from frequency-multiplying oscillator is stepped up to 80 v peak-to-peak, with 1-microsec rise time, for accurate triggering of decade counter.—W. O. Brooks, Stepping up Frequency with Counter Circuits, *Electronics*, 32:29, p 60-62.



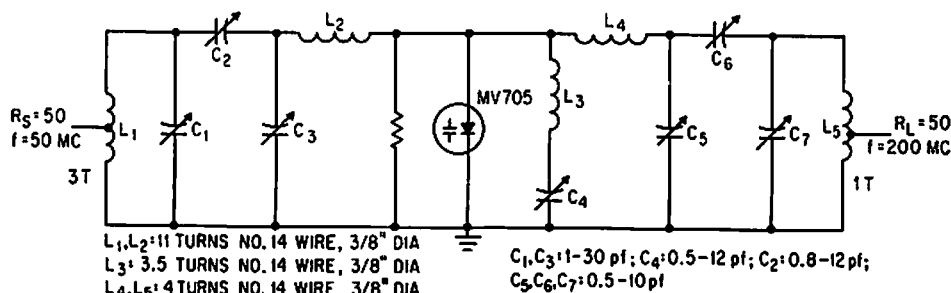
TRIPLER-DOUBLER GIVES 700-1,200 MC—First stage is grounded-grid amplifier, with plate tuned by 1-turn coil and variable capacitor. Common-grid tripler and doubler are tuned

with coaxial resonators.—A. E. Anderson and H. D. Hern, F-M Exciter For Sight or Scatter Systems, *Electronics*, 31:11, p 148-151.



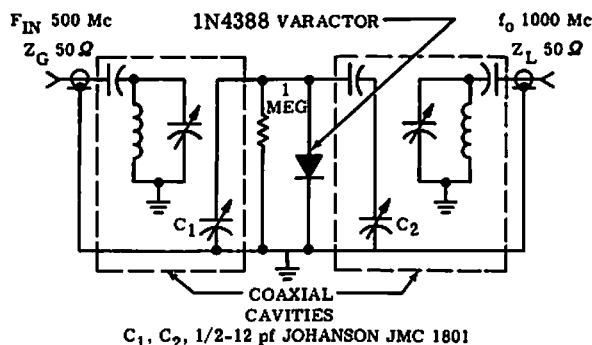
121.5 MC TO 243 MC DOUBLER—Input is tuned to fundamental and output to second harmonic. Combination series-parallel trap in

collector circuit rejects fundamental.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 328.



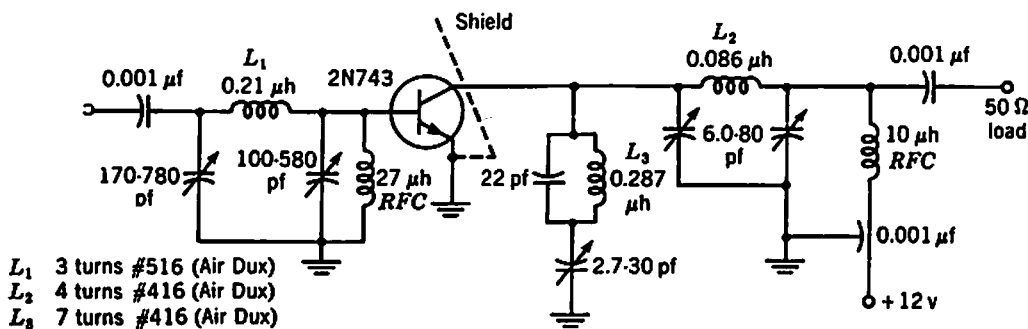
VARACTOR FREQUENCY QUADRUPLER—With 50-Mc input, output is 22 w at 200 Mc. Series-tuned idler circuit L3-C4 is omitted for

frequency-doubling.—L. E. Clark, E. B. Mack, and R. C. Hojhall, Highlights of Small-Signal Circuit Design, *Electronics*, 36:49, p 46-50.



500 MC TO 1,000 MC DOUBLER—Single varactor gives up to 15 w output from 25 w input, with output linear up to 11 w. Conversion efficiency is 50%.—G. Schaffner and J.

Cochran, "Varactor Diodes and Circuits for High Power Output and Linear Response," Motorola Application Note AN-191, Aug. 1965.

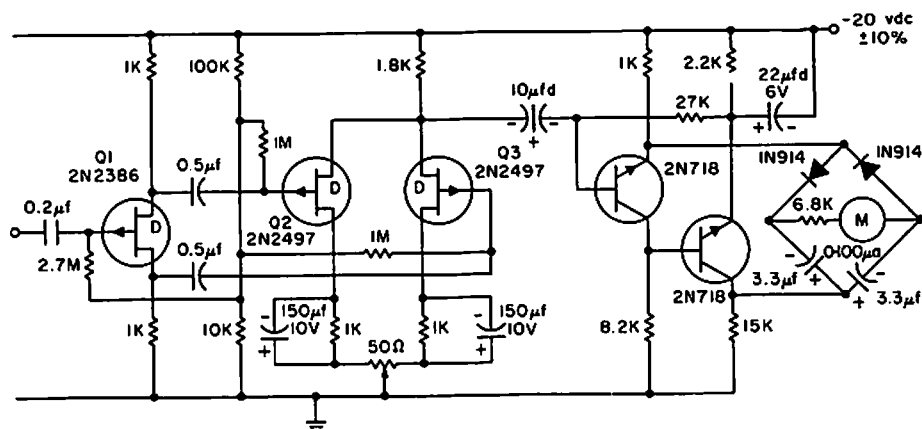


21 MC TO 42 MC DOUBLER—Combination series-parallel trap in collector circuit provides 50 db rejection of fundamental in output

that is tuned to second harmonic.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 328.

CHAPTER 38

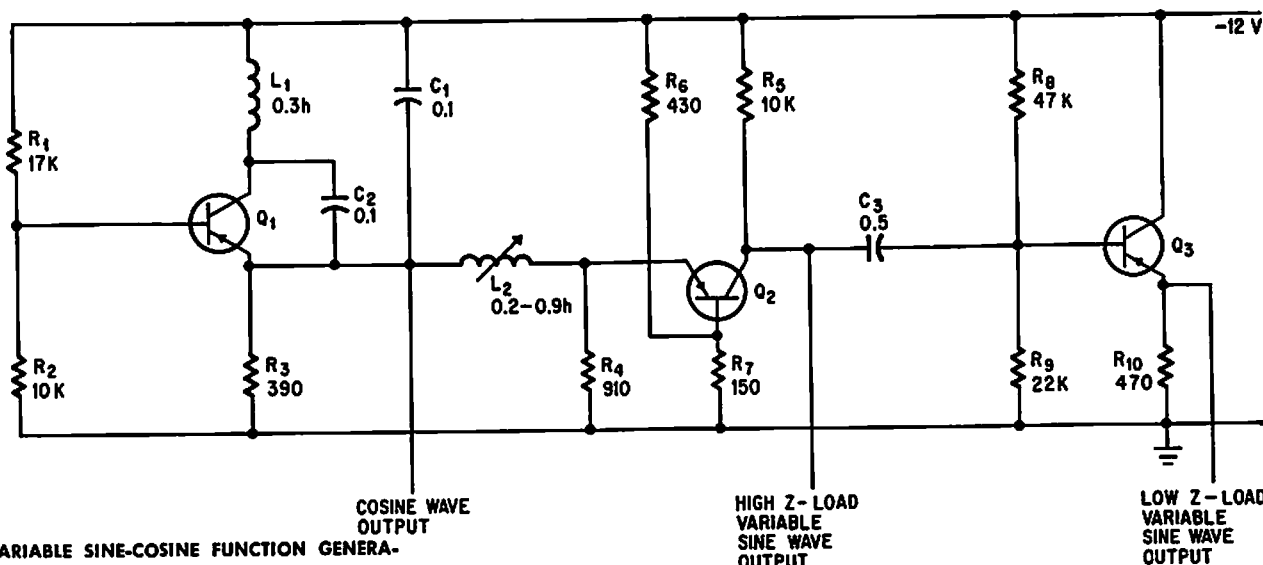
Function Generator Circuits



AUDIO FET SQUARING CIRCUIT—Used in noise investigation, analog computation, and measurement of power in complex waveforms, where squarer of high accuracy and

wide bandwidth is required. First stage is phase divider whose outputs drive squaring fet's Q2 and Q3. Output of squarer is coupled through capacitor to motor rectifier

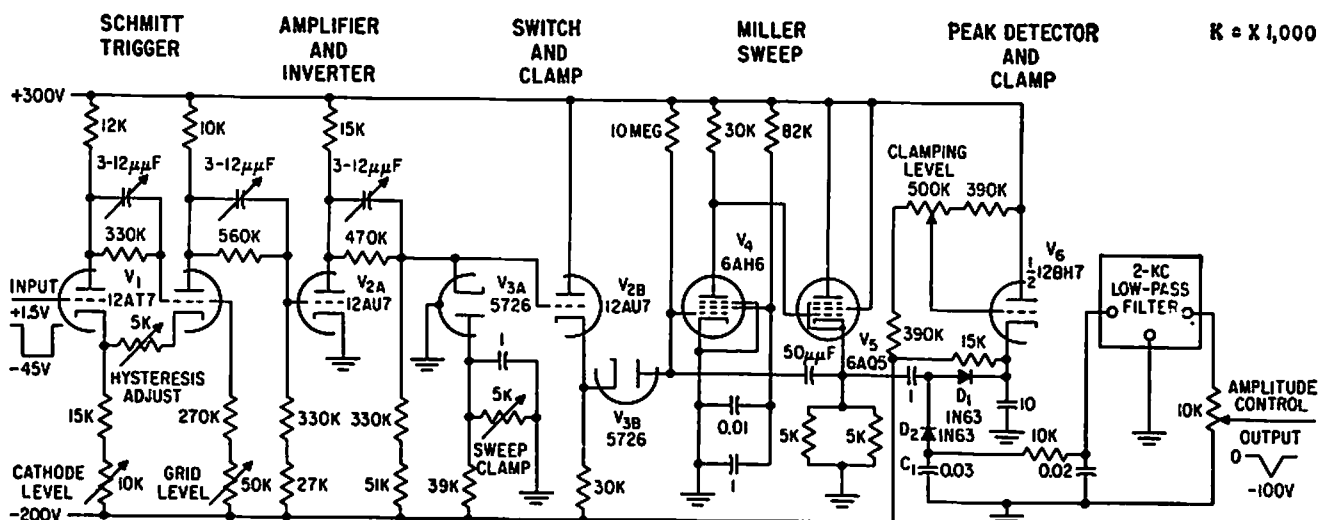
whose reading is proportional to square of amplitude of input voltage.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 83.



VARIABLE SINE-COSINE FUNCTION GENERATOR—For testing phase detector, one output gives reference cosine function and other gives waveform that can be varied in phase from sine to cosine function, with choice of

high or low output impedance. Q1 is modified Colpitts. Frequency stability is 1% from 1 kc to 100 kc with regulated power supply.

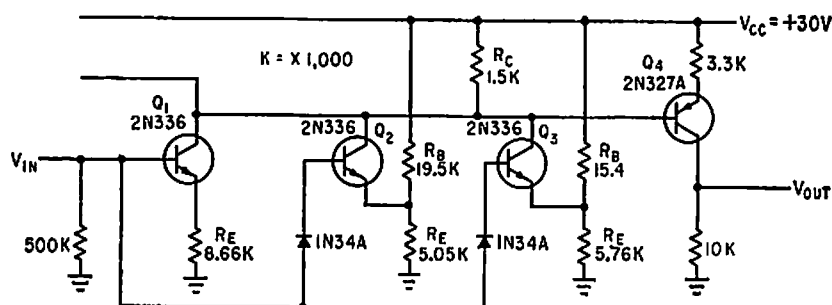
—J. G. Peddie, Oscillator Generates Sine, Cosine Waves Simultaneously, *Electronics*, 37:22, p 74.



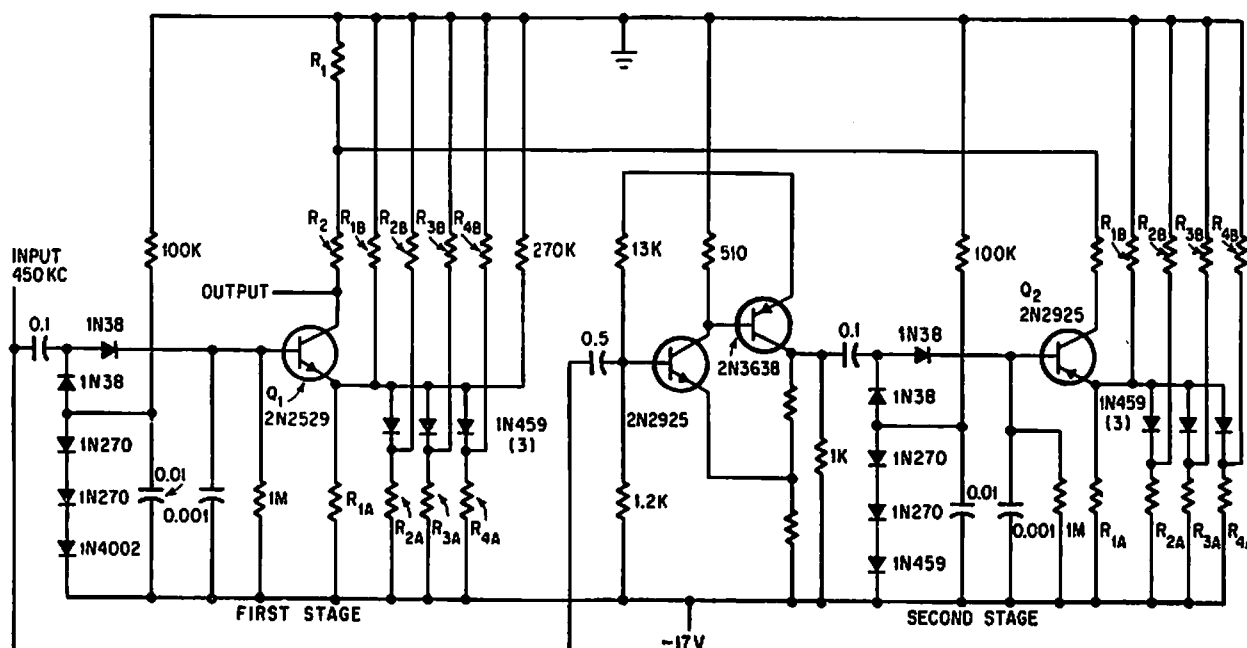
PHOTOELECTRIC FUNCTION GENERATOR—Open-loop photoelectric function generator generates any single-value function with 1%

accuracy, as required for duplicating particular antenna patterns in radar simulator. Uses horizontal sweep of 10 millise (100 cps)

with 5-kc vertical sweep.—B. Silverberg, Function Generator for Radar Simulator, *Electronics*, 32:2, p 52-55.



FUNCTION GENERATOR—Desired function of input voltage is developed across RC and inverted by Q4. One application is for computing ground range of radar target from slant range. Transistors eliminate need for d-c amplifier.—D. R. Chick, Boosting Function Generator Output with Transistors, *Electronics*, 33:13, p 75-76.

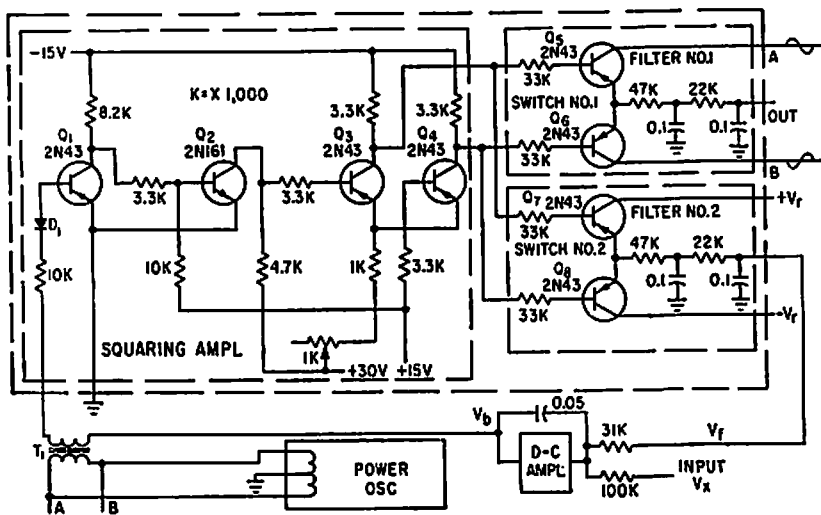
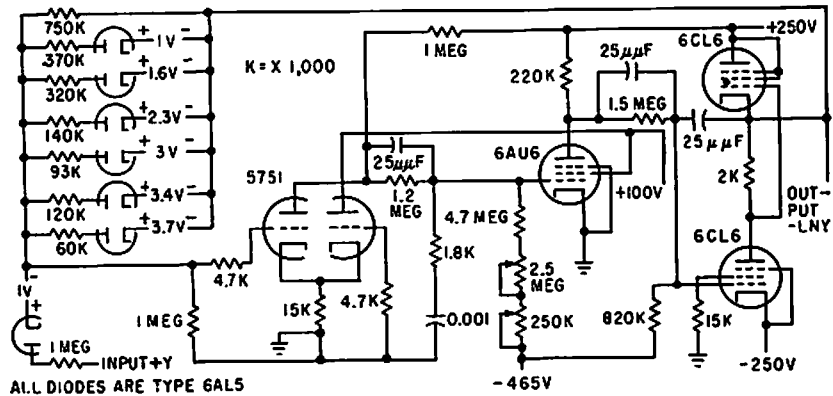


SQUARE-LAW OUTPUT—Diode network and detector provide output proportional to square of input voltage. Input range of 40 db is split into two 20-db segments. Each stage saturates and gives constant output

for voltages above operating range. For voltages below operating range, stage is cut off and has zero output. Combination of two stages gives desired square-law characteristic. Resistor values are: R1A, R3A = 3.18K,

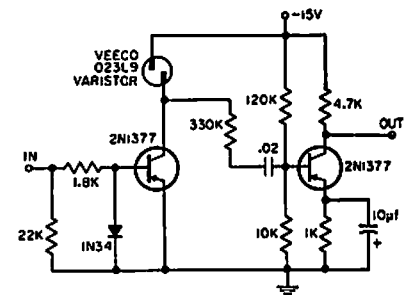
R1B = 57.9K, R3B = 18.75K, R5 = 3K, R7 = 10, R2A, R4B = 5.06K, R4A = 1.86K, R2B = 64.2K, R6 = 300 and R8, R9 = 990.—R. J. Matheson, Square-Law Detector has 40-db Dynamic Range, *Electronics*, 39:18, p 95-97.

EXPONENTIAL FUNCTION GENERATOR—Output is exponentially decaying curve in which exponent is determined by other components of system. Used in analog computer to integrate curve of dye concentration in blood stream to obtain flow rate.—R. L. Skinner and D. K. Gehmlich, *Analog Computer Aids Heart Ailment Diagnosis*, *Electronics*, 32:40, p 56-59.

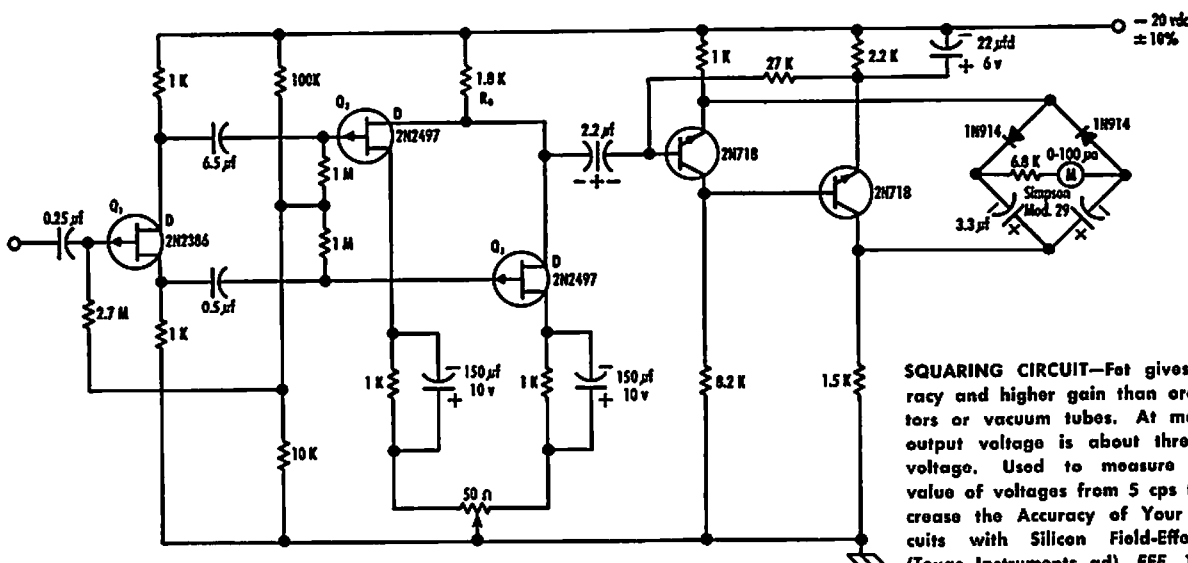


COSINE/SINE GENERATOR—Based on fact that area under sine curve varies as cosine function. Input voltage is converted into pulse width that controls electronic switch to cut off one portion of sine wave. Resulting rec-

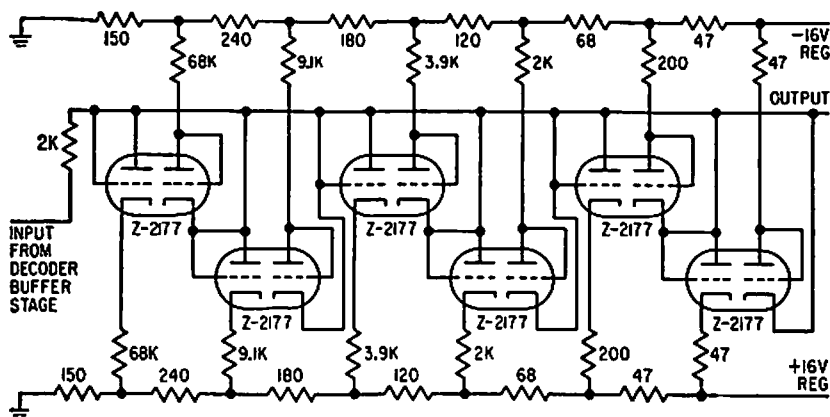
tangular pulses are symmetrical with respect to positive peak of sine wave.—H. Schmid, *Function Generator for Sines or Cosines*, *Electronics*, 32:4, p 48-50.



DYNAMIC RANGE COMPRESSOR—Transistorized version of vacuum-tube drawdown limiter or compressor amplifier limits dynamic range of any negative input signal without a threshold or saturation level. Output is approximately proportional to cube root of input, thus giving effective dynamic range compression. Good over audio range, yet can operate up to megacycle region if used with suitable high-frequency transistors and series inductance to varistor. Maximum input signal of 200 mv produces 3 v output, corresponding to gain of 15 at maximum permissible compression.—D. E. Lancaster, *Dynamic Range Compressor*, *EEE*, 11:2, p 25.



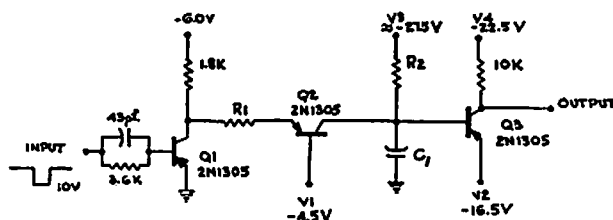
SQUARING CIRCUIT—Fet gives higher accuracy and higher gain than ordinary transistors or vacuum tubes. At maximum input, output voltage is about three times input voltage. Used to measure mean-squared value of voltages from 5 cps to 100 kc.—Increase the Accuracy of Your Squaring Circuits with Sillicon Field-Effect Transistors (Texas Instruments ad), *EEE*, 11:7, p 6-7.



SINE-COSINE APPROXIMATOR—Converts triangular first approximation of sine and cosine of azimuth angle to accurate approxi-

mation of these functions by use of two diode-connected triodes as function generators, one for positive signals and the other

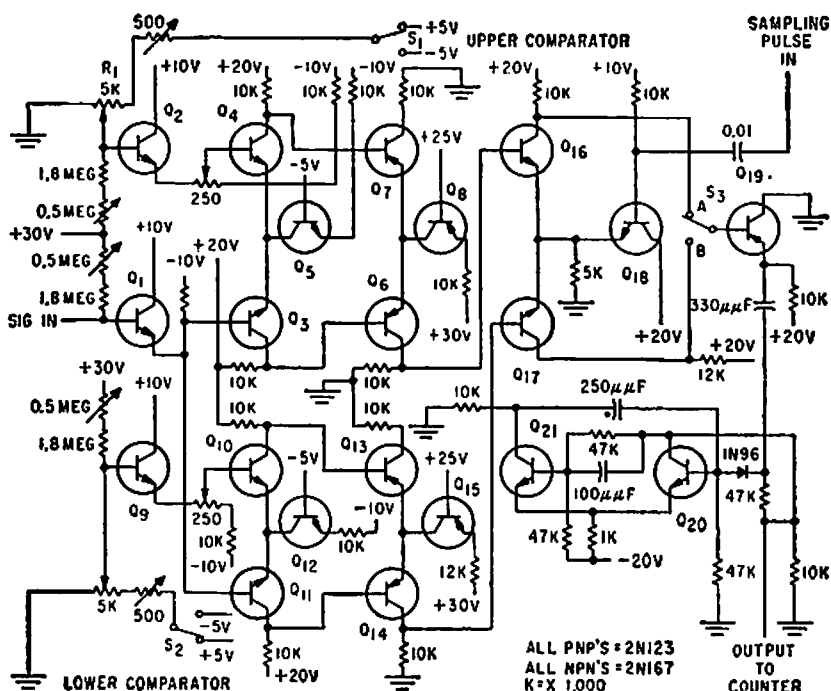
for negative signals.—B. L. Bair, Logical Design of SAGE Input Monitor, *Electronics*, 31:33, p 78-81.



TIME AMPLIFIER—Output pulse width is linear function of input pulse width. With two stages, circuit can amplify nanosec pulse widths to seconds. Article gives design procedure for choosing values of R1, C1, and R2

for desired time amplification and input pulse range. For input of 0.1 - 1 microsec and time amplification of 1,000, R1 = 51, C1 = 8,200 mfd, and R2 = 3.2 meg. For 10 - 100 microsec input and amplification of

100, R1 is increased to 510 ohms. For 100 to 1,000 microsec input and amplification of 10, R1 = 510, C1 = 0.82 mfd, and R2 = 320K.—R. W. Fergus, Time Amplifier, *EEE*, 11:8, p 26-27.



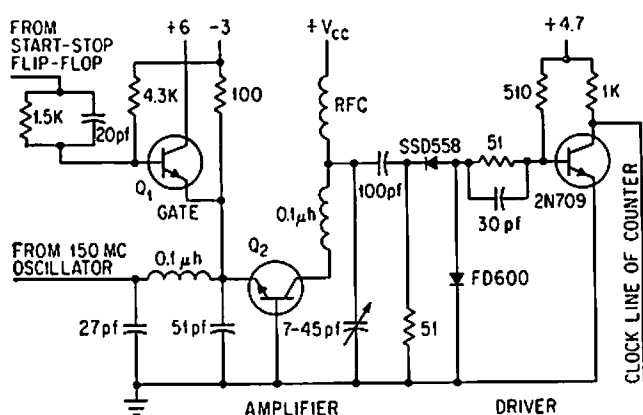
RANDOM-NOISE ANALYZER—Provides digital information from which amplitude probability distribution function and probability

density function can be plotted. Consists of two amplitude comparators followed by logic circuits and sampling network.—D. Hoffman

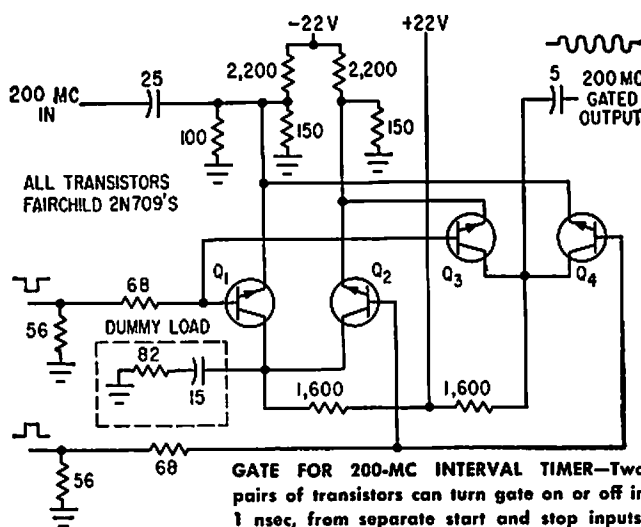
and E. Schutzman, Statistical Analysis of Noise-Signal Amplitudes, *Electronics*, 32:30, p 48-49.

CHAPTER 39

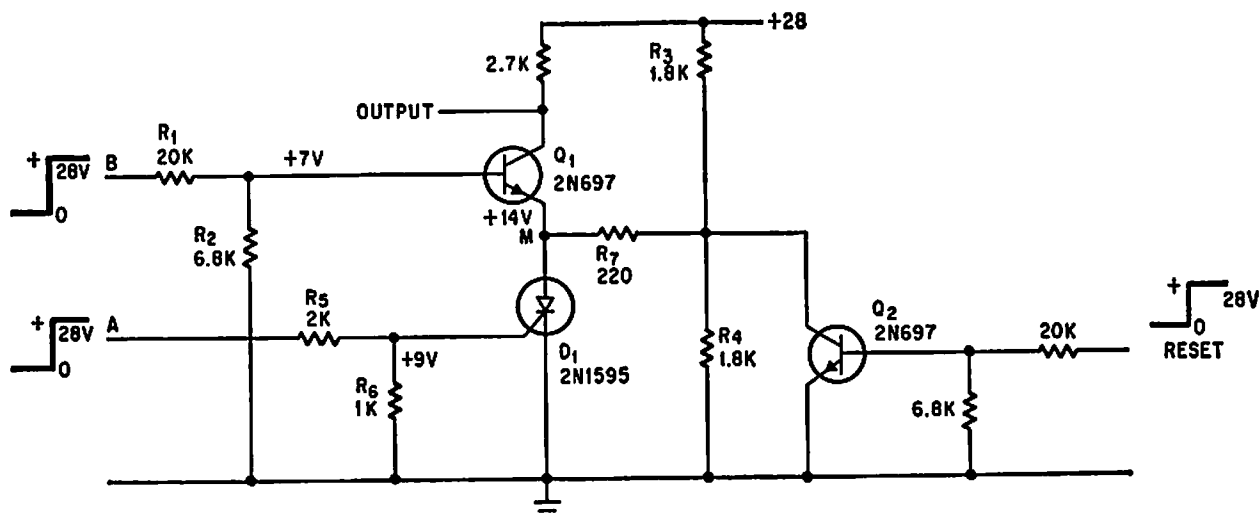
Gate Circuits



150-MC CLOCK GATE AND DRIVER—When gate transistor Q1 (2N2368) is turned on, 200-Mc bandwidth amplifier Q2 is off to provide isolation and permit switching within one cycle of clock. Driver output goes to decade counter.—L. C. Drew, *Using Microcircuits in High-Resolution Range Counters*, *Electronics*, 36:47, p 31-33.



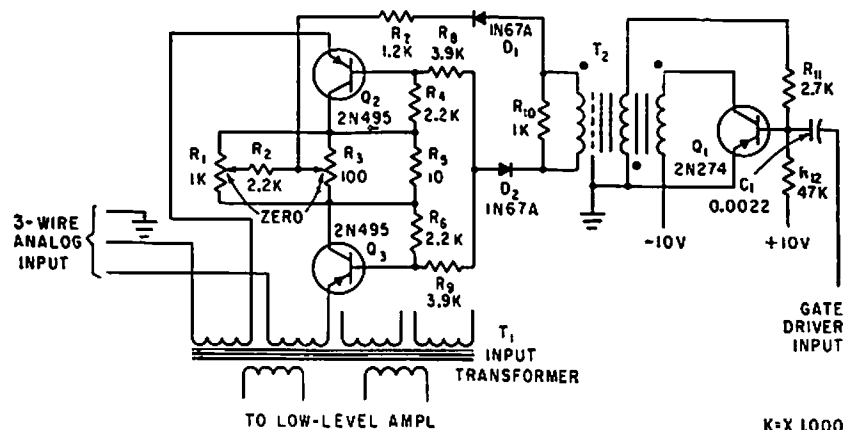
GATE FOR 200-MC INTERVAL TIMER—Two pairs of transistors can turn gate on or off in 1 nsec, from separate start and stop inputs. Only one transistor per pair conducts at a time.—C. S. Coffey, *VHF Counter Measures Time Intervals Precisely*, *Electronics*, 36:34, p 27-29.



PULSE SEQUENCE DETECTOR—Output occurs only when event signal at A precedes event

signal at B. Other sequences are ignored. —R. A. Wilson, *Two Events, in Sequence*, *Electronics*, 39:16, p 120-121.

Produce Detector Output, *Electronics*, 39:16, p 120-121.

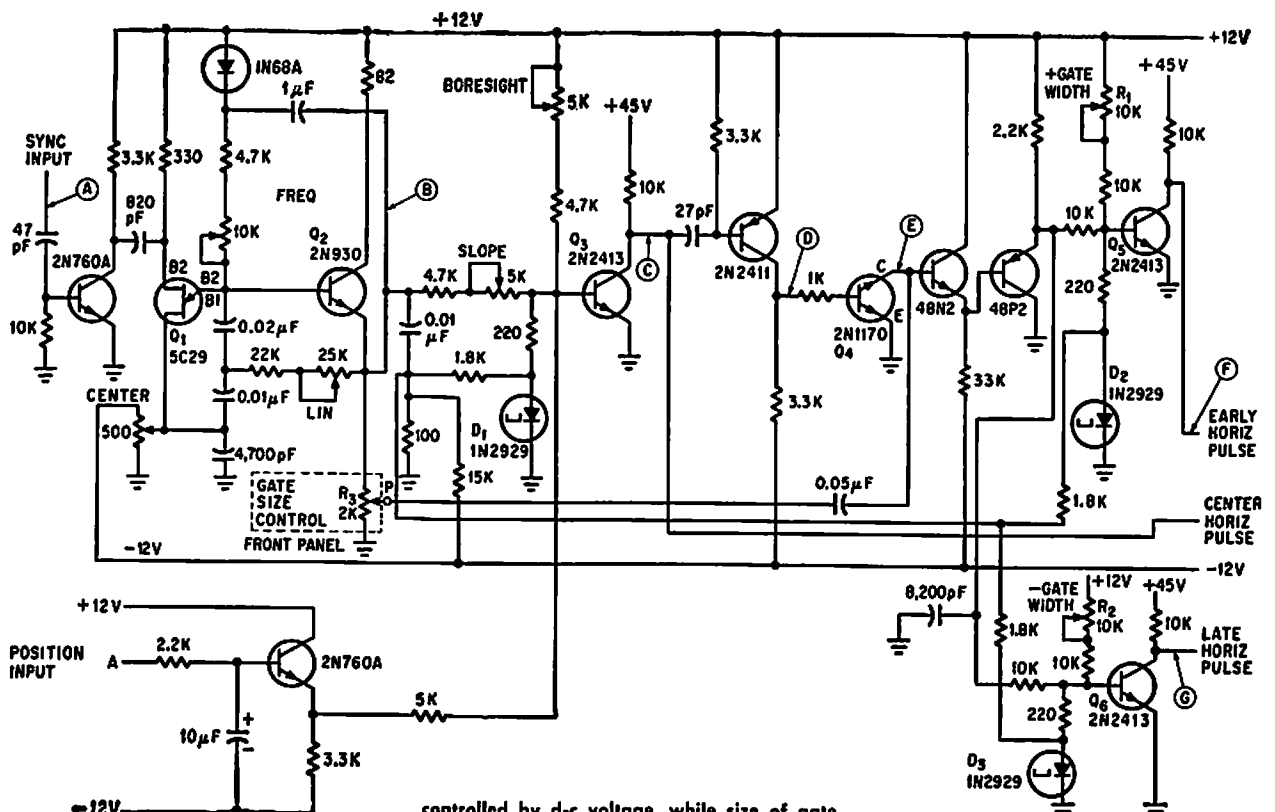
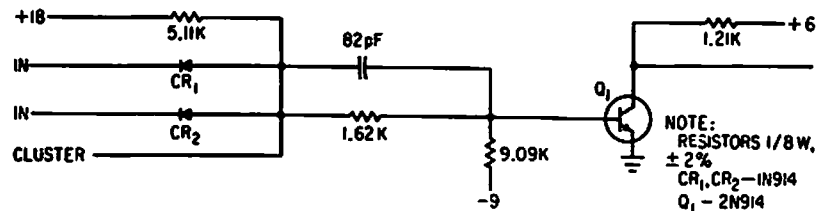


TRANSISTOR SERIES SWITCH—Two transistors back-to-back in inverted connection serve as and gate between analog input from in-

strumentation transducer and input transformer of multiplexer. Gate driver receives key pulse from timing matrix.—C. E. Griffin, J. P.

Knight, and J. H. Searcy, Low-Level Multiplexing for Digital Instrumentation, *Electronics*, 33:41, p 64-66.

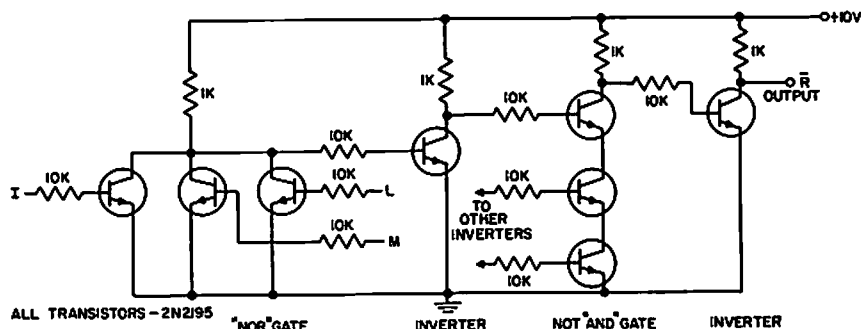
STANDARD MICROMODULE LOGIC GATE—Single-transistor gate can have maximum fan-in of 20 and maximum fan-out of 4. Power dissipation is 75 mw average, pair delay is 60 nsec, and rise time 30 nsec.—A. S. Rettig, Computers in the Front Lines: Micromodules Make it Possible, *Electronics*, 36:1, p 77-81.



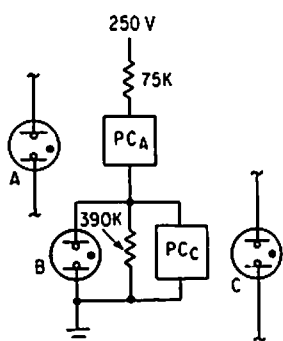
GATE GENERATOR FOR MISSILE TRACKER—Position of rectangular gate on tv display is

controlled by d-c voltage, while size of gate can be adjusted manually without affecting its center position.—J. R. Kruso, Automatic

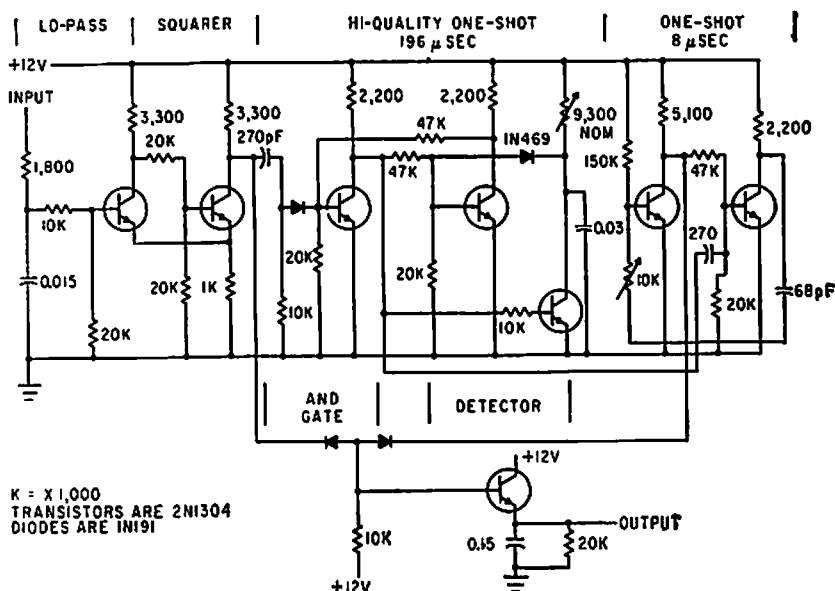
Tv Tracker Keeps Eye on Missiles, *Electronics*, 34:13, p 82-87.



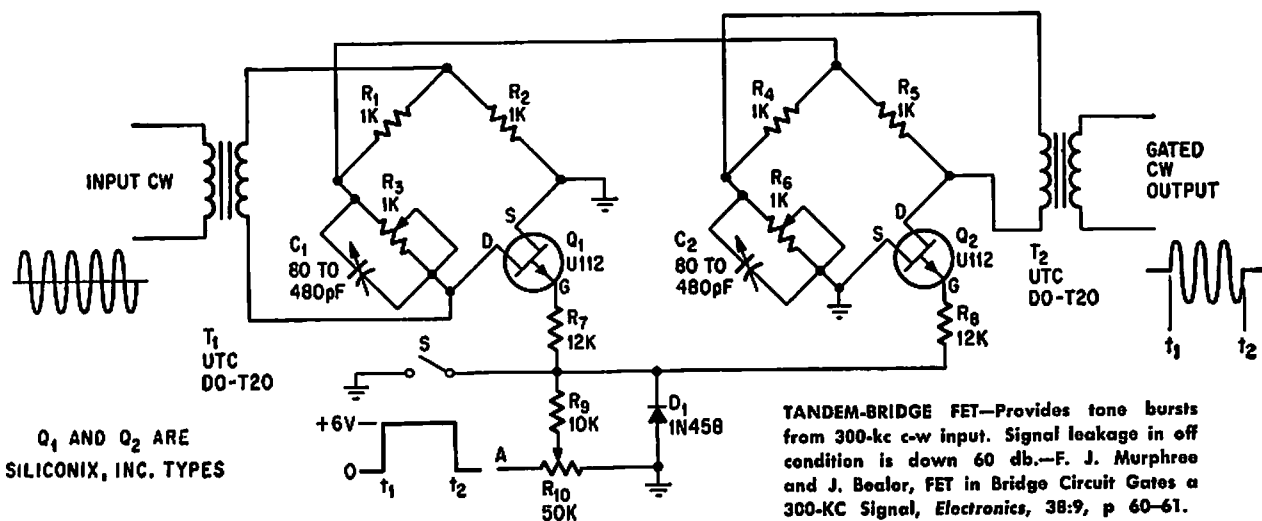
GATE OUTPUT INVERTER—Inverter stages compensate for phase inversion of three



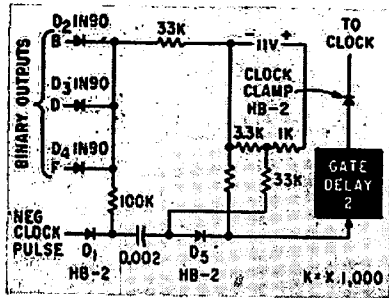
NEON PHOTOCONDUCTOR IN LOW-COST LOW-SPEED GATE—Cadmium selenide or cadmium sulfide photoconductors PC deposited on common substrate are used in pairs with Ne-2H miniature neon indicator lamps to replace electromechanical relays in low-speed digital gate. Firing time of neons is reduced and stabilized by applying 350-v, 60-cps voltage between one neon electrode and adjacent external electrode, to maintain ambient light that gives low level of ionization in lamp.—J. L. Patterson, Will Neon Photoconductors Replace Relays in Low-Speed Logic?, *Electronics*, 36:18, p 46-49.



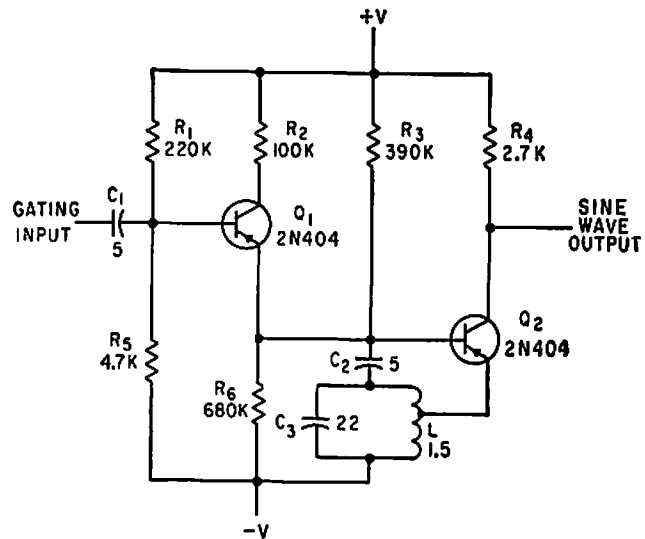
SLOT FILTER—Interval-sensitive gate will detect tone in range from 4,900 to 5,100 cps regardless of other frequencies present.—A.



TANDEM-BRIDGE FET—Provides tone bursts from 300-kc c-w input. Signal leakage in off condition is down 60 db.—F. J. Murphree and J. Bealor, FET in Bridge Circuit Gates a 300-KC Signal, *Electronics*, 38:9, p 60-61.

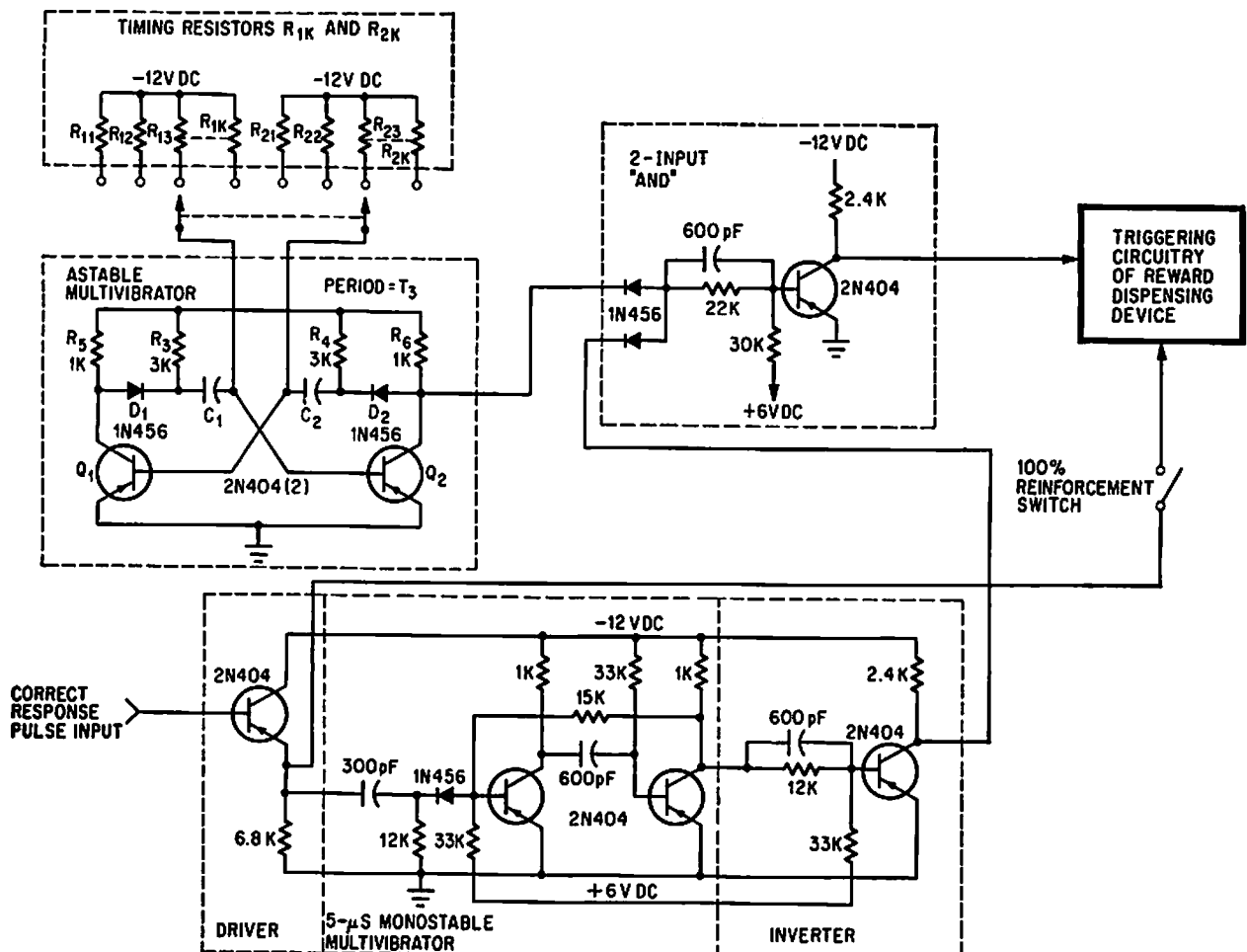


GATED DELAY—Negative-output clock pulses of 256-parameter microwave system checker are applied to gating diode D1, which ordinarily blocks signal to delay mvbr. During eighth pulse of code train, diodes D2-D3-D4 receive negative voltage from their binary outputs and make D1 trigger mvbr through D5. At end of mvbr delay, clamp is removed until eight more pulses arrive.—J. B. Bullock, Pulse-Coded Fault Alarm in Microwave Systems, *Electronics*, 33:1, p 82-84.



GATED OSCILLATOR—Drives pair of pulse generators for selective calling system.—A. I.

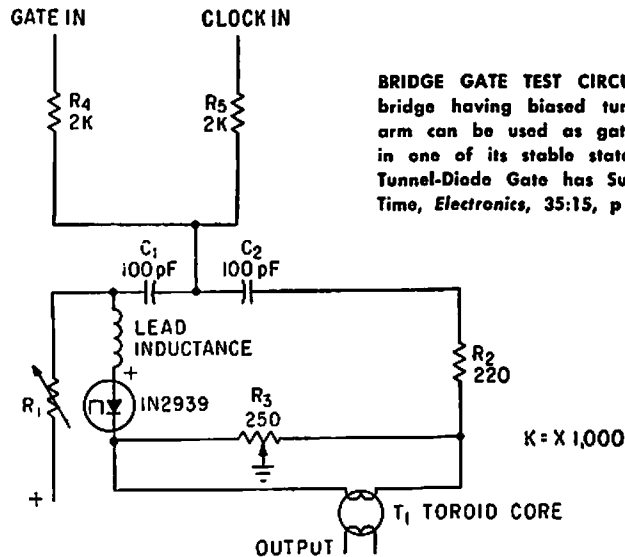
Perlin, Selective Calling for Data Link Systems, *Electronics*, 33:18, p 108-110.



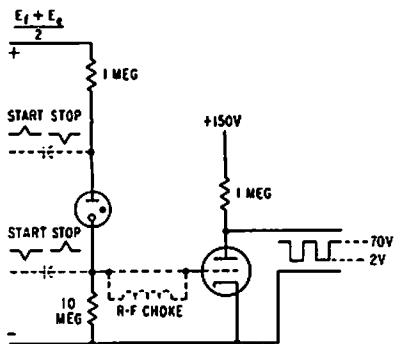
TEACHING-MACHINE REWARD GATE—Astable and monostable multivibrators feed and gate that triggers reward-dispensing device (such as candy dispenser) when number of

correct answers exceeds preset percentage of random probability. Circuit may also give 100% reinforcement for correct response but with candy reward only at spaced intervals.

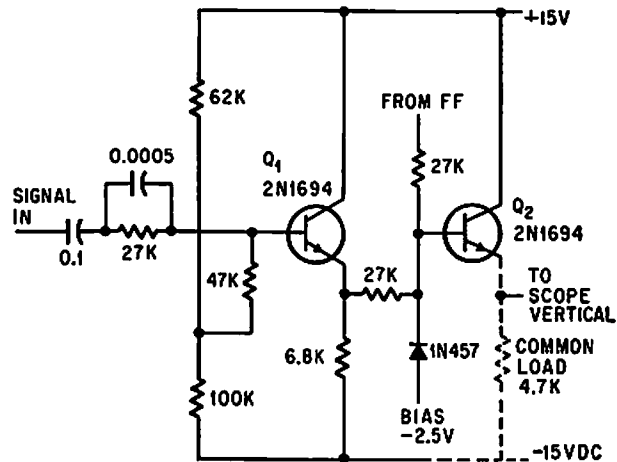
—G. S. Pennington, Jr. and J. A. Boehm, III, Gate Varies Rewards from Teaching Machine, *Electronics*, 39:10, p 92-93.



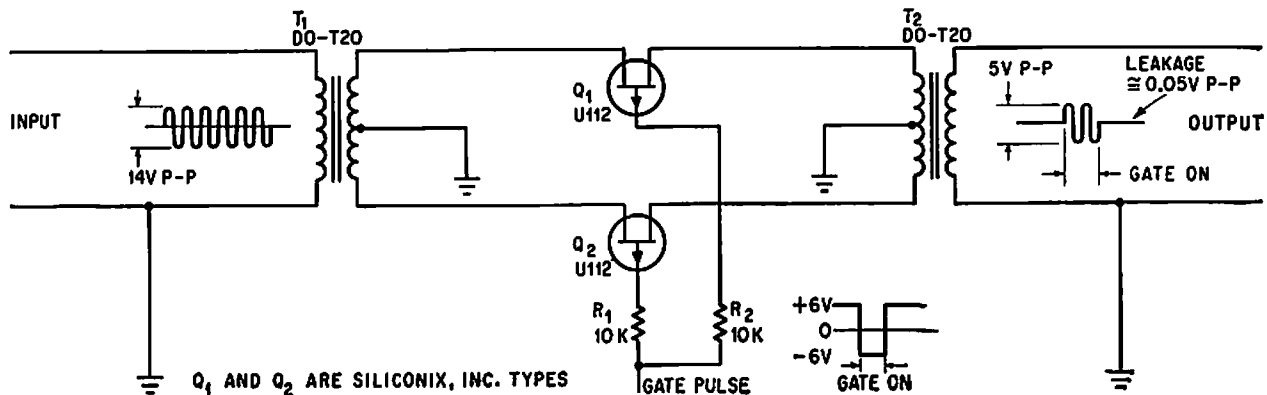
BRIDGE GATE TEST CIRCUIT—Two-state a-c bridge having biased tunnel-diode in one arm can be used as gate when balanced in one of its stable states.—F. W. Kantor, Tunnel-Diode Gate has Subnanosecond Rise Time, *Electronics*, 35:15, p 62-64.



NEON-TRIODE OFF-ON GATE—Supply voltage is set midway between firing and extinction voltages of neon tube. Neon conducts when triggered by momentary increase in voltage, and continues conducting until supply voltage is momentarily lowered below extinction voltage. Can be used to produce low-repetition-rate pulses. Triode may be 6AV6 or ½ 12AX7.—R. L. Ives, Neon Triode Gives Low-Speed Gate, *Electronics*, 31:11, p 170-174.



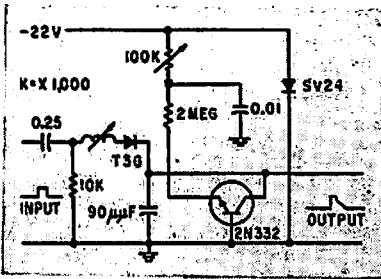
DIODE GATE—Input signal from ring counter applies reverse bias through isolating transistor Q1 to diode gate and base of Q2, which then supplies current to common load of multichannel scope display.—J. E. Russell, Ten Signals at a Glance, *Electronics*, 37:19, p 54-57.



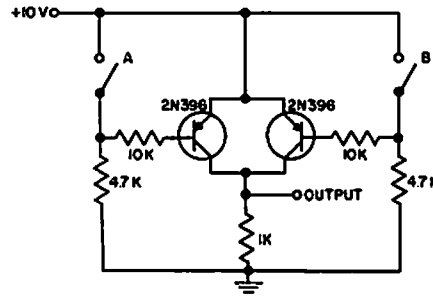
SIGNAL-BRIDGE FET—Provides pulsed c-w output from 300-kc c-w input. On-off ratio is 50 db. Insertion loss is 15 db with 510-ohm

output load. Requires no adjustment. Used in sonar experiments.—F. J. Murphree and J. Bealor, FET in Bridge Circuit Gates a 300-KC

Signal, *Electronics*, 38:9, p 60-61.

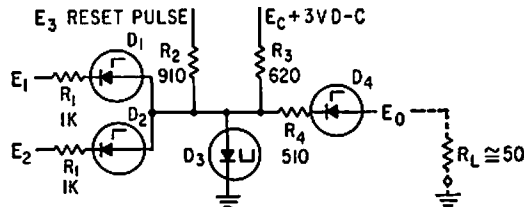


PULSE HEIGHT-TO-WIDTH CONVERTER—Converts 0 to 2-v pulse to gate for pulse height to pulse width conversion. Gate width, directly proportional to data pulse amplitude, is applied to clock circuits.—W. W. Grannemann et al., Pulse-Height-to-Digital Signal Converter, *Electronics*, 33:2, p 58-60.



BASIC PNP GATE—Circuit is and gate if closing of switch is an input. Circuit is or gate if opening of switch is an input. Provides phase inversion of input without complicat-

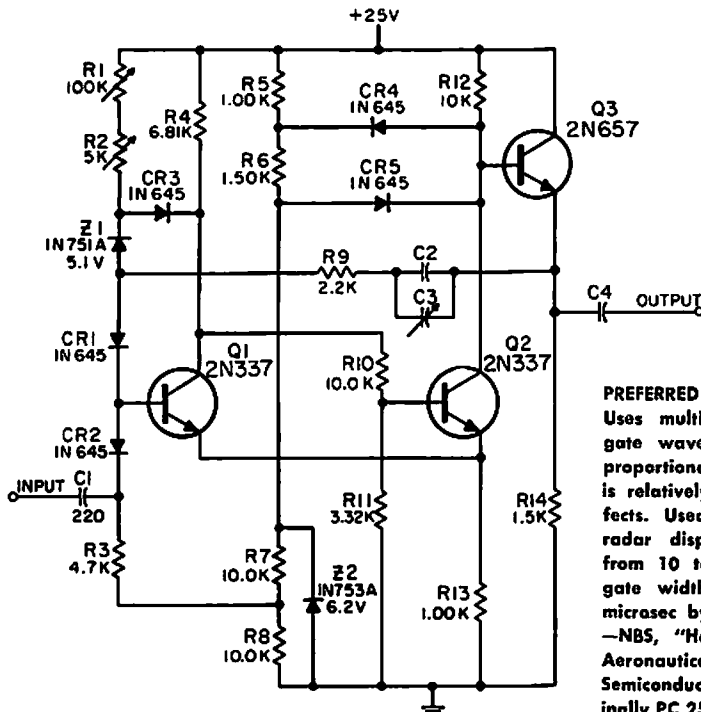
ing overall circuitry.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 176.



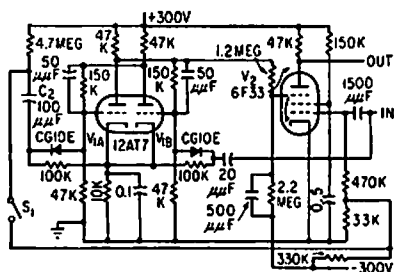
FAST TUNNEL-DIODE GATE—At coincidence between sampling pulse at one input and 100-Mc block pulse at other input, 11 ma current through D3 switches D3 to high level,

making D4 pass current to load.—A. A. Fleischer and E. Johnson, New Digital Conversion Method Provides Nanosecond Resolution, *Electronics*, 36:18, p 55-57.

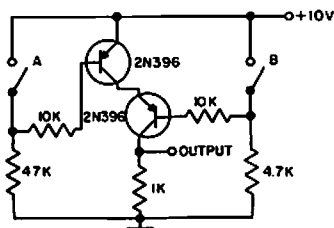
$E_{CS} + 3V D-C$
 $E_1, E_2 = +3V PULSES$
 $E_3 = -4V PULSE$
 $D_1, D_2, D_4 = BD-4$
 $D_3 = IN3149$



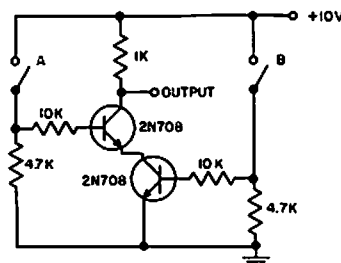
PREFERRED VARIABLE GATE GENERATOR—Uses multivibrator to generate rectangular gate waveform whose duration is directly proportional to setting of potentiometer and is relatively independent of temperature effects. Used to produce movable markers for radar displays. Input signal is negative, from 10 to 20 v. Output is 12.5 v, with gate width adjustable from 10 to 10,000 microsec by changing values of R1 and C2.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 16 (originally PC 252), p 16-2.



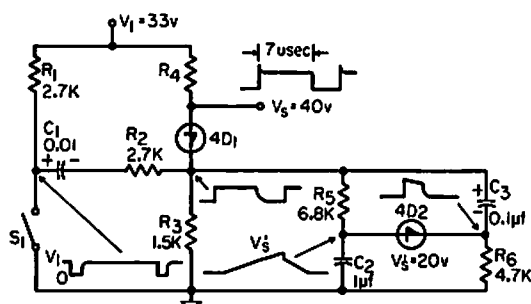
SYNCHRONIZED GATE—Continuous train of pulses is applied to control grid of gating tube. When S1 is closed, next pulse in train opens gate, then shuts gate after itself.—J. K. Goodwin, Time and Pulses Control Gates, *Electronics*, 32:3, p 72-73.



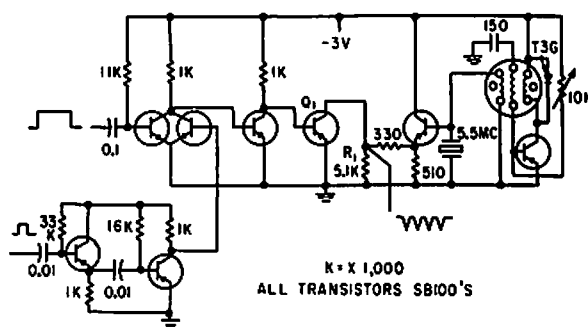
PNP SERIES-TRANSISTOR GATE—Circuit is or gate if closing of switch is an input. Circuit is and gate if opening of switch is an input. Provides phase inversion of input.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 176.



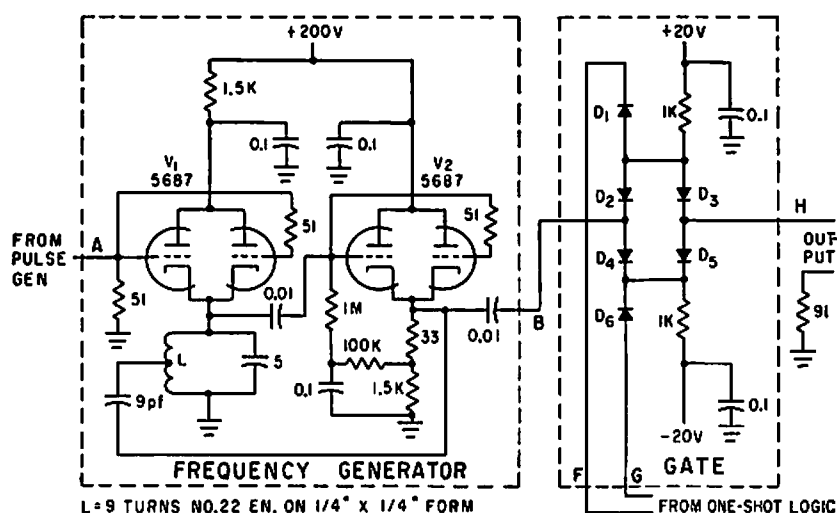
NPN SERIES-TRANSISTOR GATE—Circuit is and gate if closing of switch is an input. Circuit is or gate if opening of switch is an input. Provides phase inversion of input.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 176.



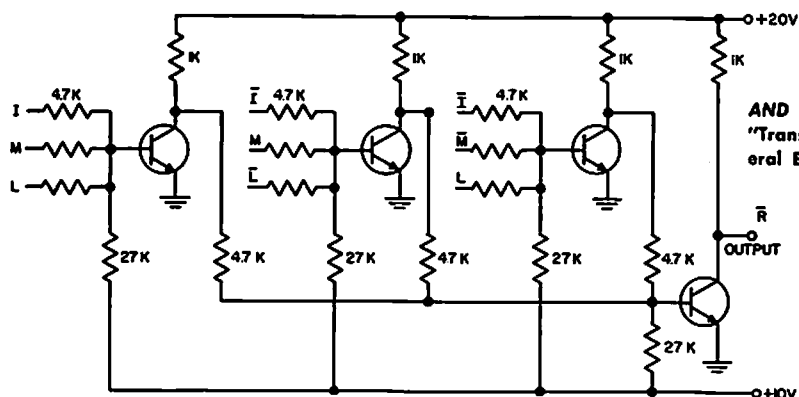
COMBINATION GATE AND ONE-SHOT—Uses two four-layer switching diodes, and draws no current from source except during gate pulse. Can also be used as one-shot. Width of pulse depends on supply voltage and R5-C2.—R. E. Amsterdam, Gate/One-Shot Uses Four-Layer Diodes, *EEE*, 12:12, p 58-60.



5.5-MC CRYSTAL OSCILLATOR GATE—Provides train of pulses to be read by 7-digit binary counter. Number of pulses in train depends on width of gate pulse received from gate amplifier, and is proportional to amplitude of data input pulse.—W. W. Grannemann et al, Pulse-Height-to-Digital Signal Converter, *Electronics*, 33:2, p 58-60.

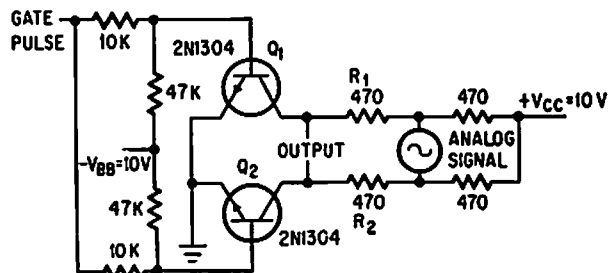
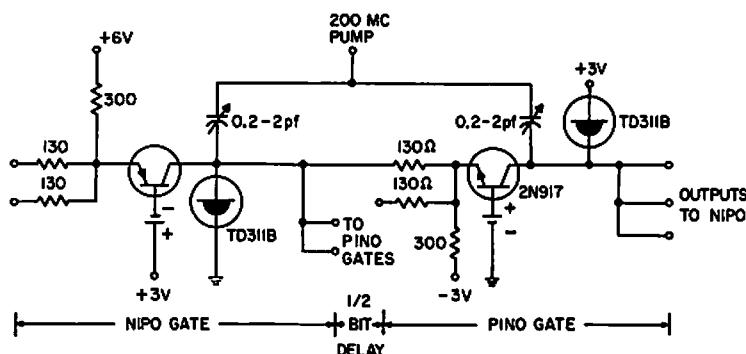


ONE-SHOT LOGIC CONTROLS TRANSMISSION GATE—Frequency generator, activated by negative pulse at A, generates 32-Mc burst in which number of cycles depends on input pulse width. Pushbutton-controlled one-shot logic allows diode transmission gate to pass first complete 32-Mc burst occurring after pushbutton is actuated, after which gate is closed. One-shot has sync connection to pulse generator.—V. Kann, One-Shot Gating Circuit Generates Sinewaves for Testing Counters, *Electronics*, 34:23, p 114-116.



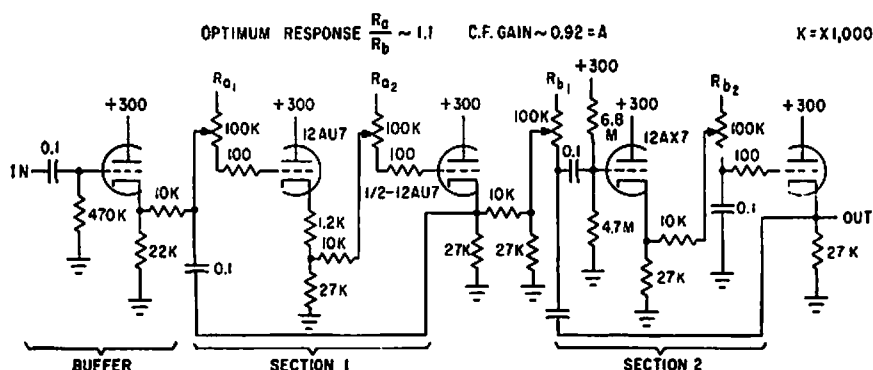
AND GATE—Uses *nor* logic with inversion.—*"Transistor Manual,"* Seventh Edition, General Electric Co., 1964, p 179.

PUMPED TUNNEL-DIODE LOGIC GATES—Shows method of interconnecting negative-input positive-output gate with positive-input negative-output gate for high-speed computer logic.—*"Transistor Manual,"* Seventh Edition, General Electric Co., 1964, p 370.



SINGLE POSITIVE PULSE GATES SINE WAVE SIGNAL—Two transistors act as balanced shunting switch, eliminating pedestal effects that normally necessitate dual-polarity gate pulses for analog signals. Positive 10-v pulse saturates both transistors, thereby grounding

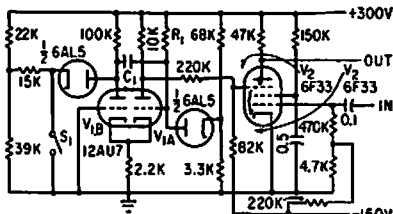
both output terminals to give isolation from input signal. Analog signal being gated can be as low as 5-mv rms.—L. E. Frenzel, Jr., Gate Circuit Eliminates Pedestal Effects, *Electronics*, 37:15, p 77.



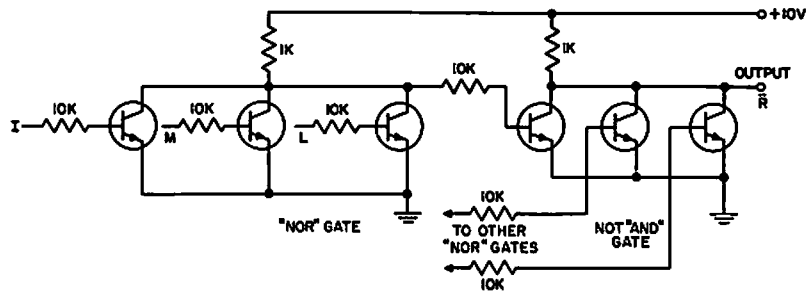
ZERO-CROSSING SWITCH FOR HEART SOUNDS—Active filter with three amplifiers provides output pulse for opening sliding

gate at instant when heart sound being monitored is zero. Filter has sharp cutoff above highest-frequency heart sound (600

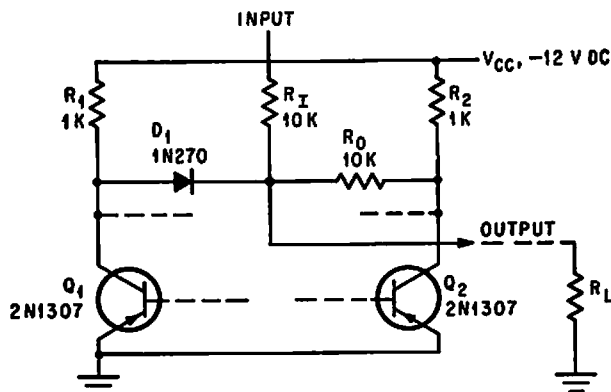
cps).—R. Weiss, Heart-Sound Discriminator Simplifies Medical Diagnosis, *Electronics*, 34:24, p 52-55.



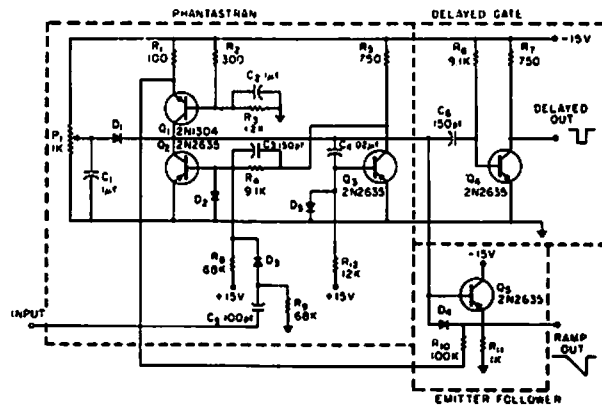
TIME-CONTROLLED GATE—Pentode gate is controlled by period of astable mvbr. Gate is closed when V1A is conducting; when S1 is closed, mvbr opens gate for period determined by C1-R1.—J. K. Goodwin, *Time and Pulses Control Gates*, *Electronics*, 32:3, p 72-73.

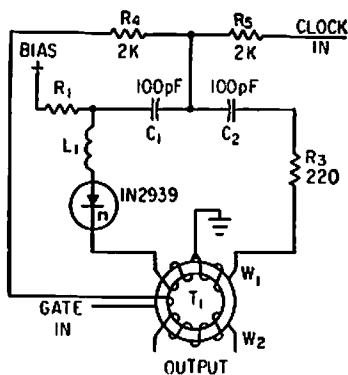


PHASE INVERSION WITHOUT INVERTERS—Used to achieve and and or functions from same circuit. Base resistors ease requirements for saturation voltage and base input voltage.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 177.

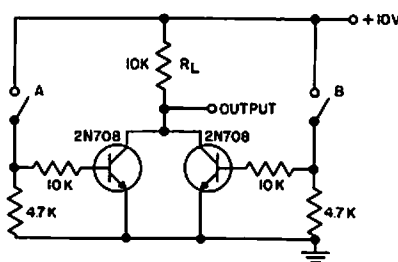


MVBR CONTROLS BILATERAL GATE—Q1 and Q2 are part of saturated multivibrator, and D1 with R0 form gate that permits output when Q2 turns on and blocks D1. When Q2 turns off, D1 and Q1 clamp output close to ground.—S. H. Tsao, *Multivibrator Controls Single-Diode Gate*, *Electronics*, 39:15, p 101.

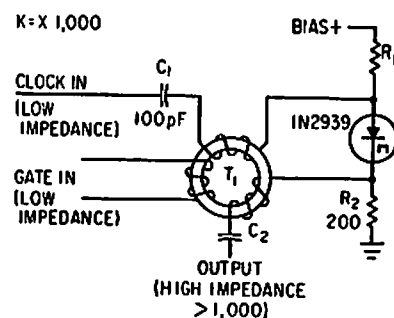




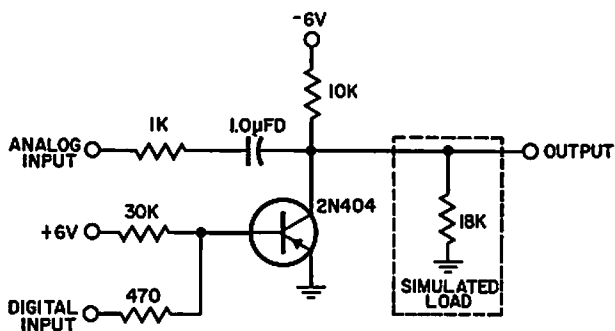
GATE WITH TAPPED TOROID—Arrangement of toroid windings minimizes number of components in a-c bridge used as computer gate, while keeping gating pulse out of output. R3 adjusts balance. Bias is adjusted for stable switching.—F. W. Kantor, Tunnel-Diode Gate has Subnanosecond Rise Time, *Electronics*, 35:15, p 62-64.



NPN NOR GATE—Circuit is or gate if closing of switch is an input. Circuit is and gate if opening of switch is an input. Provides phase inversion of input. If both switches are open, both transistors are nonconducting. When either switch is closed, output is negative, or not or, because of phase inversion, and circuit is therefore nor gate.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 176.

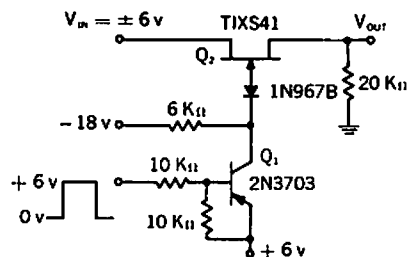


FAST-READOUT MEMORY—Voltage-divider version of tunnel-diode gate is used with toroid to give extremely fast readout, for use with computers having clock rates above 500 Mc. Tertiary winding cancels gating spike.—F. W. Kantor, Tunnel-Diode Gate has Subnanosecond Rise Time, *Electronics*, 35:15, p 62-64.

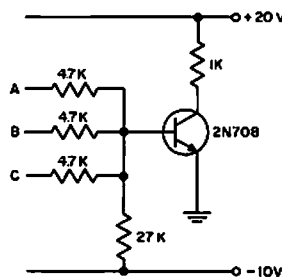


POSITIVE TRANSMISSION GATE—Is equivalent to digitally controlled analog switch, for frequency range of 8 to 650 kc. Output signal never passes through active device, hence is not attenuated, distorted, or de-

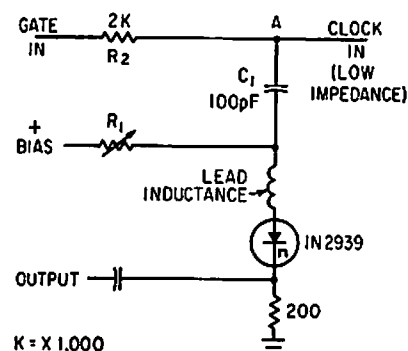
layed. Will pass a-c signal with zero average value. Ratio of on voltage to off voltage is 420:1 (4.2 v p-p to 10 mv p-p), for isolation of 54.5 db.—V. A. Bloom, Positive Transmission Gate, *EEE*, 10:9, p 26-27.



FET ANALOG GATE—Series connection of chopper-type fet permits high-accuracy analog switching. Resistance of Q2 when on is only about 20 ohms, and drain gate leakage current is less than 0.1 nanoamp.—Six More Semiconductor Advances From TI (Texas Instruments ad), *EEE*, 14:8, p 120-121.



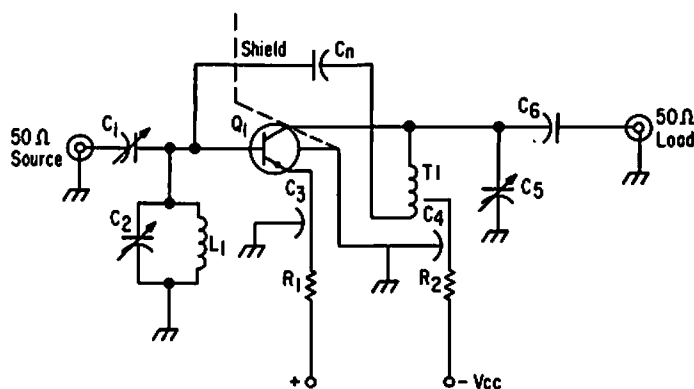
BASIC NOR GATE—Transistor conducts heavily if any of inputs is raised from 0 to +12 v.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 178.



TUNNEL-DIODE GATE—Impedance of tunnel diode is part of voltage divider, eliminating need for a-c bridge in gate operating above 500 Mc.—F. W. Kantor, Tunnel-Diode Gate has Subnanosecond Rise Time, *Electronics*, 35:15, p 62-64.

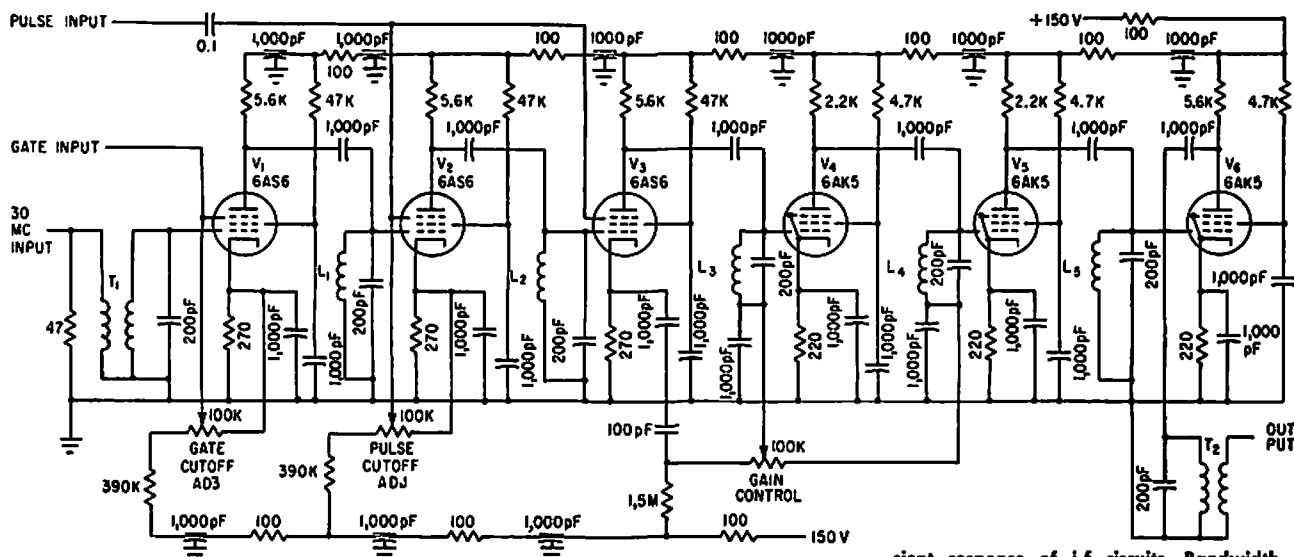
CHAPTER 40

I-F Amplifier Circuits



$Q_1 = 2N2996$
 $C_1, C_2 = 6.0-140 \text{ pf}$
 $C_3, C_4 = .001 \mu \text{f}$
 $C_5 = 2-30 \text{ pf}$
 $C_6 = 10 \text{ pf}$
 $C_n = 3.9 \text{ pf}$
 $R_1 = 2.7 \text{ k}$
 $R_2 = 1.0 \text{ k}$
 $L_1 = 0.06 \mu \text{h}$
 T_1 5 Turns No. 516 air dux tapped 4T from collector

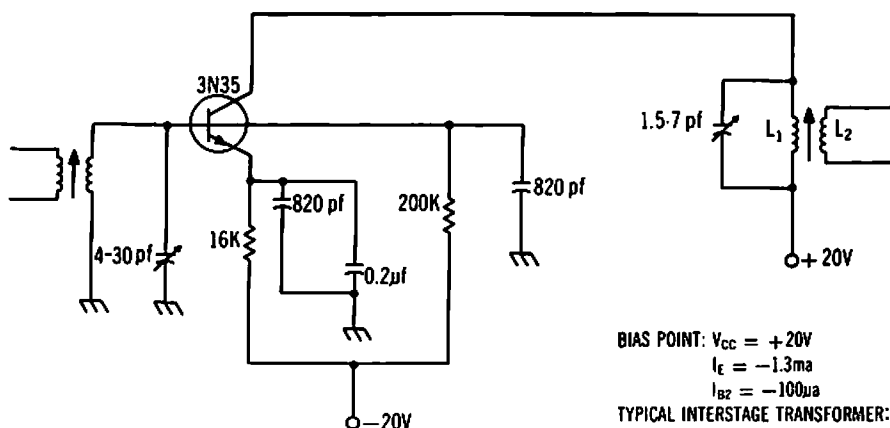
70-MC NEUTRALIZED—Designed to give maximum power gain in single stage while maintaining good stability. Noise figure is less than 3 db with power gain of 27 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 313.



PULSED-GATED 30-MC I-F—Control signals are fed to suppressor grids of early amplifier

stages, to generate groups of i-f pulses for simulating radar scanning or for testing trans-

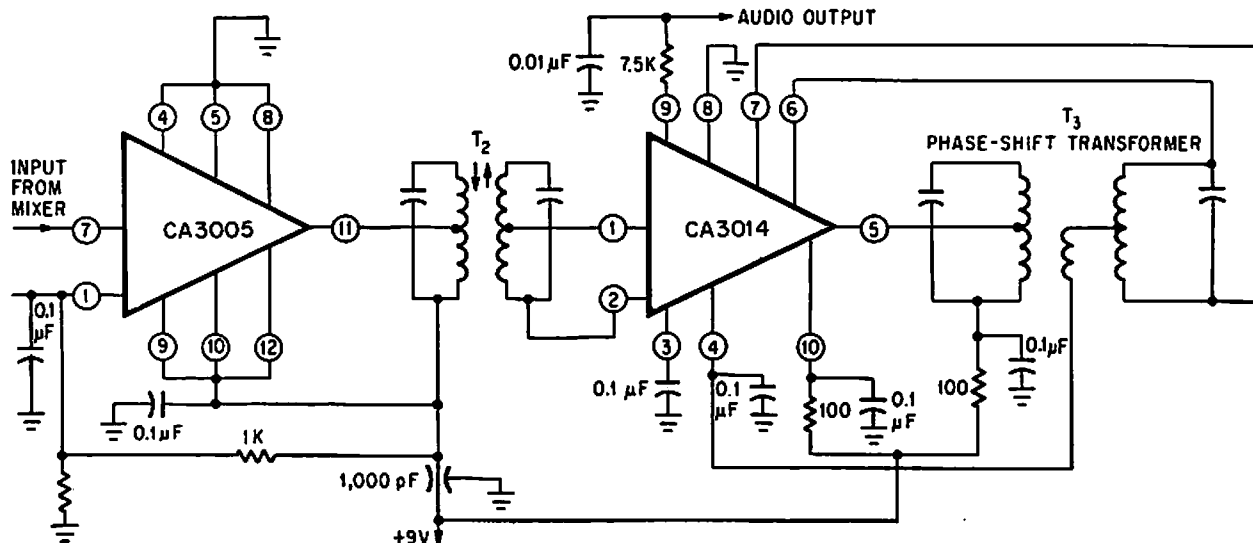
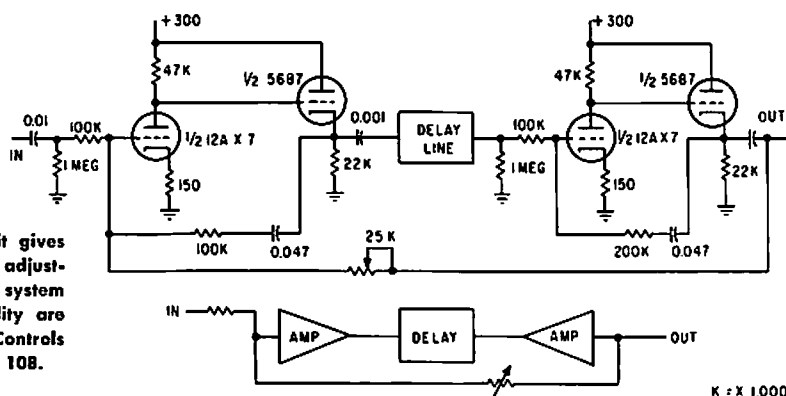
sient response of i-f circuits. Bandwidth is 1.2 Mc.—C. D. Rasmussen, Suppressor Gating for I-F Amplifiers, *Electronics*, 34:34, p 62.



60-MC TETRODE I-F—Use of 3N35 gives excellent agc characteristics. Stage gain is 12 db.—Texas Instruments Inc., "Solid-State

Communications," McGraw-Hill, N.Y., 1966, p 311.

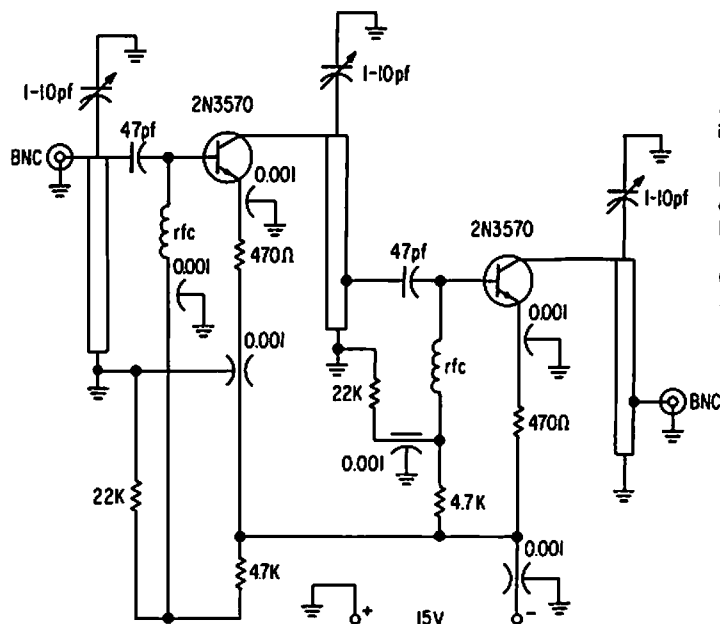
I-F TUNING WITH DELAY LINE—Circuit gives Q of 285 at 500 kc, and is tuned by adjusting delay time. Can be used in any system where high gain, high Q, and stability are needed.—I. F. Barditch, Delay-Line Controls Tuned Amplifier, *Electronics*, 33:31, p 108.



TWO-CHIP LOW-COST STRIP—Detector and 10.7-Mc i-f strip give 95 db gain. Differential amplifier in both RCA chips provides

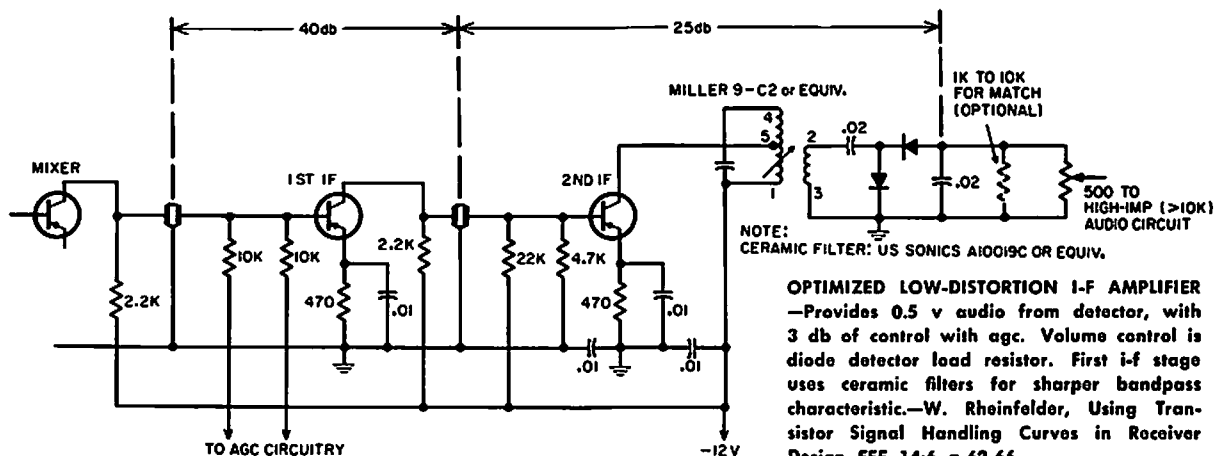
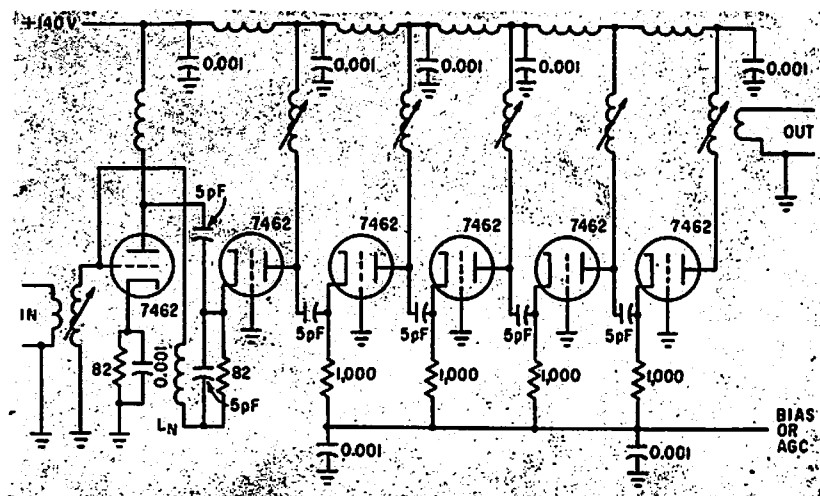
symmetrical limiting over wide input voltage range. Audio output is 220 mv.—R. L. Sanquini, Integrated Circuits Make A Low-

Cost F-M Receiver, *Electronics*, 39:16, p 133-138.

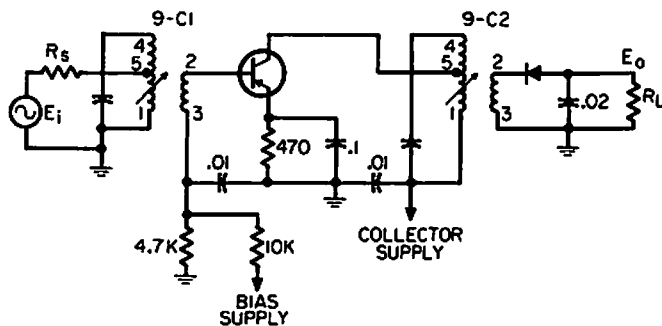


500-MC STAGGER-TUNED I-F—Slight staggering in two stages gives excellent stability, so circuit will not oscillate when either source or load is open. Bandwidth is 90 Mc for 1 db down and 110 Mc for 3 db down, with mid-band gain of 21 db. Draws only 7 ma at 15 v.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 315.

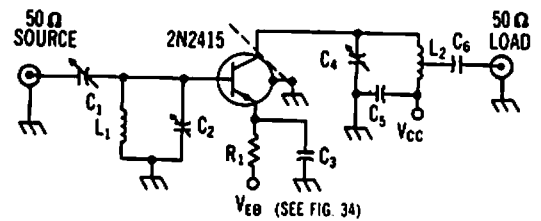
GROUND-GRID STRIP—Six-stage low-noise 60-Mc i-f strip amplifier, using microminiature ceramic triodes, gives 75-db overall gain, 1.7-db noise figure, and 6.5-Mc bandwidth. First stage is cascode and other five are grounded-grid triodes.—J. W. Rush, Designing Grounded-Grid Amplifiers with Controlled Gain, *Electronics*, 33:52, p 50-53.



OPTIMIZED LOW-DISTORTION I-F AMPLIFIER—Provides 0.5 v audio from detector, with 3 db of control with agc. Volume control is diode detector load resistor. First i-f stage uses ceramic filters for sharper bandpass characteristic.—W. Rheinfelder, Using Transistor Signal Handling Curves in Receiver Design, *EEE*, 14:6, p 62-66.



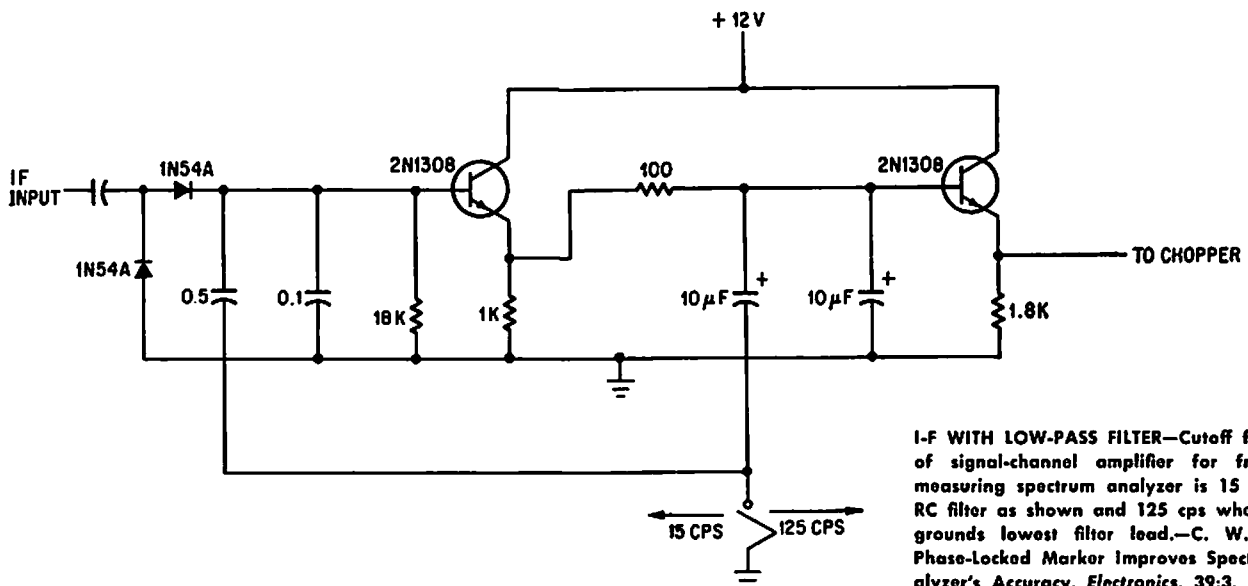
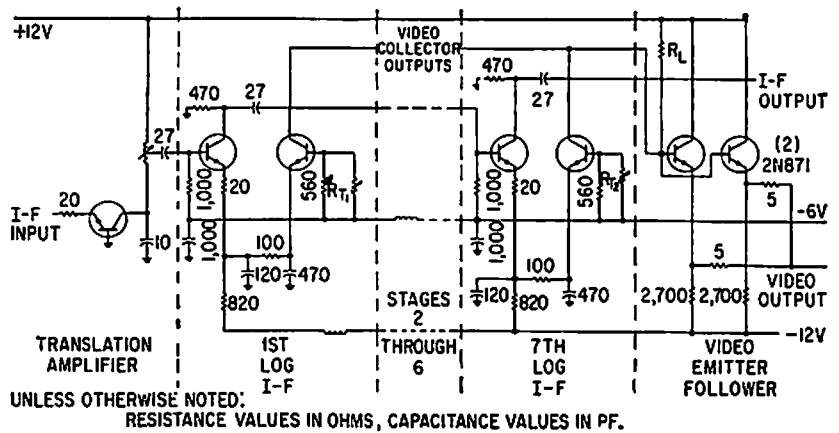
SINGLE-TUNED FINAL I-F—Circuit is example of optimum design based on use of transistor signal-handling curves. Transformers are single-tuned.—W. Rheinfelder, Using Transistor Signal Handling Curves in Receiver Design, *EEE*, 14:6, p 62-66.



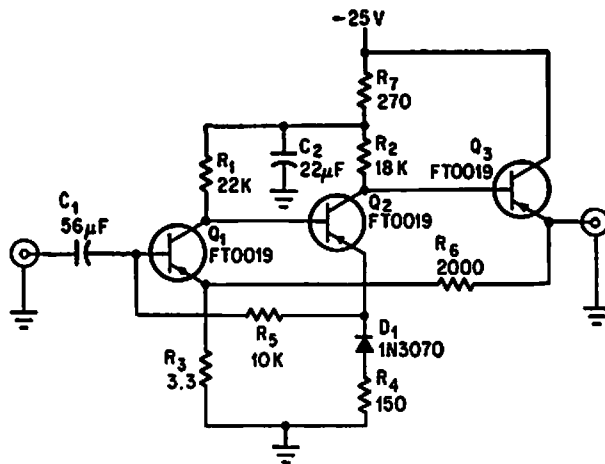
$C_1 = 5.3 - 102$ pF L_1 3T $\approx$ 14 WIRE $\frac{1}{8}$ " DIA: $\approx$ 0.25 uH
 $C_2 = 3.7 - 52$ pF L_2 4T $\approx$ 610 AIR DUX (OR EQUIV.): $\approx$ 0.35 uH
 $C_3 = 0.01$ μ F TAPPED APPROX. 1 TURN UP FROM BOTTOM
 $C_4 = 3.7 - 52$ pF C_5 IS ADJUSTED TO GIVE DESIRED VALUE OF R_L
 $C_6 = 0.01$ μ F *4th LEAD GROUNDED. $R_1 = 1K \frac{1}{2}$ W
 $C_7 = 0.01$ μ F BIAS POINT. $-6V$. I_E (SEE FIG. 34)

70-MC LOW-NOISE I-F—Noise figure ranges from 2 to 4 db depending on generator resistance and emitter current. Power gain is 24 db.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 312.

THIN-FILM LOG I-F AMPLIFIER—Translation amplifier limits bandwidth at input, while video emitter-follower matches 10-v output to load. All seven log i-f stages use thin-film circuits.—R. Leslie and T. Townsend, Inductors No Problem: New Thin-Film Amplifier, *Electronics*, 36:23, p 46-49.

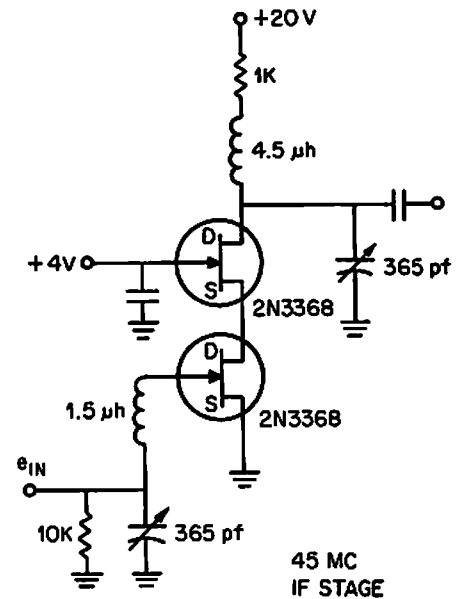


I-F WITH LOW-PASS FILTER—Cutoff frequency of signal-channel amplifier for frequency-measuring spectrum analyzer is 15 cps with RC filter as shown and 125 cps when switch grounds lowest filter lead.—C. W. Wilson, Phase-Locked Marker Improves Spectrum Analyzer's Accuracy, *Electronics*, 39:3, p 88-92.

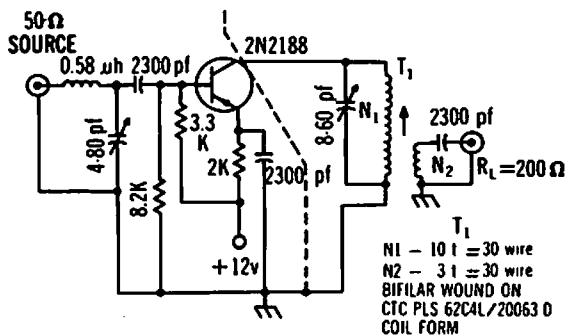


LOW-OUTPUT-IMPEDANCE I-F—Can provide impedances as low as 2 ohms and low noise figure, to take advantage of superior noise performance of backward diodes as mixers and detectors while overcoming their very low impedance at intermediate frequen-

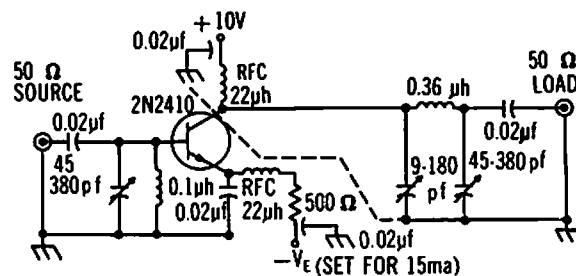
cies in range from 1 kc to 100 kc. Used in continuous-wave doppler radar systems.—R. O. Wright, New Twist for Backward Diode: Help from Low-Noise Amplifier, *Electronics*, 39:14, p 74-77.



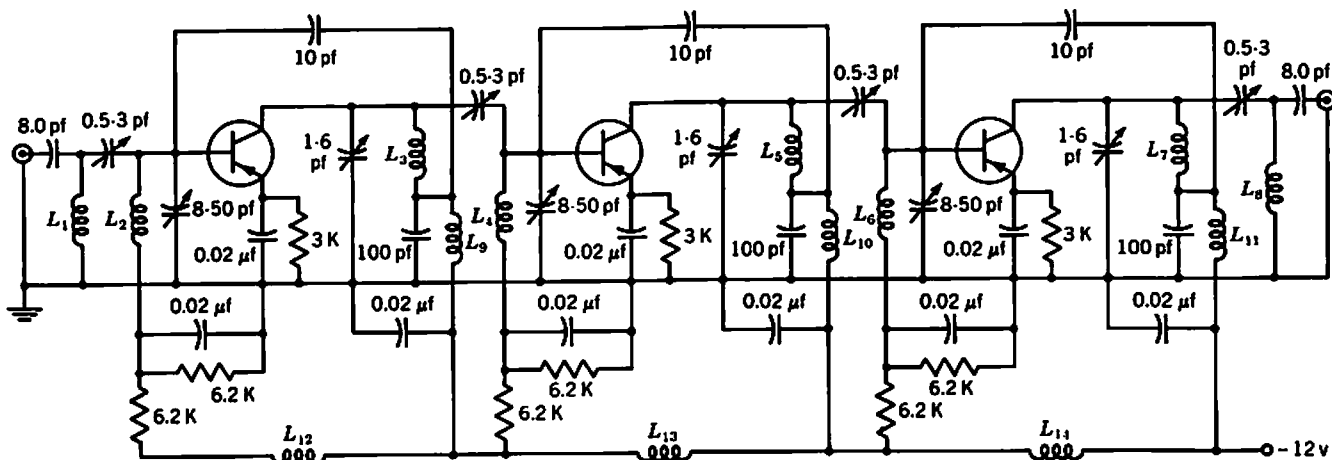
45-MC CASCODE FET—Operates without neutralization, giving 20 db power gain and 6-Mc bandwidth.—Cascode with FET's (Siliconix ad), *Electronics*, 39:2, p 109.



30-MC I-F USING 2N2188—Circuit includes L-section to give generator resistance of 350 ohms from 50-ohm source. Power gain is 13 db, noise figure 4 db, and bandwidth 5 Mc.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 309.

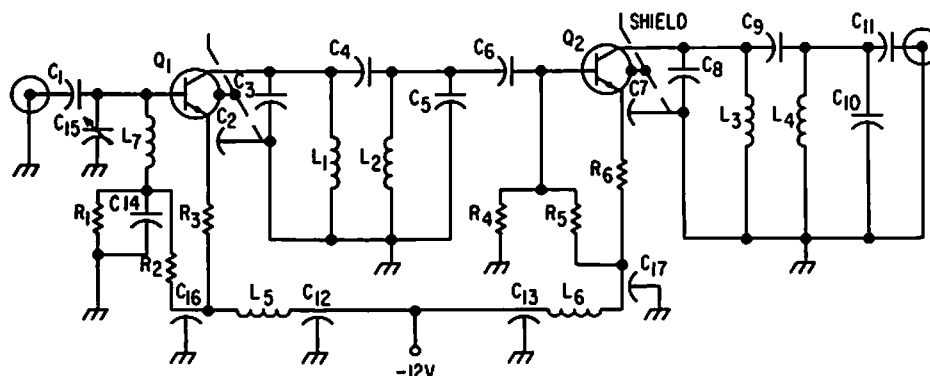
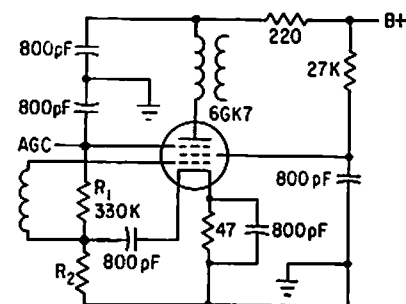
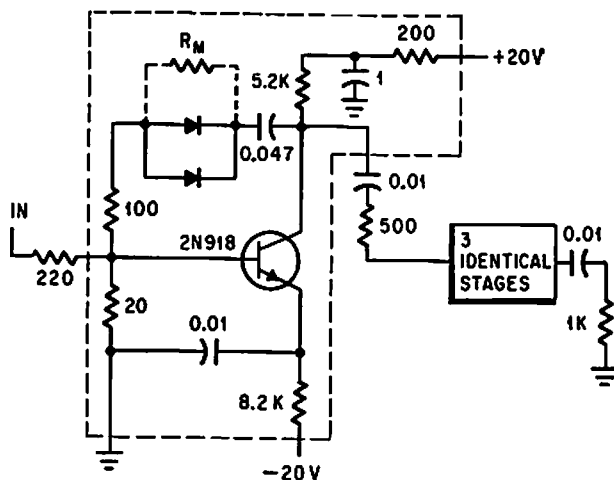
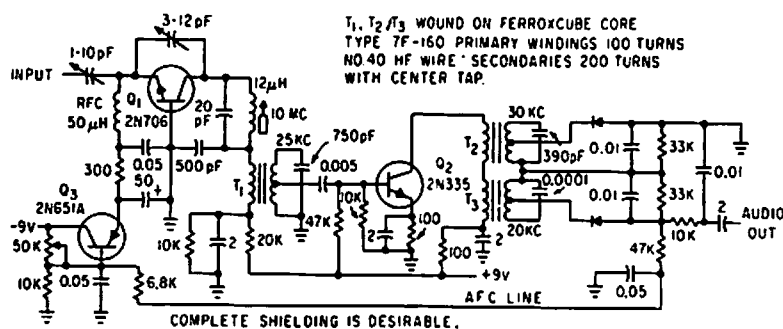


30-MC I-F USING 2N2410—Single stage gives power gain of 16 db, permitting use as final stage of i-f strip.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 308.



L_1, L_8 -3.5 μ h 23 turns #36 heavy Formvar closewound on Cambion LS-9 form
 L_2, L_4, L_6 -0.53 μ h 9 turns #36 heavy Formvar space $\frac{1}{62}$ " apart on Cambion LS-9 form
 L_3, L_5, L_7 -5.3 μ h 29 turns #36 heavy Formvar closewound on Cambion LS-9 form
 L_9, L_{14} -15 μ h rfc

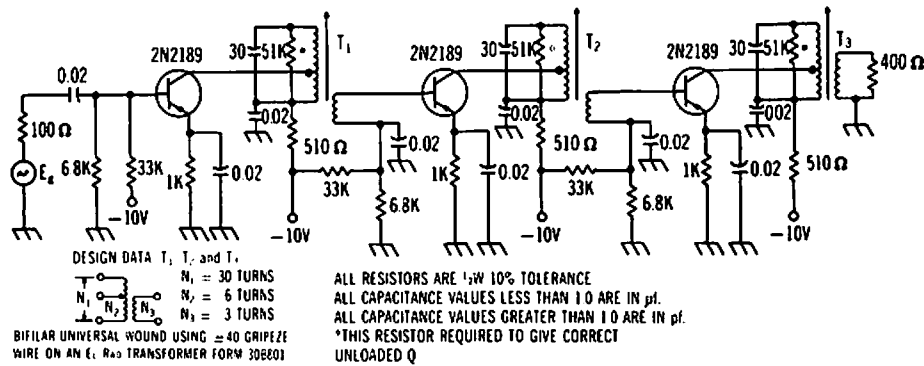
30-MC I-F STRIP—Gain is 70 db for 3-Mc bandwidth, using 2N1405 transistors. Design equations are given.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 276.



Q1,2=2N2996
 L1,L2,L3,L4=2 1/2 TURNS # 30 WIRE ON CAMBION
 LS9 COIL FORM (ADJUSTED TO RESONATE WITH 31pf AT 105mc)
 L5,L6=3.3 μH
 L7=0.07 μH
 R1,R2,R4,R5=3K
 R3,R6=1.5K

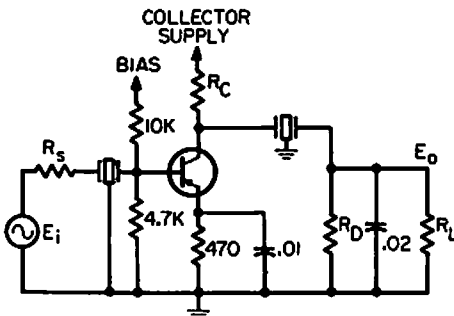
C1=250pf
 C2,C7,C12,C13,C14=1000pf
 C3,C5,C8,C10=9-35 pf
 C4,C9=1.3-5.4 pf
 C6=1.5-20pf
 C11=10pf
 C15=9-180pf
 C16,C17=1000pf

Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 314.

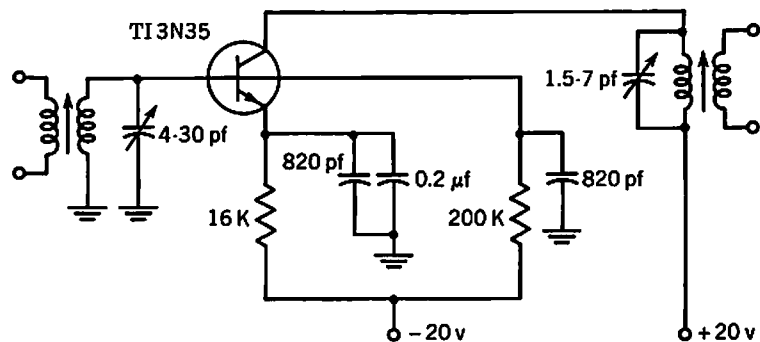


5.5-MC I-F—Three germanium transistors give power gain of 62 db with noise figure of only 4 db for bandwidth of 0.18 Mc.—Texas

Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 307.

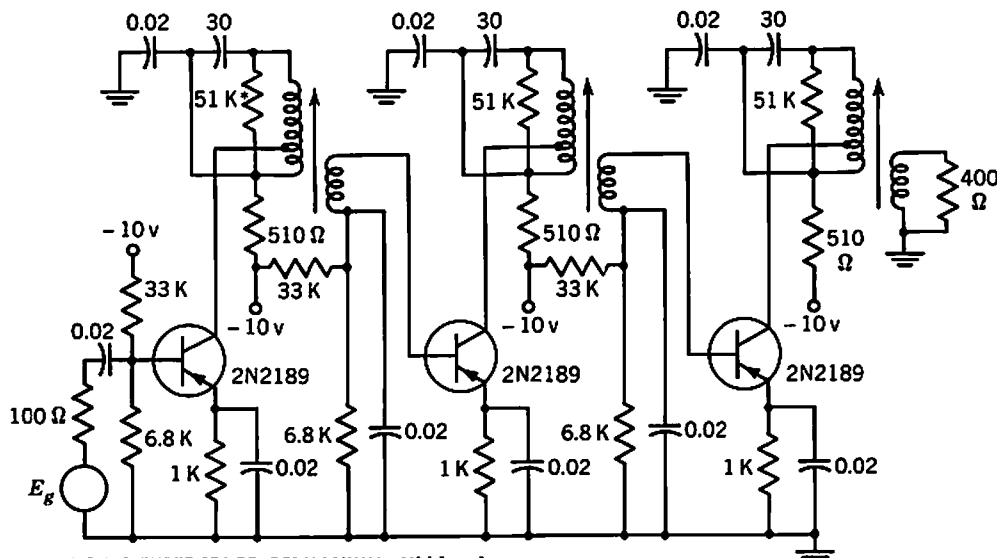


CERAMIC-FILTER I-F STAGE—Article covers design procedure using transistor signal handling curves. Collector resistance is used to minimize drift.—W. Rheinfelder, Using Transistor Signal Handling Curves in Receiver Design, *EEE*, 14:6, p 62-66.



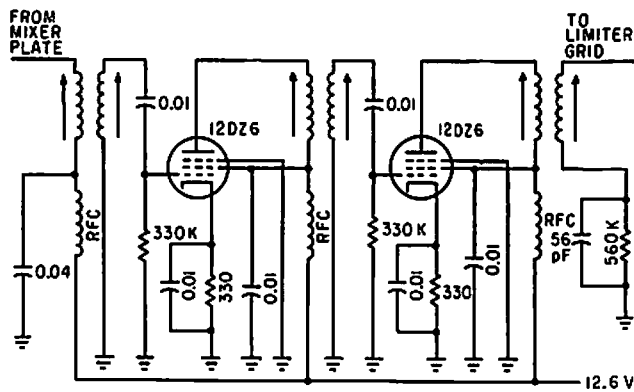
60-MC SILICON TETRODE STAGE—Used in eight-stage strip having identical stages except for input and output, whose transformers are designed for driving and load resistances. Transitionally coupled double-

tuned interstages are used, with 5:1 mismatch providing stability and ease of alignment along with stage gain of 12.5 db.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 292.

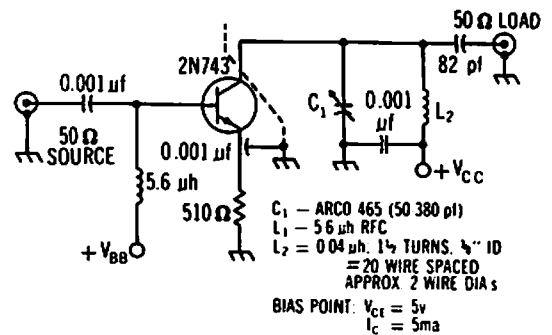


5.5-MC THREE-STAGE GERMANIUM—Mid-band gain is 60 db and 3-db bandwidth is 200 kc. Interstage networks consist of single-tuned transformers with collectors tapped down on

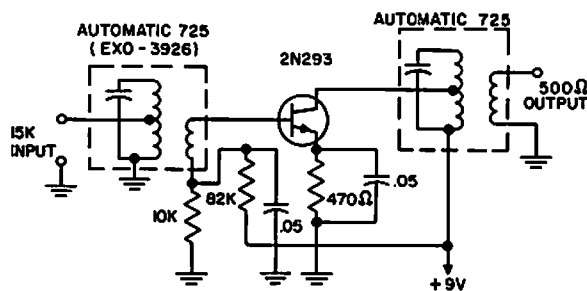
primary.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 294.



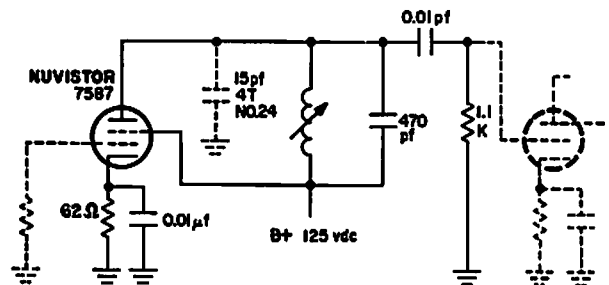
MOBILE I-F—Two 455-kc i-f stages provide gain of 20 per stage and average bandwidth of 12 kc.—C. Gonzalez and R. J. Nelson, *Design of Mobile Receivers with Low-Plate-Potential Tubes*, *Electronics*, 33:34, p 62-65.



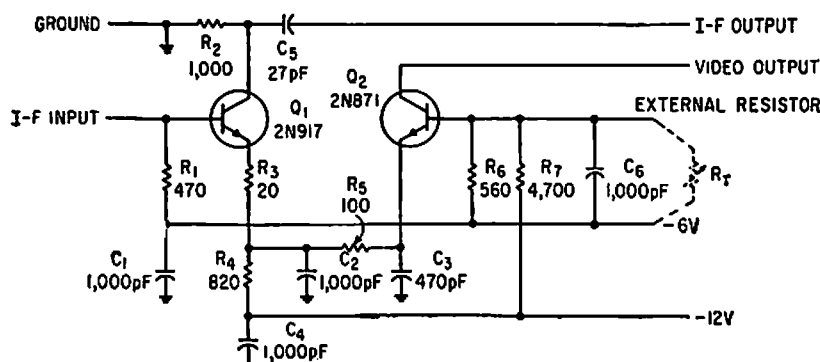
60-Mc I-F WITH 2N743—Silicon epitaxial transistor has unconditional stability at this frequency, simplifying alignment. Gains up to 16 db per stage are possible with conjugate match at output. Noise figure is good.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 311.



SINGLE-TRANSISTOR I-F AMPLIFIER—Designed for broadcast-band transistor radio. Neutralization is unnecessary with 2N293 rate-grown npn transistor used.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 285.



LOW-Q 22-Mc I-F DESIGN—Article gives detailed design procedure, with example worked out for 480-kc bandwidth and gain of 92 db. For high-Q stage, 1.1K load resistor is changed to 12K.—J. F. Klari, *A Systematic Approach For Designing IF Amplifiers*, *EEE*, 12:3, p 40-44.

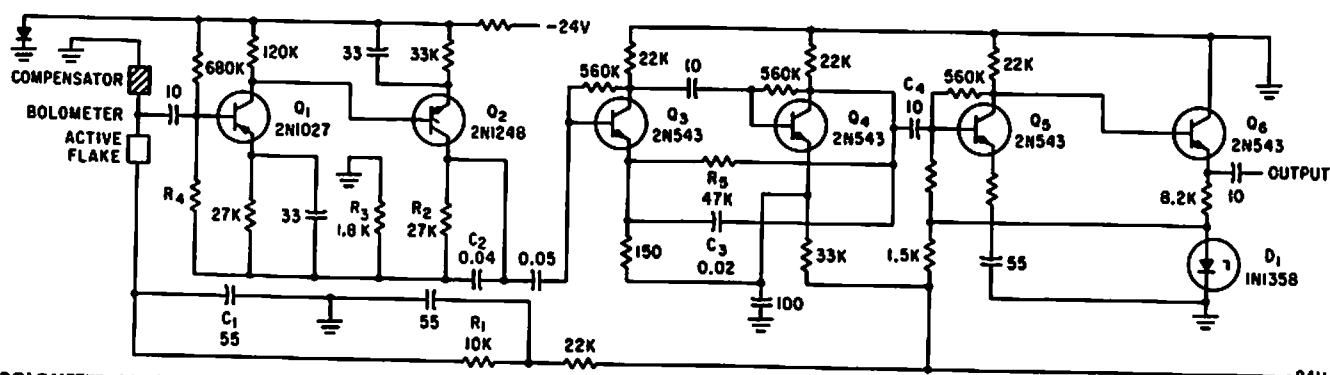


LOGARITHMIC THIN-FILM I-F AMPLIFIER—Untuned stages eliminate need for inductors in 60-Mc log i-f module while giving gain of 10

db.—R. Leslie and T. Townsend, *Inductors No Problem: New Thin-Film Amplifier*, *Electronics*, 36:23, p 46-49.

CHAPTER 41

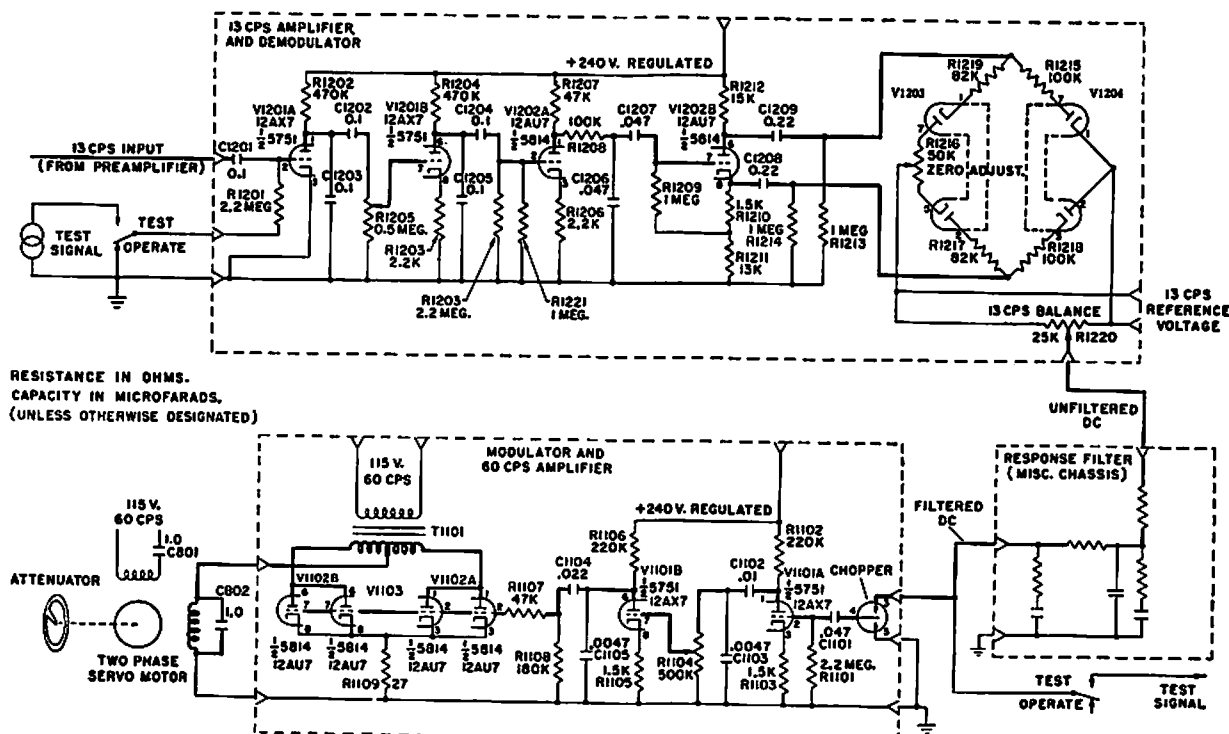
Infrared Circuits



BOLOMETER AMPLIFIER AND DETECTOR—Used in infrared horizon sensor of meteorological

satellite. Zener diode D1 provides low-impedance constant-voltage source of bias for

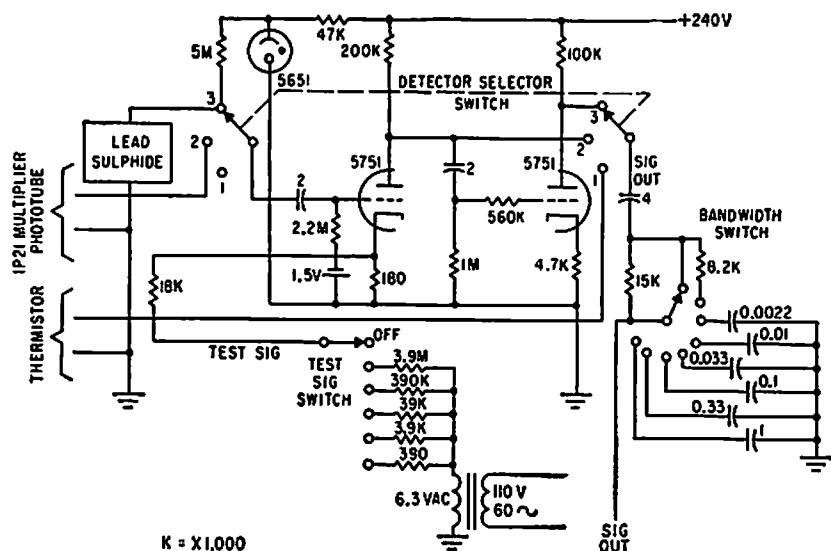
detector.—F. Schwarz and W. Chou, *Tiros Weather Satellites, Electronics*, 34:39, p 136-137.



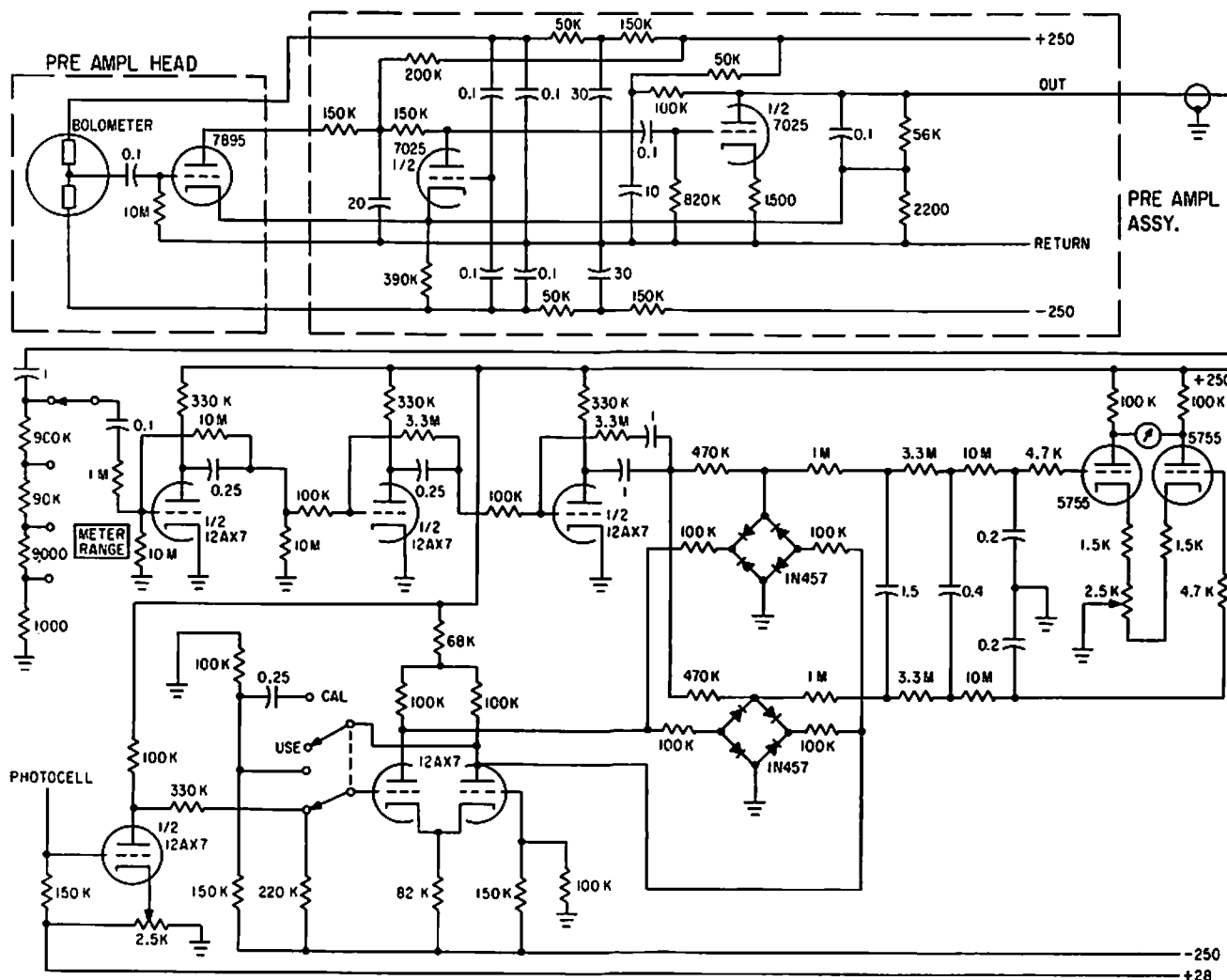
INFRARED ANALYZER—Circuit shows phase-sensitive demodulator, 13-cps amplifier, and modulator of servo system used in Perkin-Elmer Tri-Non triple-beam analyzer for meas-

uring amount of infrared energy absorbed by component of interest in flowing sample of industrial process stream. Servo motor turns variable null-path attenuator to cancel

radiation unbalance and restore null.—G. C. Carroll, *Industrial Instrument Servicing Handbook*, McGraw-Hill, N.Y., 1960, p 8-51.



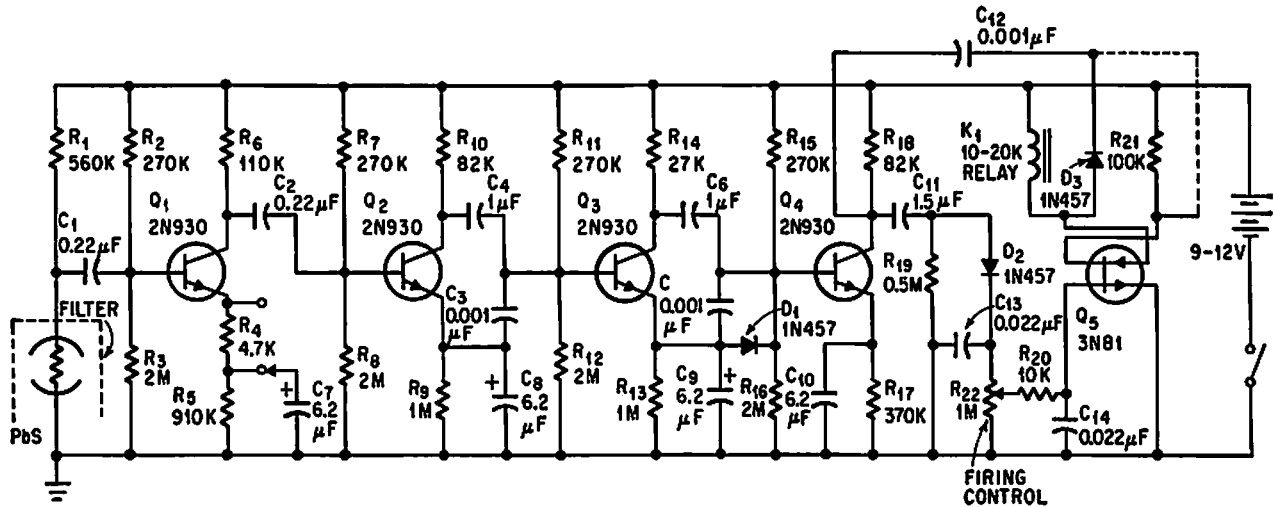
MONOCHROMATOR PREAMP—Used in scanning missile plumes to identify missile. Input may be either multiplier phototube or lead sulfide detector covering range from 0.35 micron in visible spectrum to 2.9 microns in infrared. Bias for lead sulfide detector is provided by 5651 regulator across plate supply. Test voltage is fed to cathode of first stage for gain calibration.—J. N. Day, Jr., *Spectrometric Analysis of Missile Flights*, *Electronics*, 33:21, p 86-88.



INFRARED POWER MONITOR—Output of infrared signal generator is monitored by two-thermistor bolometer, low-noise nuvis-

tor preamp, and synchronous detector driving multirange motor, all operating from two highly regulated power supplies.—A.

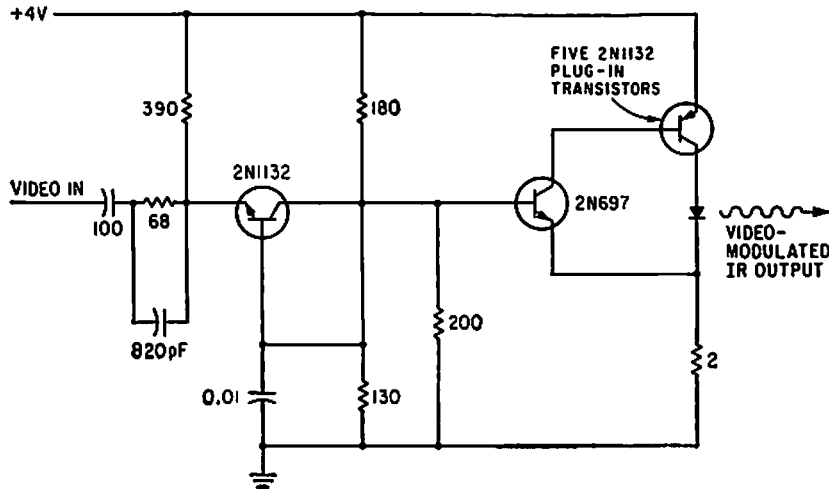
Glaser, Signal Generator for Infrared Region, *Electronics*, 35:8, p 40-43.



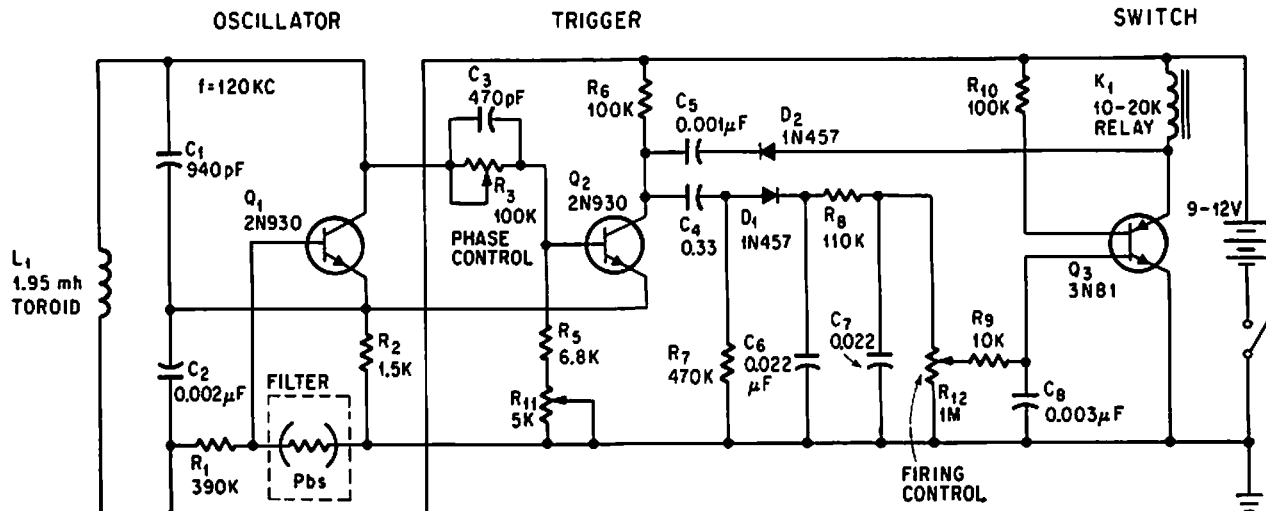
INFRARED HOT-ENGINE DETECTOR—Output relay resets parking meter to zero when

lead sulfide detector senses heat of engine when parked car is started.—W. E. Osborne,

Farewell To Free Time On City Parking Meters, *Electronics*, 37:32, p 72-74.



TV ON INFRARED BEAM—Forward-biased gallium arsenide diode converts video input signal to video-modulated infrared radiation with up to 85% efficiency.—R. H. Rediker et al., Gallium-Arsenide Diode Sends Television by Infrared Beam, *Electronics*, 35:40, p 44-45.



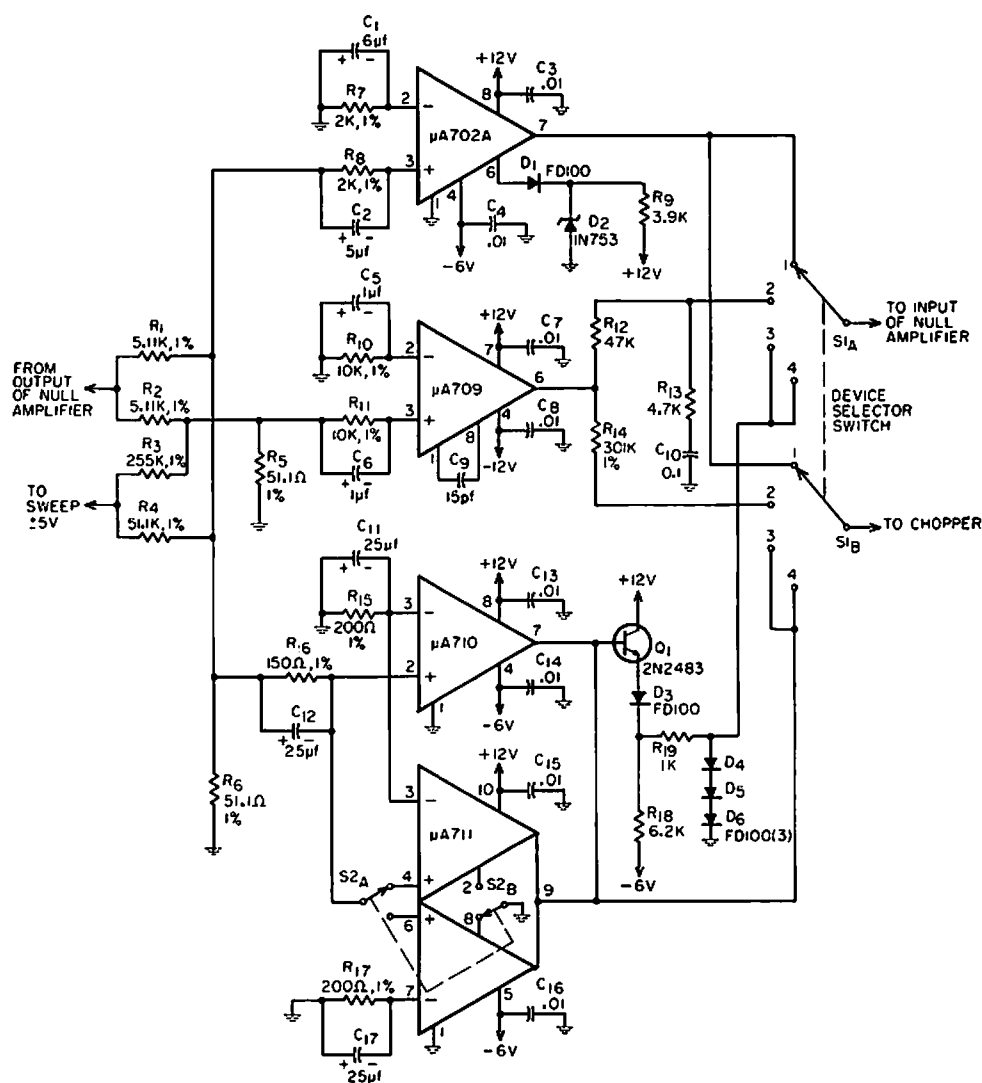
INFRARED HOT-ENGINE DETECTOR—Hot-engine alarm using only two transistors and an scr resets parking meter to zero when lead

sulfide infrared detector senses engine heat as parked car starts. Circuit combines Colpitts oscillator with Schmitt trigger.—W. E.

Osborne, Farewell To Free Time On City Parking Meters, *Electronics*, 37:32, p 72-74.

CHAPTER 42

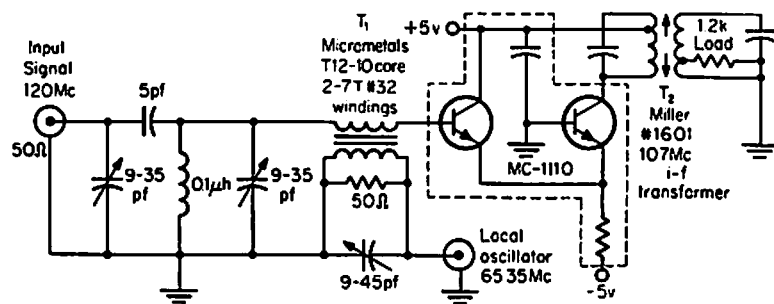
Integrated Circuits



LINEAR IC TESTER—Basic lab tester circuit displays transfer function, offset voltage, gain, linearity, and output voltage swing on single scope trace. High-gain null operational amplifier (such as Fairchild 709 IC) is used in feedback loop around linear inte-

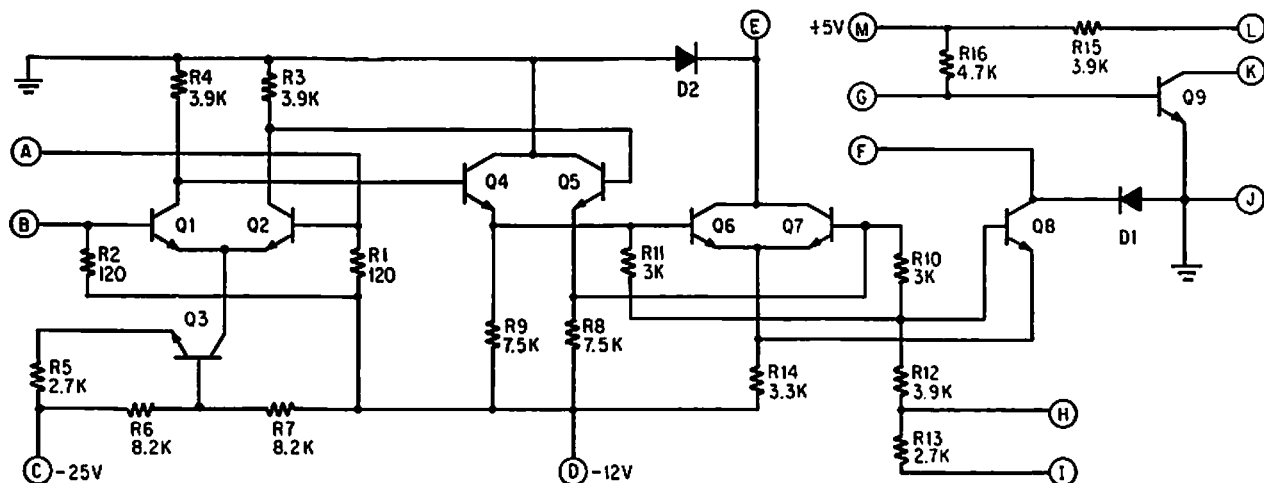
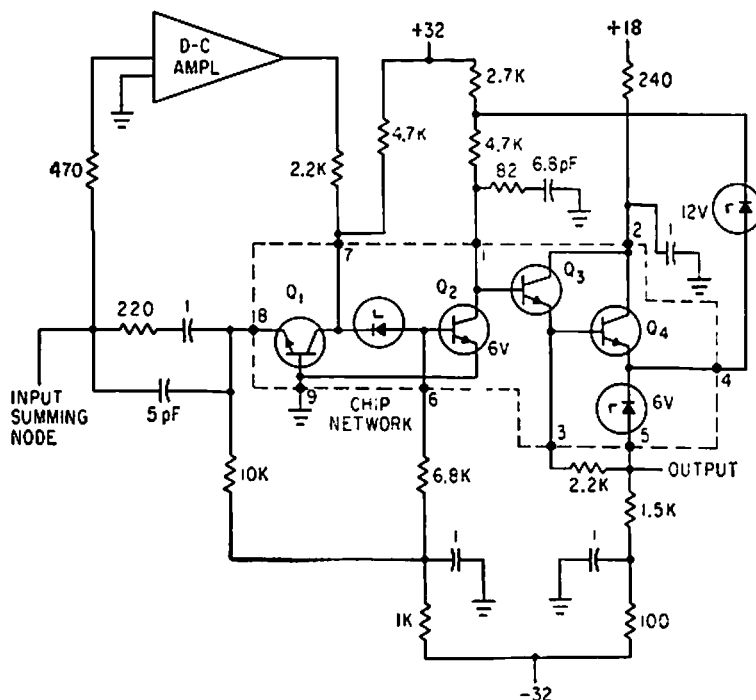
grated-circuit amplifier under test, to hold output of amplifier under test at zero by adjusting its d-c input voltage to equal the offset. Chopper on vertical scope inputs allows simultaneous display of offset voltage and transfer function, by switching in syn-

chronism with horizontal sweep. Separate TO-5 socket is provided for each type of integrated circuit to be tested.—J. N. Giles, How to Measure Linear-IC Performance, *EEE*, 14:8, p 62-68 and 161.



HARMONIC MIXER—Two-transistor integrated circuit is used in nonlinear mode for converting 120 Mc to 10.7 Mc with conversion of 29.4 db, noise figure of 11 db, and sensitivity of -105 dbm. Bandwidth, including i-f stages that follow mixer, is about 500 kc.—J. E. Thompson, "An Integrated Harmonic Mixer," Motorola Application Note AN-154, December, 1965.

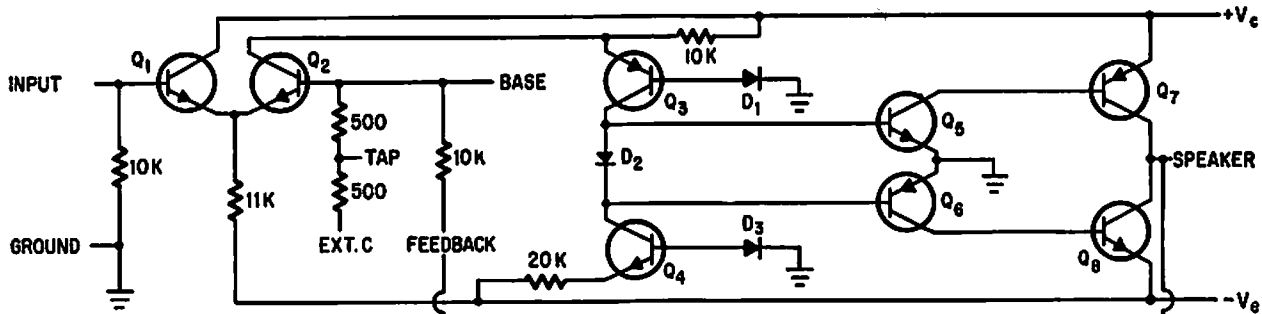
WIDEBAND ANALOG AMPLIFIER—D-C input summing mode signals are amplified in low-drift d-c amplifier and reinserted into amplifier signal path at input to common-emitter stage. Response of d-c amplifier in parallel with common-base stage is complementary to high-frequency amplifier Q2-Q3-Q4, maintaining unit slope down to 1 kc, where gain is 100 db. Loop delay is less than 0.1 nsec.—F. D. Waldhauer, Latest Approach to Integrated Amplifier Design, *Electronics*, 36:22, p 24-27.



SENSE AMPLIFIER—General-purpose amplifier can be used with most coincident-current memories without redesign. Has adjustable threshold, good noise rejection, and drives any standard logic gate with positive or

negative output. Bandwidth is 10 Mc. Drift is only 22 microvolts per $^{\circ}\text{C}$. Circuit is differential amplifier whose inputs are connected to opposite ends of sense winding. Input accepts both polarities, but output is always

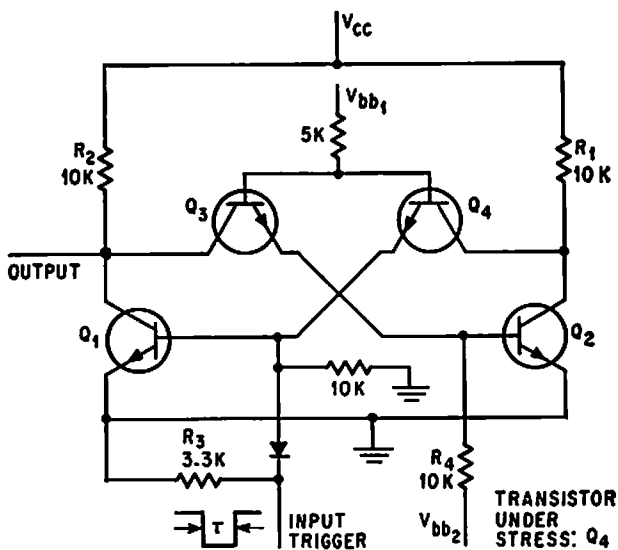
same polarity. For negative output pulse, connect F to G; for positive output, connect E to G.—B. Johnson, Sense Amplifier Fits Any Memory, *Electronics*, 39:18, p 89-94.



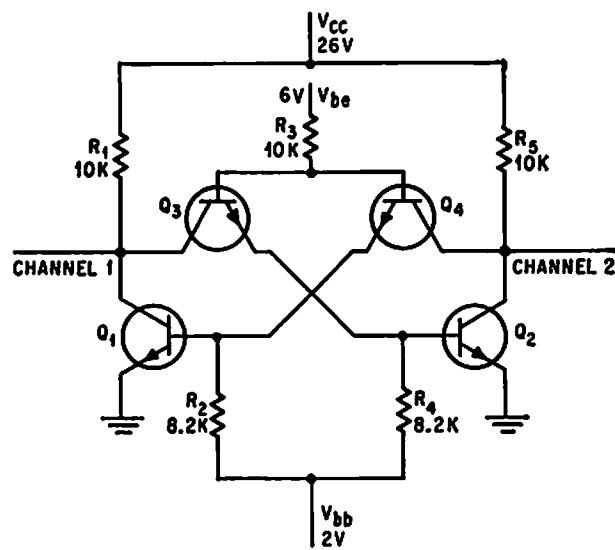
1-WATT AUDIO AMP—Negative d-c and a-c feedback is applied to one side of differential input stage and signal to other side. With

balanced power supplies, d-c output is at ground, permitting direct drive of speaker without large d-c decoupling capacitor in

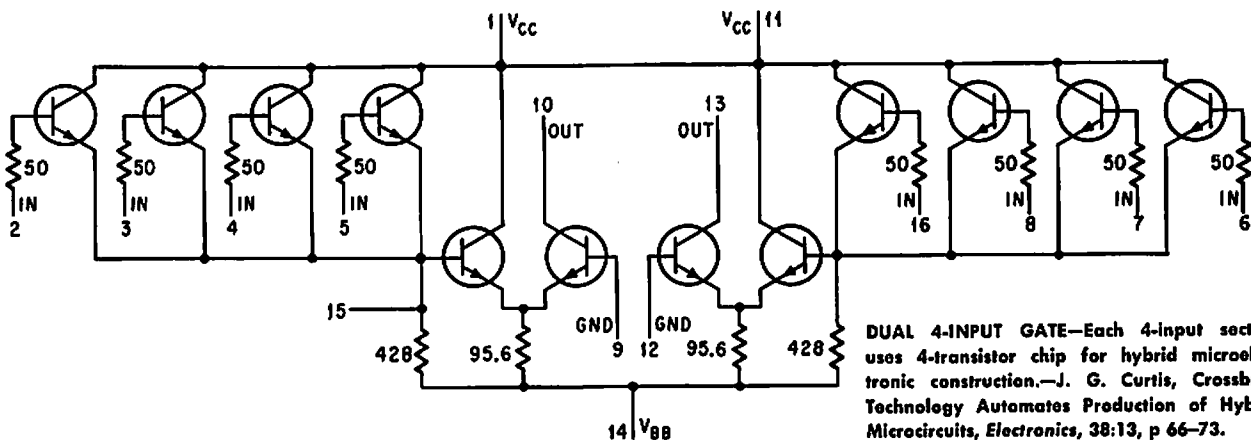
MC1524 integrated circuit.—R. Hirschfeld, IC's Improve Differential Amplifiers—and Vice Versa, *Electronics*, 38:16, p 75-79.



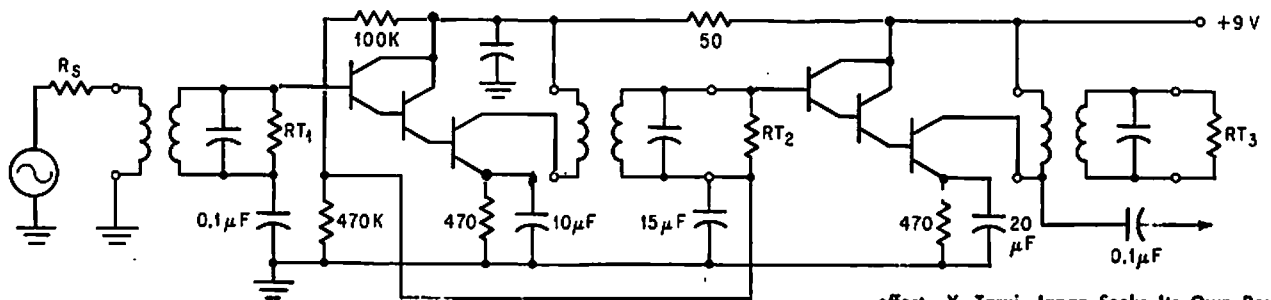
MONOSTABLE MVBR—Pulse width of stress-sensitive RC103 integrated circuit is reduced from 1.5 to 1.0 microsec when 7 grams of force is applied to one transistor.—R. C. Wonson, *Stress-Sensitive Integrated Circuits*, *Electronics*, 38:14, p 81-84.



ASTABLE MVBR—Stress-sensitive RC103 integrated circuit shifts free-running frequency from 500 kc to 1 Mc when 7 grams of force is applied to one transistor.—R. C. Wonson, *Stress-Sensitive Integrated Circuits*, *Electronics*, 38:14, p 81-84.



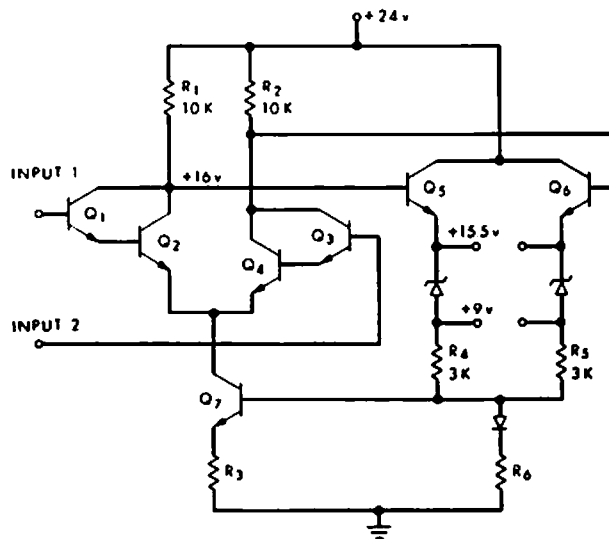
DUAL 4-INPUT GATE—Each 4-input section uses 4-transistor chip for hybrid microelectronic construction.—J. G. Curtis, *Crossbred Technology Automates Production of Hybrid Microcircuits*, *Electronics*, 38:13, p 66-73.



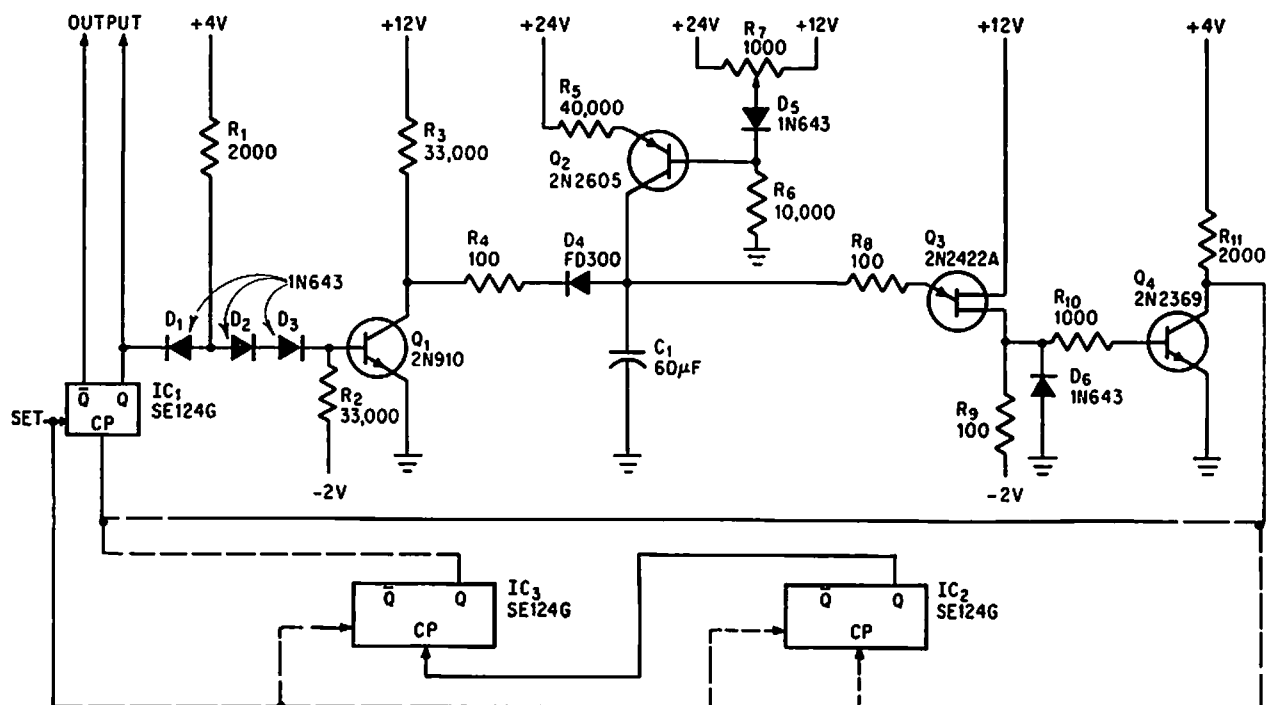
IMPROVED DARLINGTON—Separate collector circuit for third transistor of Darlington con-

figuration prevents reflection of high capacitance back to input, thereby dissipating Miller

effect.—Y. Tarui, Japan Seeks Its Own Route to Improved IC Techniques, *Electronics*, 38:25, p 90-98.



DARLINGTON-INPUT OPERATIONAL AMPLIFIER—Single-stage version uses emitter-follower output to lower impedance and shift d-c level of output. Q7 and R3 form constant-current source. Zeners, formed as base-emitter junctions of transistors, shift d-c level of outputs negative by 6 v to make them compatible with input voltages and permit cascading of monolithic integrated circuits directly.—C. L. Hoizman and D. G. Paterson, *Circuit Analysis: A Monolithic Integrated Operational Amplifier*, *EEE*, 13:5, p 80-84.

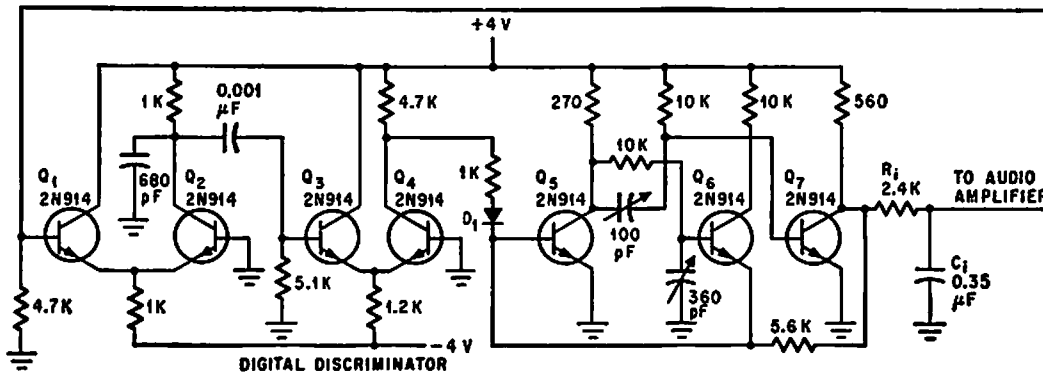
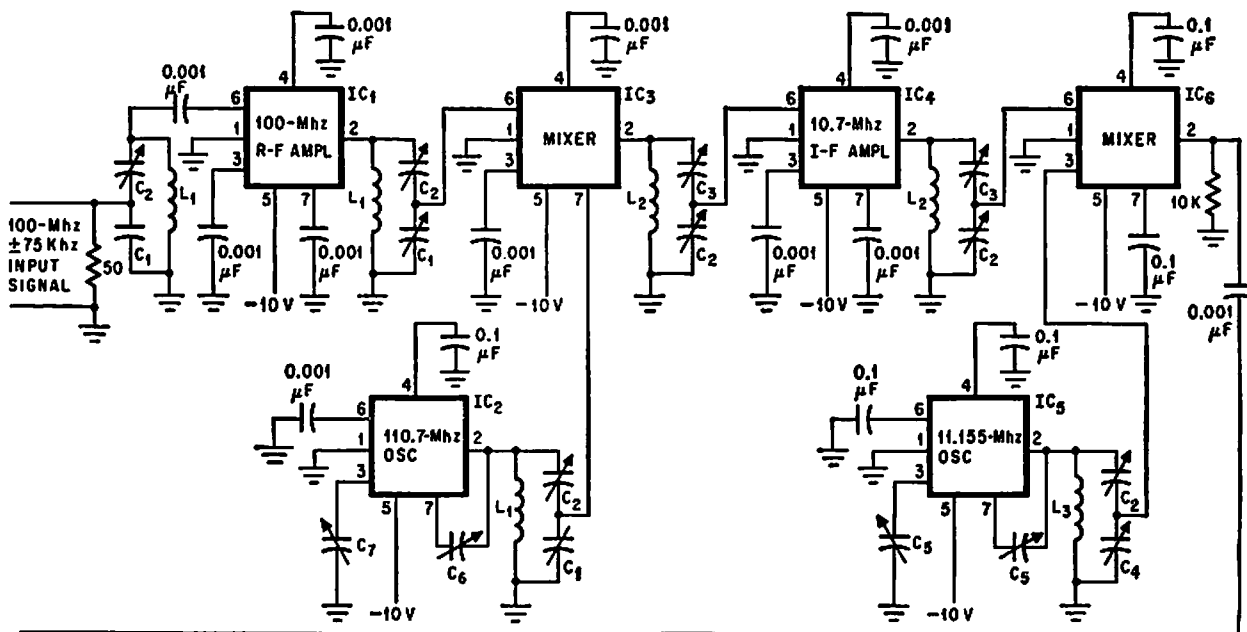
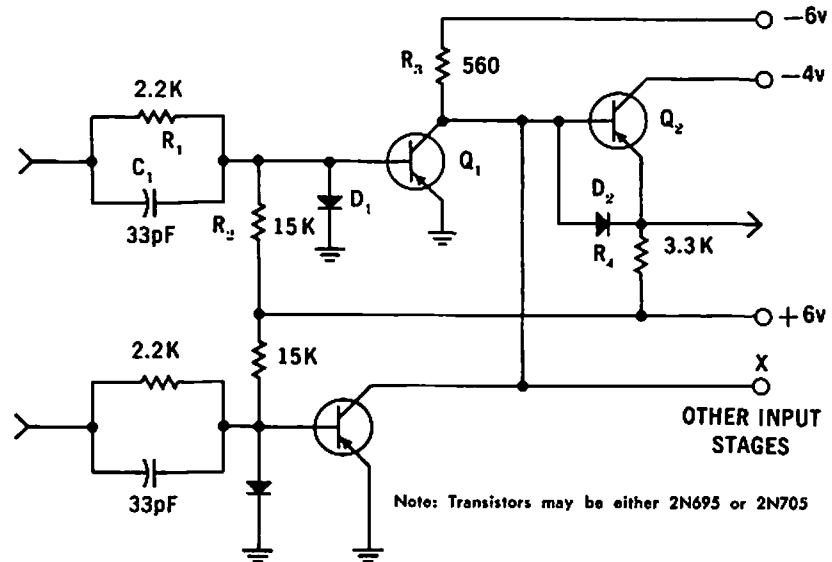


2-SEC-TO-10-MIN TIMER—Number of monolithic diode-transistor logic circuits determines range of time delay provided by

potentiometer R7. For shorter delays, one or both of integrated circuits connected with broken lines may be eliminated.—A. A. Lam-

pell, Off-the-Shelf Integrated Circuits for Versatile and Accurate Timer, *Electronics*, 38:25, p 70-73.

BASIC RCTL LOGIC—Transistors used provide ultrahigh switching speed for computers and have low stored charge along with gain-bandwidth product above 300 Mc. Circuit can readily be adapted as flip-flop, nor circuit, and Schmitt trigger.—W. D. Roehr, "For Computers . . . Basic RCTL Circuits," Motorola Application Note AN-129, Nov. 1965.

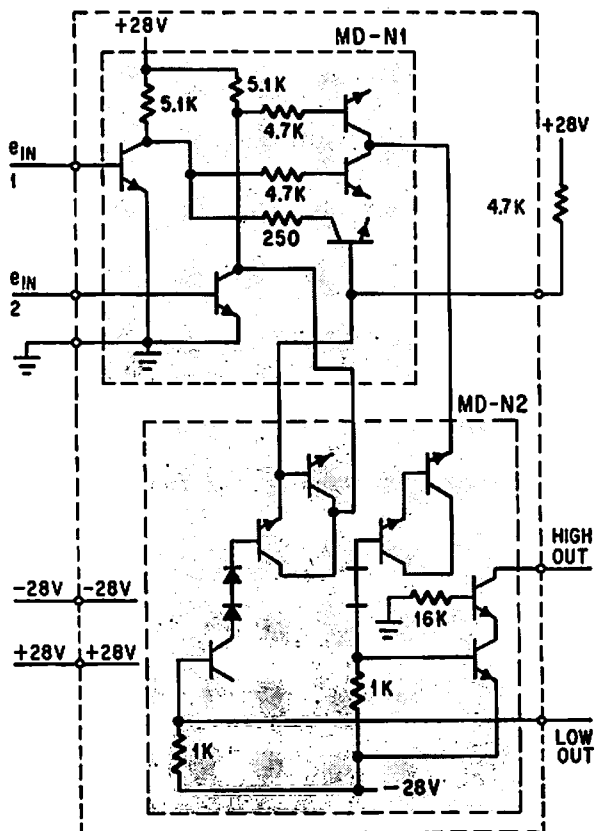


INDUCTORS (μH)	
L ₁	0.044
L ₂	3.9
L ₃	5
CAPACITORS (pF)	
C ₁	7-100
C ₂	14-150
C ₃	55-300
C ₄	37-250
C ₅	90-400
C ₆	1.5-20
C ₇	1-12

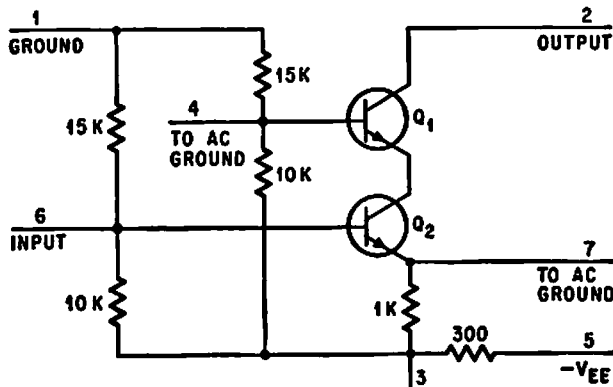
F-M RECEIVER—Multipurpose integrated-circuit chip consisting of six resistors and two identical transistors in cascode amplifier configuration serves three different functions in

single-frequency 100-Mc f-m receiver. Although discrete components are used in digital discriminator, circuit requirements and component values here are compatible

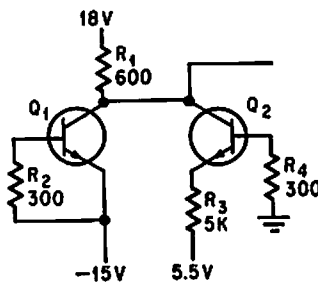
with monolithic techniques.—R. L. Sanquini, Multipurpose Chips Cut Costs of F-m Receiver, *Electronics*, 39:10, p 80-82.



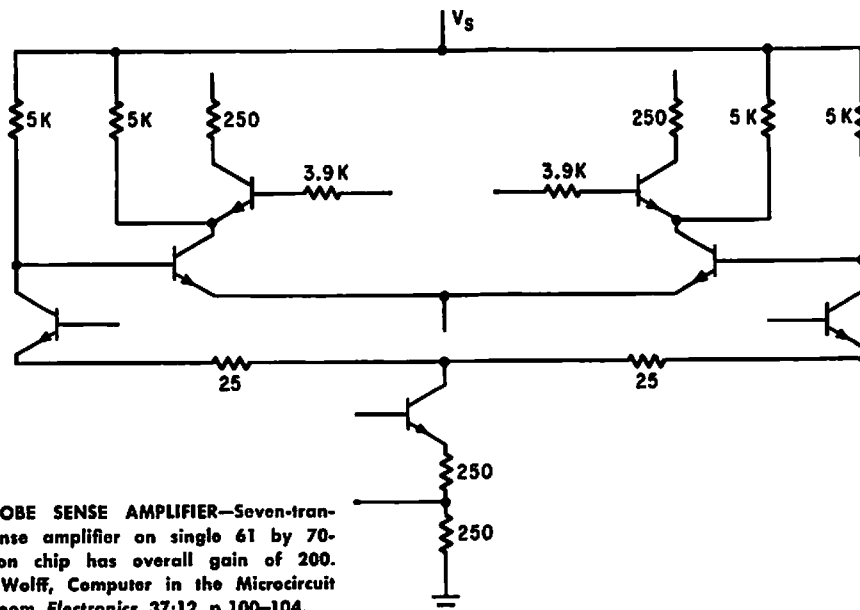
LOGIC AND BUFFER AMPLIFIER—Designed to be driven by dual Schmitt trigger.—D. D. Robinson, *Linear Microcircuits Scarce? Now You Can Breadboard Your Own*, *Electronics*, 37:27, p 58-64.



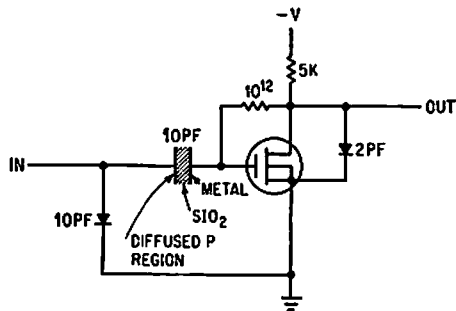
MULTIPURPOSE CHIP—Monolithic chip consisting of six resistors and two identical transistors serves three different functions in f-m receiver. Two transistors permit cascode amplifier configuration, giving low noise figure and good power gain at high frequencies.—R. L. Sanquini, *Multipurpose Chips Cut Costs of F-m Receiver*, *Electronics*, 39:10, p 80-82.



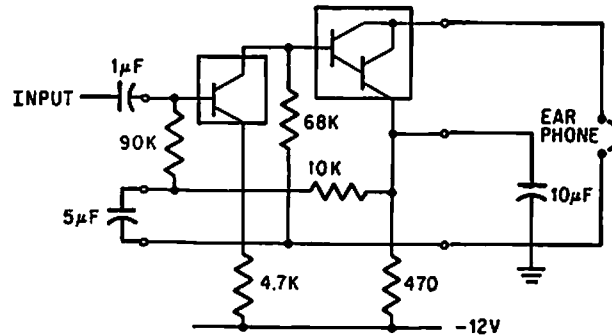
PRESSURE-CONTROLLED—Frequency of avalanche oscillator in integrated circuit using RC103 transistors varies linearly from 100 to 124 kc as stylus pressure on transistor Q_2 is increased from zero to 7 grams. — R. C. Wonson, *Stress-Sensitive Integrated Circuits*, *Electronics*, 38:14, p 81-84.



TWO-STROBE SENSE AMPLIFIER—Seven-transistor sense amplifier on single 61 by 70-mil silicon chip has overall gain of 200.—M. F. Wolff, *Computer in the Microcircuit Design Room*, *Electronics*, 37:12, p 100-104.



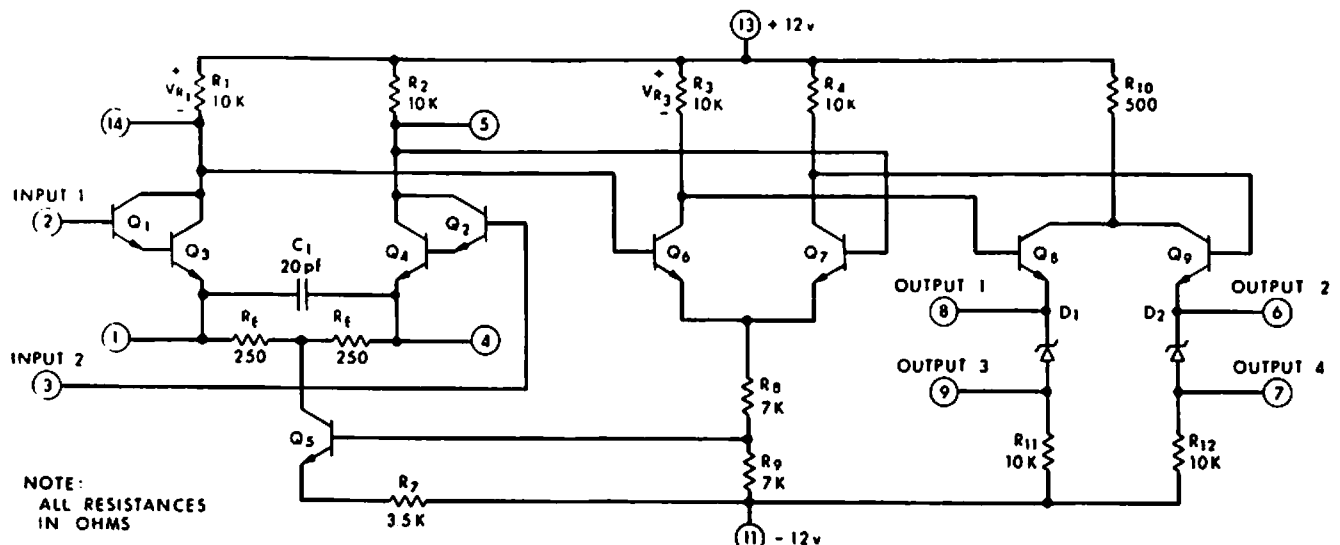
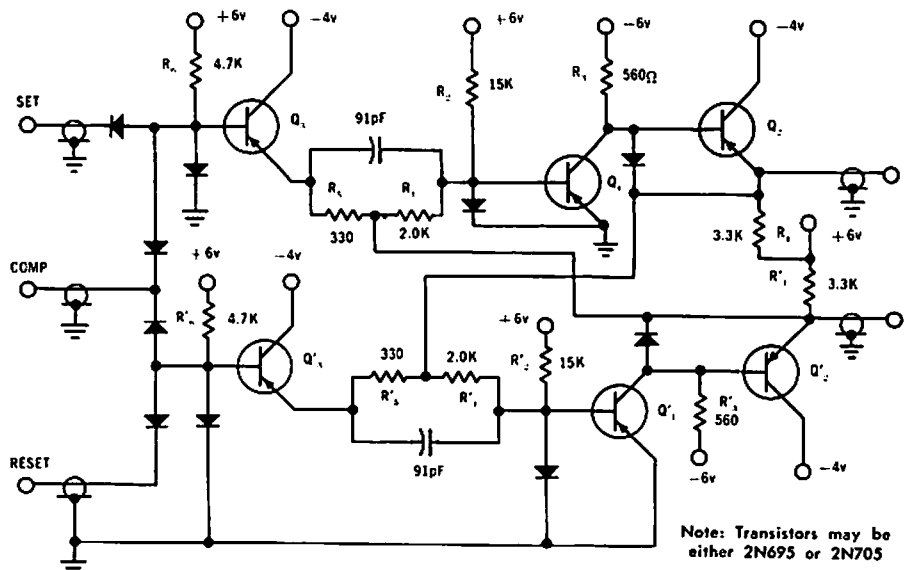
INTEGRATED p-MOST BROADBAND AMPLIFIER—Hole-conducting metal-oxide semiconductor transistor (p-most) and metal-oxide semiconductor capacitor give gain of 5 down to a few cps for integrated stage.—F. M. Wanlass, Novel Field-Effect Device Provides Broadband Gain, *Electronics*, 36:44, p 30-33.



AUDIO AMPLIFIER—Uses Mitsubishi chromium-silicon and nickel-chromium thin-film resistors in hybrid arrangement with conven-

tional transistors.—Y. Tarui, Japan Seeks Its Own Route to Improved IC Techniques, *Electronics*, 38:25, p 90-98.

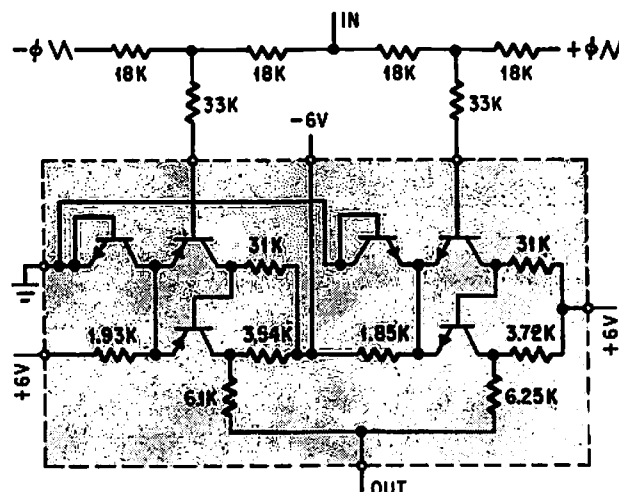
CROSS-CONNECTED INVERTERS AS FLIP-FLOP—Output levels are 0 and 3.5 v. Switching times are 20 to 34 nsec for resistive loads and 30 to 44 nsec for capacitive loads.—W. D. Roehr, "For Computers . . . Basic RCTL Circuits," Motorola Application Note AN-129, Nov. 1965.



TWO-STAGE OPERATIONAL AMPLIFIER—Maximum gain at room temperature is 36,000. Emitter-follower output stages are used with zener diodes to shift d-c level. Input stage

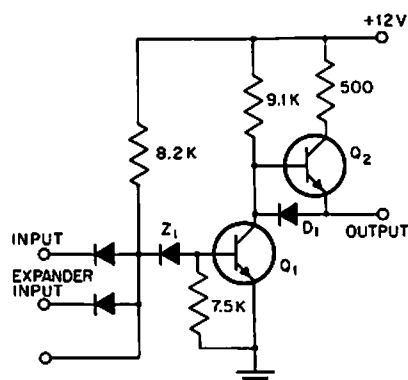
uses Darlington inputs. Input impedance is above 1 meg. Frequency rolloff of 6 db/octave begins at 50 kc.—C. L. Heizman and D. G. Paterson, *Circuit Analysis: A Mono-*

lithic Integrated Operational Amplifier, *EEE*, 13:5, p 80-84.

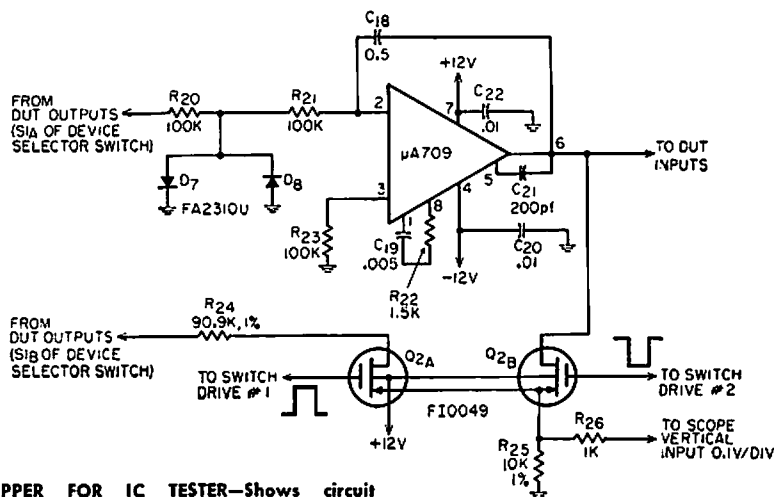


COMPLEMENTARY DUAL SCHMITT TRIGGER—Provides inverting and noninverting outputs for pulse-width modulation.—D. D. Robin-

son, Linear Microcircuits Scarce? Now You Can Breadboard Your Own, *Electronics*, 37:27, p 58-64.

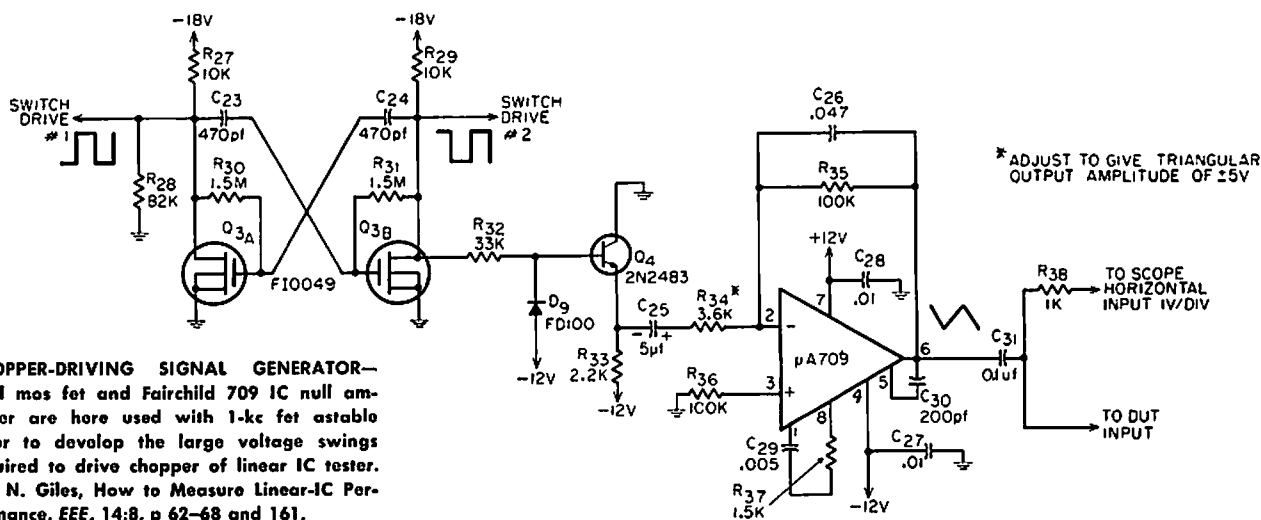


HIGH-NOISE-IMMUNITY LOGIC—Basic gate uses zener with 5.5-v breakdown to give high noise immunity for variety of logic circuits, at penalty of relatively high supply voltage. D1 prevents Q1 and Q2 from being on simultaneously, even during severe transients.—Higher-Voltage ICs Crack Noise Barrier, *EEE*, 14:8, p 40-42.



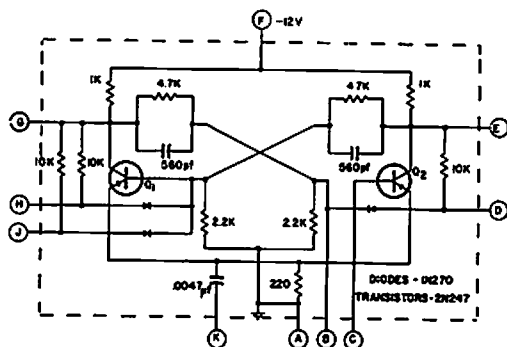
CHOPPER FOR IC TESTER—Shows circuit using Fairchild 709 IC null amplifier in feedback loop around linear integrated-circuit tester, and FI 0049 dual mos fet serving as chopper for displaying offset voltage and

transfer function simultaneously on scope by switching in synchronism with horizontal sweep.—J. N. Giles, How to Measure Linear-IC Performance, *EEE*, 14:8, p 62-68 and 161.

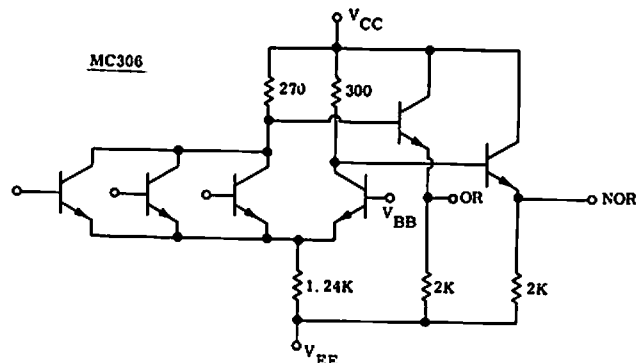


CHOPPER-DRIVING SIGNAL GENERATOR—Dual mos fet and Fairchild 709 IC null amplifier are here used with 1-kc fet astable mvbr to develop the large voltage swings required to drive chopper of linear IC tester.—J. N. Giles, How to Measure Linear-IC Performance, *EEE*, 14:8, p 62-68 and 161.

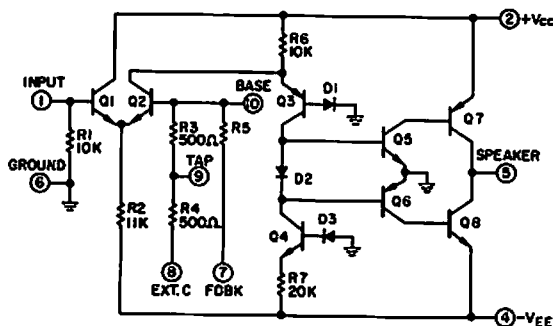
* ADJUST TO GIVE TRIANGULAR OUTPUT AMPLITUDE OF $\pm 5V$



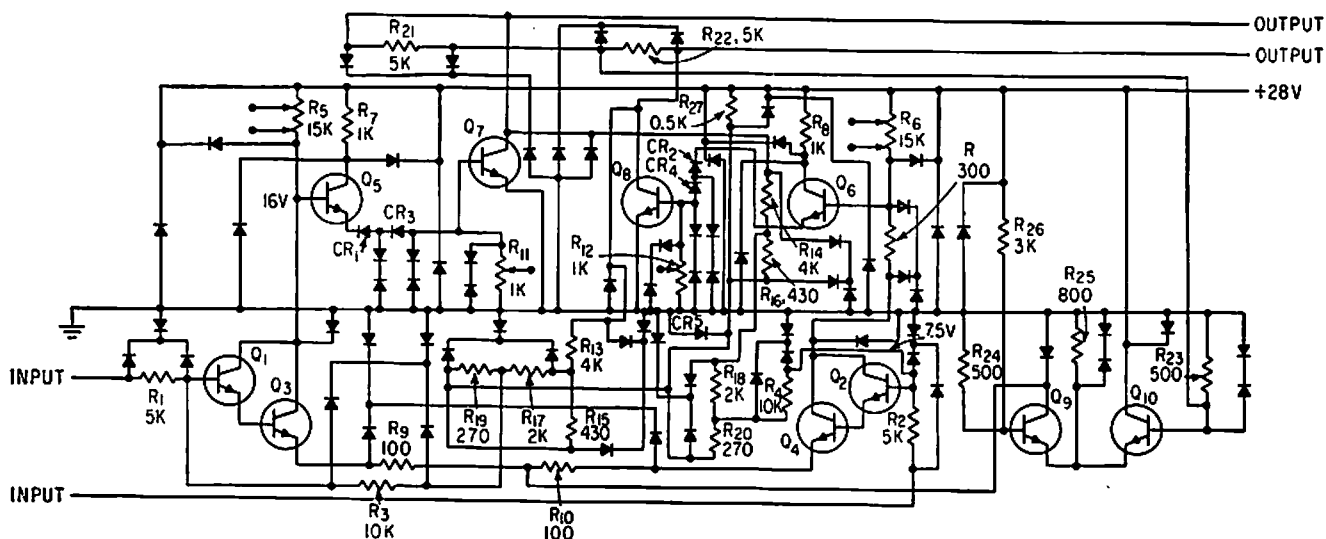
QUAD BLOCK—With appropriate interconnections of external terminals, can be used either as binary counter, monostable mvbr, Schmitt trigger, or selector. Although original design uses encapsulated construction, can readily be adapted for integrated circuit production techniques. Requires 10 ma for all applications, and gives output pulses with 0.1 microsec rise time and up to 0.4 microsec fall time.—F. K. Luteran, *Four-Way "Quad" Circuit Building Block*, *EEE*, 10:6, p 66-67.



BASIC IC GATE—Circuit is basic element of current mode logic family. If reference voltage V_{BB} is supplied to one side of gate, constant current that normally flows through 1.24 K emitter resistor can be switched from one side of gate to other by switching input signals above and below reference voltage. Complementary output is provided from single gate.—S. T. Robertson, *"Integrated Circuit Line Driver,"* Motorola Application Note AN-187, Aug. 1965.



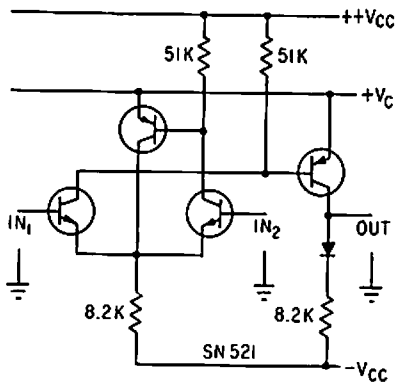
AUDIO POWER AMPLIFIER—MC1524 chip gives high efficiency, low distortion, and wide range along with highest output power permitted by dissipation of TO-5 case. Chip is combined with superior power-handling of standard bottom-collector output transistors to give monobrid amplifier providing 1 w to speaker. True class-B output circuitry gives low standby current, with crossover distortion of class B minimized by using current source Q3-Q4 for quad Q5-Q6-Q7-Q8. Diode D2 further reduces crossover distortion.—R. A. Hirschfeld, *"Audio Power Applications Using Integrated Circuits,"* Motorola Application Note AN-162, Aug. 1965.



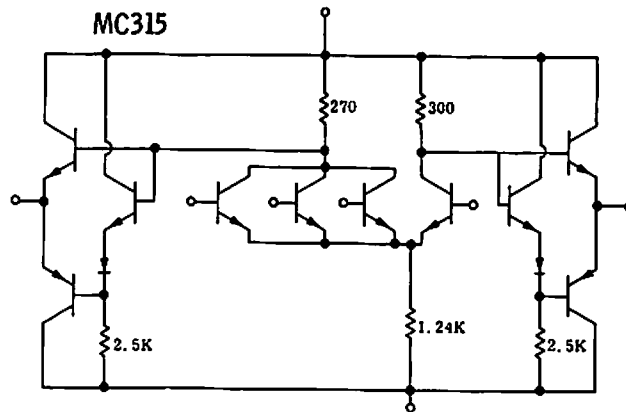
FINGERTIP-SIZE SERVO AMPLIFIER—Direct-coupled class A servo amplifier diffused into 0.75-inch-diameter silicon wafer gives

power output of 1.5 w and overall closed-loop gain of 200. Distributed diode planes are introduced by substrate.—M. W. Aarons,

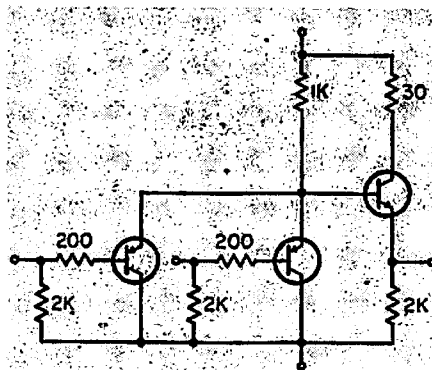
Putting a Servo Amplifier on a Small Silicon Wafer, *Electronics*, 35:52, p 33-35.



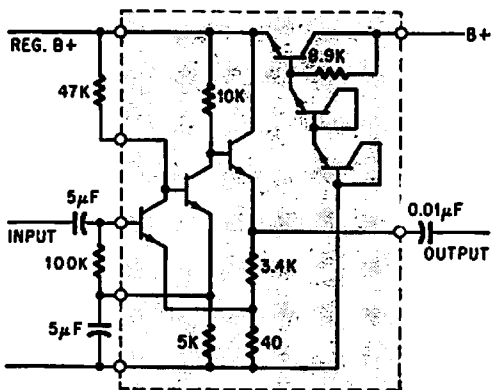
D-C OPERATIONAL AMPLIFIER—Open-loop voltage gain is 62 db, input impedance is 18,000 ohms differential and 10,000 ohms to ground, and output impedance is 8,000 ohms.—Operational Amplifiers are Getting Smaller, *Electronics*, 35:52, p 66.



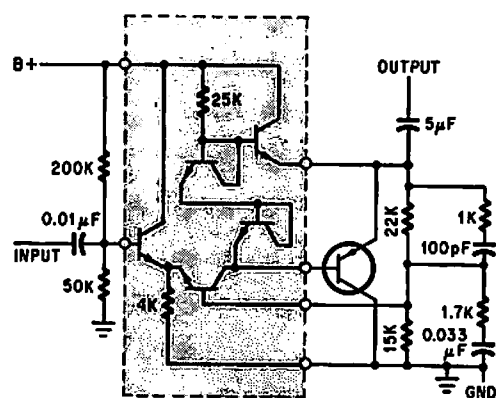
LINE DRIVER—Designed as line or capacitance driver, but is same as basic gate except for output stages. Pnp transistors are hybrid, while other parts are on monolithic chip.—S. T. Robertson, "Integrated Circuit Line Driver," Motorola Application Note AN-187, Aug. 1965.



COMPLEMENTARY-TRANSISTOR LOGIC—Uses both pnp and npn transistors, with pnp emitters tied together and returned through 1K resistor to positive voltage supply. Propagation delay is 3 to 5 nsec for fanouts of 1 to 10.—D. Christiansen, Logic Schemes Reviewed, *EEE*, 13:11, p 64-79.



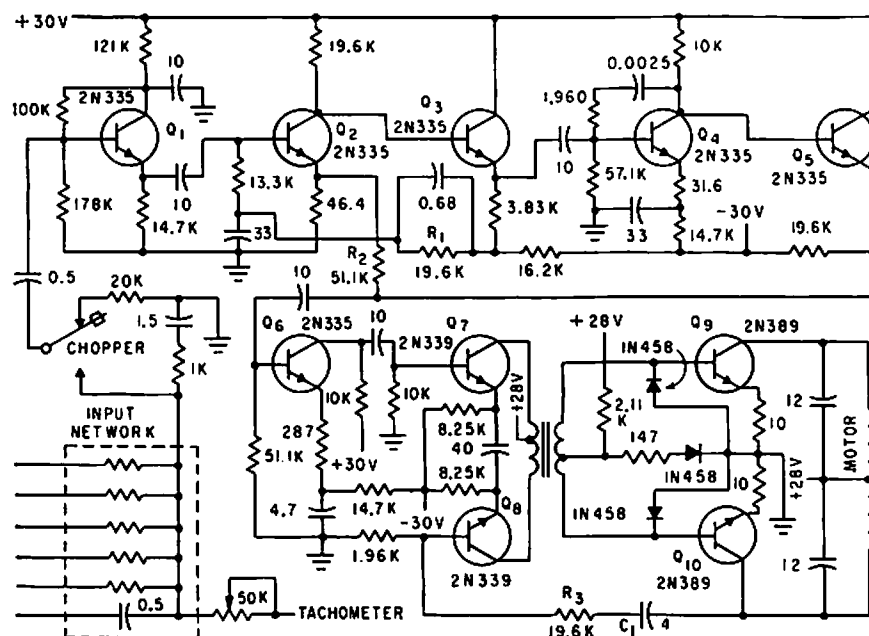
HIGH-GAIN AUDIOPREAMP—Has strong negative feedback and built-in power supply series regulator.—D. D. Robinson, Linear Microcircuits Scarce? Now You Can Breadboard Your Own, *Electronics*, 37:27, p 58-64.



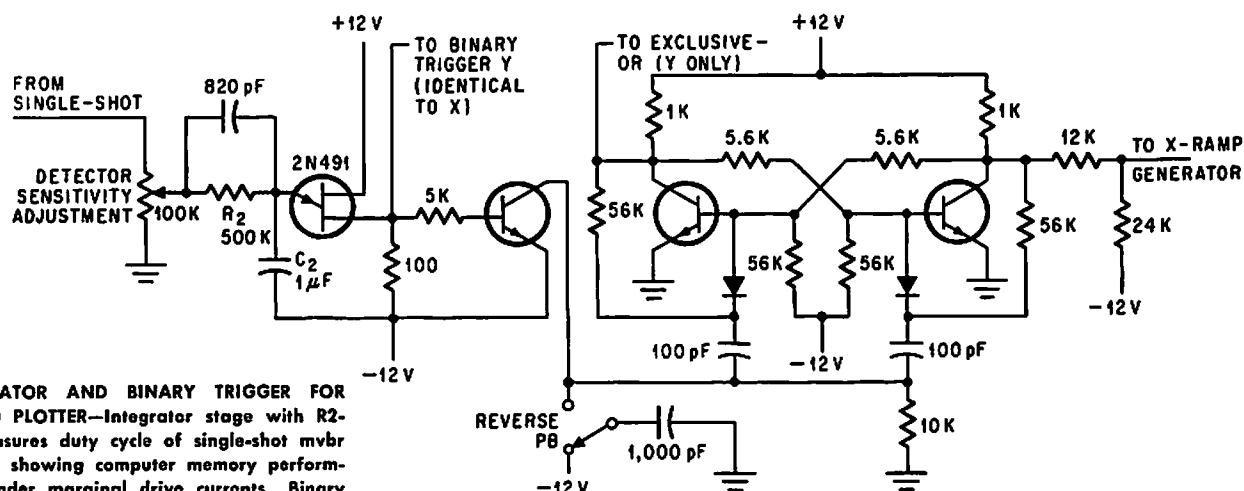
OUTPUT BUFFER AMPLIFIER—Integrated construction (shaded) is used with external pnp transistor.—D. D. Robinson, Linear Microcircuits Scarce? Now You Can Breadboard Your Own, *Electronics*, 37:27, p 58-64.

CHAPTER 43

Integrator Circuits



HIGH-GAIN AMPLIFIER—Integrator amplifier using ten silicon transistors in five voltage gain stages gives gain of 250,000. To prevent saturation by spurious microvolt signals, input network is shielded by Mu-metal can grounded to signal ground and overall steel can grounded to power ground. Power supply ripple must be below 0.01%. Amplifier drives 6-w a-c servomotor having d-c tachometer on same shaft.—S. T. Cap and N. P. White, Guidance Systems in Manned Space Flight, *Electronics*, 32:33, p 49-51.

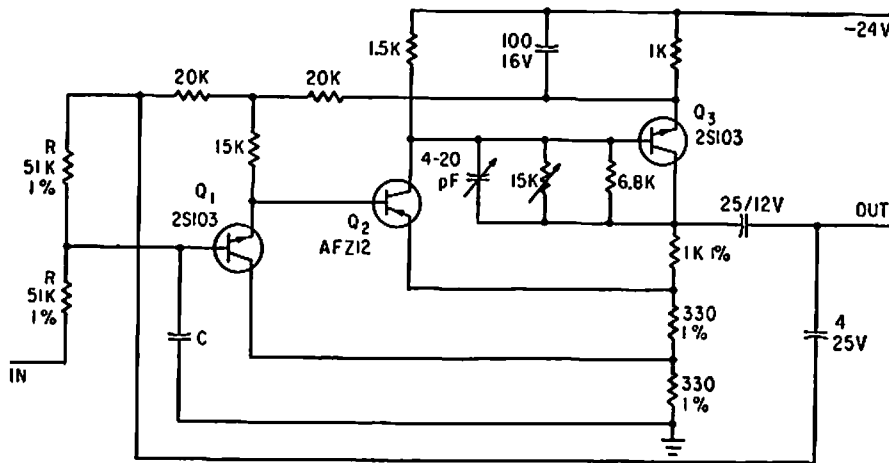
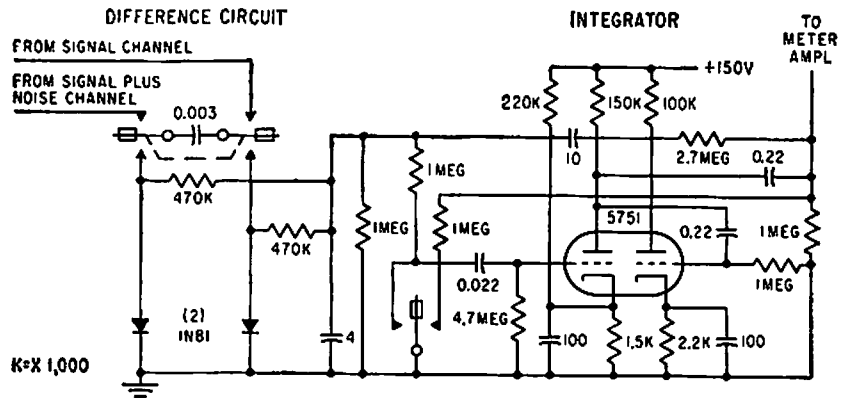


INTEGRATOR AND BINARY TRIGGER FOR SHMOO PLOTTER—Integrator stage with R2-C2 measures duty cycle of single-shot mvbr used in showing computer memory performance under marginal drive currents. Binary triggers control direction of ramp generators and increase or decrease memory drive cur-

rent. Npn transistors are 2N706 and diodes are 1N921.—J. E. Gersbach, The Great Shmoo

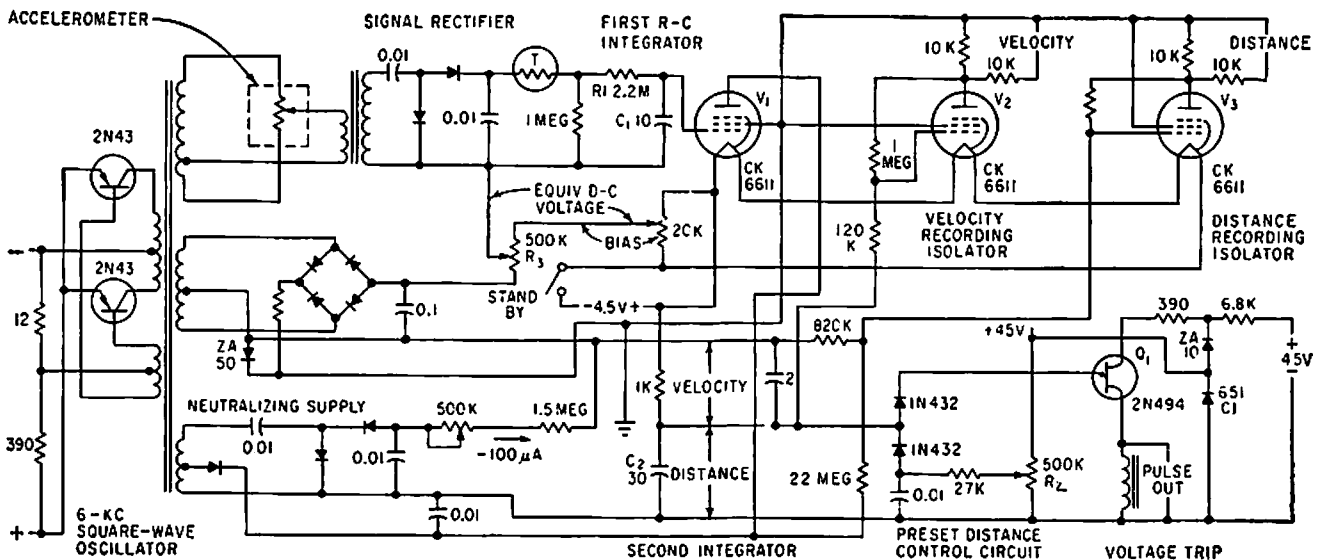
Plot: Testing Memories Automatically, Electronics, 39:15, p 127-134.

ABSOLUTE-DIFFERENCE INTEGRATOR—Uses two-pole chopper with capacitor connected between the two reeds. During half of chopper cycle, capacitor is charged to voltage difference between outputs of two signal-processing channels. During other half-cycle, charge is transferred to integrator circuit and positive side of capacitor is clamped to ground by one of the two diodes. Integrator is chopper-stabilized d-c amplifier with capacitive feedback, having gain of 200 and integration time constant of 1.5 hours.—H. Schwarzlander, *Intelligibility Evaluation of Voice Communications, Electronics*, 32:22, p 88-91.



COMPENSATED R-C INTEGRATOR—Overshoot is less than 2% at 3 db down for bandwidth of 20 Mc.—S. Berglund and S. Wester-

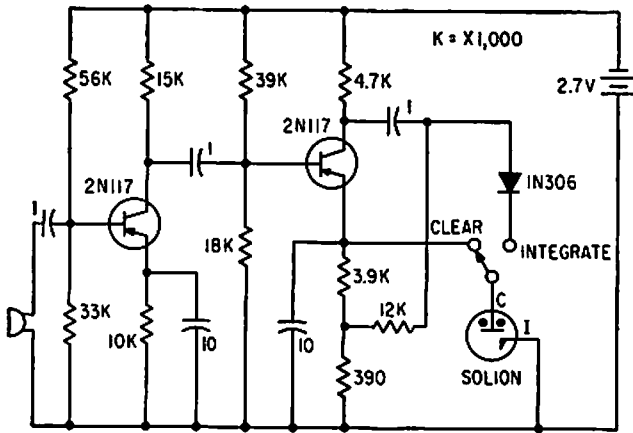
lund, *Probes for Plasma Research with Wideband Integrators, Electronics*, 35:24, p 44-45.



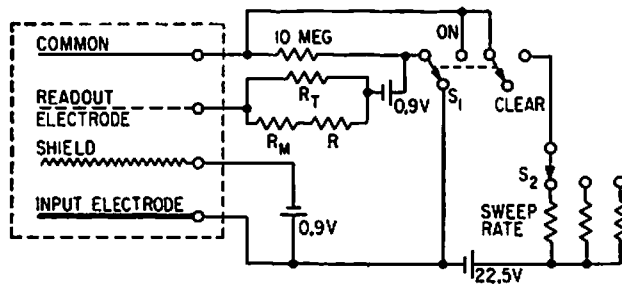
DOUBLE INTEGRATOR—Used to measure distances up to 150 feet, from information supplied by slide-wire accelerometer. Trans-

istor operates as voltage trip, providing output pulse when distance reaches preset value. Two other tubes provide velocity and dis-

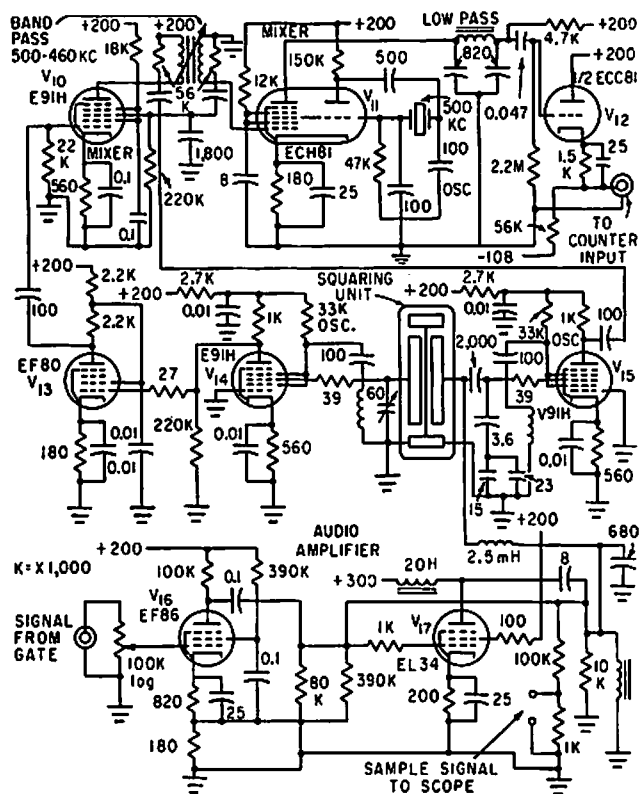
tance information for recorder.—T. R. Nisbet, *Double Integrator Finds Distance, Electronics*, 32:21, p 64-66.



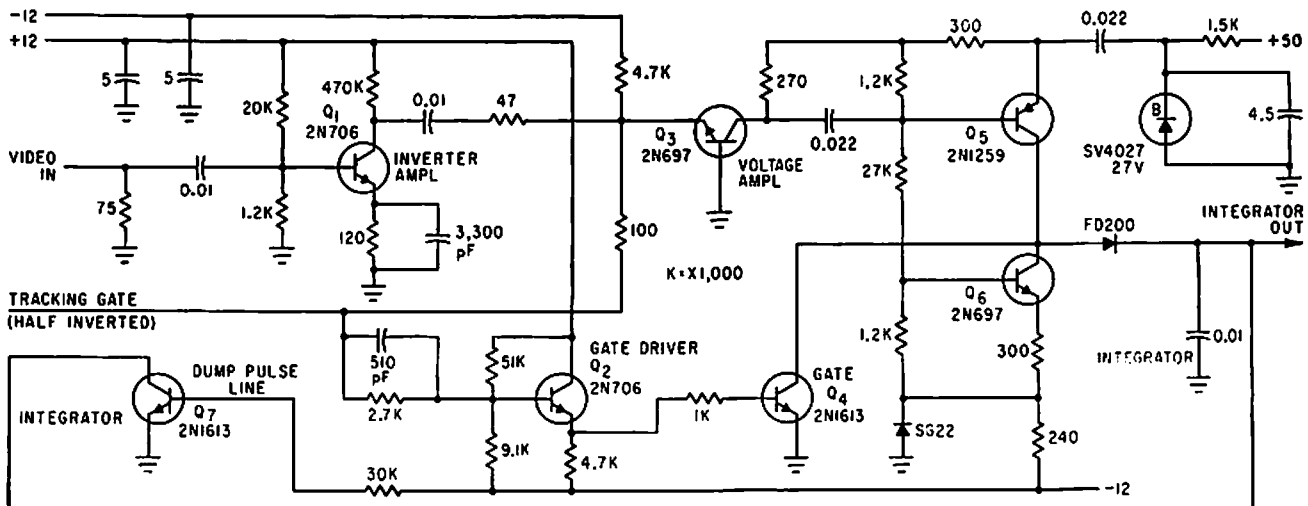
SOLION INTEGRATING NOISE METER—Audio amplifier for microphone feeds visual-read-out solion integrator through rectifying diode. Used to measure extent of exposure of person to dangerously high levels of noise.—R. N. Lane and D. B. Cameron, *Current Integration with Solion Liquid Diodes*, *Electronics*, 32:9, p 53-55.



SOLION TIME BASE—Electrical readout integrator consisting of solion liquid diode provides readout current that increases linearly with time for constant current input, permitting use of input to axes of X-Y recorder.—R. N. Lane and D. B. Cameron, *Current Integration with Solion Liquid Diodes*, *Electronics*, 32:9, p 53-55.



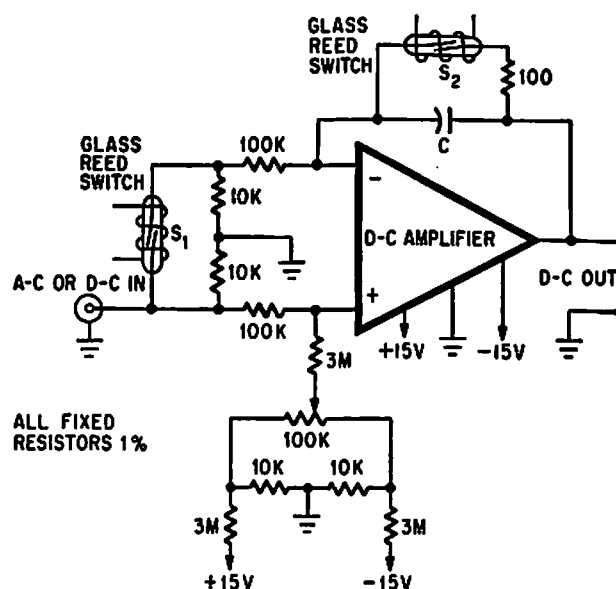
ELECTROSTATIC SQUARER—Used to obtain integrated reading of reflected sound patterns when measuring acoustic characteristics of auditoriums. Electrostatic squarer incorporates frequency-determining elements of two transistor negative-resistance oscillators (3.3 and 3.8 Mc). Amplified outputs are mixed to obtain 500-ke difference signal which in turn is mixed with 500-ke crystal oscillator output to give from 0 to 35 ke for feeding to counter.—J. P. A. Lochner and P. Moffert, *Electrostatic Squarer for Acoustic Measurements*, *Electronics*, 33:35, p 66-68.



RADAR VIDEO INTEGRATOR—Accepts all video and noise signals within first half of tracking gate and performs voltage-time integration. Identical integrator performs sim-

ilar function for second half. Results of integrations (integrator out) go to difference detector for comparison.—D. L. Nepveux, *Digital Circuits Achieve Automatic Control of*

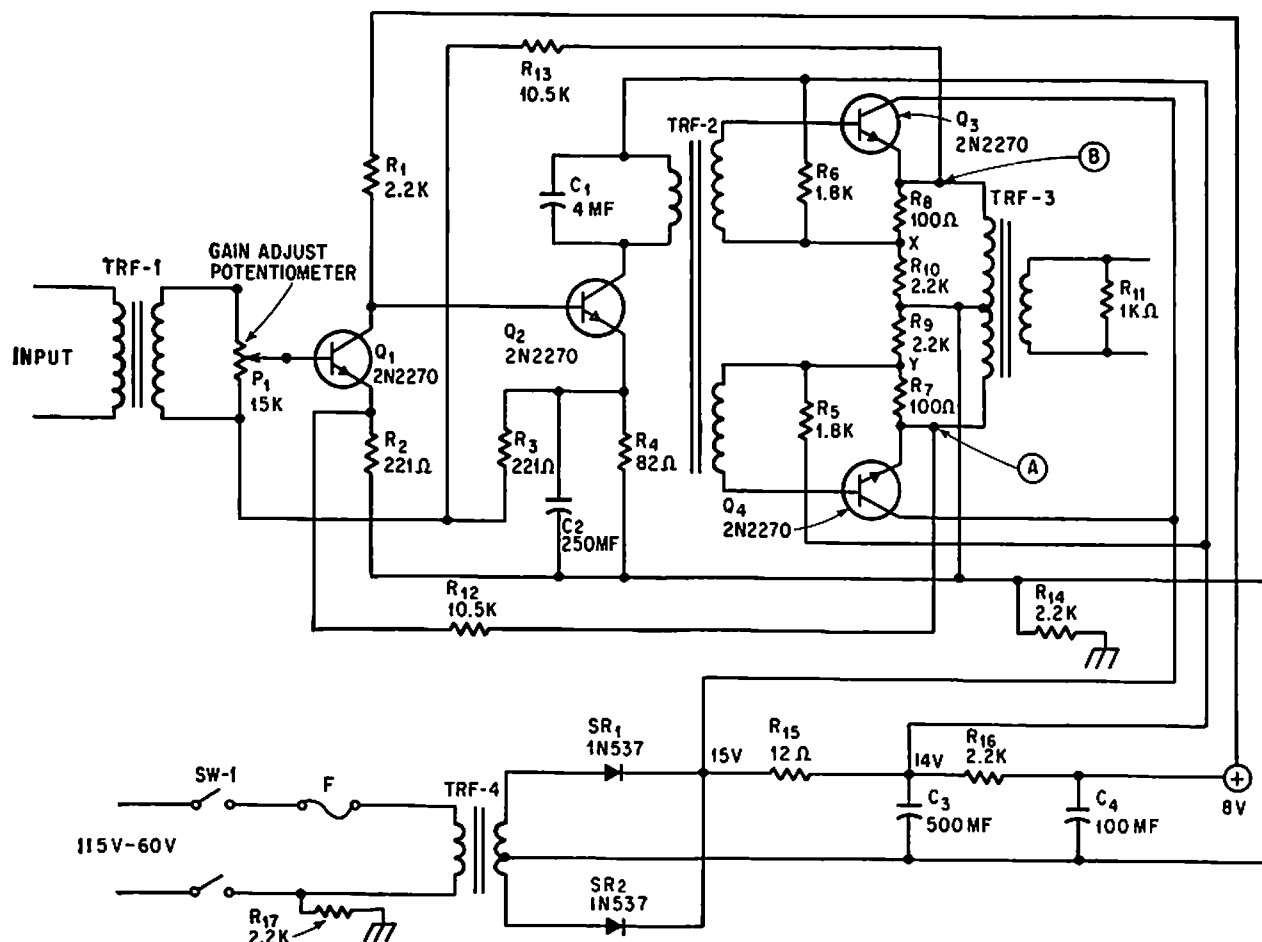
Radar Range Tracking, *Electronics*, 34:52, p 46-50.



REED SWITCH CONTROLS OPERATIONAL AMPLIFIER—Circuit can gate out unwanted signals, maintain integrated output at specified level, or operate as synchronous de-

factor. Maximum switching speed is 300 kc. Opening and closing S1 in synchronism with a-c input signal allows synchronous detection and integration of signal. Amplifier

integrates only portion of signal present while switch is open.—H. Penfield, Glass Reed Switch Controls Operational Amplifier, *Electronics*, 39:17, p 97-98.



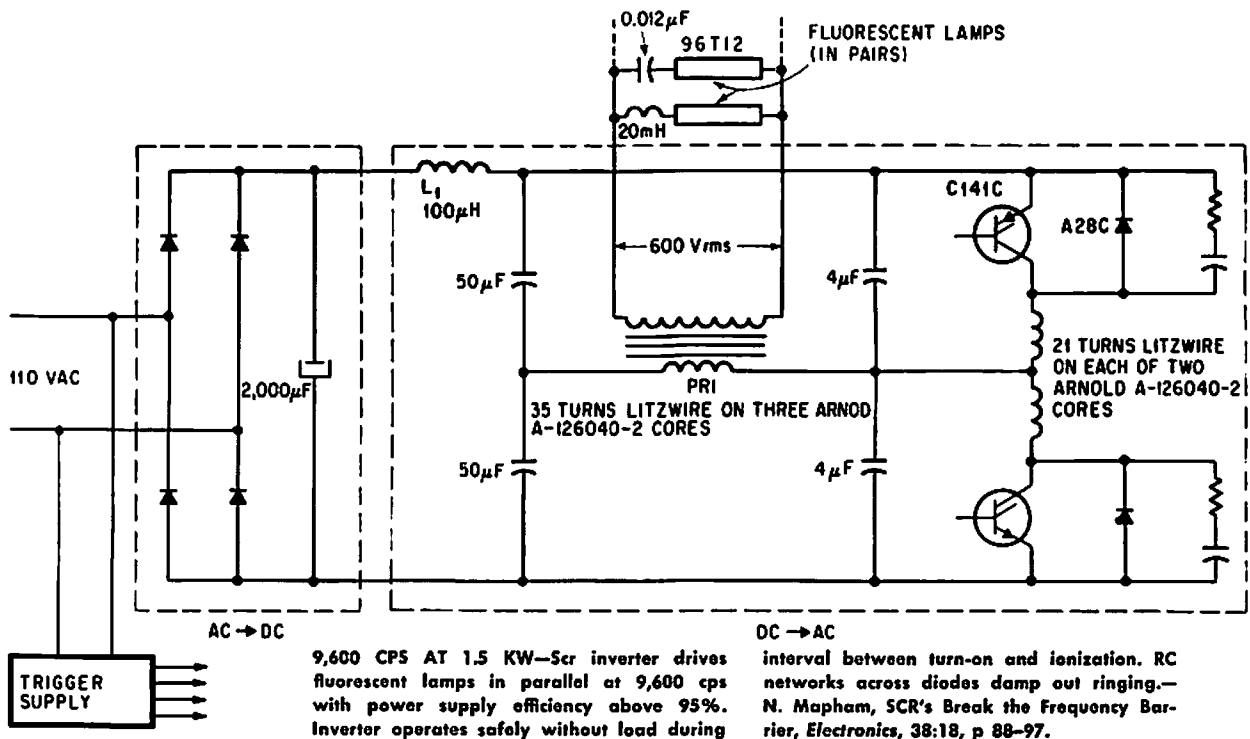
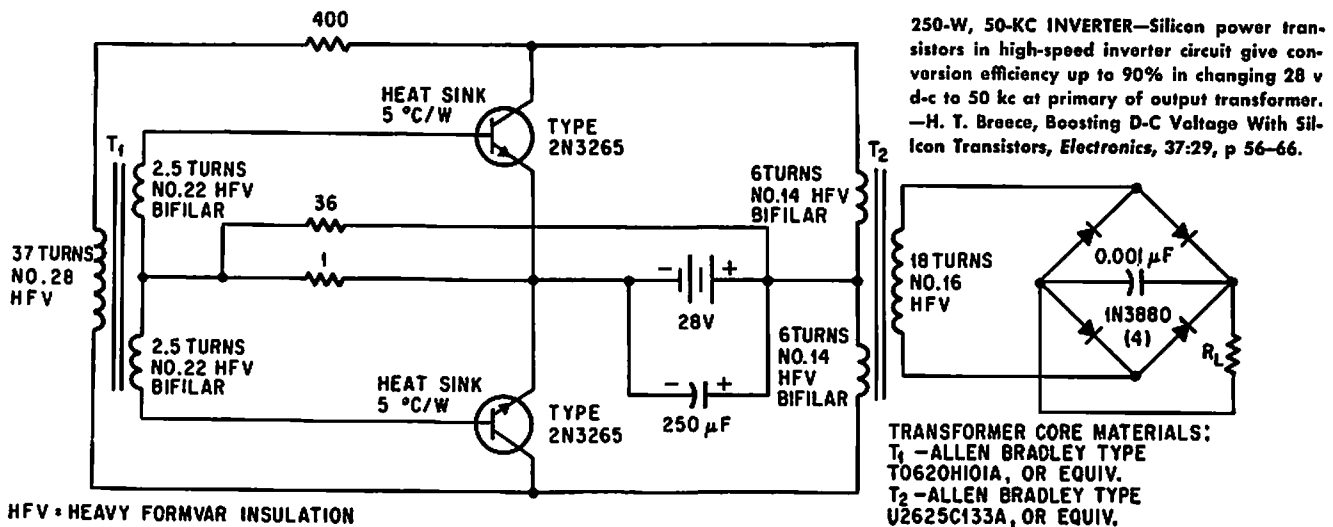
PROPORTIONAL AMPLIFIER FOR INTEGRATING CONTROL—Dual balanced feedback and form of bootstrapping give highly stable

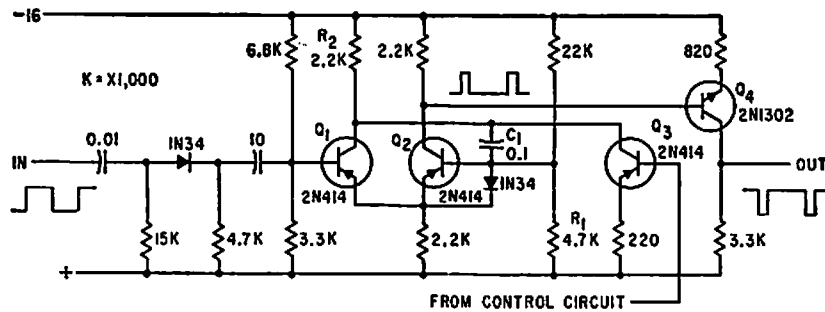
output of 35 v into 3,500 ohms, with voltage gain adjustable from 0 to 30 while output transformer return is to ground rather

than B+.—C. H. Smoot and F. J. Karlov, **Boiler Control: Simple Controller for a Complex Job**, *Electronics*, 37:18, p 88-93.

CHAPTER 44

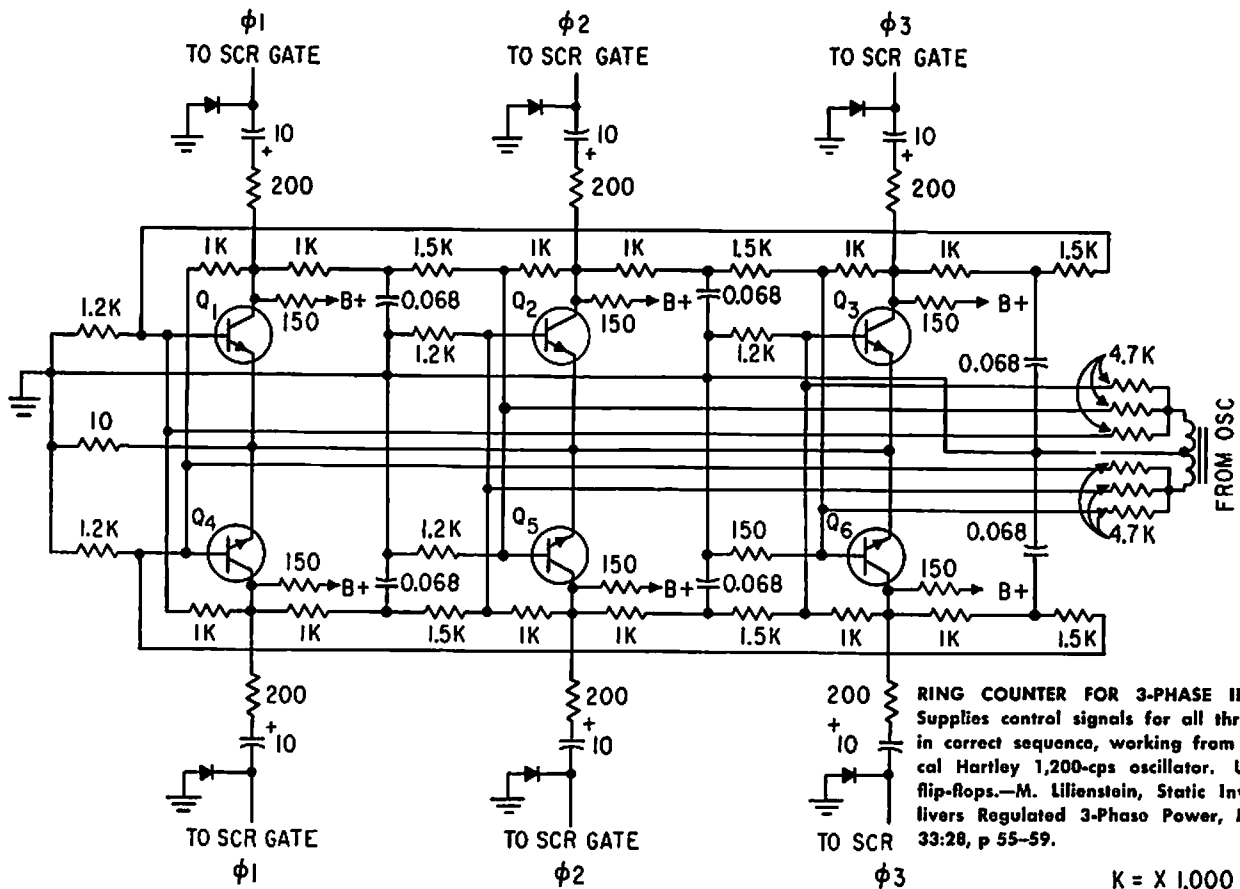
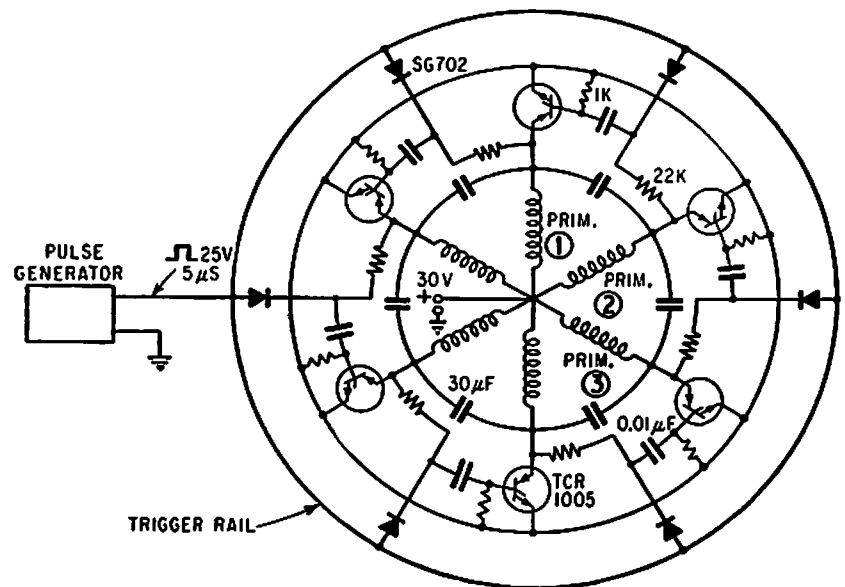
Inverter Circuits





400-CPS THREE-PHASE INVERTER DELAY CIRCUIT—One-shot mvbr Q1-Q2 triggered by input signal, and Q3 controlled by error-detecting signals at output of static inverter, together determine delay at output of Q4. Complete inverter uses six identical delay circuits together with solid-state servo loops to control output voltages and phase angles despite unbalanced loads.—T. J. Gilliam, *Three-Phase Inverter with Feedback Loops*, *Electronics*, 35:12, p 48-51.

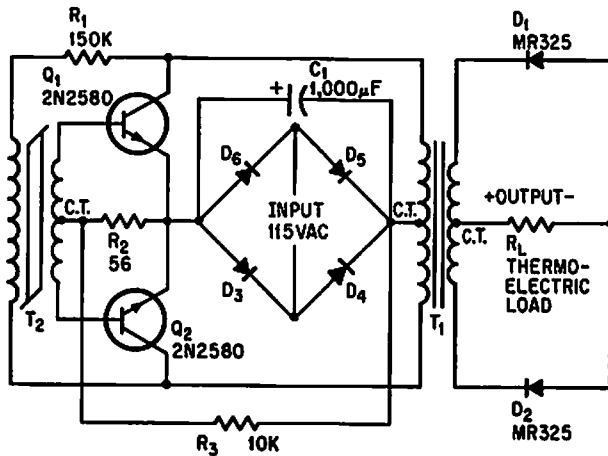
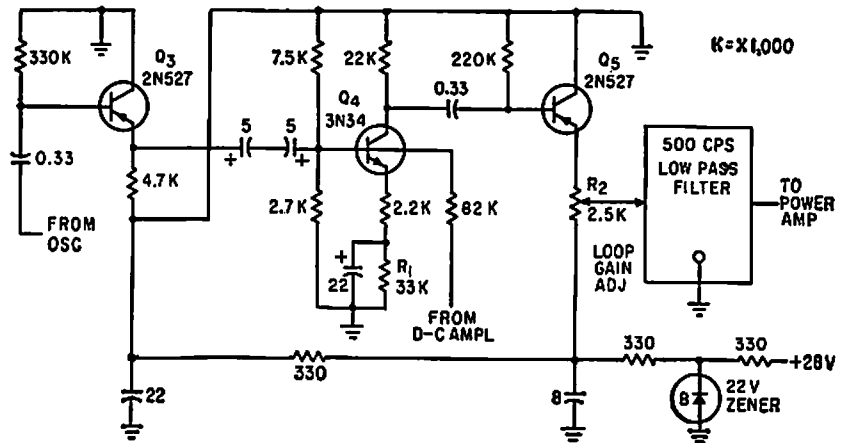
SCR STATIC ALTERNATOR—Silicon-controlled rectifiers serve as current-switching elements in ring-counter inverter that delivers three-phase a-c output requiring no filtering, without moving parts. With 2.4-kc pulse generator, output is 400-cps three-phase.—R. H. Murphy, *Developing True Solid-State Static Alternators*, *Electronics*, 36:21, p 58-61.



RING COUNTER FOR 3-PHASE INVERTER—Supplies control signals for all three phases in correct sequence, working from symmetrical Hartley 1,200-cps oscillator. Uses three flip-flops.—M. Lillienstein, *Static Inverter Delivers Regulated 3-Phase Power*, *Electronics*, 33:28, p 55-59.

K = X 1,000

VARIABLE-GAIN INVERTER AMPLIFIER—Gives gain variations of up to 10 to 1, with less than 10% harmonic distortion at 1 v output through use of tetrode transistor, to provide precise voltage regulation of output of d-c to 400-cps a-c inverter. Error current from d-c amplifier of inverter is applied to base 2 of Q4, causing a-c voltage gain of stage to vary with magnitude of error current.—R. Wilman, *Linear Circuits Regulate Solid-State Inverter*, *Electronics*, 33:16, p 61-63.

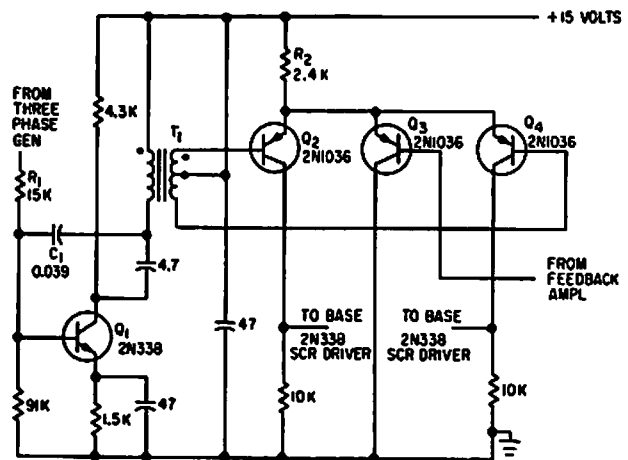


T₁ CENT TRAN W-23-104

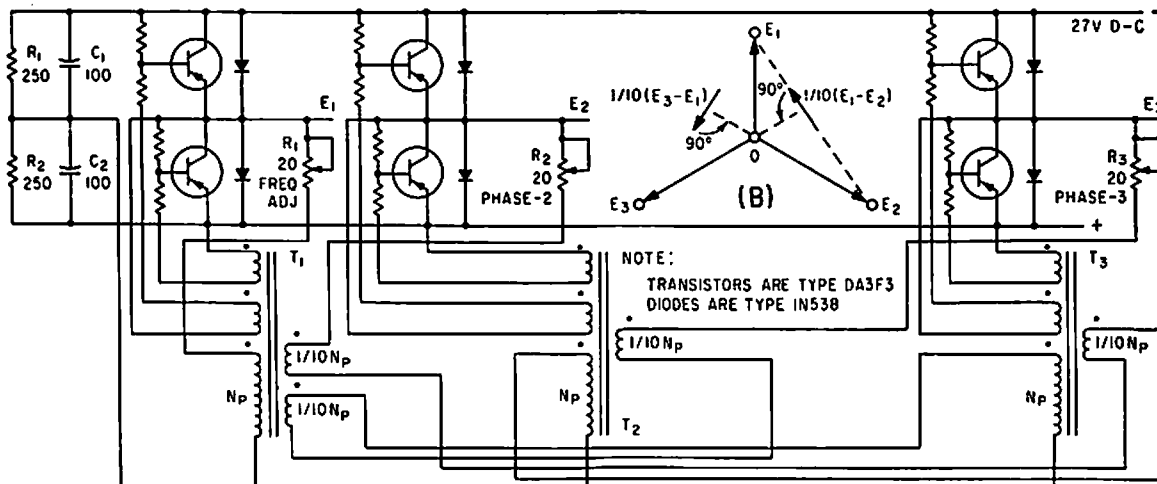
T₂ CENT TRAN W-23-105

D₃-D₆ MDA952-5 (BRIDGE RECTIFIER ASS'Y)

D-C/D-C—Line voltage is rectified, inverted at 18 kc, stepped down, and rectified to give 23 a at 6 v for thermoelectric heat-pump system.—A. L. Wennerberg and F. H. Schroeder, *High-Current Converter is Small, Quiet, Low-Cost*, *Electronics*, 37:30, p 41.



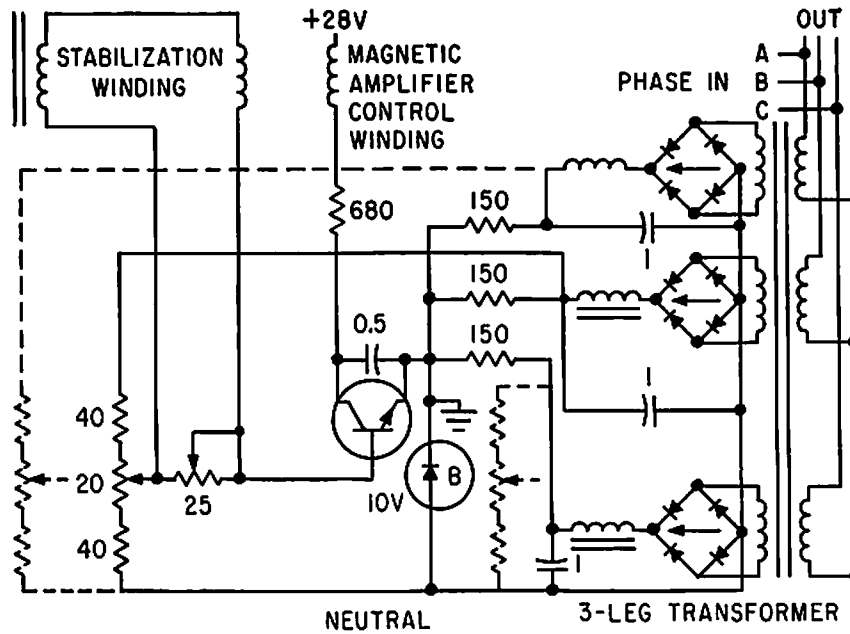
PULSE-WIDTH MODULATOR—Determines length of quasi-square waves used in connection with triggering of scr's in output stage of inverter.—R. J. Kearns and J. J. Rolfe, *Three-Phase Static Inverters Power Space-Vehicle Equipment*, *Electronics*, 34:18, p 70-73.



SELF-EXCITED 3-PHASE NON-PHASE-AMBIGUOUS BRIDGE—Each oscillating section is half-bridge converter operating in square-

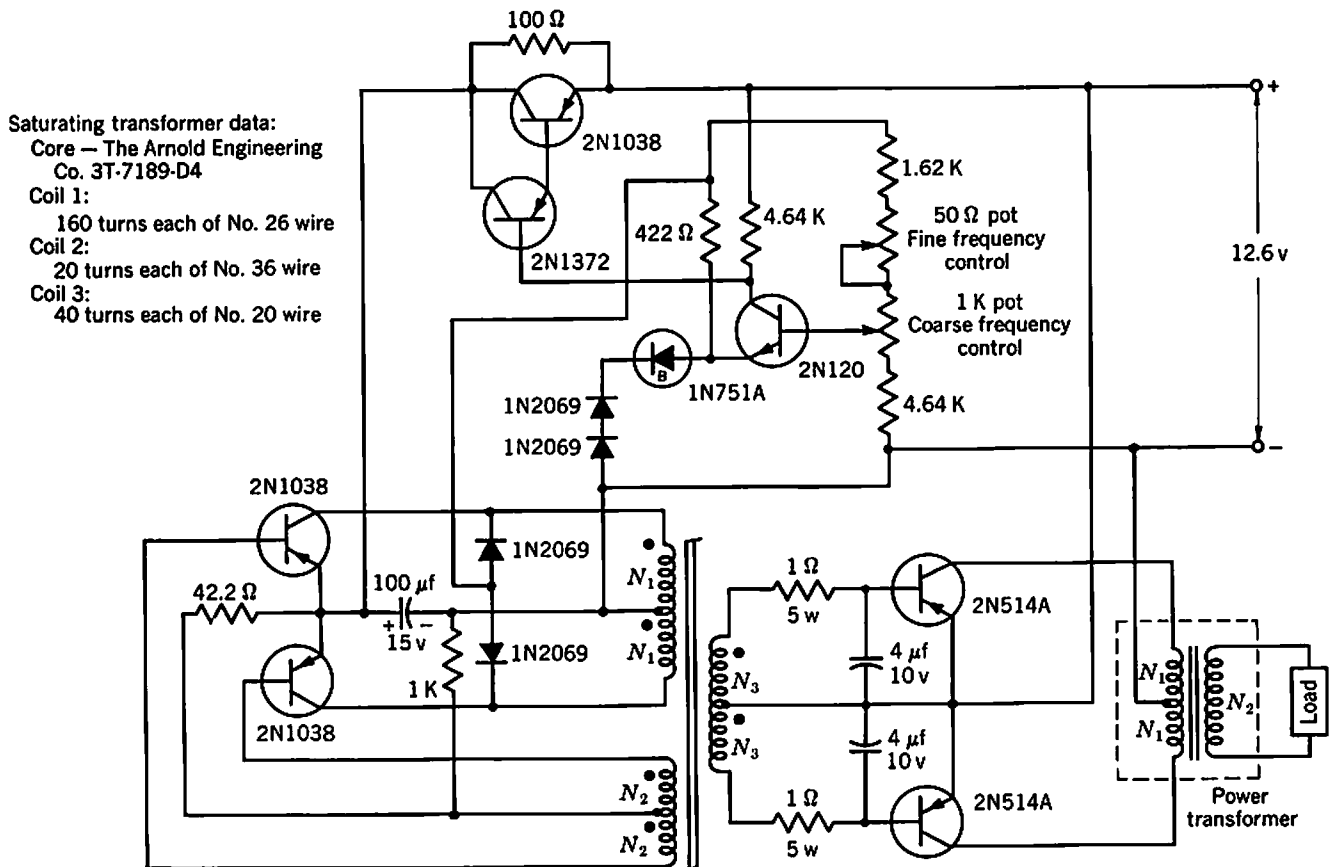
wave mode. Phase diagram shows derivation of synchronizing voltages.—A. G. Lloyd, *Half-Bridge Inverter Provides Economical*

Three-Phase Power, *Electronics*, 34:37, p 62-65.



THREE-PHASE REGULATOR—When used with three-phase static inverter employing scr's and magnetic amplifiers, provides 25-millisecond recovery time when load is switched from 2.5 amp to 0. The three bridges use one zener

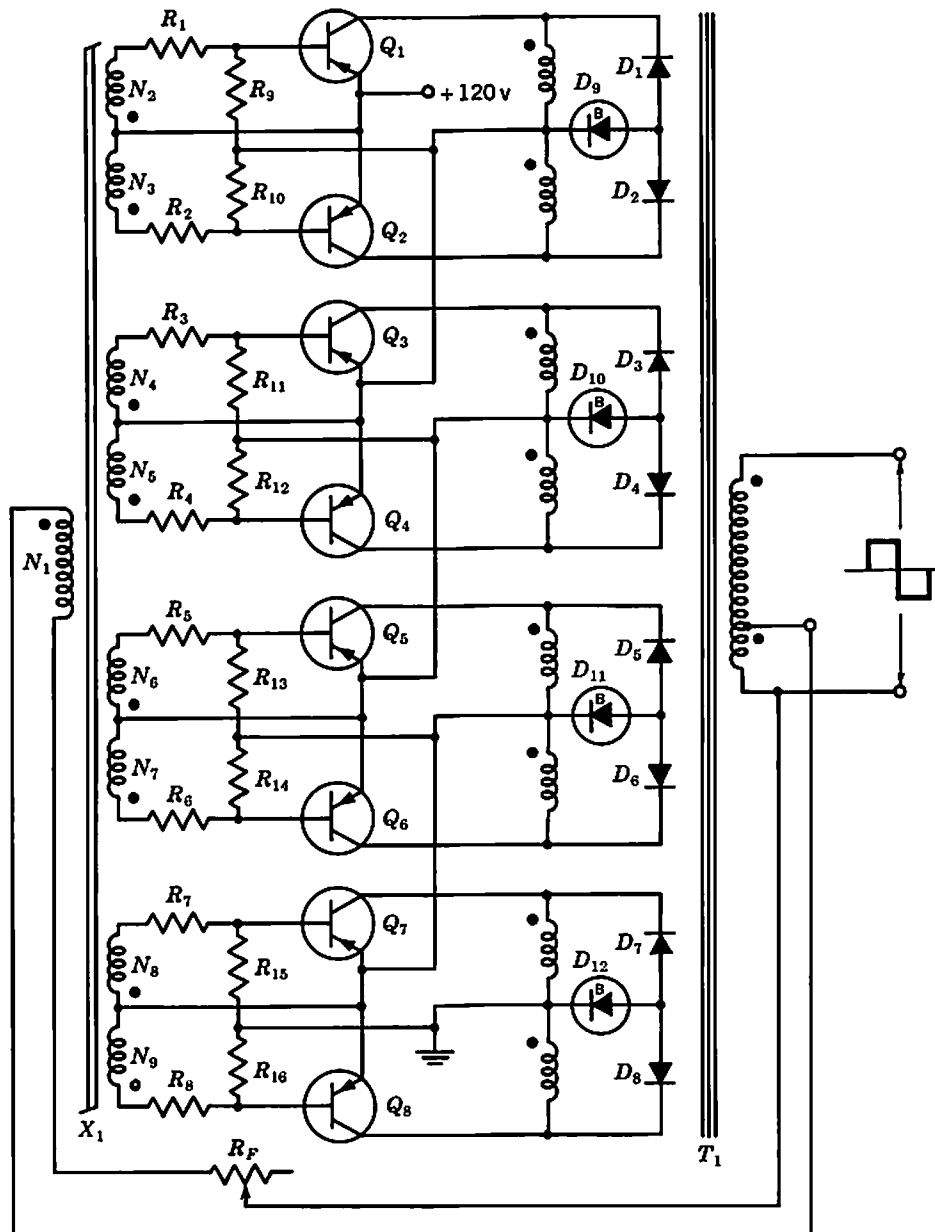
diode in common as non-linear element. Silicon transistor amplifies unbalance in each bridge.—M. Lilienstein, *Static Inverter Delivers Regulated 3-Phase Power*, *Electronics*, 33:28, p 55-59.



200-W 60-CP5 115-V POWER FROM 12 V—Uses saturating-core oscillator. Complete design procedure is given. Maximum fre-

quency drift is below 0.5% for change from no load to full load and for input change from 11.5 to 13.5 v. Efficiency is about 86%.

No-load input power is 8.5 w.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 451.



Parts List

Q_1 through Q_8 —2N458A

D_1 through D_8 —1N2069

D_9 through D_{12} —1N1825R

R_1 through R_8 —5 ohms, 1 watt

R_9 through R_{16} —910 ohms, 1 watt

R_F —25-ohm 5-watt rheostat

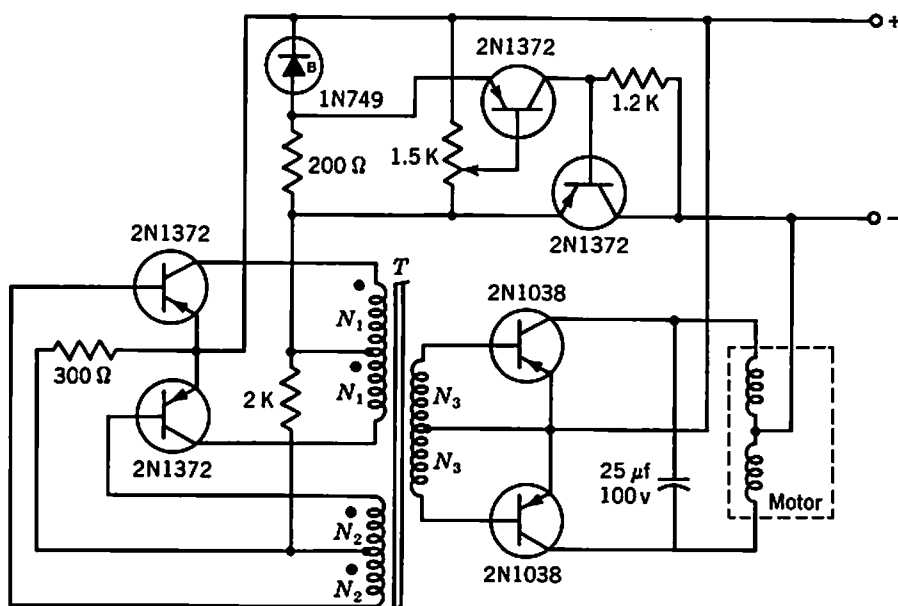
T_1 —Texas Instruments transformer #440220 or equivalent

X_1 —Tapewound toroidal core, 51425-4A Magnetics Inc., or 5772-D4 Arnold Co.

N_1 —448 turns, #22 heavy Formvar

N_2 through N_9 —112 turns, #28 heavy Formvar

400-W 60-CPS DUAL-TRANSFORMER INVERTER—Input voltage is divided equally among four series primaries so each transistor is subjected only to 60 v when off. Output is 140-v square wave. Efficiency is 95% at full load. Use of dual-transformer configuration makes frequency of oscillation easy to adjust by changing setting of R_F , to give exactly 60 cps for any value of load current.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 459.



Transformer data:

Core—Magnetics, Inc., 50076-4A

Coil 1: 1,100 turns each of No. 36 wire

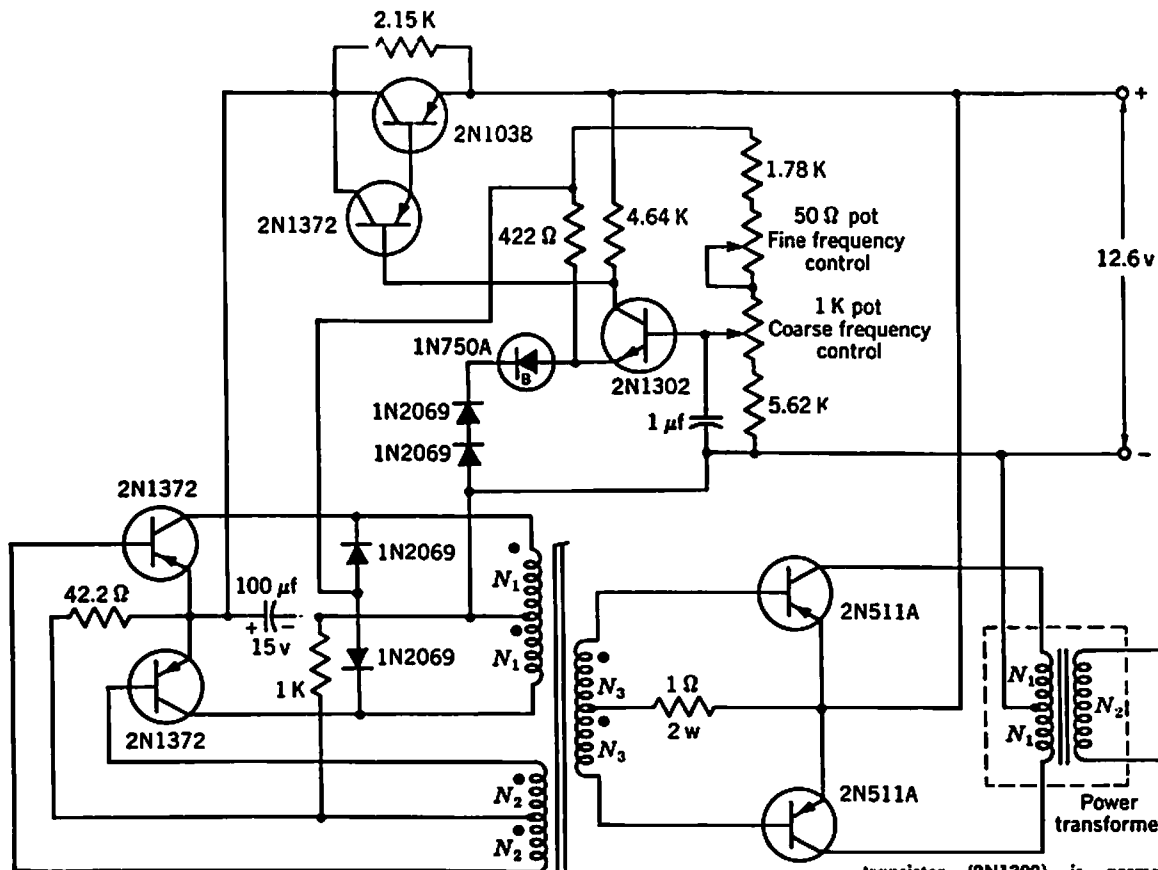
Coil 2: 130 turns each of No. 36 wire

Coil 3: 200 turns each of No. 36 wire

Note: All resistors are ½ watt

20-W 60-CP5 INVERTER—Low-power version was designed to drive timer. Maximum frequency variation was only 1% for supply-

voltage range of 11.5 to 14.5 v.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 458.

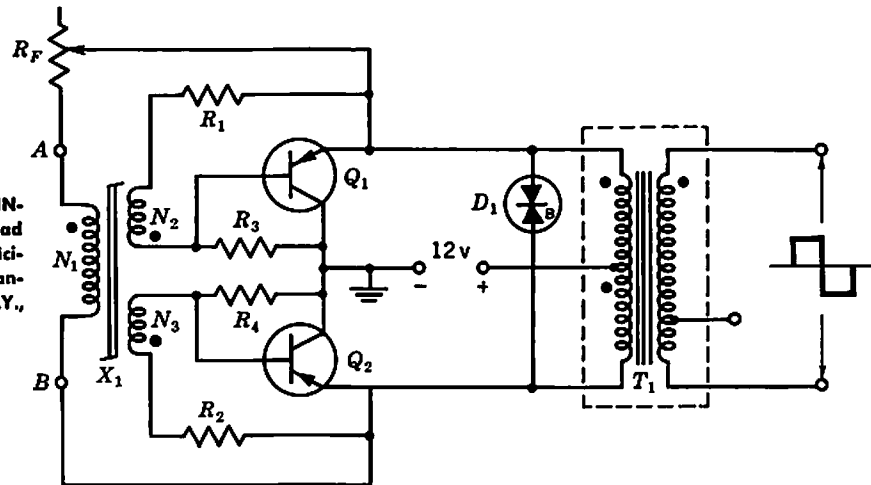


100-W 60-CP5 INVERTER—Permits operation of small a-c appliances from auto or boat

storage battery. Frequency changes somewhat with temperature because sensing-input

transistor (2N1302) is germanium.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 457.

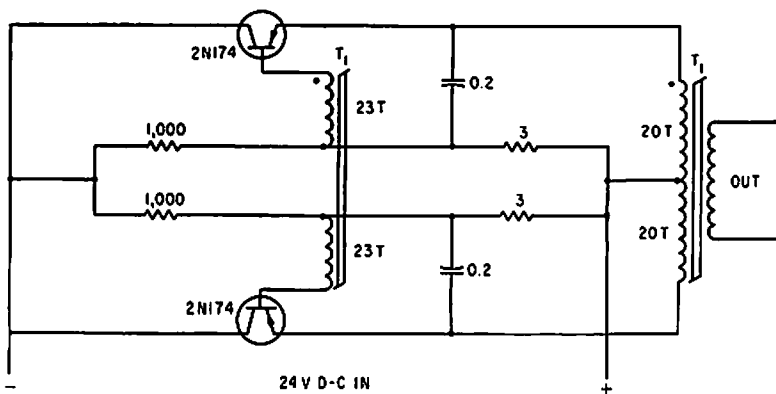
250-W 60-CPS DUAL-TRANSFORMER INVERTER—Provides square-wave output to load from 12-v d-c supply, at 130 v, with efficiency of 85%.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 458.



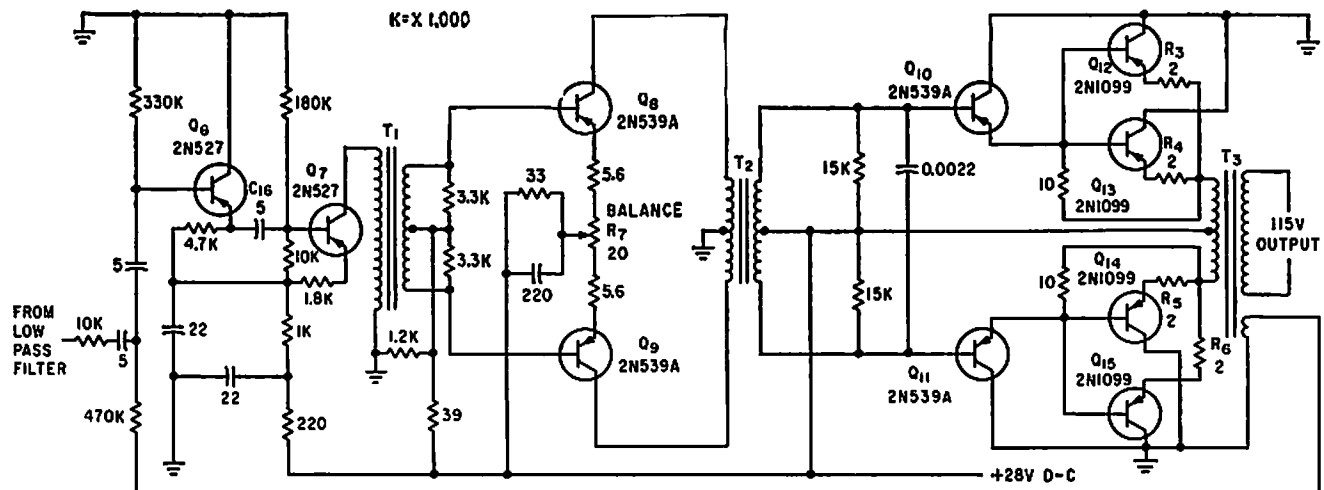
Parts List

Q_1, Q_2 —2N514
 D_1 —1N1823 (27-volt double-anode clipper)
 R_F —20-ohm 5-watt rheostat
 R_1, R_2 —1 ohm, 5 watts
 R_3, R_4 —150 ohms, 1 watt

X_1 —Tape-wound toroid, 5320-D4 Arnold Engineering Co., or 5000-4A Magnetics, Inc.
 N_1 —316 turns, #24 heavy Formvar
 N_2, N_3 —79 turns, #22 heavy Formvar
 T_1 —Texas Instruments transformer #440401 or equivalent



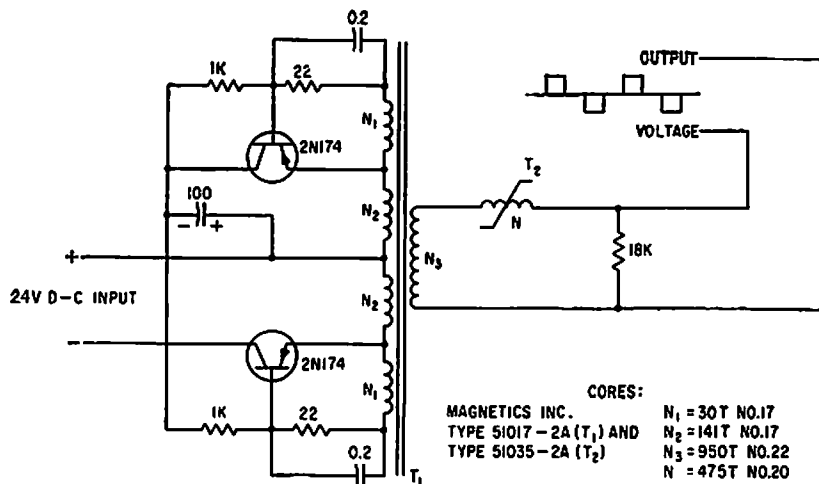
COLLECTOR CAPACITORS SPEED SWITCHING
 —Capacitors acting with 3-ohm resistors in collector circuits provide energy storage to increase switching speed.—A. G. Lloyd, Speed-Up Circuits Improve Switching of Transistor Inverters, *Electronics*, 34:45, p 92-94.



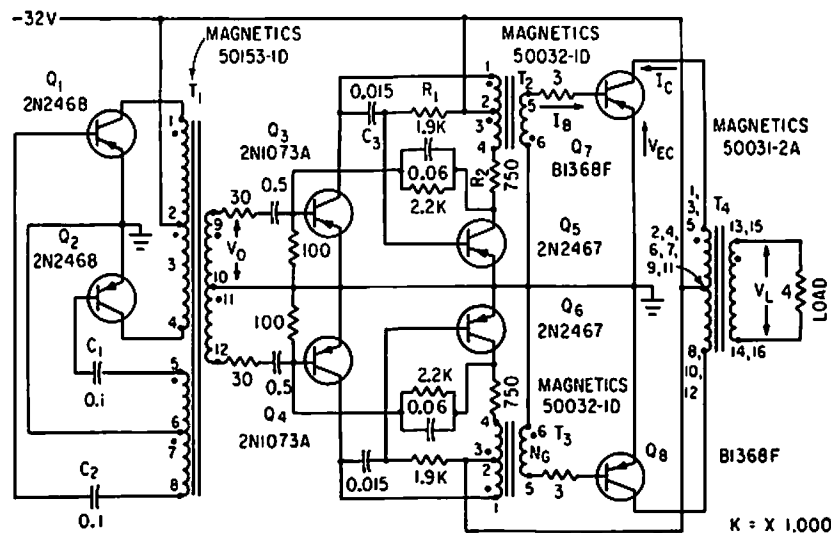
400-CPS INVERTER POWER AMPLIFIER—Has low internal impedance and low distortion, to provide good output voltage regulation

for d-c to 400-cps a-c inverter. Uses compounded common-collector output stage. Delivers 55 w at 400-cps with only 2.4% total

harmonic distortion.—R. Wileman, Linear Circuits Regulate Solid-State Inverter, *Electronics*, 33:16, p 61-63.

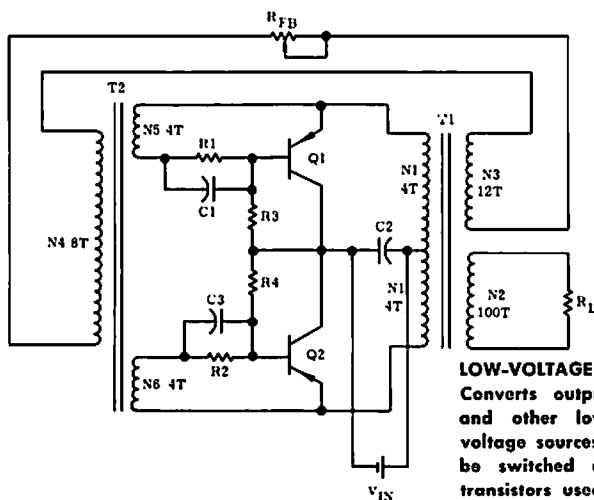


24 V D-C TO SQUARE-WAVE A-C—Will replace sine-wave source because square-wave output is modified by series saturable reactor to have same rms and average values as pure sine wave.—D. Levy, Replacing Sine Wave Sources with Solid-State Inverters, *Electronics*, 34:26, p 80-83.



100 W AT 50 KC—Square-wave oscillator Q1-Q2 drives monostable gates Q3-Q5 and Q4-Q6 in parallel, and gates in turn drive push-pull amplifier using 25-amp transistors

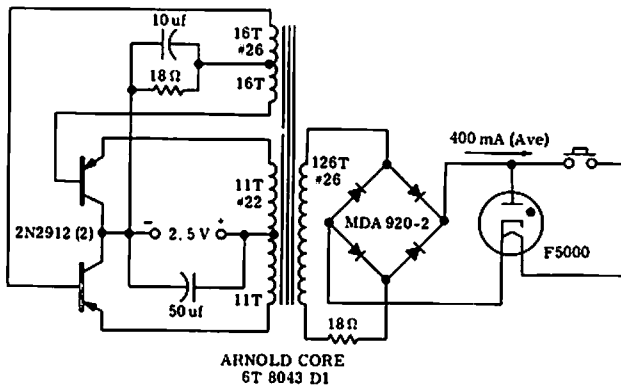
Q7-Q8 to deliver 100 w to load at 50 kc with overall efficiency of 70%.—S. L. Chin, New Circuit Design Raises Inverter Frequency Limits, *Electronics*, 35:43, p 59-60.



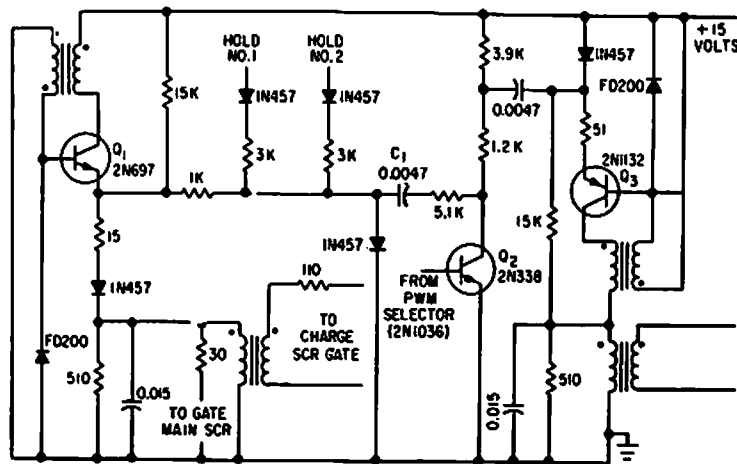
LOW-VOLTAGE, HIGH-CURRENT INVERTER—Converts output of solar cells, fuel cells, and other low-voltage sources to higher voltage sources. Currents up to 50 amp can be switched efficiently by 2N2728 power transistors used. Circuit shown provides a-c output which can easily be changed to d-c at

desired higher voltage by rectifying and filtering. Use of two transformers minimizes core losses when switching high collector currents. Efficiency is 70% at 60 w output.—J. Takesuye, "A Low Voltage High Current Converter," Motorola Application Note AN-169, Dec., 1965.

R ₁	0.75Ω, 5 w
R ₂	0.75Ω, 5 w
R ₃	7.5Ω, 5 w
R ₄	7.5Ω, 5 w
R _{FB}	1Ω, 5 w
C ₁	20 μf, 6 v
C ₂	10,000 μf, 6 v
C ₃	20 μf, 6 v
T ₁	Phoenix Transformer PX2127
T ₂	Phoenix Transformer PX2126
Q ₁ , Q ₂	2N2728
V _{IN}	2V 50A



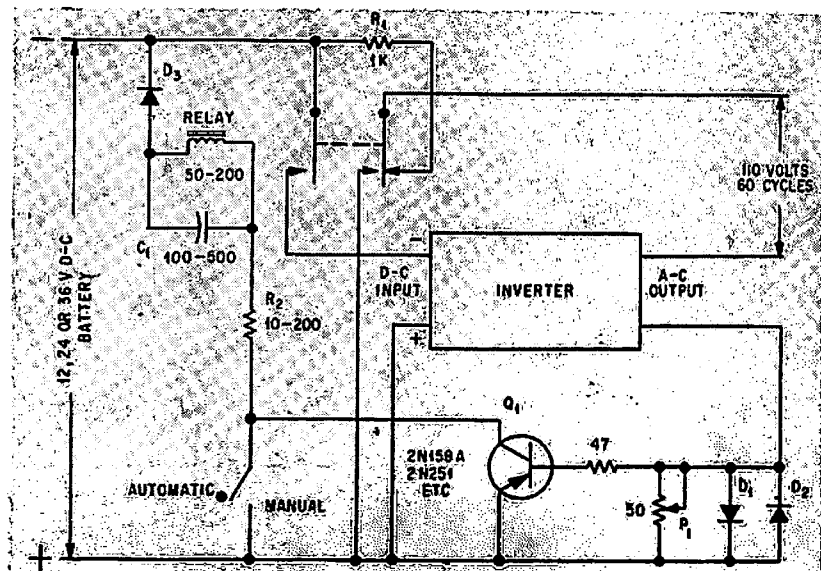
BLACK-LIGHT INVERTER—Operates from 2.5-v rechargeable battery and supplies 400 ma at 26 v to gas arc tube for portable ultra-violet lamp. Efficiency is 80%.—H. F. Weber, "Low Voltage Inverter Features High Frequency Operation with High Efficiency," Motorola Application Note AN-174, Feb. 1966.

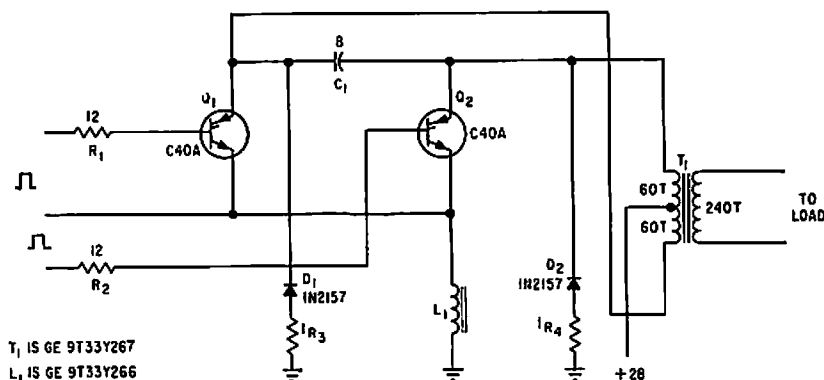


SCR DRIVER—Two types of blocking oscillators generate required turn-on and turn-off pulses for power-switching output stage of

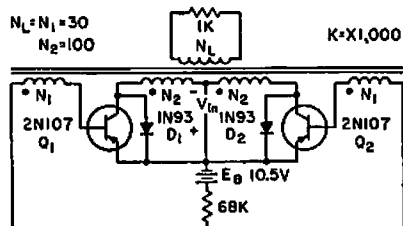
inverter.—R. J. Kearns and J. J. Rolfe, Three-Phase Static Inverters Power Space-Vehicle Equipment, *Electronics*, 34:18, p 70-73.

CONTROL CIRCUIT CUTS INVERTER IDLING CURRENT—Reduces standby current to less than 1 ma. Sensing element is pair of back-to-back silicon diodes, D1 and D2. Used when a-c power must be available on demand at many remote outlets even though actually used only few hours a day.—D. W. R. McKinley, Inverter Control Circuit Saves Power, *Electronics*, 34:31, p 56.

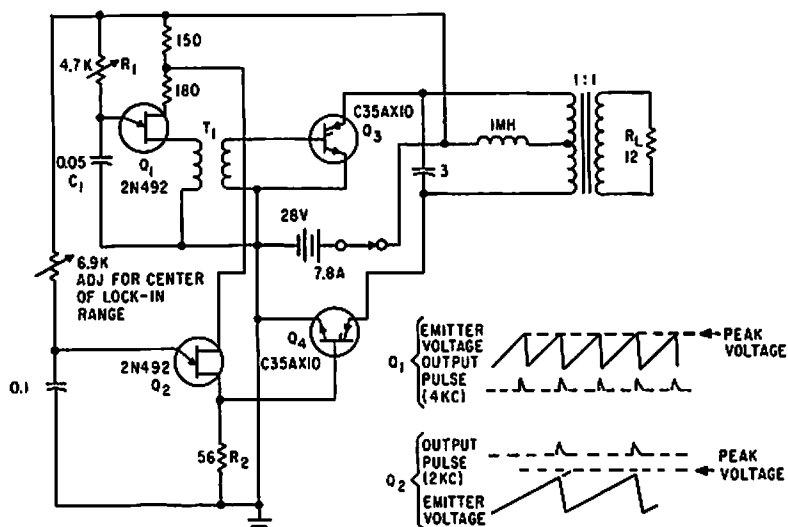




PARALLEL INVERTER FOR REACTIVE LOADS—Produces square-wave output under all load conditions, without creating high voltages across silicon controlled rectifiers during light loads.—D. V. Jones, Turn-Off Circuits for Controlled Rectifiers, *Electronics*, 33:32, p 52-55.



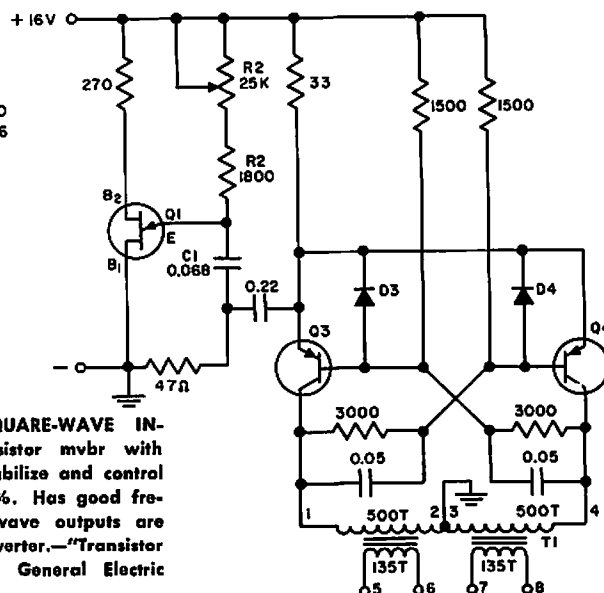
TRANSISTOR-MAGNETIC INVERTER—Signal conversion performance is comparable to that of electromechanical vibrator converters. For power conversion, can be substituted for dynamo in producing high voltage from low-voltage d-c power source.—C. H. R. Campling, Magnetic Inverter Uses Tubes or Transistors, *Electronics*, 31:11, p 158-161.



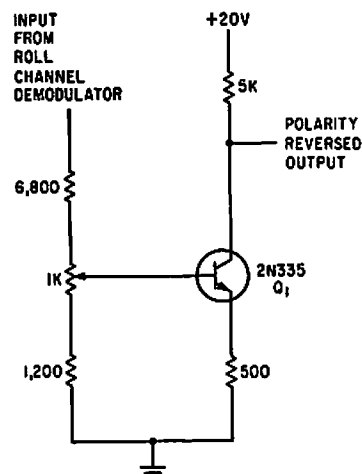
2-KC SCR INVERTER—Circuit shows parallel inverter, but unijunction relaxation oscillators Q1 and Q2 could also trigger series inverter, giving symmetrical operation. Q1 operates at

twice the frequency of Q2.—D. V. Jones, Turn-Off Circuits for Controlled Rectifiers, *Electronics*, 33:32, p 52-55.

Q1—GE 2N1671A OR USAF 2N490
 Q3 Q4—GE 2N526 OR JAN 2N526
 D3 D4—GE IN4009
 T1—GE 9T93Y1338



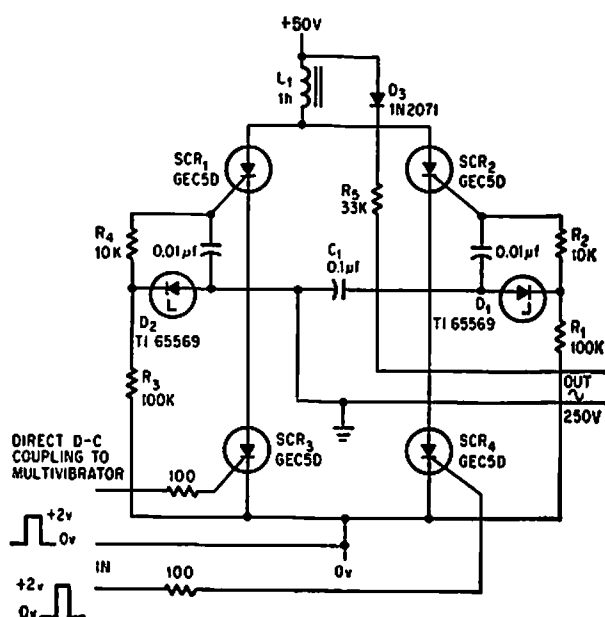
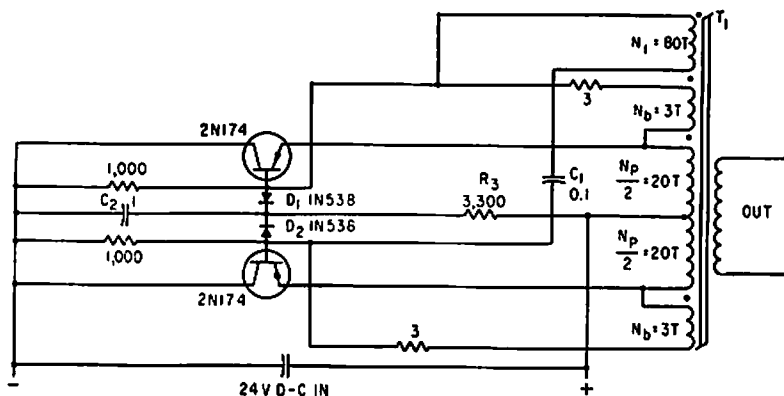
16-W 400-3,200 CPS SQUARE-WAVE INVERTER DRIVE—Uses transistor mvbr with unijunction transistor to stabilize and control frequency. Efficiency is 85%. Has good frequency stability. Square-wave outputs are used to drive d-c to a-c inverter.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 236.



UNITY-GAIN INVERTER—Provides for differential roll motion of missile autopilot.—J. H. Porter, Miniaturized Autopilot System for Missiles, *Electronics*, 33:43, p 60-64.

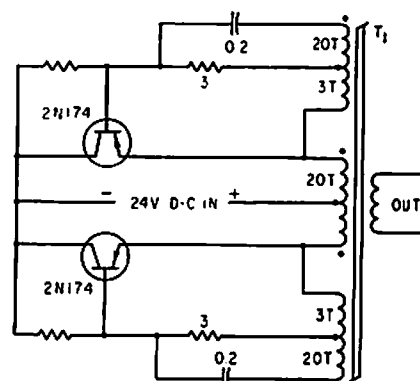
TRANSFORMER WINDING SPEEDS SWITCHING

—Addition of speed-up winding N_1 to conventional transformer arrangement of inverter, with C_1 in series with N_1 , increases switching speed. Clipping of resulting base-collector voltage spikes is provided by network D_1 - D_2 - C_2 - R_3 .—A. G. Lloyd, Speed-Up Circuits Improve Switching of Transistor Inverters, *Electronics*, 34:45, p 92-94.

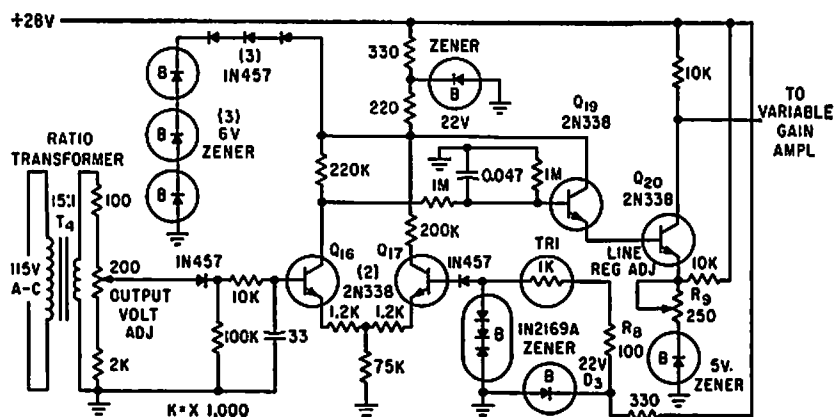


TRANSFORMERLESS SCR BRIDGE INVERTER—Slave-triggering of SCR1 and SCR2 with capacitive load cuts cost in half by eliminating costly gate transformers. Input of 800 pps (both half-cycles of 400-cps mvbr) gives

400-cps sine-wave output with peak amplitude of 350 v, because bridge switching inverts alternate pulses.—L. M. Tibbets, Scr Bridge Inverter Eliminates Transformers, *Electronics*, 39:18, p 98-99.

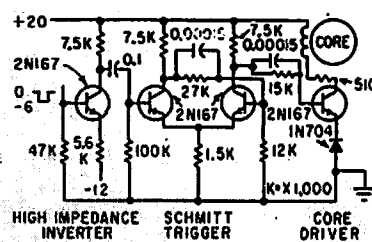


INVERTER BASE SPEED-UP WINDINGS—Individual speed-up windings and series capacitors for each transistor base reduce switching times to as little as 4 microsec for 2N174's and to 2 microsec for some germanium power transistors.—A. G. Lloyd, Speed-Up Circuits Improve Switching of Transistor Inverters, *Electronics*, 34:45, p 92-94.

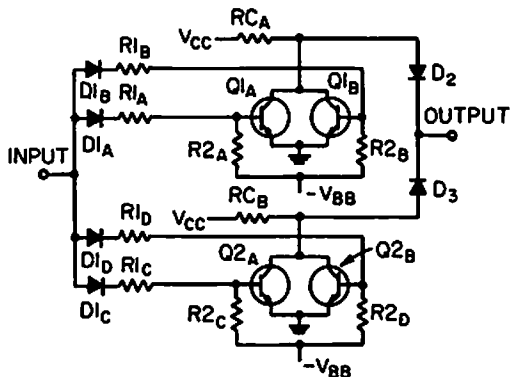


INVERTER REFERENCE ELEMENT—Accuracy and stability of d-c to 400-cps a-c inverter are achieved by temperature compensation of 1N2169A zener reference element. Silicon

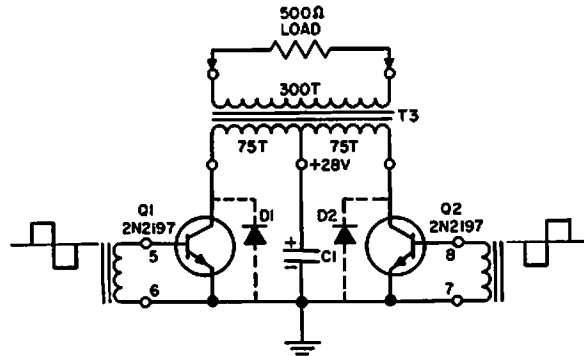
transistors are used where d-c levels are handled.—R. Wilman, Linear Circuits Regulate Solid-State Inverter, *Electronics*, 33:16, p 61-63.



PHASE INVERTER FOR CODE CONVERTER—In addition to phase reversal of input signal, single npn transistor provides isolation between negative input pulse and core driver, which is Schmitt trigger having discrimination level within 10% of -4.5 v including phase inverters. Signals below that level are disregarded. Signal range from 6 to 12 v will cause pulse output from Schmitt, with duration dependent on duration of input signal. When Schmitt trigger emits pulse, 1N704 driver supplies 30-ma current pulse, writing a ONE into its associated core.—R. Wasserman and W. Nutting, Solid-State Digital Code-to-Code Converter, *Electronics*, 32:50, p 60-63.

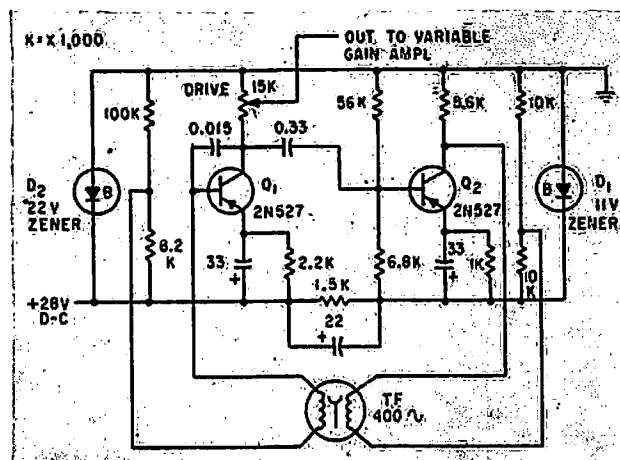


LOGICALLY REDUNDANT INVERTER—Symmetrical design with series-based diodes makes inverter independent of any single component failure.—T. Gelstein, *Reliable Circuits Through Redundancy*, *EEE*, 11:3, p 56-59.



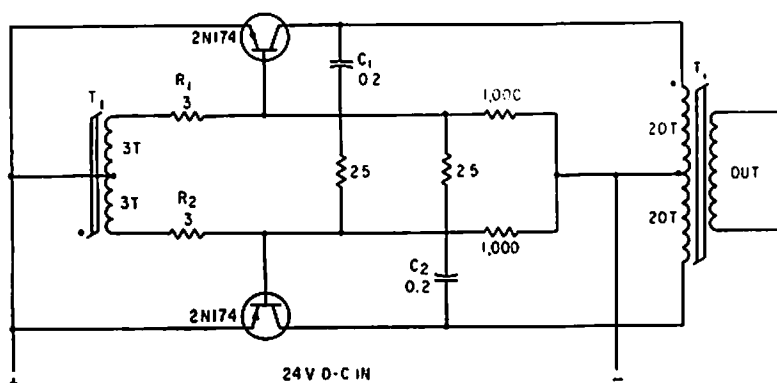
PARALLEL SQUARE-WAVE D-C TO A-C INVERTER—Receives square-wave inputs from separate drive circuit (not shown), causing Q1 to conduct half the time while Q2 is blocking, and vice-versa. Current from 28-v

supply flows alternately through halves of transformer primary, to produce 400-cps a-c voltage across load.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 235.

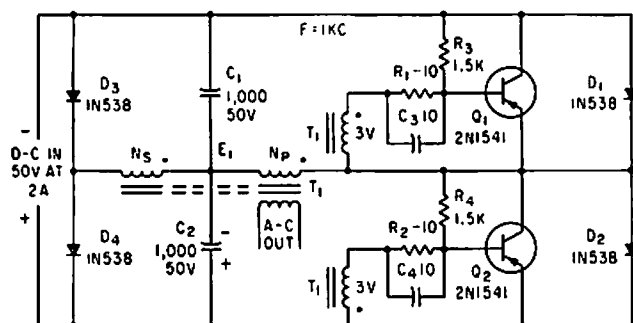


400-CPS FORK CONTROL—Tuning-fork oscillator gives frequency accuracy of 0.01% for d-c to a-c inverter. Consists essentially of two-stage amplifier, output of which is re-

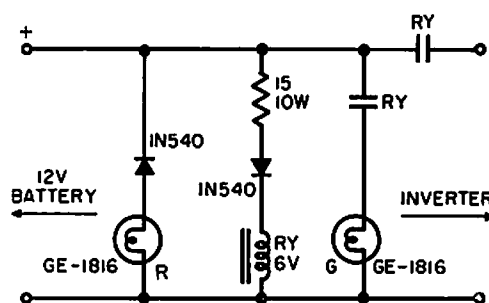
generatively coupled to input through tuning fork.—R. Wileman, *Linear Circuits Regulate Solid-State Inverter*, *Electronics*, 33:16, p 61-63.



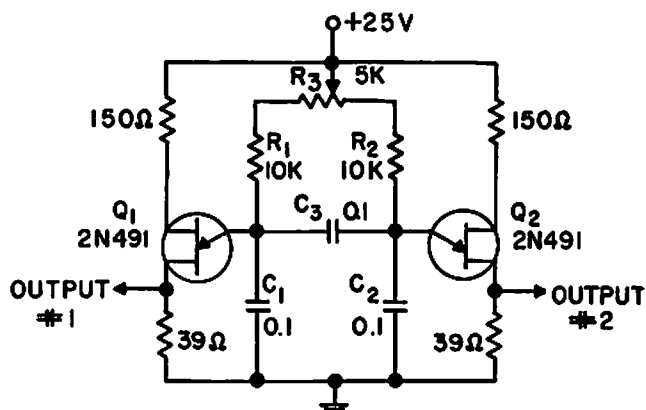
POSITIVE FEEDBACK BOOSTS SWITCHING SPEED—Base resistors R1 and R2 allow addition of cross-coupled positive-feedback capacitors C1 and C2 to increase high-frequency gain of feedback loop and provide energy storage to drive off transistor fully on when core saturates.—A. G. Lloyd, *Speed-Up Circuits Improve Switching of Transistor Inverters*, *Electronics*, 34:45, p 92-94.



SELF-EXCITED HALF-BRIDGE—Uses only two transistors. Suitable for both two-phase and three-phase applications. Basic circuits can be connected in series for high-voltage operation. Maximum transistor off voltage equals input voltage of half-bridge, making 80-v inverter practical for germanium transistors and 150 v for silicon.—A. G. Lloyd, Half-Bridge Inverter Provides Economical Three-Phase Power, *Electronics*, 34:37, p 62-65.

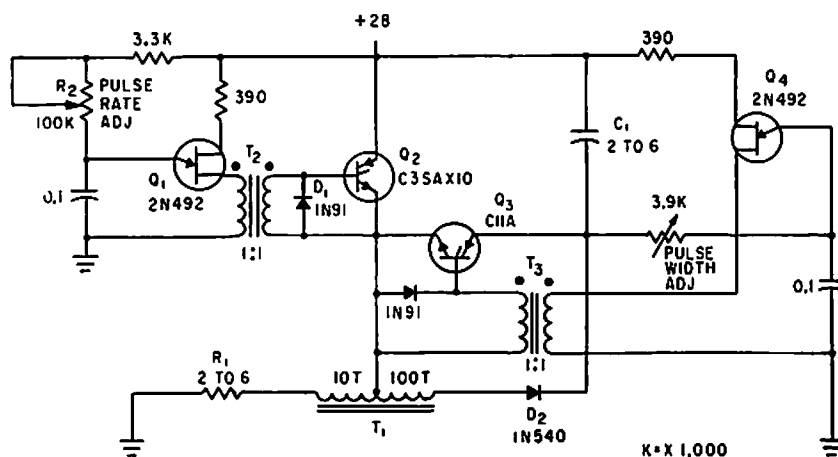


WRONG-POLARITY PROTECTION—Provides built-in protection of transistors in inverter from incorrect polarity of connection to 12-v battery. Used between inverter and battery. Green bulb G lights and relay operates for correct polarity. With wrong polarity, relay does not operate and red bulb R comes on to indicate error.—J. J. Pirch, Inverter Control, *EEE*, 11:3, p 44.

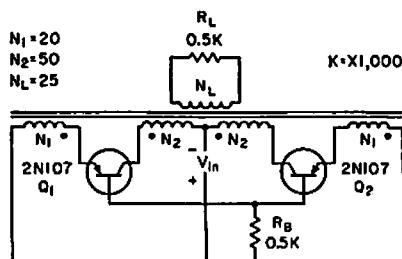
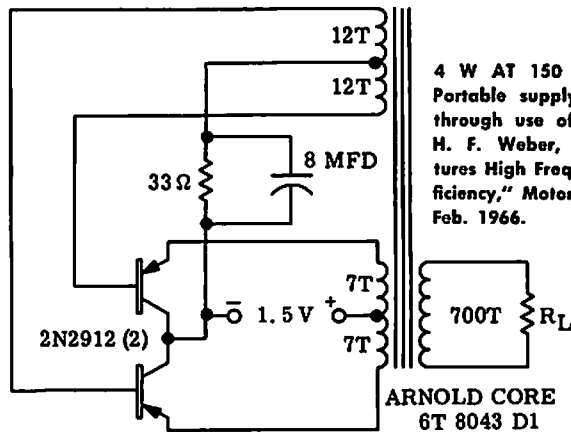


SYNC FOR SCR PARALLEL INVERTER—Double-output pulse generator uses two relaxation oscillators synchronized by C3 to produce high-energy pulses alternately from two separate sources, in correct timing sequence from instant that supply voltage is switched

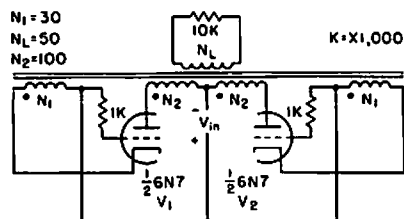
on. Synchronism is required to prevent inverter failure.—SCR Parallel Inverters in Correct Timing Sequence, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 66.



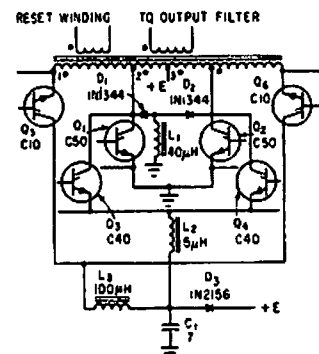
INVERTER WITH CAPACITOR TURNOFF—Load power factor variations do not affect turnoff time. Varying trigger rate of Q2 varies power delivered to R1. Trigger rate can be adjusted automatically for load regulation.—D. V. Jones, Turn-Off Circuits for Controlled Rectifiers, *Electronics*, 33:32, p 52-55.



DIFFERENTIAL-MVBR INVERTER—Magnetic in-
verter circuit with differentially connected
windings oscillates reliably without use of
current bias. Small spike in square-wave
output can be eliminated by connecting small
capacitor between collector and emitter of
each transistor.—C. H. R. Campling, *Magnetic
Inverter Uses Tubes or Transistors*, *Electronics*,
31:11, p 158-161.



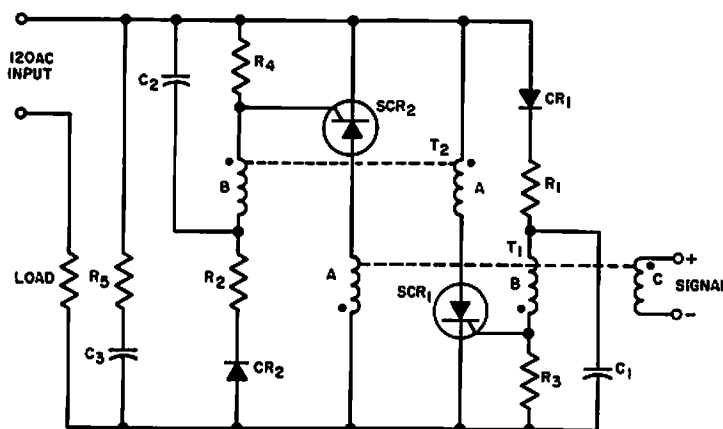
DUAL-TRIODE DIFFERENTIAL INVERTER—Uses
electron tubes as switching elements in place
of transistors. Although tubes are less effi-
cient, availability of a suitable combination
of voltage rating, current rating, and high-
speed switching capacity may make tubes
better than transistors in some signal or
power converter applications.—C. H. R. Cam-
pling, *Magnetic Inverter Uses Tubes or Tran-
sistors*, *Electronics*, 31:11, p 158-161.



THREE-PHASE OUTPUT STAGE—Scr's provide
power switching for static inverter designed
to develop 500 w of three-phase 115-v 400-
cps power from input of 22 to 29 v d-c.—
R. J. Kearns and J. J. Rolfo, *Three-Phase Static
Inverters Power Space-Vehicle Equipment*,
Electronics, 34:18, p 70-73.

CHAPTER 45

Latching Circuits

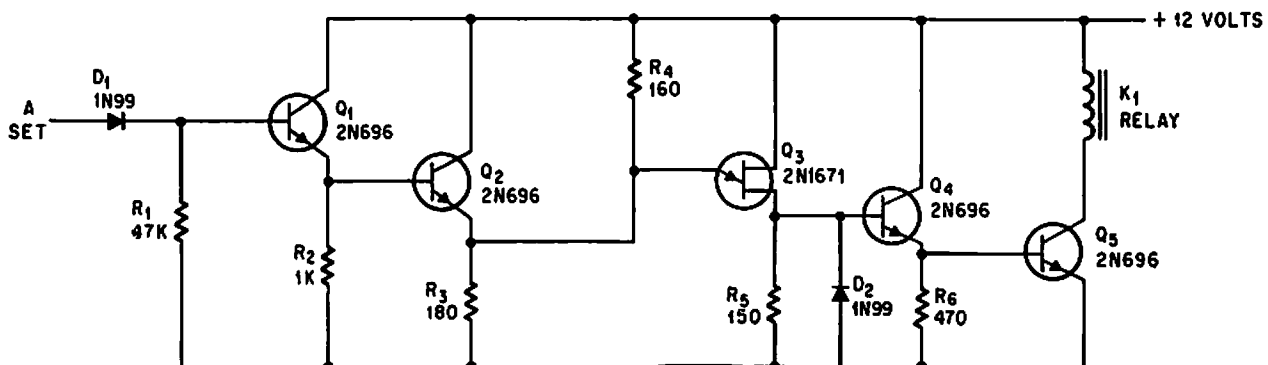


R_1, R_2 — 10K, 1 WATT
 R_3, R_4 — 10 OHMS, 1/2 WATT
 R_5 — 15 OHMS, 1 WATT
 C_1, C_2 — 0.25 MFD, 200 VOLTS
 C_3 — 0.5 MFD, 600 VOLTS
 SCR_1, SCR_2 — G-E C35B OR C11B
 CR_1, CR_2 — G-E IN1695

T_1 — MAGNETICS, INC ORTHONOL CORE
 #50007-1A
 A 5 TURNS — #14 AWG
 B 200 TURNS — #28 AWG
 C 100 TURNS — #28 AWG
 T_2 — MAGNETICS, INC ORTHONOL CORE
 #50007-1A
 A 5 TURNS — #14 AWG
 B 200 TURNS — #28 AWG

A-C STATIC LATCHING RELAY—Is equivalent to single-pole electromechanical latching relay with electrically isolated solenoid. Once turned on, circuit remains in conducting state even though line voltage is interrupted for

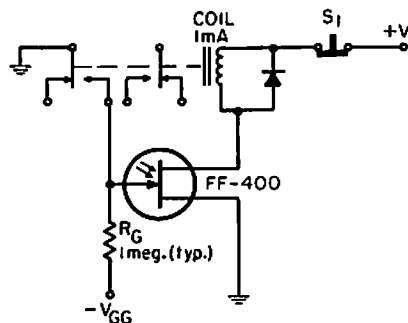
long periods of time. Positive reset action requires that minimum load current of 1 amp flow whenever circuit is closed.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 106.



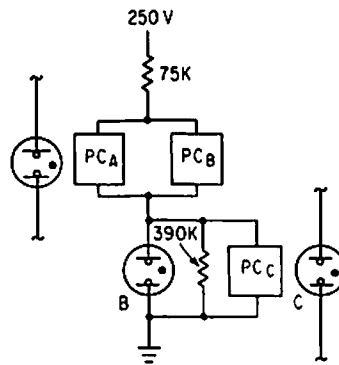
TRANSISTORS DRIVE RELAY—Relay latches on with +12 v set pulse at A, and is unlatched

by +12 v reset pulse at B.—S. E. Summer, Unijunction Transistor Latches Relay With

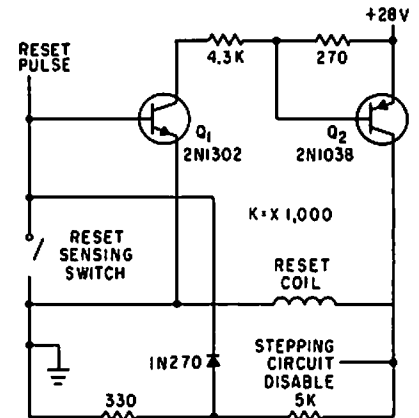
Short Pulses, *Electronics*, 38:9, p 62.



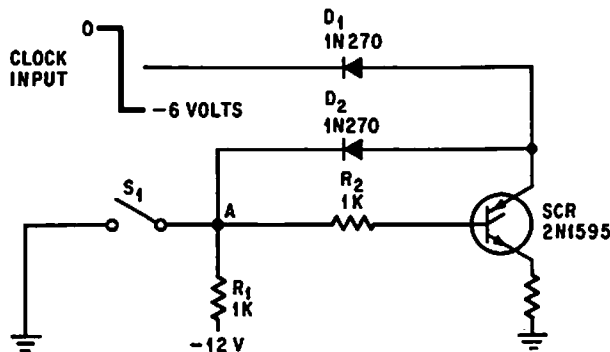
PHOTOELECTRIC LATCHING RELAY—Photo-sensitive fet serves as relay in light-activated smoke detectors, end-of-tape sensing in tape recorders, and light-activated alarms.—B. R. Smith, Light-Activated Latching Relay, *EEE*, 14:8, p 167.



NEON-PHOTOCONDUCTOR LATCHING CIRCUIT—Cadmium sulfide photoconductor PC and Ne2H neon lamps give low-cost latch. When neon C is energized to provide input to PCA, neon B remains on, independent of input A, due to feedback from neon B to PCB. Latch is reset by input to PCC.—J. L. Paterson, Will Neon Photoconductors Replace Relays in Low-Speed Logic?, *Electronics*, 36:18, p 46-49.

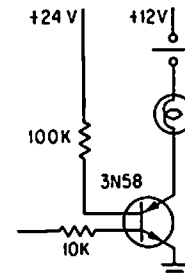


STEPPER RELAY RESET AND LATCH—Reset circuit deenergizes flip-flop that controls coils of stepper relay, and provides latching to keep reset coil energized until wiper senses reset contact.—F. W. Kear, Coils Operate Stepping Relay at Higher Speed, *Electronics*, 35:6, p 60-63.

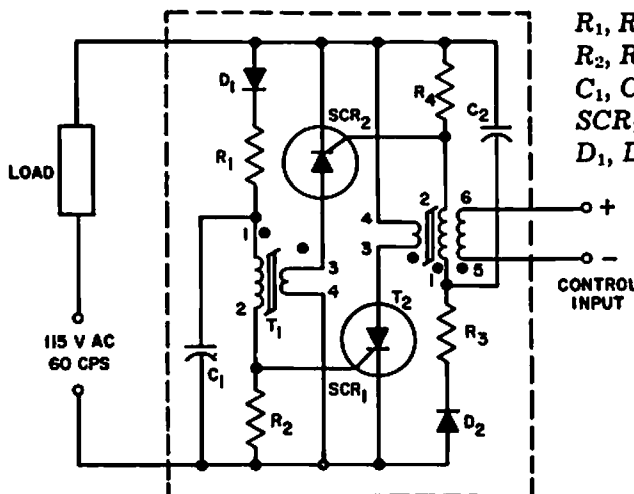


POSITIVE GATING OF CLOCK PULSES—Adding scr latch to diode gate allows output to follow clock input when S1 is closed. When S1 is open, output will be fixed at existing

clock level, without putting extra count into register.—R. A. Wilson, Latching Gate Removes Counter Ambiguity, *Electronics*, 39:7, p 91-92.



TRANSIENT-IMMUNE SCR LATCHING CIRCUIT—With 100,000-ohm resistor of silicon controlled switch returned to +24 v, latching circuit for lamp is immune to transient spikes of up to 12 v as well as to rate effect when turned off.—R. A. Stasior, How to Suppress Rate Effect in PNP Devices, *Electronics*, 37:2, p 30-33.

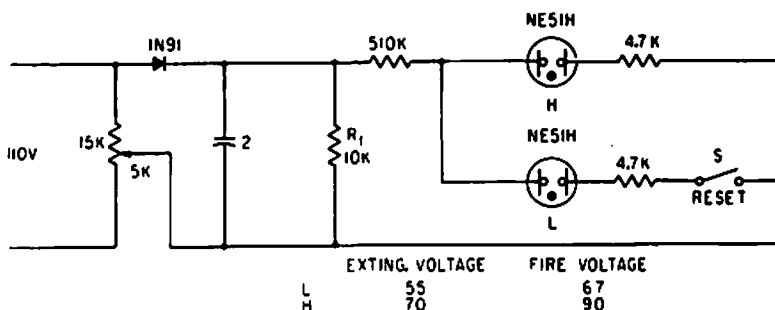
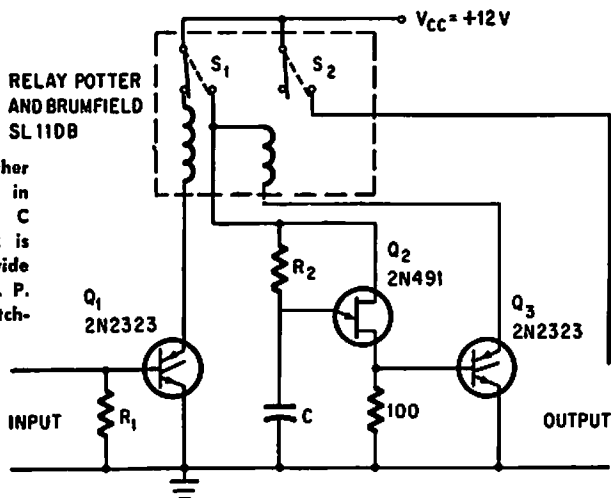


R_1, R_3 10 K, 1 W,
 R_2, R_4 , 10 ohms $\frac{1}{2}$ W,
 C_1, C_2 , 0.25 μ f,
 SCR_1, SCR_2 C10H or C35H
 D_1, D_2 1N1695

T_1, T_2 Magnetics Inc.
 Orthonol #50007-1A
 1-2 200 turns #28 AWG
 3-4 5 turns #14 AWG
 5-6 100 turns #28 AWG

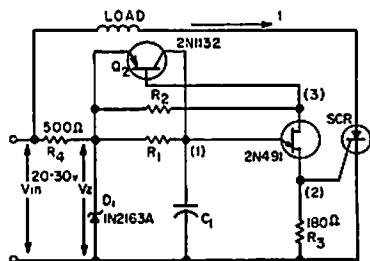
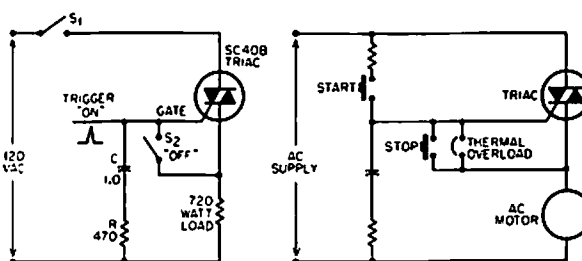
SCR A-C LATCHING RELAY—Relay is activated by d-c or a-c control current in single electrically isolated control winding. Can switch load power up to 1.7 kw. Uses magnetic firing circuit in which saturable core is not required to sustain gate voltage for full half-cycle, thereby permitting use of smaller core. Load current must be above 1 amp for conduction to be maintained.—Solid State Latching Relay, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 226.

TRANSIENT DETECTOR—Transient or other pulse at input turns on scr Q1, pulling in dpdt latching relay. Q2 then charges C through R2 until emitter voltage of Q2 is high enough to make it conduct and provide trigger pulse for Q3 to unlatch relay.—D. P. Lynch, Unijunction Transistor Turns off Latching Relay, *Electronics*, 38:23, p 109.



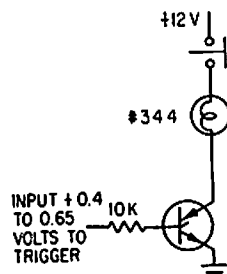
VOLTAGE DIP INDICATOR—Two neon lamps in parallel, with different striking voltages, form neon latch. After circuit is activated, next line voltage dip below preset level turns on lamp L. Circuit is then disabled until reset button is pressed.—T. D. Koranyo, Thyatron Monitors Line-Voltage Dips, *Electronics*, 34:1, p 126.

120 V A-C LATCH—Bidirectional controlled rectifier (G.E. Triac) replaces more complicated scr or power transistor circuits for static switching of a-c power circuits. Can be adapted to simulate action of magnetic starter for a-c motor. Momentary closing of start switch (in circuit at right) latches Triac on and starts motor.—F. W. Gutzwiller, Simplified 120 VAC Latching Circuit, *EEE*, 13:8, p 77.

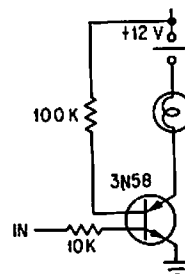


UJT LATCHUP FOR SCR's—Insures that scr will turn on properly when driving inductive load under control of unijunction transistor. Circuit action holds unijunction in saturation, causing scr gate voltage to be continuous rather than pulsating. Values of R1 and C1 are chosen to give desired time delay.—J. W. McNally, Unijunction Latchup for SCR's Driving Inductive Loads, *EEE*, 11:7, p 31.

SCR LATCHING CIRCUIT WITH RATE-EFFECT SUPPRESSION—With basic scr latching circuit (at left) for latching on lamp when input voltage level is exceeded, resetting of circuit by opening supply load exposes scr to fast transients and possible turn-on due to



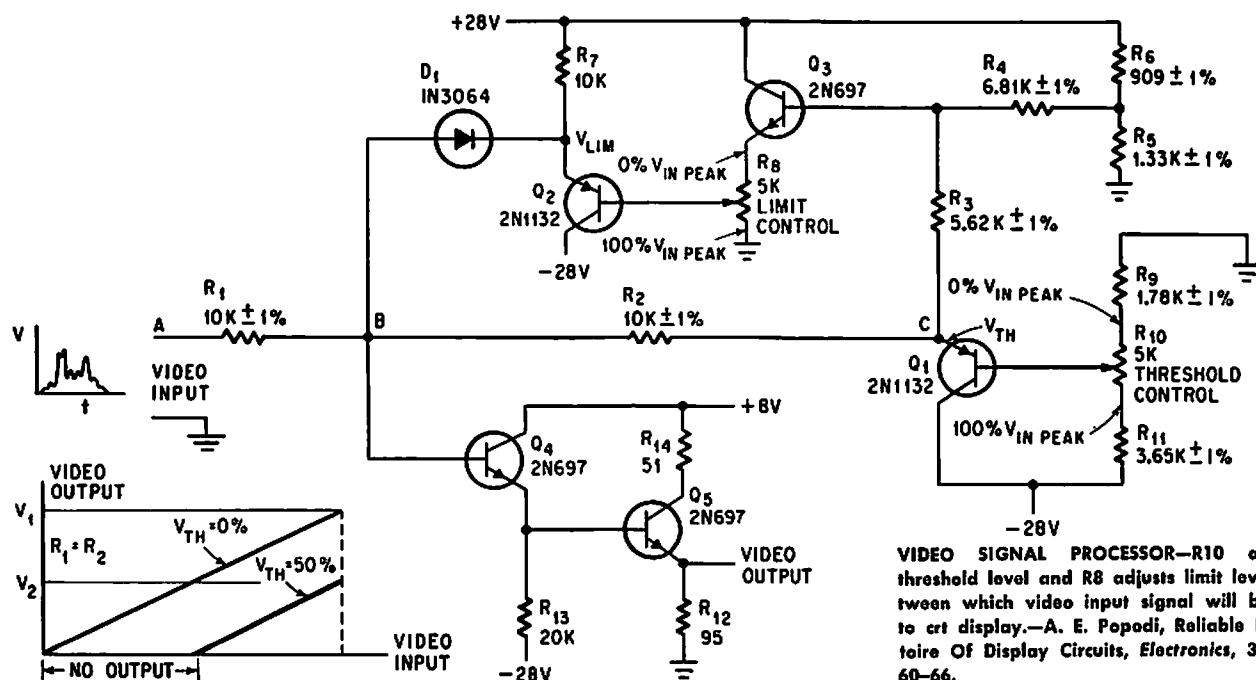
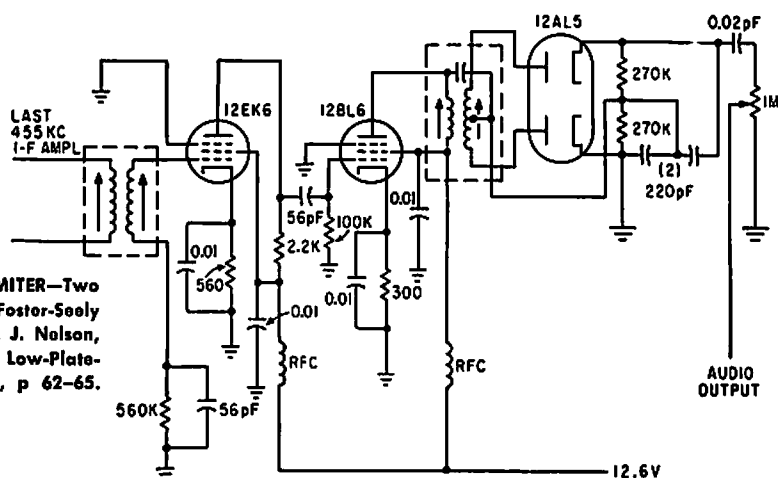
rate effect. Adding 100,000-ohm resistor and using four-terminal silicon controlled switch suppresses rate effect.—R. A. Stasior, How to Suppress Rate Effect in PNP Devices, *Electronics*, 37:2, p 30-33.



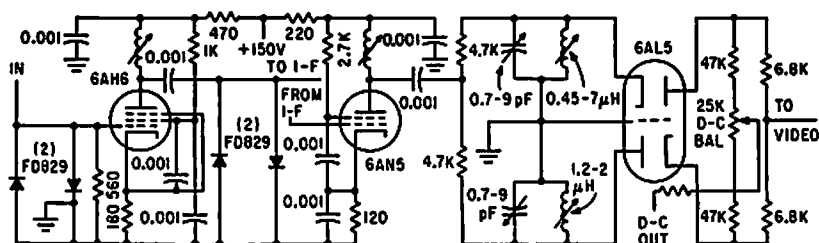
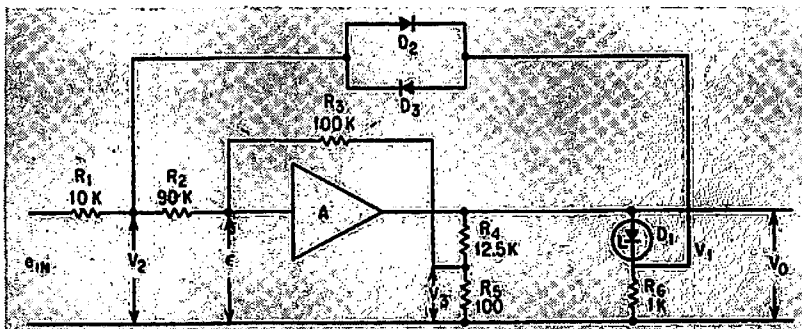
CHAPTER 46

Limiter Circuits

MOBILE DISCRIMINATOR AND LIMITER—Two limiter stages in cascade precede Foster-Seely discriminator.—C. Gonzalez and R. J. Nelson, Design of Mobile Receivers with Low-Plate-Potential Tubes, *Electronics*, 33:34, p 62-65.

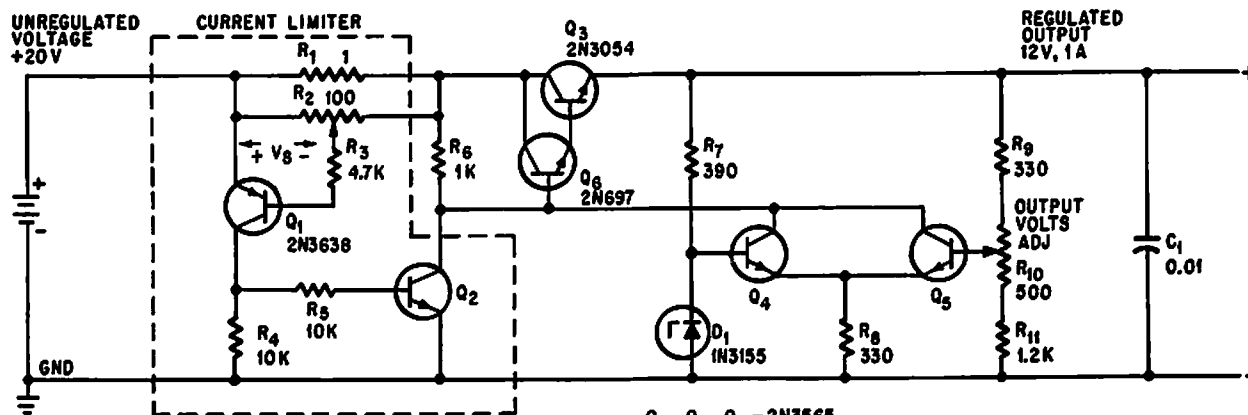
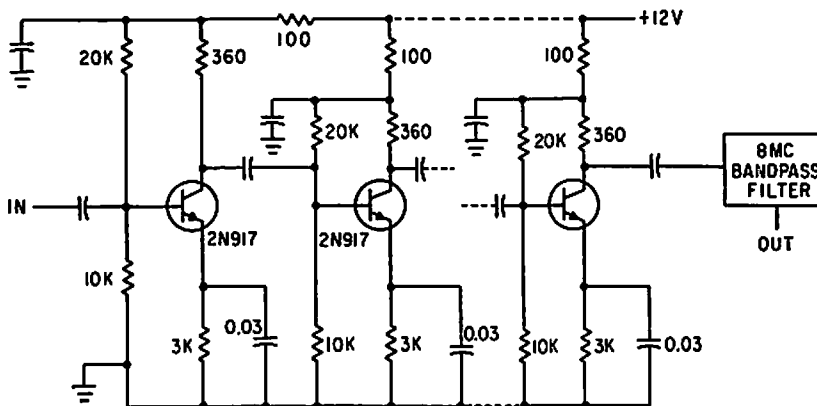


PREVENTS AMPLIFIER OVERLOAD—Zener 6.8-v diode shunts output while feedback diodes limit input. With input below 40 mv, output is below -5 v and diodes D1, D2, and D3 are biased off. When output exceeds -6.3 v, diodes act to clamp output at -6.8 v and maintain linear voltage relationships within the amplifier, preventing its saturation and allowing recovery from overloads. —J. V. Dirocco and J. W. Paghini, Low-Level Encoding Approach: Latest Details of Titan II Telemetry, *Electronics*, 35:47, p 36-39.



DIODE PAIRS—High-speed silicon diode pairs in two-stage limiter for telemetry, measuring, afc systems, and f-m systems give 5% linearity over 6-Mc bandwidth. Associated discriminator uses two single poles resistively coupled to driving tube.—High-Speed Diodes Make Limiting Smooth, *Electronics*, 35:27, p 80.

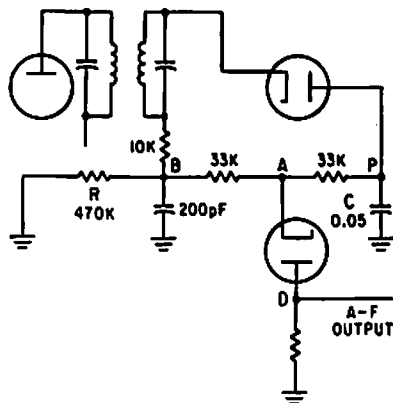
80 DB DYNAMIC RANGE AT 8 MC—Uses five identical cascaded stages with filter to restore sinusoidal waveform. Phase-shift variations are only 10°. Limits input signal by collector current cutoff only. Used in multi-channel-tracking receiving system.—S. P. W. Stranddorf, High-Frequency Limiter Amplifier Solves Phase-Shift Problems, *Electronics*, 35:46, p 44-45.



ADJUSTABLE CURRENT LIMITER—Q1 conducts when current exceeds limiting value determined by setting of R3, turning on Q2 and in

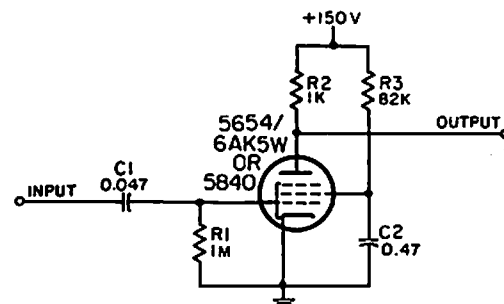
effect grounding base of Q6, to prevent significant current flow in Q3. Circuit resets automatically when overload is removed.—P.

Galluzzi, Adjustable Current Limiter for Regulated Power Supply, *Electronics*, 39:5, p 107.

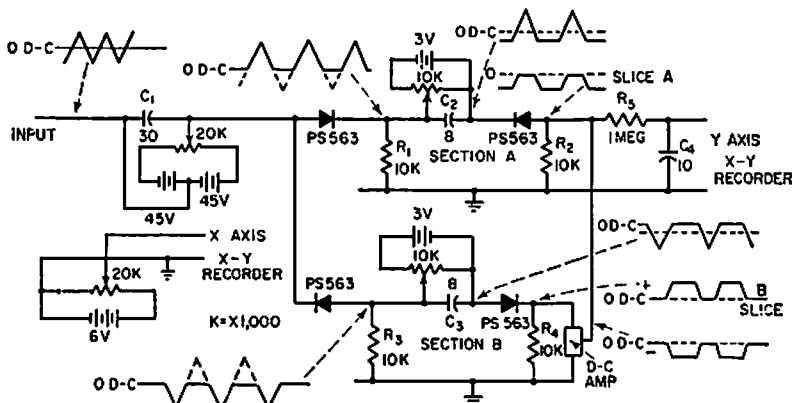


SUPPRESSING NOISES UP TO 1,000 TIMES SIGNAL LEVEL—Improved noise limiter for airborne transceiver uses large RC time constant. Plate of detecting diode is negatively charged by a-f signal, held steady by C.

For noise impulses, point A swings positively and limiter diode blocks rectified noise signal. —K. Makino and T. Yamanaka, Servo-Tuned Transceiver for Airborne VHF Communications, *Electronics*, 35:1, p 82-85.

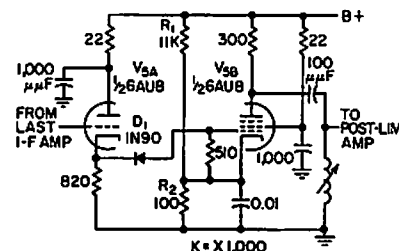


PREFERRED VIDEO LIMITER—Used to amplify and limit low-level video signals. Capable of handling very fast rise times. Maximum duty factor is 4%. Limiting level is within 35% of 4.8 v, depending on variations in tube and components.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 21, p 21-2.

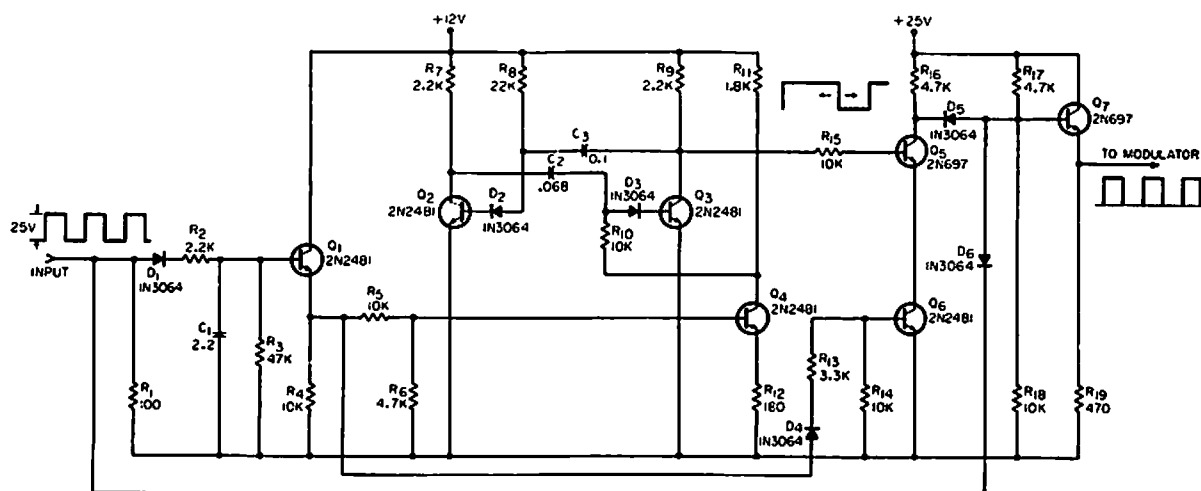


RADAR NOISE CLIPPER-LIMITER—Used in plotting amplitude-distribution density of noise and vibration signals over range of 1 to 10,000 cps. Section A samples d-c biased input signal between zero and positive half of slice width. Section B similarly handles

negative half. Output of B is inverted and biased in d-c amplifier to produce positive square wave. Recorder plots average of combined outputs from A and B sections.—D. J. Zoll, Simple Plotter Analyzes Radar Noise Rapidly, *Electronics*, 31:11, p 162-164.



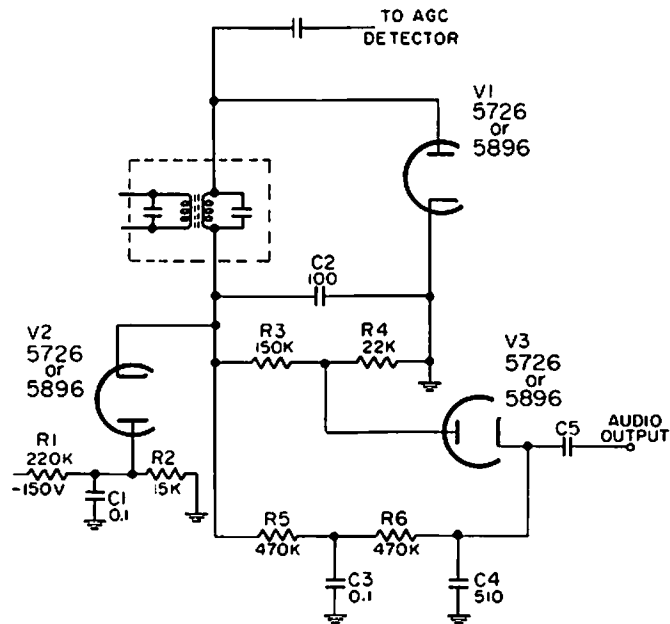
SYMMETRICAL LIMITER—Used in visual receiver of microwave relay. Signal is fed through triode cathode follower and diode-coupled to grid of pentode. D1 cuts off on positive r-f swing above d-c bias set by R1 and R2, to prevent grid of V5B from going positive and provide clipping on negative swing.—T. G. Custin and J. Smith, Relay System Duplexes Audio and Color Video, *Electronics*, 31:25, p 64-67.



DUTY-CYCLE LIMITER—When duty cycle exceeds 1%, countdown begins and duty cycle

is held at about 1%. Uses voltage-controlled astable mvbr consisting of Q2, Q3, and Q4,

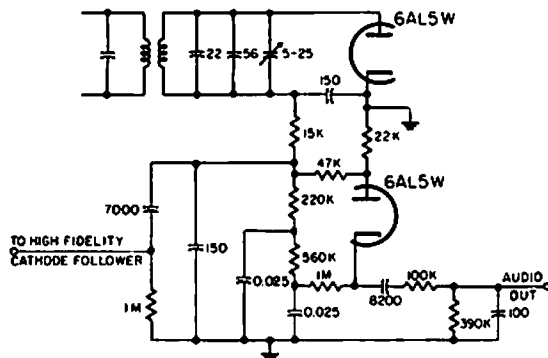
which runs unsynchronized with input prf.—C. Samocki, Duty-Cycle Limiter, *EEE*, 13:9, p 76.



PREFERRED DETECTOR AND NOISE LIMITER—
Used in a-m receivers to demodulate i-f out-
put and reduce effect of short-duration elec-

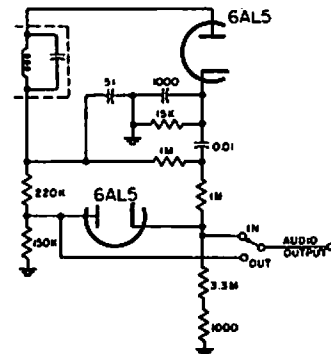
trical disturbances or impulse interference. Audio output is 20 to 150 mv rms, and upper 3-db frequency is 7,000 cps.—NBS, "Hand-

book Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 62, p 62-2.

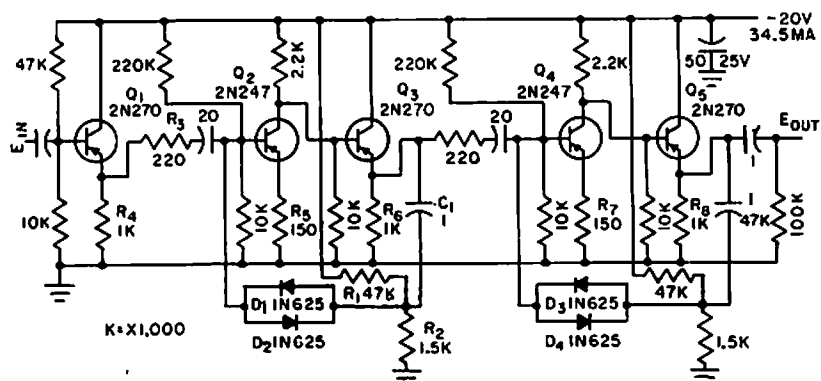


AUDIO DETECTOR WITH NOISE LIMITER—Conventional 6AL5 series noise limiter and diode detector are here augmented by elaborate tone control network.—NBS, "Handbook

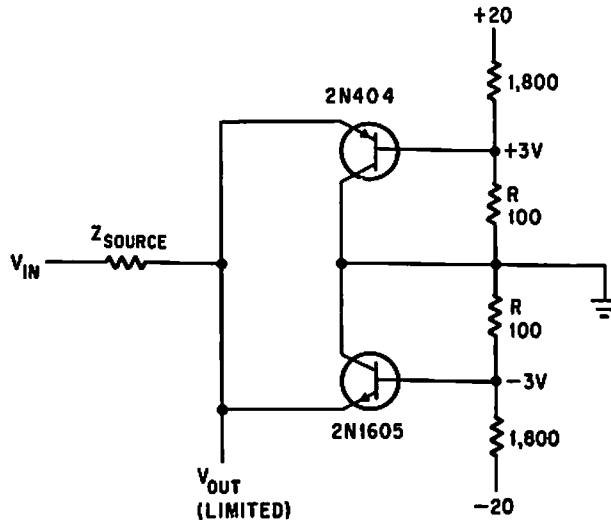
Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N12-1.



AUDIO DETECTOR WITH NOISE LIMITER—Uses conventional 6AL5 series noise limiter arrangement.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N12-1.

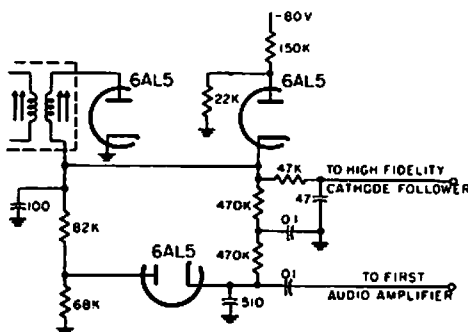


FAST-ACTING LIMITER—Provides compression of sine or square waves with minimum of phase distortion. Feedback paths in two direct-coupled transistor pairs improve limiting action of 100-kc pulse amplifier in which signals under 5 mv get 35 db of gain, with gain dropping to unity as signal strength increases to peak of 400 mv.—L. H. Dulberger, *Pulse Amplifier with Nonlinear Feedback*, *Electronics*, 31:45, p 86-87.

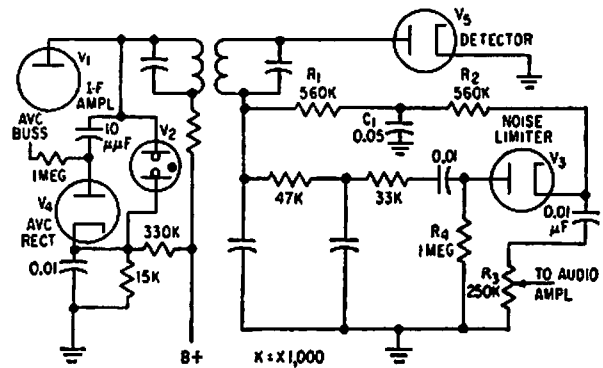


3-V BIPOLAR LIMITER—Upper transistor conducts when positive input pulse exceeds 3 v, and lower transistor shunts excess current to ground similarly for negative inputs above 3

v, to keep output at 3 v for both polarities. Circuit provides own reference voltage.—S. B. Gray, Bipolar Limiter Reduces D-C Loss, *Electronics*, 38:24, p 65.



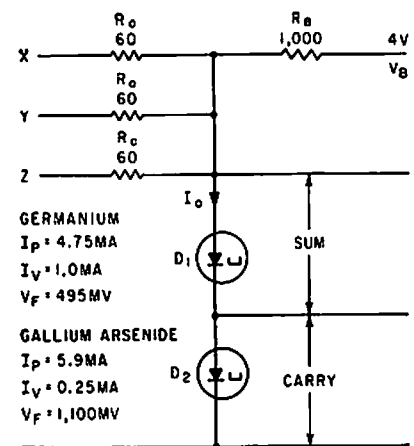
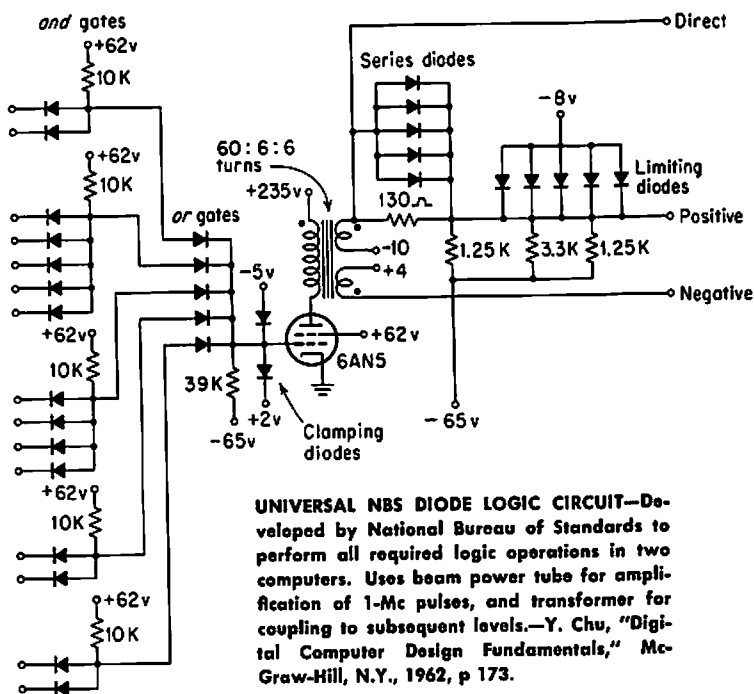
AUDIO DETECTOR WITH NOISE LIMITER—Uses shunt limiter in conjunction with series limiter, so noise pulses are prevented from operating agc circuit and thus desensitizing i-f amplifier of communication receiver. Broad-band cathode follower is connected to output of diode detector.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N12-1.



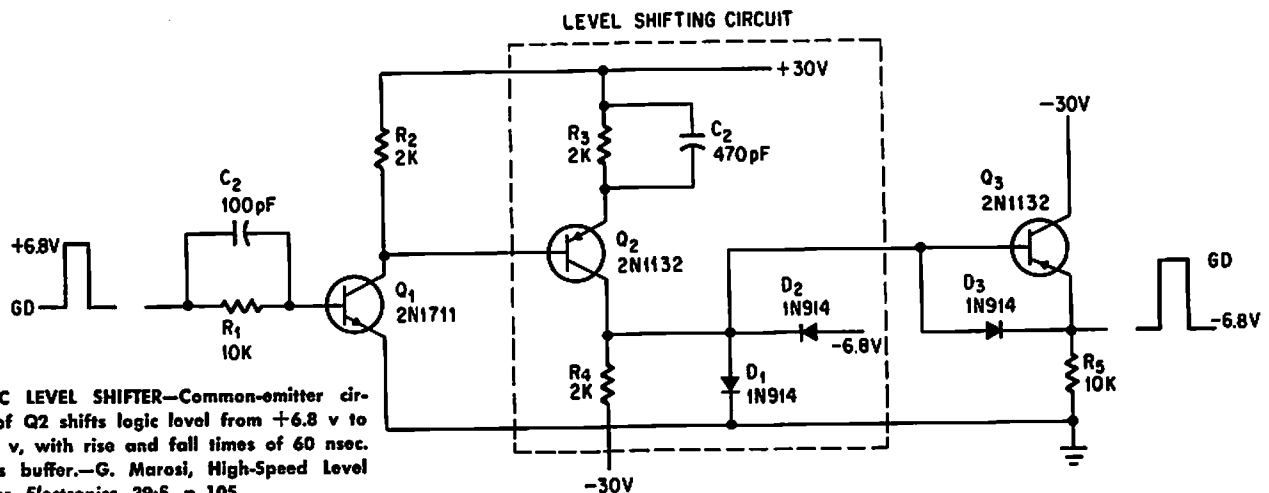
CLASS-D CB NOISE LIMITER—Two-step noise silencing system is almost essential for 27-Mc a-m receiver, because of ignition interference. Large noise pulse swings plate of V3 negative and cuts off diode V3, to prevent noise pulse and audio signal from reaching volume control R3. Circuit recovers quickly, allowing audio signals to pass.—L. G. Sands, Citizens Radio Revision Spurs Equipment Design, *Electronics*, 32:15, p 55-57.

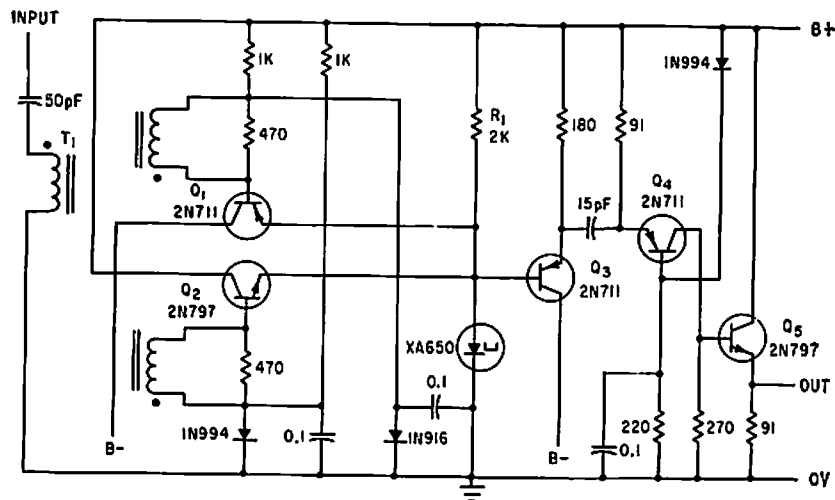
CHAPTER 47

Logic Circuits



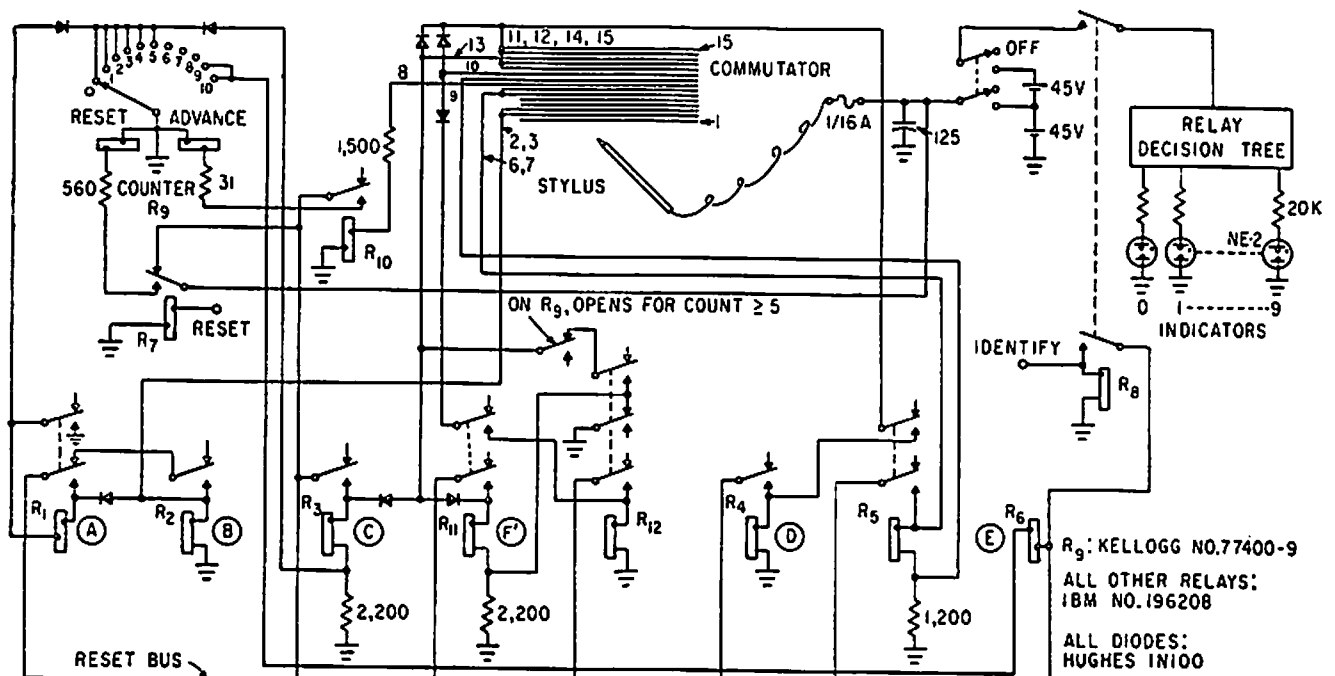
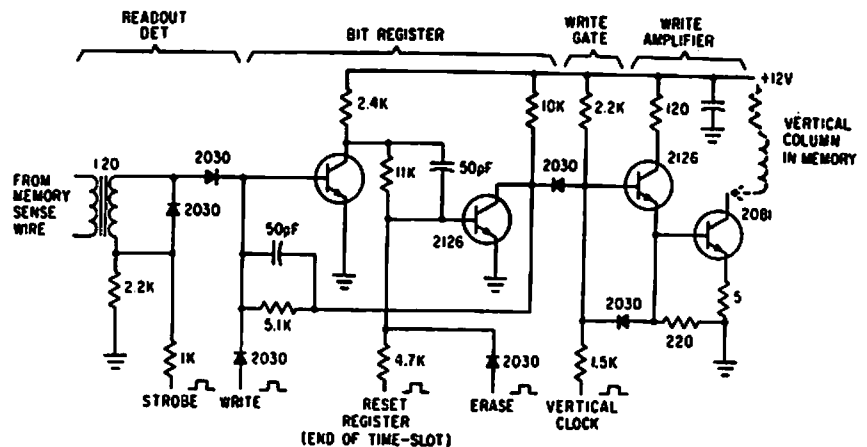
FULL BINARY ADDER—Two tunnel diodes in series perform arithmetic function of full addition.—B. Rabinovici and J. Klapper, Designing Tunnel-Diode Circuits Using Composite Characteristics, *Electronics*, 35:7, p 46-48.





HIGH-SPEED TUNNEL-DIODE BINARY—Tunnel diode with Q1-Q2, driven by series of positive or negative pulses at input repetition rates up to 140 Mc, can provide pulses capable of triggering successive pulse amplifier stages Q3, Q4, and Q5.—W. V. Harrison and R. S. Foote, Tunnel Diodes Increase Digital-Circuit Switching Speeds, *Electronics*, 34:32, p 154-156.

READ-WRITE AMPLIFIER—Each of 28 vertical circuits for coincident-flux memory consists of readout detector, bit register (flip-flop), write gate, and two-stage write amplifier.—H. F. Priebe, Jr., Three-Hole Cores for Coincident-Flux Memory, *Electronics*, 33:31, p 94-97.

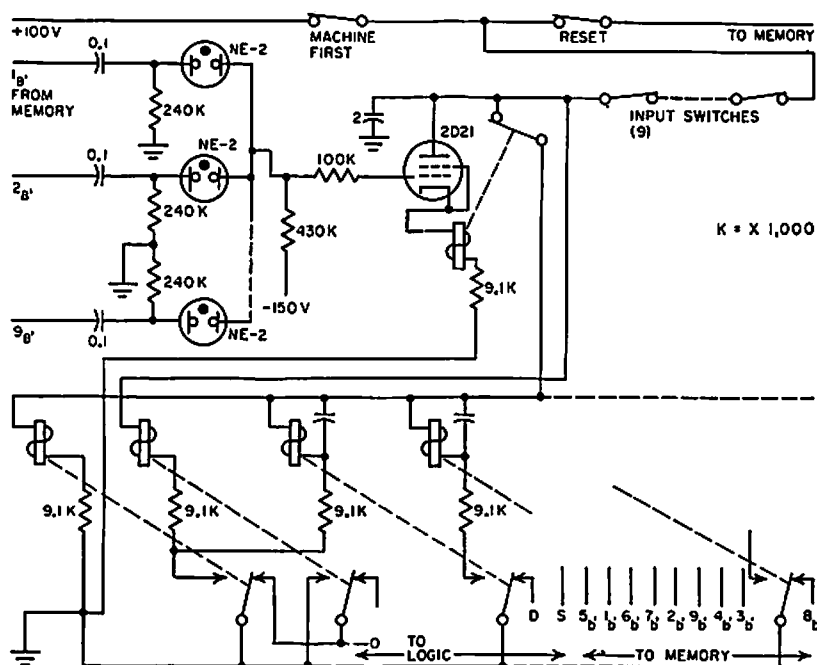
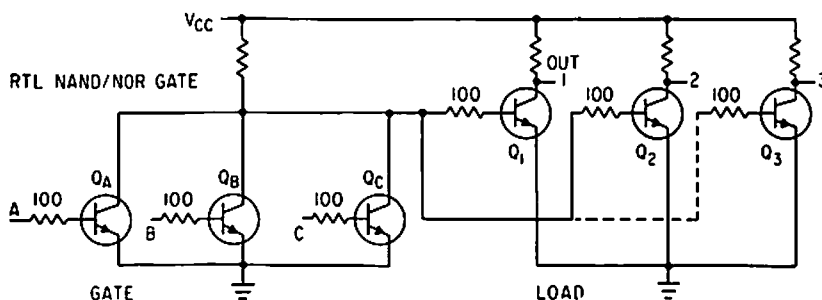


HANDWRITING READER—Spelled-out digits written with wire stylus on striated conductive surface are recognized by detecting risers, descenders, dots, word length, re-cross-

ings, and several other characteristics of spelled-out zero to nine, using only 12 relays, 8 diodes, and 10 neon indicator lamps. Accuracy is about 97% with the simple se-

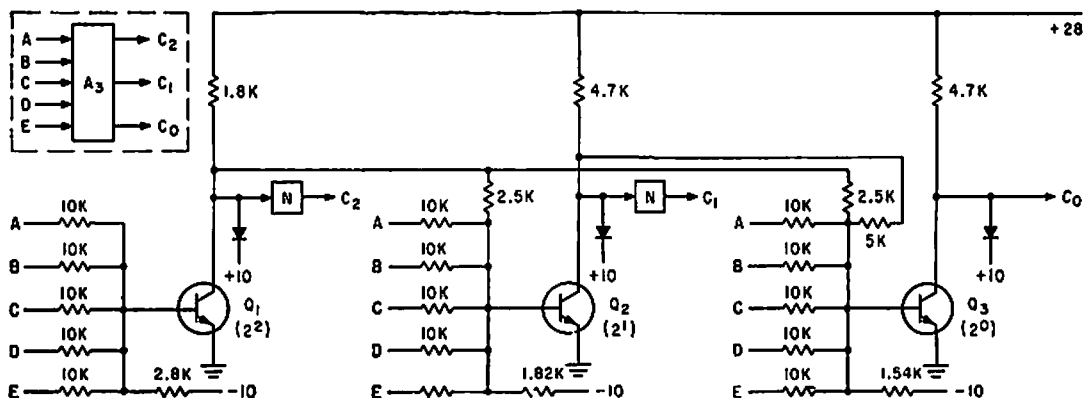
quential logic used for recognition.—L. D. Harmon, Handwriting Reader Recognizes Whole Words, *Electronics*, 35:34, p 29-30.

RESISTOR-TRANSISTOR NAND/NOR GATE—For integrated circuits, 100-ohm resistor in base load of each transistor reduces waste current, increases fan-out, and gives logic swing of 1 v.—A. E. Skoures, *Choosing Logic for Microelectronics*, *Electronics*, 36:40, p 23-26.



TICK-TACK-TOE LOGIC—Neon lamps serve as diode gates and indicate positions and moves on game board. Thyratron-relay combination serves as memory, while relays re-

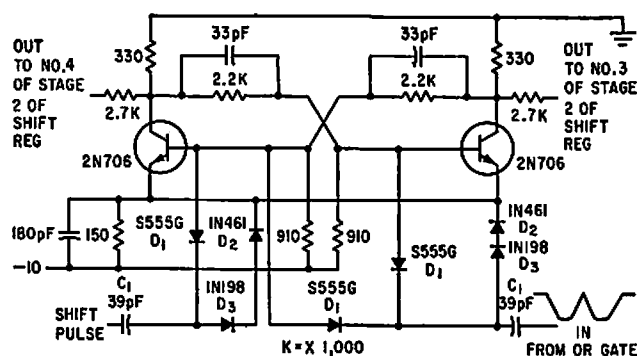
free sequence to prevent two successive moves by either player.—C. E. Hendrix and R. B. Purcell, *Neon Lamp Logic Gates Play Tick-Tack-Toe*, *Electronics*, 31:25, p 68-69.



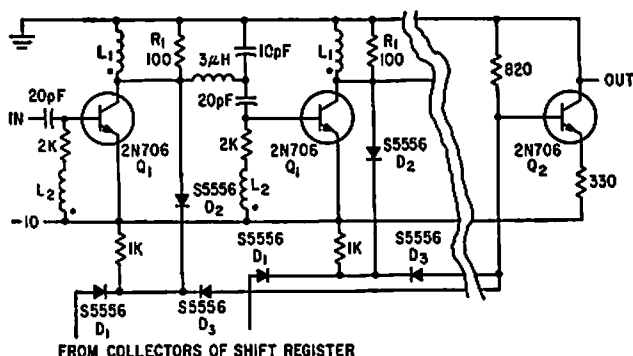
THREE-STAGE ENCODER—With 2N1499 transistors, settling time of encoder for simultaneous multiplier is less than 0.4 microsec, and

maximum time to produce 8-bit product is about 1.2 microsec.—S. C. Chao, *High Speed Encoding with Resistor-Transistor-Logic*

Circuits, Electronics, 35:6, p 48-51.

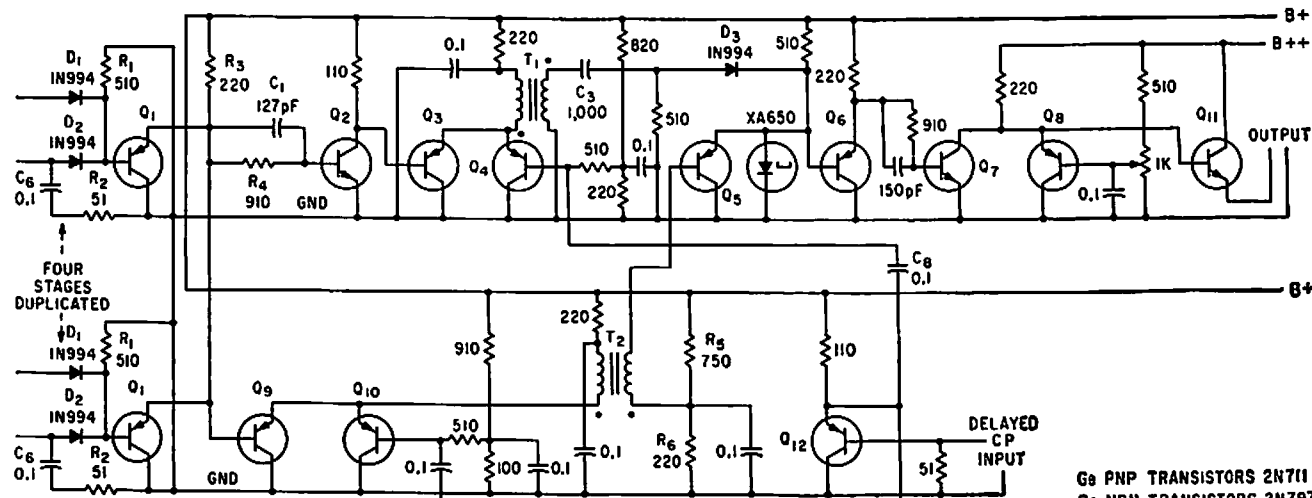
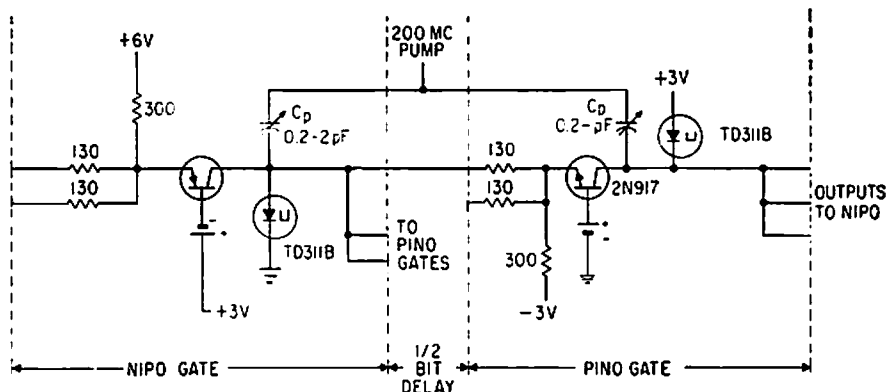


HIGH-SPEED FLIP-FLOP—Used in producing complex pulse sequences up to 4 billion bits in length. Drives n-stage shift generator that provides modulo-2 additions.—B. K. Erickson and J. D. Schmidt, *Random Pulse Generator Tests Circuits, Encodes Messages, Electronics*, 34:25, p 56-59.



BLOCKING OSCILLATOR FOR SHIFT REGISTER—Used to generate series of ten pulses, 20 nsec wide and spaced 40 nsec apart. Each of the ten blocking oscillator stages Q1 is allowed to overshoot and trigger the following stage through an LC coupling network that provides additional delay.—B. K. Erickson and J. D. Schmidt, *Random Pulse Generator Tests Circuits, Encodes Messages, Electronics*, 34:25, p 56-59.

PUMPED TUNNEL-DIODE TRANSISTOR LOGIC GATE—Nipo gate accepts negative inputs and provides positive outputs, while pino gate accepts positive inputs and provides negative outputs. With no inputs, 200-Mc pump or clock has sufficient amplitude to fire nipo stage tunnel-diode on positive half-cycle and pino stage tunnel-diode on negative half-cycle. When input signal is present, pump cannot fire that tunnel diode; this is basic *nor* gate action, with output pulse only when there is no input.—E. Gottlieb and J. Giorgis, *Tunnel-Diode Switching Circuits, Electronics*, 36:27, p 26-31.

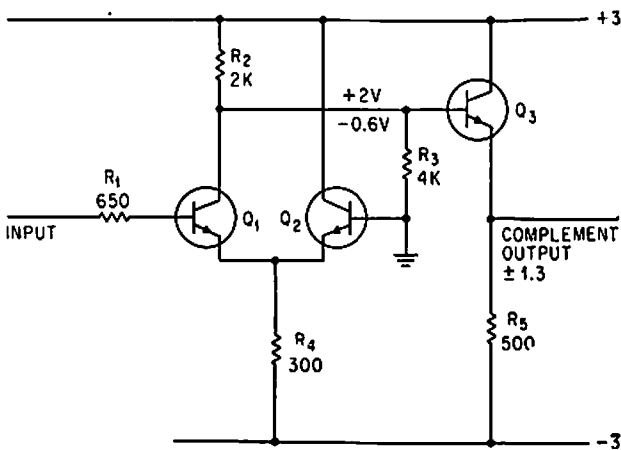
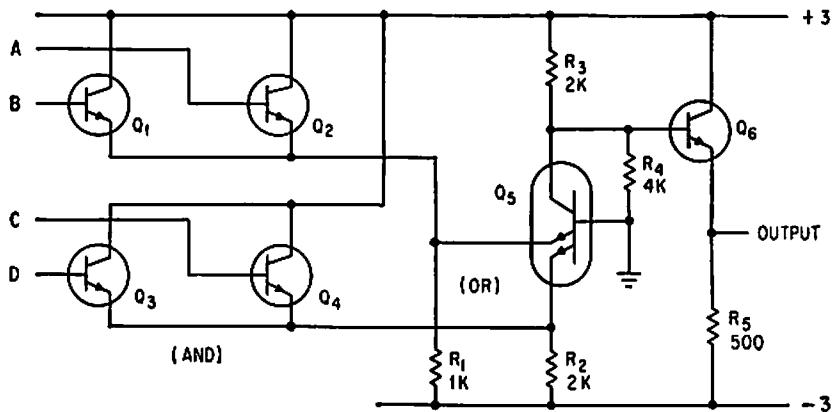


TUNNEL-DIODE OR CIRCUIT AND ENVELOPE GENERATOR—Used as part of program pulse generator incorporating ring of four stages, diode-matrix with ring of three stages to

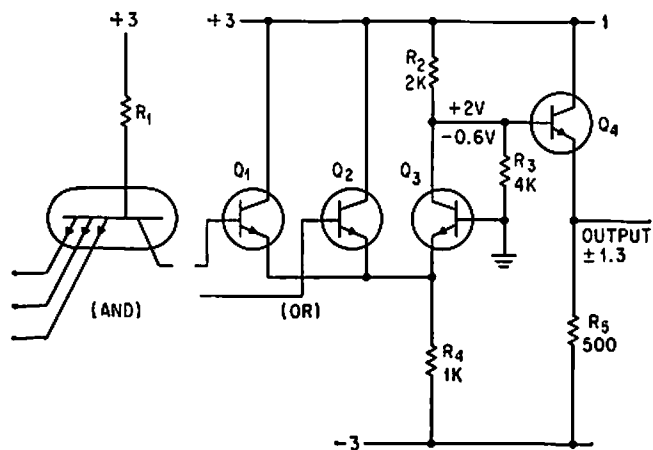
provide twelve-bit words at 30-Mc clock rate. C6 and R2 are a-c terminations for coax from output of ring counter.—W. V. Harrison

and R. S. Foote, *Tunnel Diodes Increase Digital-Circuit Switching Speeds, Electronics*, 34:32, p 154-156.

MULTIEMITTER TRANSISTOR ALTERNATES BETWEEN AND/OR LOGIC—Circuit performs and function first, then or function. For integrated-circuit construction, few isolated lands are needed.—P. M. Thompson, *Logic Principles for Multi-emitter Transistors*, *Electronics*, 36:37, p 25-29.

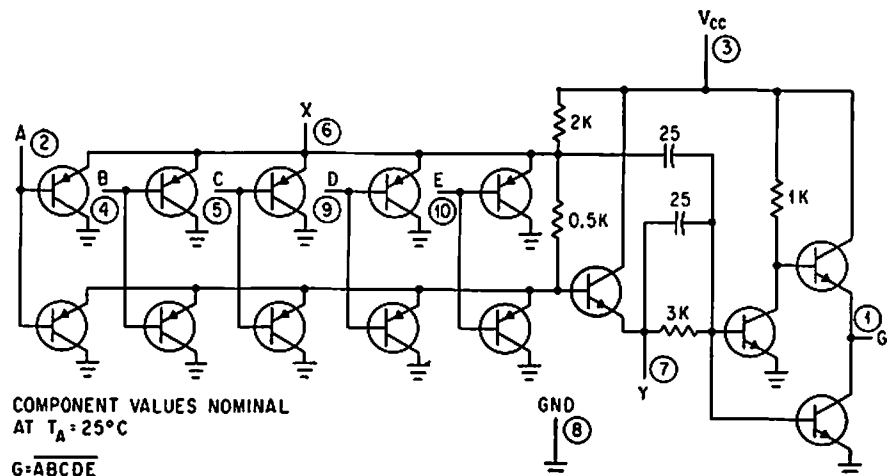


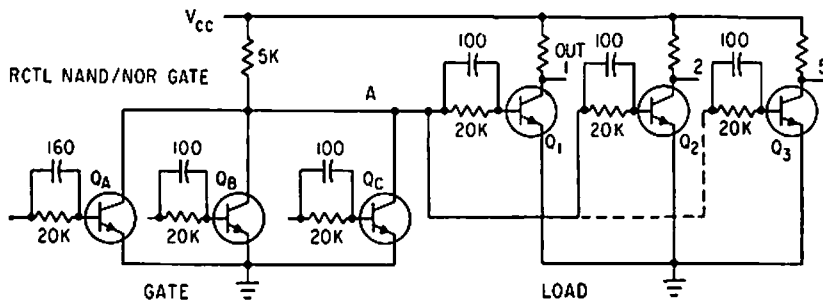
INVERTING AMPLIFIER GIVES COMPLEMENT OUTPUT—Only one transistor has isolated collector, so only three isolated lands are needed for integrated-circuit construction.—P. M. Thompson, *Logic Principles for Multi-emitter Transistors*, *Electronics*, 36:37, p 25-29.



MULTIEMITTER-TRANSISTOR AND/OR LOGIC—Circuits may be coupled either directly or by multi-emitter transistor and gates. Components show promise for integrated circuits.—P. M. Thompson, *Logic Principles for Multi-emitter Transistors*, *Electronics*, 36:37, p 25-29.

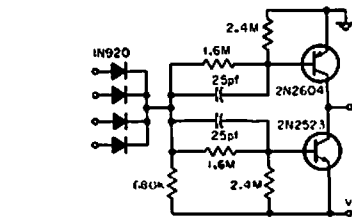
NAND LOGIC GATE—And/or gate using pnp input transistors and npn output transistors is followed by three-transistor inverting output stage. Gate has fan-in of 5.—C. R. Cook, Jr., and B. M. Martin, *New Semiconductor Networks Reduce System Complexity*, *Electronics*, 37:2, p 25-29.



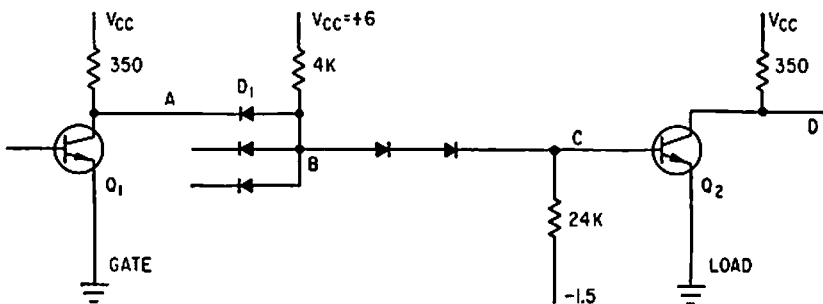


RCTL NAND/NOR GATE—Resistors and capacitors in base circuits permit higher fan-out and give logic swing of 2 v for high noise

rejection in integrated-circuit logic.—A. E. Skoures, *Choosing Logic for Microelectronics*, Electronics, 36:40, p 23-26.

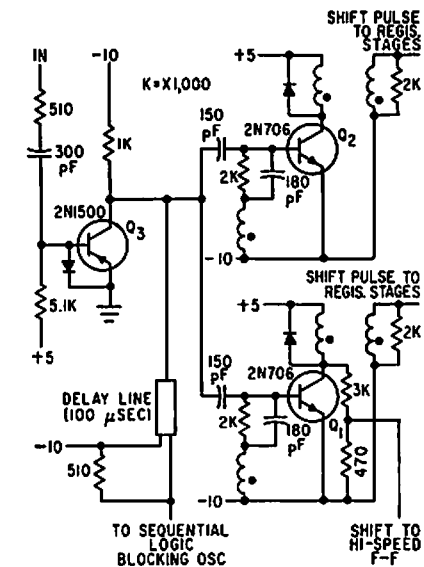


COMPLEMENTARY RDTL NOR—Alternately provides 500-na base current to pnp and npn transistors, thereby using transistor rise time at both edges of switching pulse to eliminate R-C time constant fall times of output waveform. Design reduces power drain and speeds up rise and fall times by factor of 15.—R. A. Tietsch, *Complementary Microwatt Logic Circuits*, EEE, 11:8, p 51-52.

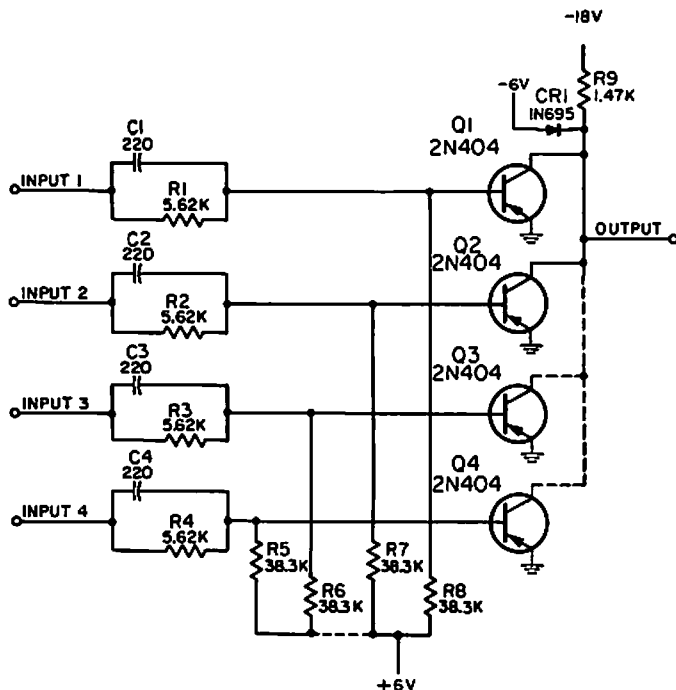


DIODE-COUPLED NAND/NOR GATE—Designed for integrated circuits, arrangement gives unlimited fan-in and high immunity to noise,

with 1.7 v logic swing.—A. E. Skoures, *Choosing Logic for Microelectronics*, Electronics, 36:40, p 23-26.

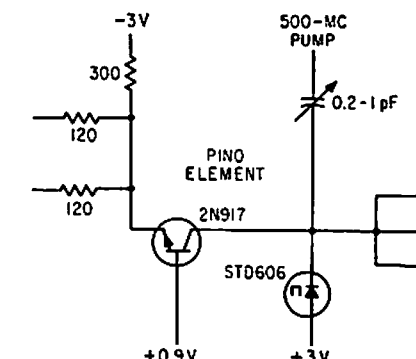


DRIVERS FOR SHIFT REGISTER—Inverter Q3 pulses 300-ma drivers Q1 and Q2 and feeds 100-nsec delay line that provides time for shift register stages to reach final values in new state.—B. K. Erickson and J. D. Schmidt, *Random Pulse Generator Tests Circuits*, Encodes Messages, Electronics, 34:25, p 56-59.

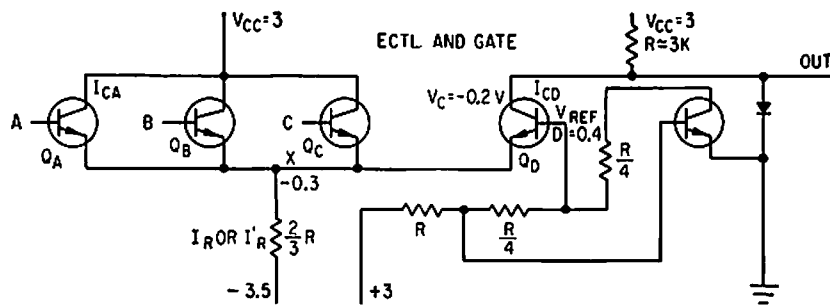


2 AND 4-INPUT-PULSE NOR GATE—Circuit is special-purpose nor gate for computer, control, and communication equipment. Used for the and operation when a general-purpose nor gate would be unsatisfactory be-

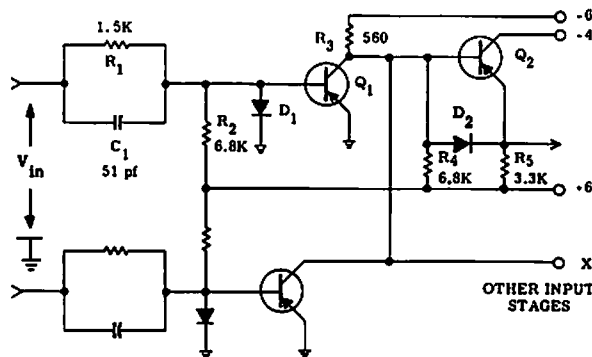
cause of possible spurious pulses in output.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 8 (original PC 211), p 8-2.



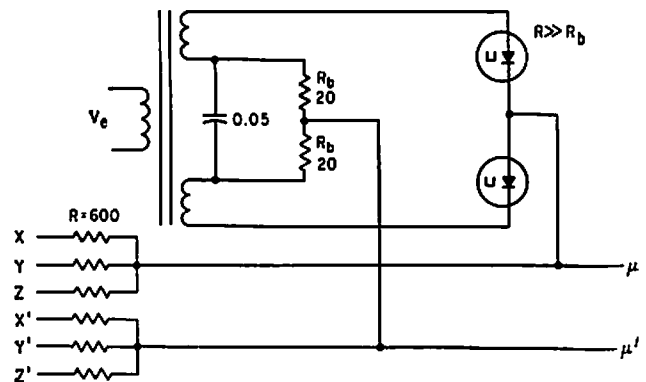
TWO-INPUT PINO NOR GATE—Pumped solid-state logic for uhf shift register using positive input-negative output gate gives 2-gc rate.—Tunnel Diode-Transistor Provides Fast Logic, Electronics, 35:11, p 72.



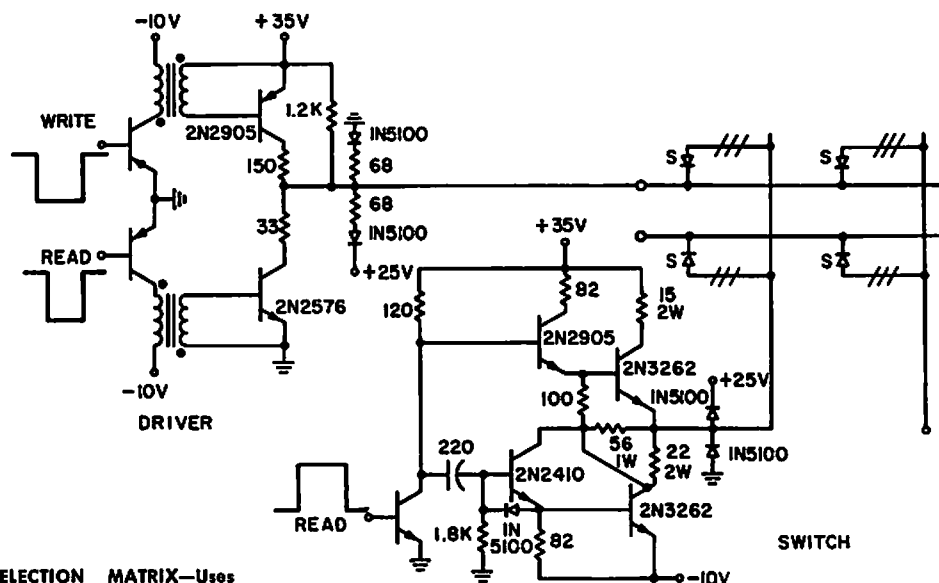
EMITTER-COUPLED TRANSISTOR AND GATE—Design gives manufacturing simplicity for integrated circuits without current hogging, but requires two power supplies. QD will conduct and QA, QB, and QC will be off when any of input A, B, or C are below 0.2 v.—A. E. Skoures, *Choosing Logic for Microelectronics*, *Electronics*, 36:40, p 23-26.



LOW-COST INVERTER AND NOR LOGIC—Inexpensive germanium pnp mesa switching transistor is used in basic inverter for high-speed computer circuits. Nor circuit is obtained by connecting other input stages to common collector load.—P. A. McInnis, "Low-Cost Computer Circuits," *Motorola Application Note AN-130*, Nov. 1965.



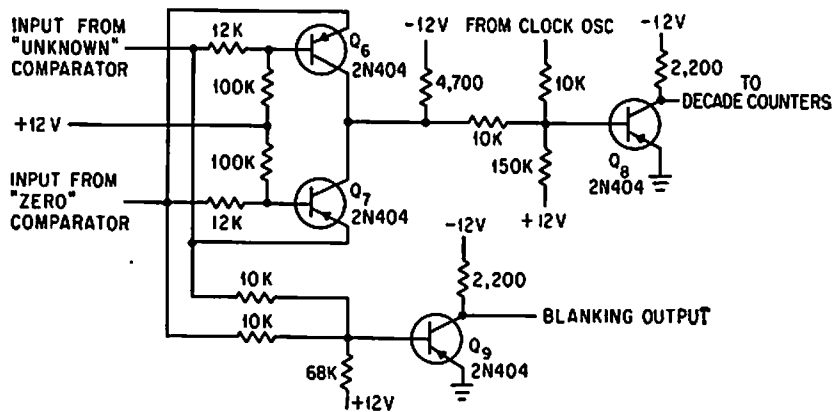
LOCKED PAIR—Ungrounded locked or Goto tunnel-diode pair permits logical inversion with no loss of speed or gain. Applications include converting analog television signals into pulse-code modulation.—C. L. Cohen, *New Approach to Locked-Pair Tunnel-Diode Logic*, *Electronics*, 35:31, p 46-47.



STORAGE-DIODE SELECTION MATRIX—Uses one diode per stored word. Four-word portion of 256-word matrix is shown. Activation of switch followed by driver drives selected diode sufficiently to permit flow of

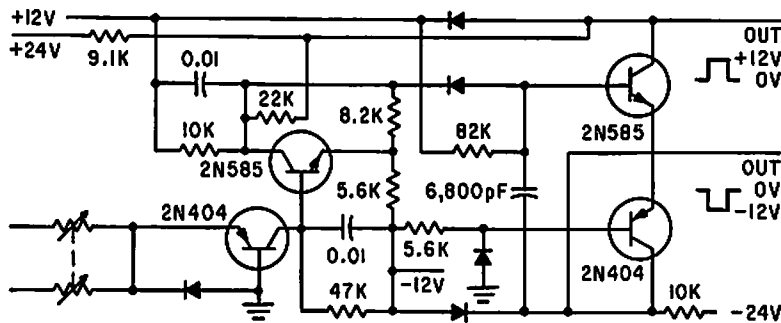
required read current. Write pulse is generated when read channel of both switch and driver are deactivated and write channel is activated.—I. Abayta, M. M. Kaufman, and

P. Lawrence, *Monolithic Ferrite Memories*, "1965 Fall Joint Computer Conference Preprints," Spartan Books, Washington, D. C., 1965.



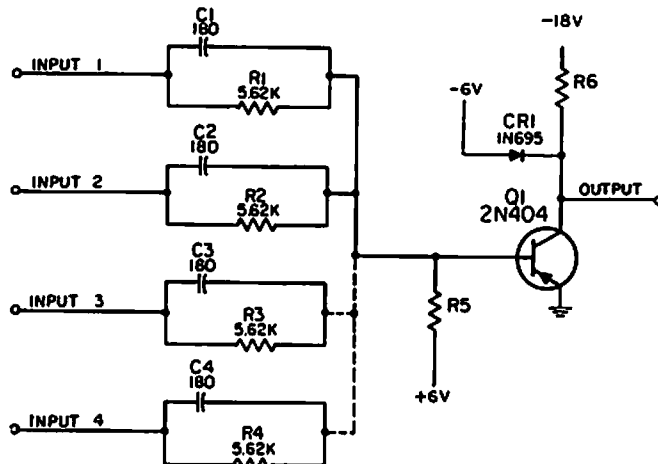
OR GATE FOR DIGITAL VOLTMETER—Ground-level signal output is produced only when inputs from the two comparators are in different states. Transistor Q8 gates continuously-running clock oscillator into decade counters of voltmeter.—R. C. Weinberg, Modified Ramp Generator Develops High D-C Input Impedance, *Electronics*, 37:8, p 33-35.

ously-running clock oscillator into decade counters of voltmeter.—R. C. Weinberg, Modified Ramp Generator Develops High D-C Input Impedance, *Electronics*, 37:8, p 33-35.



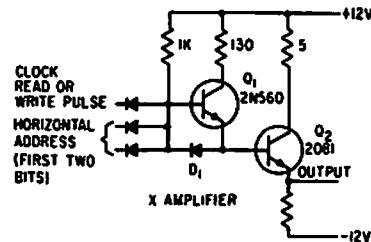
RECEPTOR-TYPE NEURON MODEL—Uses integrator quench circuit. Outputs of 100 or more such neuron circuits are combined so experiments can be repeated consistently,

with minimal interaction.—C. M. Wiley, Bionics on Program at Midwest's NEC, *Electronics*, 34:40, p 61-67.

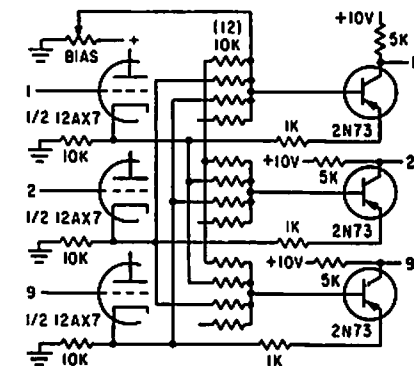


2 AND 4-INPUT NOR GATE—Performs general-purpose and, or, and inversion functions in compatible set of digital logic circuits for computer, control and communication equipment. Can be used as and gate for positive levels or positive-going pulses, as or gate for

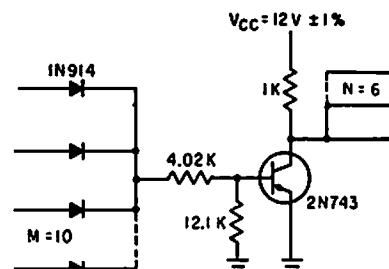
negative levels or negative-going pulses, and as inverter for both levels and pulses.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 7 (originally PC 210), p 7-2.



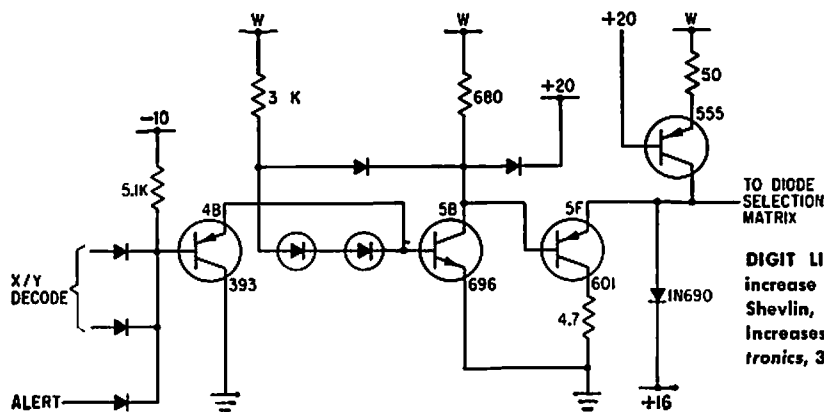
X AMPLIFIER FOR COINCIDENT-FLUX MEMORY—Inputs to and gate are clock read or write pulse and first two binary digits of horizontal address, forming one of the two translations for horizontal matrix of 1,120-bit memory.—H. F. Priebe, Jr., Three-Hole Cores for Coincident-Flux Memory, *Electronics*, 33:31, p 94-97.



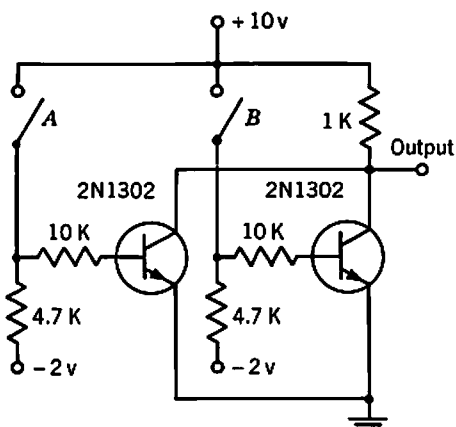
LARGEST-SIGNAL SELECTOR—Selects single channel that has greatest amplitude, using single nor-like transistor circuit per channel. Base mixer resistance network establishes signal bias level at greatest signal level encountered in all except designated channel. Channel transistor then conducts only when its signal at emitter is greater than all other signals.—L. R. Brown, Nonscanning Character Reader Uses Coded Wafer, *Electronics*, 33:48, p 115-117.



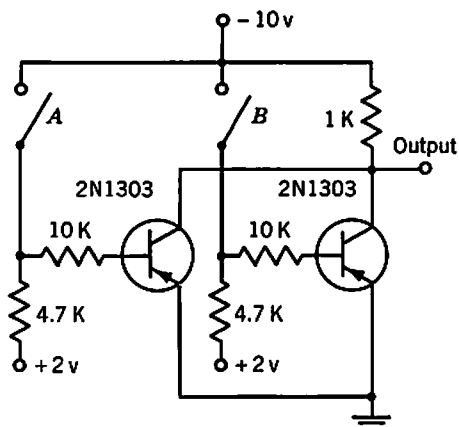
TRANSISTOR-DIODE NOR GATE—Low leakage and low storage time of silicon epitaxial transistor allow omission of base turn-off supply while giving medium-speed operation over wide temperature range, up to 2 Mc for two cascaded logic stages.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.



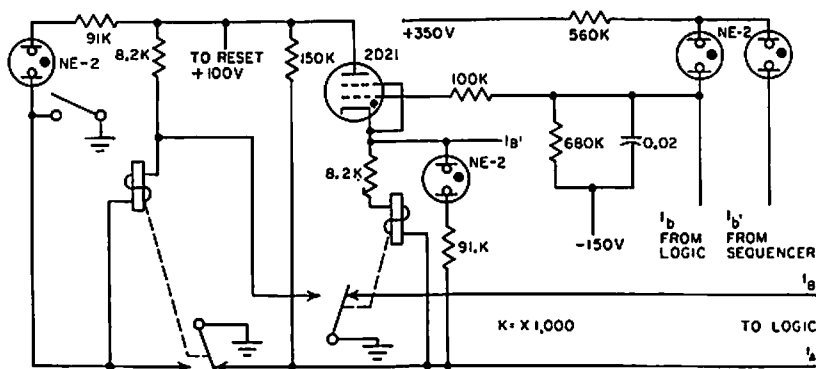
DIGIT LINE DRIVER—Uses diode steering to increase speed of memory.—A. Melmed, R. Shevlin, and W. Orvodal, Diode Steering Increases Speed of Magnetic Memories, *Electronics*, 34:37, p 68-70.



PARALLEL NPN BASIC LOGIC—Serves as or gate for normally open switches and as and gate for normally closed switches. Provides phase inversion of input.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 388.



PARALLEL PNP BASIC LOGIC—Serves as or gate for normally open switches and as and gate for normally closed switches. Provides phase inversion of input.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 388.

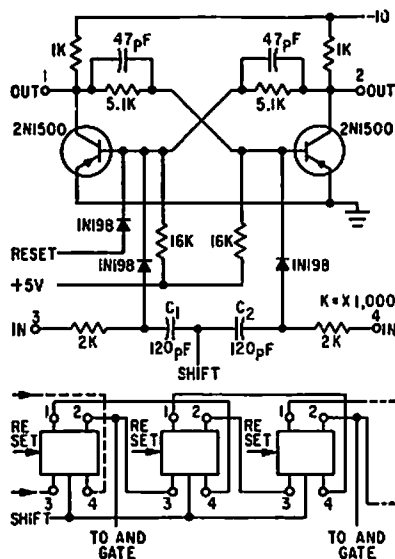
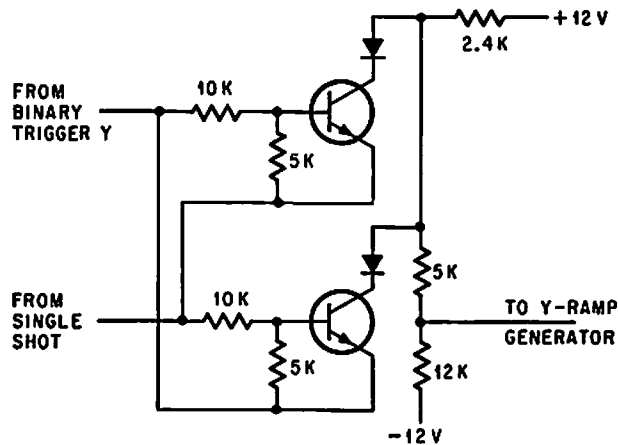


TICK-TACK-TOE MEMORY—Nine memory cells are used, one for each position on game board. Circuit shown is for position No. 1. Operator moves are entered into board by pushbuttons on display panel, energizing

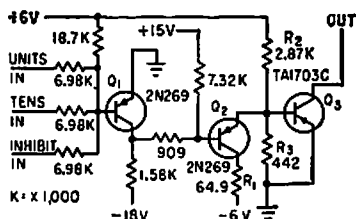
self-latching relays of memory. Board moves are determined by a separate logic section that triggers thyatron of memory and energizes relay. Neon lamps indicate when each position is filled, and by whom.—C. E.

Hendrix and R. B. Purcell, Neon Lamp Logic Gates Play Tick-Tack-Toe, *Electronics*, 31:25, p 68-69.

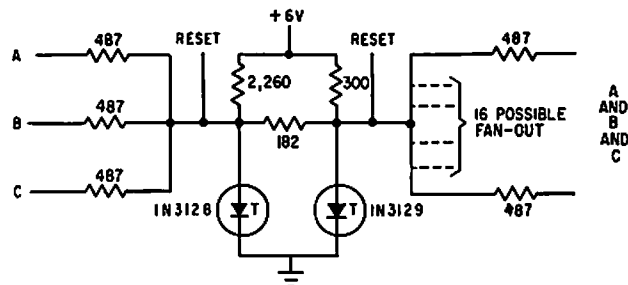
EXCLUSIVE-OR CIRCUIT—Used in tester that shows computer memory performance under marginal drive currents by plotting shmoo curves. Memory error triggers single-shot and changes direction of Y generator from positive to negative along top of shmoo curve, and from negative to positive along bottom. Transistors are 2N706 and diodes are 1N921.—J. E. Gersbach, *The Great Shmoo Plot: Testing Memories Automatically*, *Electronics*, 39:15, p 127-134.



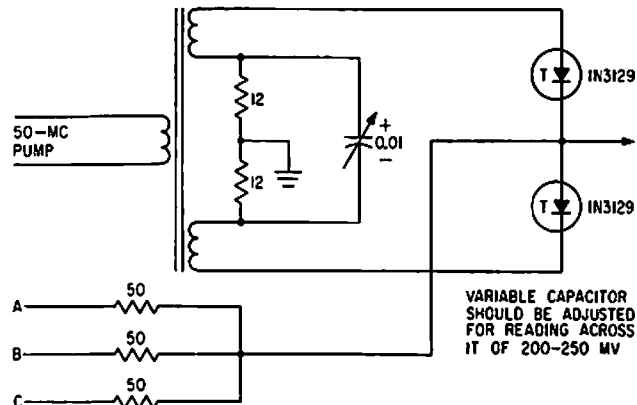
FLIP-FLOP FOR SHIFT REGISTER—Used to produce complex pulse sequences up to 4 billion bits in length, at frequencies up to 1.5 Mc. Each flip-flop provides 10 ma when turned on and draws 0.6 ma when off.—B. K. Erickson and J. D. Schmidt, *Random Pulse Generator Tests Circuits, Encodes Messages*, *Electronics*, 34:25, p 56-59.



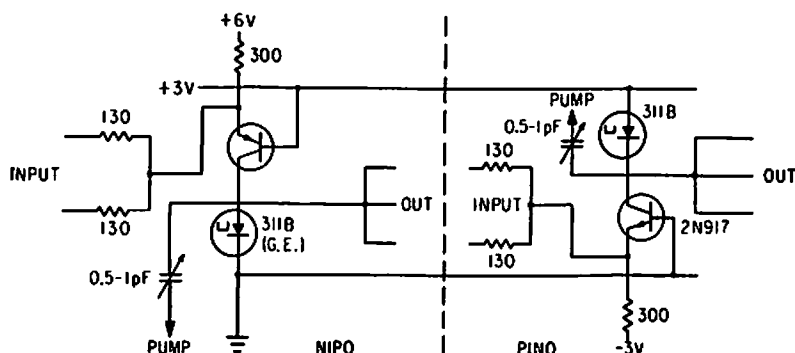
WORD SWITCH—Circuit is basically bilateral switch, which closes selected word circuit of memory used in Burroughs B-215 Visible Record Computer. Units and tens inputs are used to select particular word. Third input to gate is for special-purpose inhibit instruction.—G. E. Lund and D. R. Faulis, *Expandable Random Access Memories*, *Electronics*, 33:11, p 164-166.



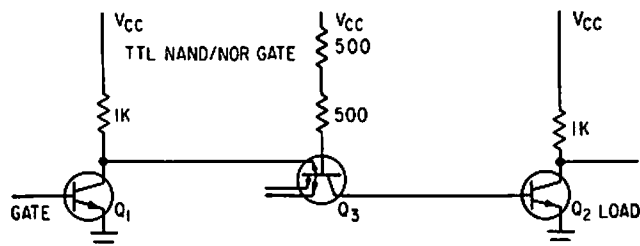
BISTABLE AND CIRCUIT WITH RESET—Uses resistance-coupled inputs to tunnel diodes. Gate is open when 1N3129 is on its negative-resistance slope, so reset pulse must be applied to close it.—F. Leary, *Computers Today*, *Electronics*, 34:17, p 64-94.



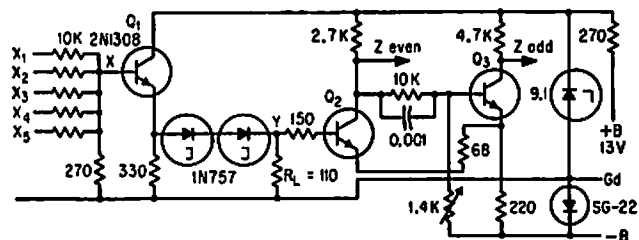
MAJORITY-DECISION—Balanced-pair tunnel-diode arrangement with phase-locked tank serves as gate operating on 50-Mc pump.—F. Leary, *Computers Today*, *Electronics*, 34:17, p 64-94.



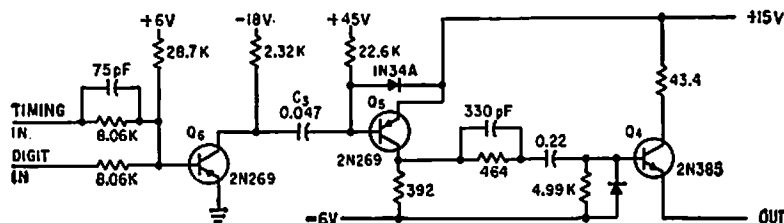
COMPUTER FOR SIX-BIT BINARY OUTPUT—Pumped tunnel-diode-transistor logic at 300 Mc converts outputs of converter subchannels into time series of six bits. In nipo element, one or more negative pulses at input inhibits positive-going pulse at output; other element operates at opposite half-cycles at pump source and gives opposite action.—H. R. Schindler, *Semiconductor Circuits in a UHF Digital Converter*, *Electronics*, 36:35, p 37-40.



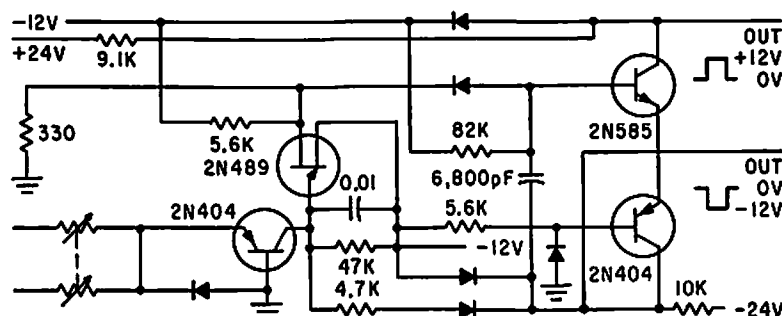
TRANSISTOR-COUPLED NAND/NOR GATE—Coupling transistor Q3 feeds its base current into base of inverting transistor Q2 when gate Q1 is cut off. When Q1 is saturated, coupling transistor Q3 clamps base of Q2 to low voltage. Logic swing of 0.4 v occurs at high speed.—A. E. Skouras, *Choosing Logic for Microelectronics*, *Electronics*, 36:40, p 23-26.



ODD-EVEN LOGIC—Accepts five binary inputs and produces signal at either of two outputs according to whether sum of inputs is even or odd. Schmitt trigger is used between tunnel diodes and load to boost output voltage to 9 v.—W. H. Ko, *Unique Tunnel-Diode Circuit Performs Odd-and-Even Logic*, *Electronics*, 35:42, p 61-62.

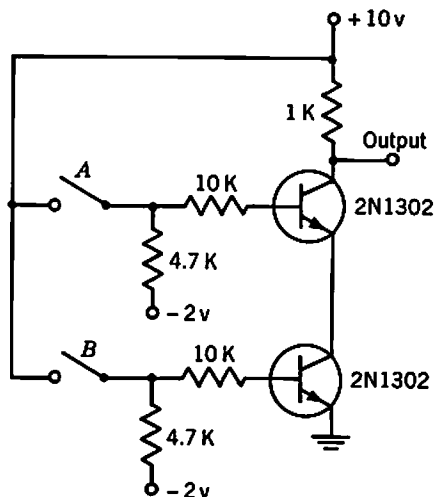


MEMORY INSERT DRIVER—Supplies current for inserting information in random-access memory.—G. E. Lund and D. R. Faulis, *Expandable Random Access Memories*, *Electronics*, 33:11, p 164-166.

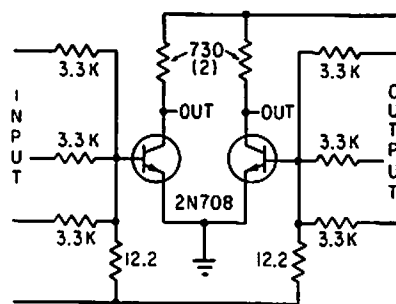


NEURON MODEL WITHOUT INTEGRATOR—Gives rectangular output pulses of either polarity. Catastrophic failure is avoided even

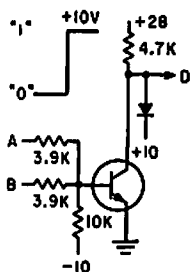
if outputs become grounded.—C. M. Wiley, *Bionics on Program at Midwest's NEC*, *Electronics*, 34:40, p 61-67.



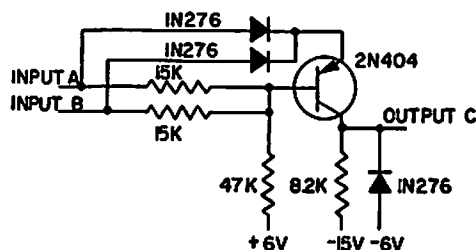
SERIES NPN BASIC LOGIC—Serves as *and* gate for normally open switches, and as *or* gate for normally closed switches. Provides phase inversion of input.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 388.



DOUBLE NOR GATE—Pulse repetition rate is 1 Mc, fan-in is 3, and fan-out is up to 6, with 30-mw dissipation for 7-v supply.—Double NOR Gate for Dense Packaging, *Electronics*, 36:12, p 100.



RESISTOR-TRANSISTOR NOR GATE—Circuit is basic building block of binary full adder for high speed encoding. Transistor is 2N1499.—S. C. Chao, High Speed Encoding with Resistor-Transistor-Logic Circuits, *Electronics*, 35-6, p 48-51.

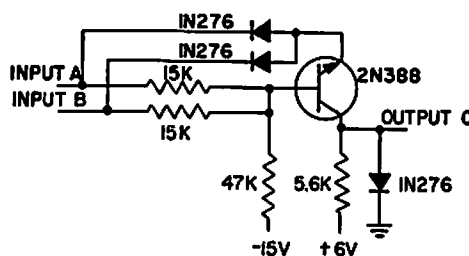


TRUTH TABLE

A	B	C
0	0	0
0	1	1
1	0	1
1	1	0

PNP EXCLUSIVE-OR LOGIC—For use with 0 (ground) and -6 v logic system. Can be adapted readily to most other logic levels. Produces logic 1 when inputs disagree.—Ex-

clusive OR Uses One Transistor, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 213.

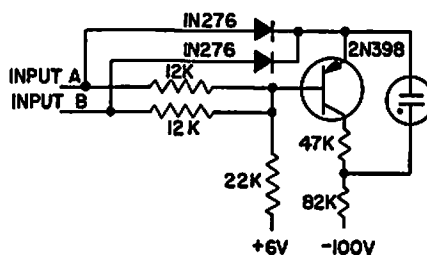


TRUTH TABLE

A	B	C
0	0	1
0	1	0
1	0	0
1	1	1

NPN AND GATE—Two diodes act as conventional *and* gate with transistor and its collector supply, using few components. Intended for 0 and -6 v logic.—Exclusive OR

Uses One Transistor, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 213.

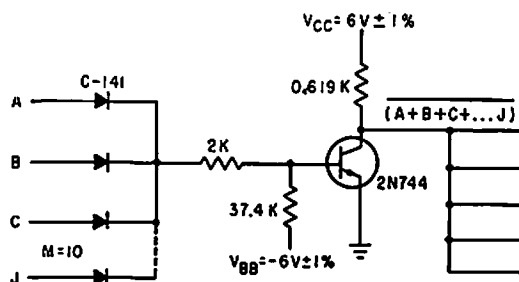


TRUTH TABLE

A	B	LAMP
0	0	ON
0	1	OFF
1	0	OFF
1	1	ON

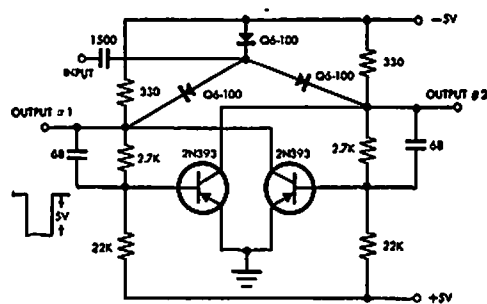
NEON WITH PNP EXCLUSIVE-OR LOGIC—Neon lamp glows when inputs are in agreement. Indicator will follow only one control level if other input is at fixed voltage such as logic 0; lamp will then turn off whenever control input is at logic 1. Similarly, if fixed

voltage is logic 1, lamp will turn off when control input is at logic 0.—Exclusive OR Uses One Transistor, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 213.

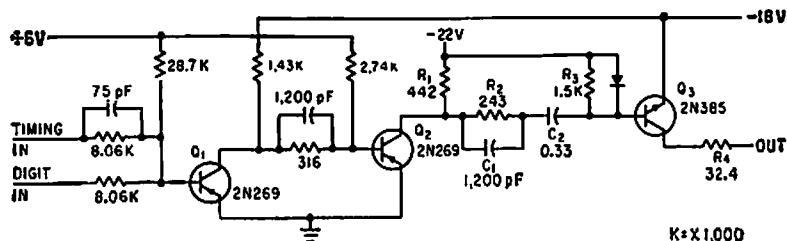


EPITAXIAL NOR GATE—Operates at up to 8 Mc from -55°C to +150°C.—D. Hall, Using

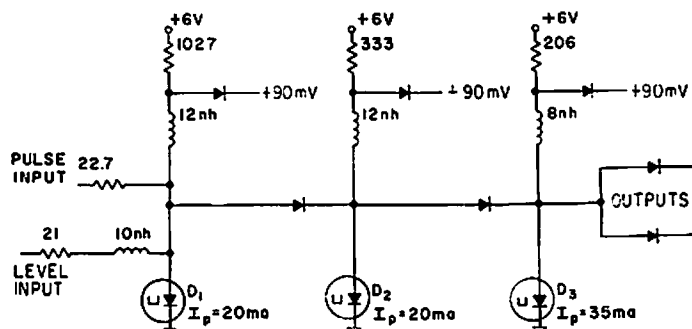
Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.



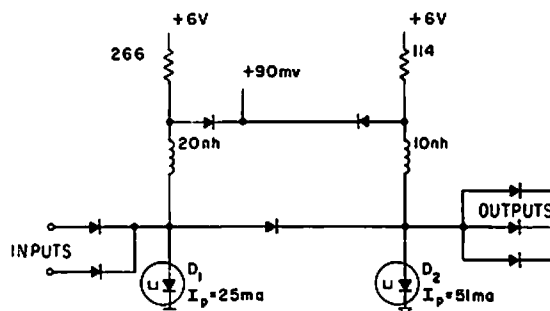
RC-COUPLED BINARY STAGE—Typical switching times are 30 and 44 millimicrosec.—Philco MAT Transistors for Logic Circuits up to 5 Mc (Philco ad), *Electronics*, 33:17, p 50.



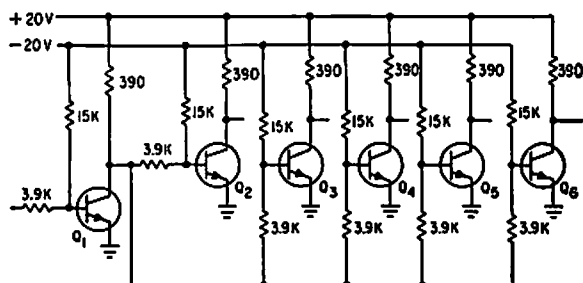
MEMORY DRIVER—Extract-driver circuit furnishes current to extract information from random-access memory of Burroughs B-251 Visible Record Computer.—G. E. Lund and D. R. Faulis, *Expandable Random Access Memories*, *Electronics*, 33:11, p 164-166.



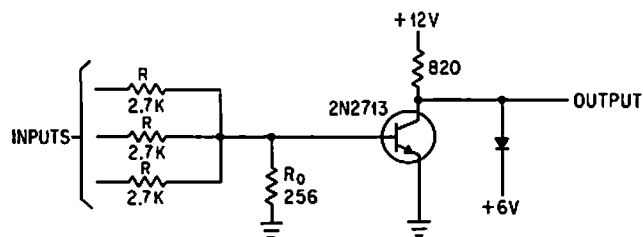
TUNNEL-DIODE AND GATE—Three cascaded monostable multivibrators provide required gain at 200 Mc.—E. Gottlieb and J. Giorgis, *Tunnel-Diode Switching Circuits*, *Electronics*, 36:27, p 26-31.



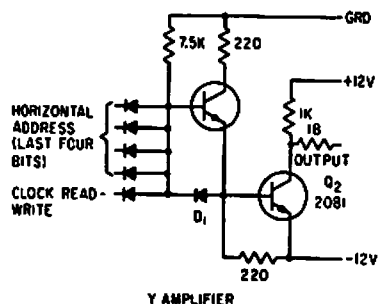
TUNNEL-DIODE OR GATE—Two monostable multivibrators are cascaded to provide current gain at 200 Mc. Output is obtained when either of input currents rises above 8 ma.—E. Gottlieb and J. Giorgis, *Tunnel-Diode Switching Circuits*, *Electronics*, 36:27, p 26-31.



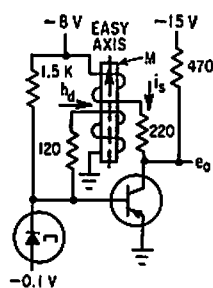
NOR CIRCUIT—With 2N834 epitaxial mesa transistors, turn-on time is 80 nsec, and turn-off 90 nsec, as compared to 111-nsec turn-on and 140-nsec turn-off for nonepitaxial 2N706 mesa transistors in same circuit.—W. D. Roehr, *Epitaxial Process Improves Transistor Characteristics*, *Electronics*, 34:9, p 52-53.



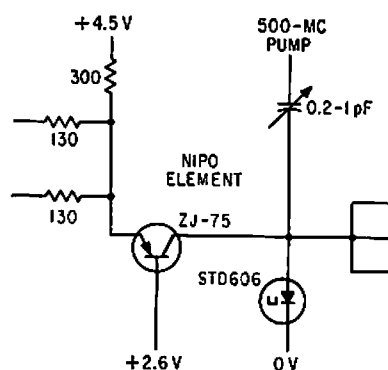
MAJORITY GATE—With odd number of inputs and resistor-summer, threshold logic transistor is virtually off up to 0.5 v base-emitter voltage and on at 0.7 v. Output is inverted.—W. A. Sauer, *How to Achieve Majority and Threshold Logic with Semiconductors*, *Electronics*, 36:48, p 23-25.



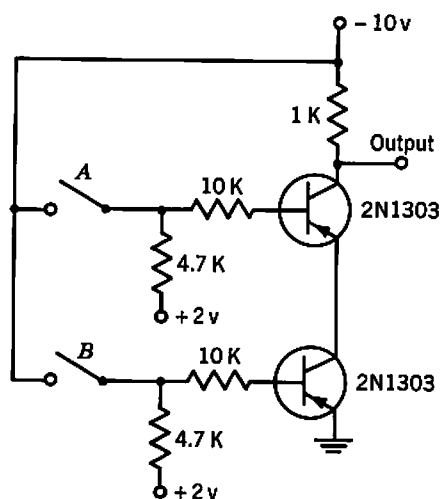
Y AMPLIFIER FOR COINCIDENT-FLUX MEMORY—Inputs to translator section of Y amplifier are last four bits of address, which perform one out of ten translations for horizontal matrix of 1,120-bit memory.—H. F. Pribe, Jr., Three-Hole Cores for Coincident-Flux Memory, *Electronics*, 33:31, p 94-97.



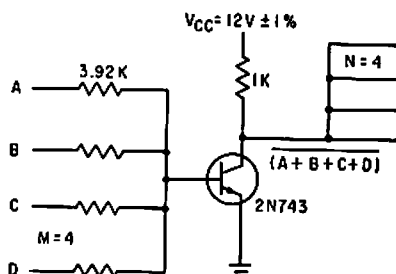
THIN-FILM TOGGLING WITH TRANSISTOR—Use of transistor stage permits cascading as for counters. Tunnel diode, which controls conducting state of transistor, is biased to have output voltages of 0.05 and 0.4 v. Additional film winding is needed because of phase reversal by transistor.—T. A. Smay and A. V. Pohm, Design of Logic Circuits Using Thin Films and Tunnel Diodes, *Electronics*, 34:35, p 59-61.



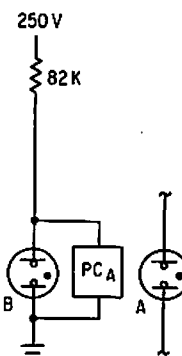
TWO-INPUT NIPO NOR GATE—Pumped tunnel diode-transistor logic gives 2-gc rate for uhf shift register using negative input-positive output gate having gain of 3 and 50-mw power drain.—Tunnel Diode-Transistor Provides Fast Logic, *Electronics*, 35:11, p 72.



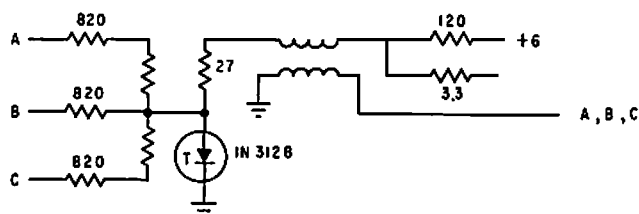
SERIES PNP BASIC LOGIC—Serves as and gate for normally open switches, and as or gate for normally closed switches. Provides phase inversion of input.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 388.



SINGLE-SUPPLY NOR GATE—Low storage time allows medium-speed operation without turn-off base bias supply. Maximum is 1 Mc for two cascaded logic stages.—D. Hall, Using Epitaxial Transistors in Switching and R-F Circuits, *Electronics*, 34:13, p 52-53.

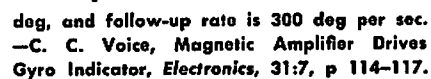
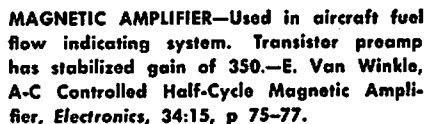


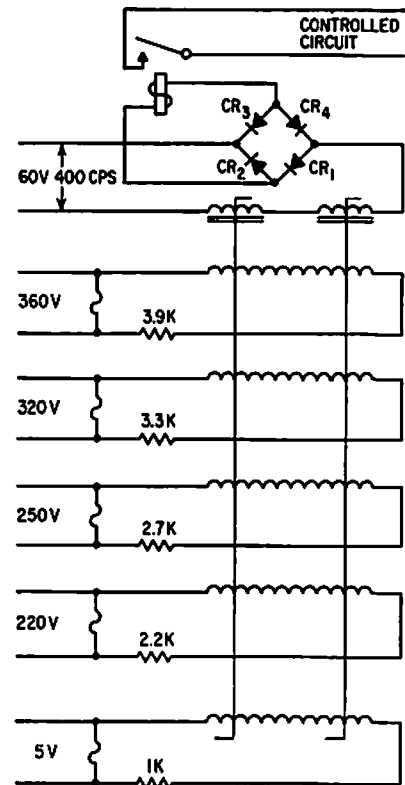
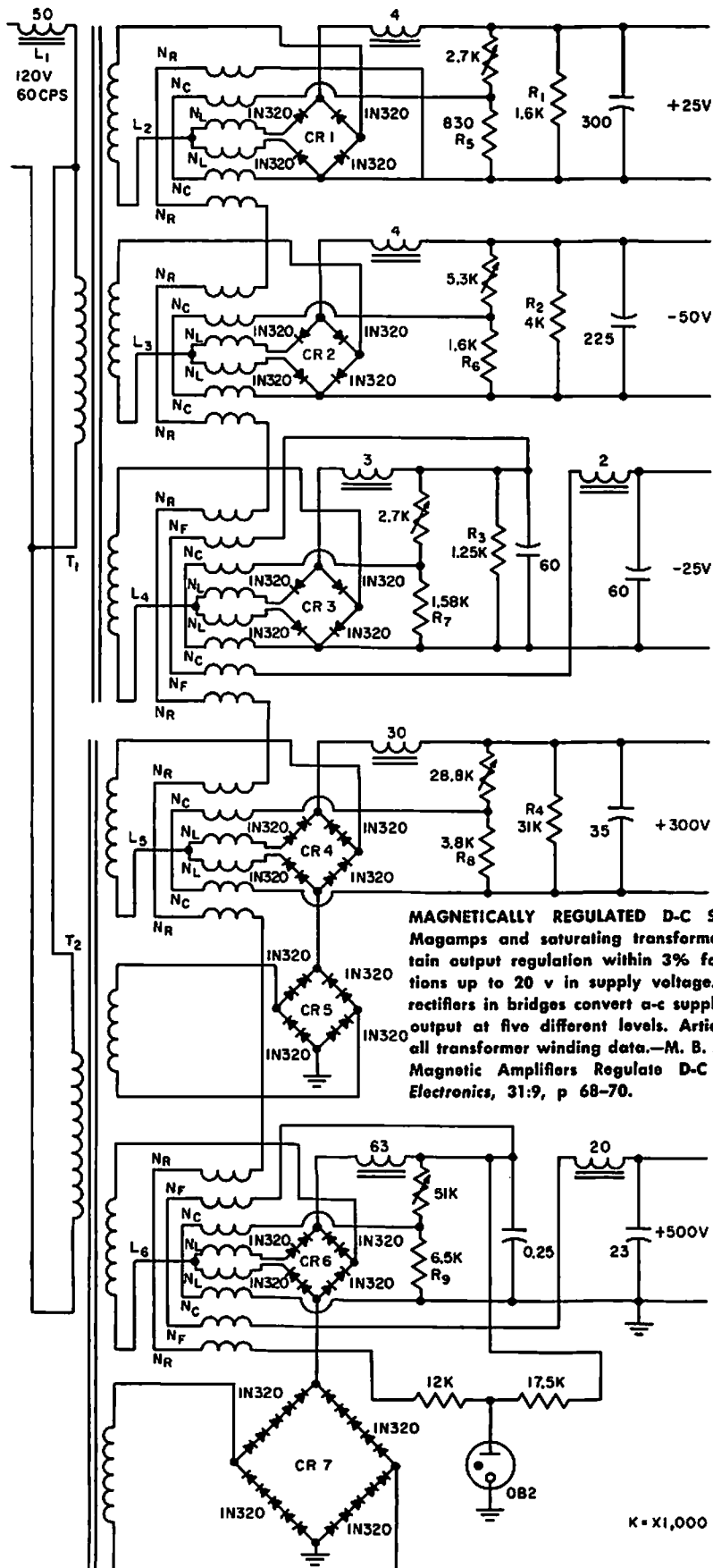
NEON PHOTOCONDUCTOR INVERTER—Cadmium selenide photoconductor PC and Ne2H neon lamps give low-speed inverter action for logic circuits at low cost. Neon B is on when there is no input. When neon A provides input, PC turns neon B off.—J. L. Patterson, Will Neon Photoconductors Replace Relays in Low-Speed Logic?, *Electronics*, 36:18, p 46-49.



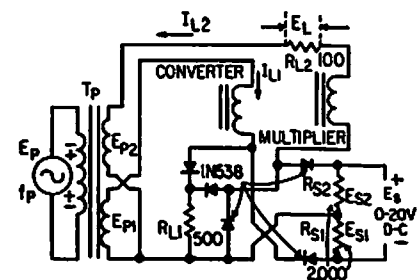
MONOSTABLE OR CIRCUIT—Uses resistance-coupled inputs to drive tunnel diode.—F.

Leary, Computers Today, *Electronics*, 34:17, p 64-94.

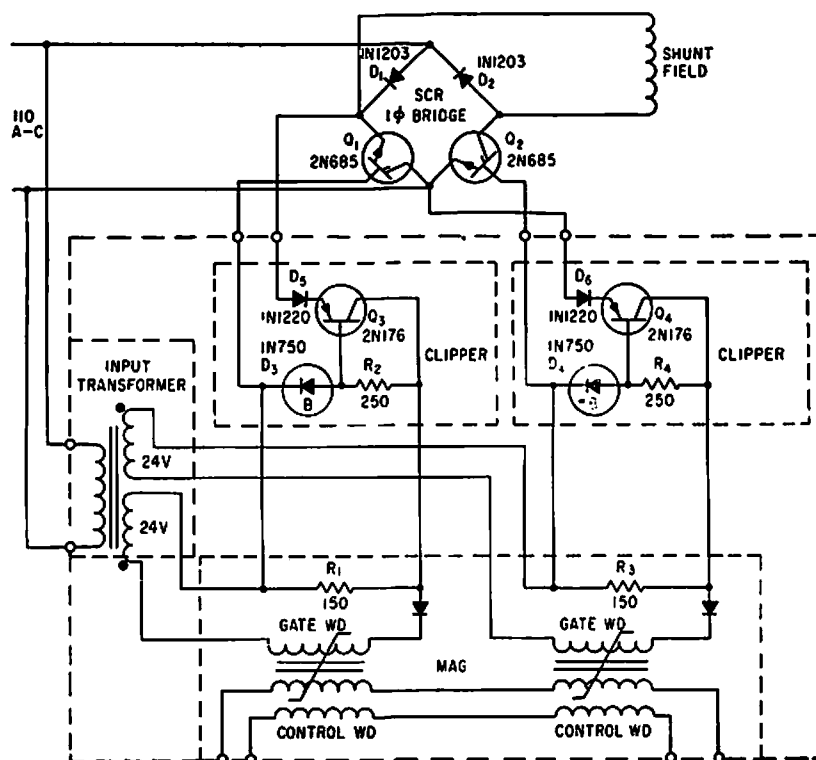




OPEN-FUSE DETECTOR—Magamp circuit uses bridge unbalance to operate control relay when protective fuse opens in digital computer, to remove d-c voltages from fused section. One of 15 control windings of series-connected magnetic amplifier is placed across each fuse, with appropriate current-limiting resistor in series with each winding. (Only representative control windings are shown.) —J. Maroz, *Magnetic Amplifier Detects Open Fuses*, *Electronics*, 31:29, p 86-92.



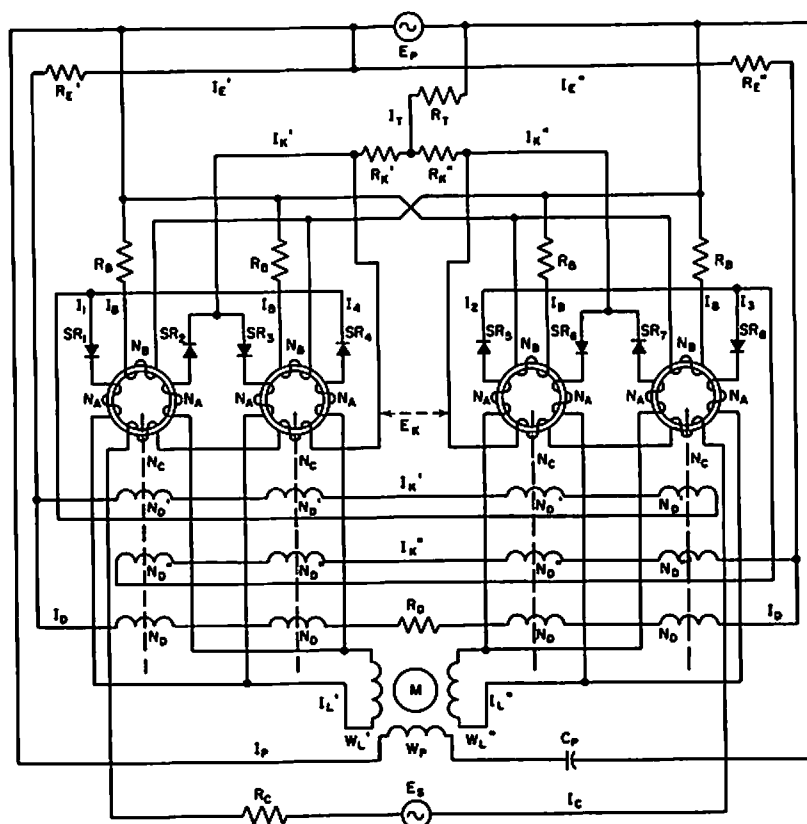
MODIFIED ANALOG MULTIPLIER—Input signal voltages are obtained from center-tapped 2,000-ohm input resistor so that each signal may change its polarity. Circuit then provides unidirectional output voltage E_1 which is equal to square of E_2 .—J. Markus, *Handbook of Electronic Control Circuits*, McGraw-Hill, N.Y., 1959, p 103.



MAGAMPS CONTROL SCR BRIDGE—Single-phase bridge is controlled by two half-wave magnetic amplifiers each having a transistor

emitter-follower used as clipper for each half-wave voltage output. Used for adjustable d-c field supply.—T. E. DeVinoy, Semi-

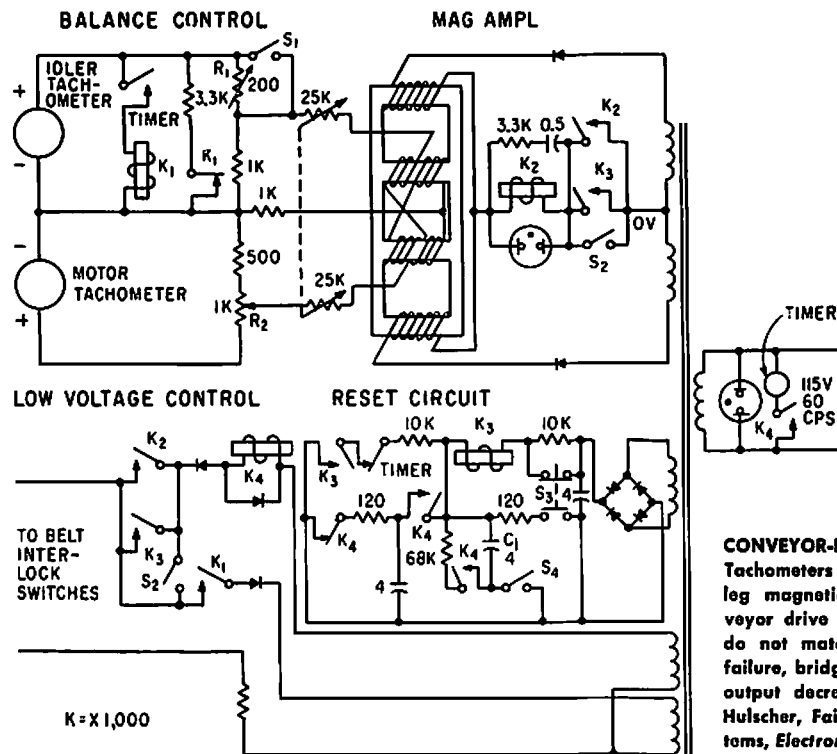
conductors Improve Reliability of Steel-Mill Control Equipment, *Electronics*, 34:23, p 104-107.



400-CPS SERVO MOTOR DRIVE—Self-balancing single-stage magnetic amplifier has high response speed, excellent stability, excellent

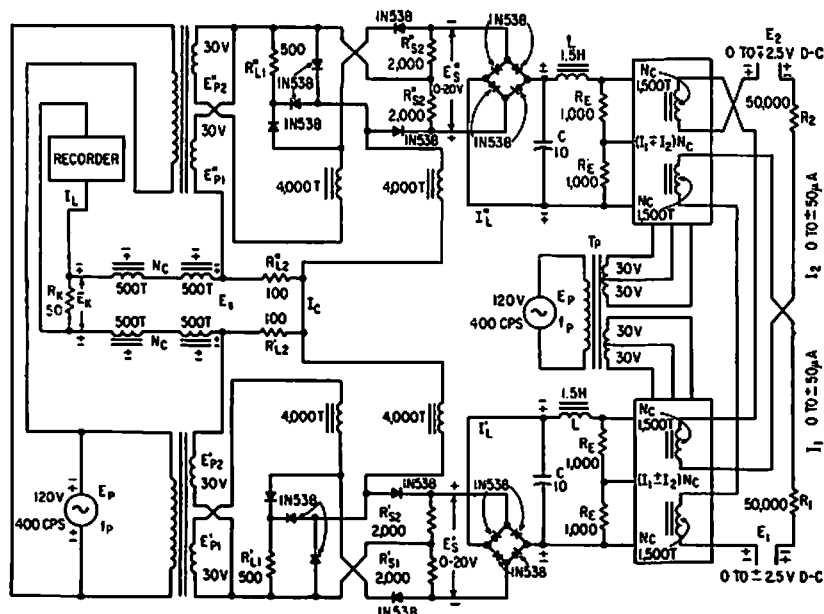
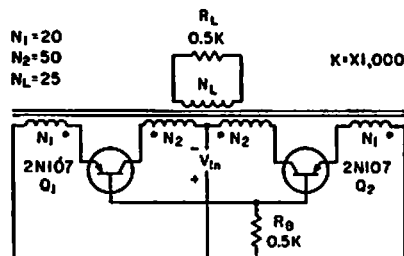
linearity, and freedom from drift. Provides half-cycle response as operational amplifier. Article gives winding data for saturable re-

actors.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, N.Y., 1959, p 107.

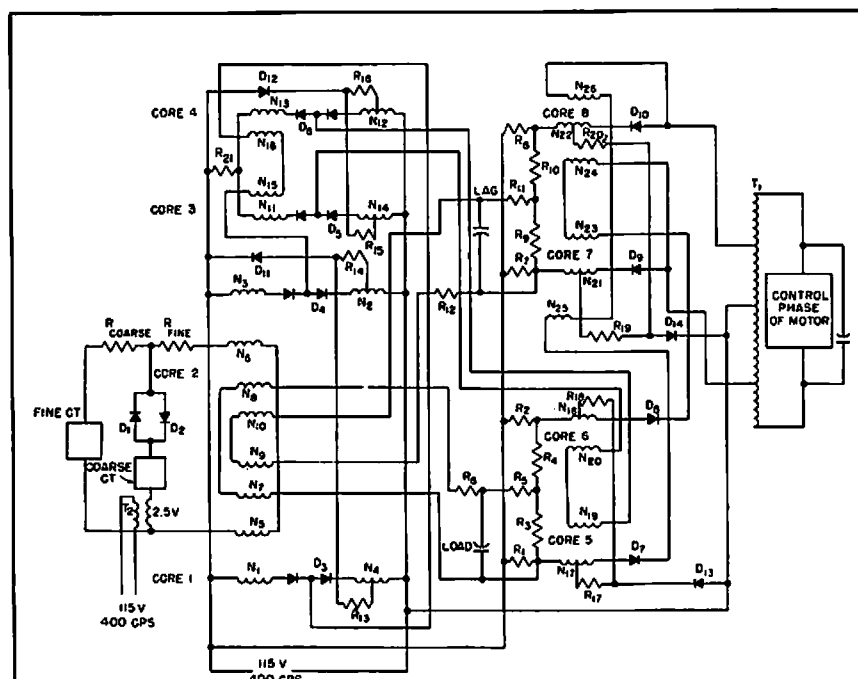


CONVEYOR-BELT OVERLOAD PROTECTION—Tachometers monitor belt slip and feed four-leg magnetic amplifier which controls conveyor drive relay K2. If tachometer outputs do not match because of belt slip or belt failure, bridge becomes unbalanced, magamp output decreases, and K2 drops out.—F. R. Hulscher, *Fail-Safe Circuits for Conveyor Systems*, *Electronics*, 32:28, p 60.

DIFFERENTIAL MAGNETIC INVERTER—Oscillates reliably without use of current bias. Excessive drive will not cause transistor overheating. Differential action of collector and emitter windings greatly improves performance as compared to conventional nondifferential inverter and eliminates need for clipping diodes.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, N.Y., 1959, p 103.



FOUR-QUADRANT ANALOG MULTIPLIER—Uses two square-law multiplier circuits containing only magnetic cores, silicon diodes, and resistors, to provide first square term of algebraic sum of currents I_1 and I_2 , and second square term of difference of these currents.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, N.Y., 1959, p 104.



Core data: first and second stages

Cores 1, 2, 3 and 4 — Orthonol, 0.002-in. tape, $3/4 \times 1 \times 3/8$ in.

Winding data

Core 1	Core 2	Core 3	Core 4	Turns	Wire Size
N_1	N_3	N_{11}	N_{13}	1,250	No. 34
N_2	N_4	N_{12}	N_{14}	1,250	No. 34
				Tapped at 650	
N_5	N_6			600	No. 37
N_7	N_8			300	No. 37
N_9	N_{10}			1,000	No. 37
		N_{15}	N_{16}	100	No. 34

Output stage

Cores 5, 6, 7 and 8 — Orthonol, 0.002-in. tape, $1 \times 1 \frac{1}{4} \times 1 \frac{1}{4}$ in.

Winding data

Core 5	Core 6	Core 7	Core 8	Turns	Wire Size
N_{17}	N_{18}	N_{21}	N_{22}	4,000	No. 32
				Tapped at 500	
N_{19}	N_{20}	N_{23}	N_{24}	7	No. 32
		N_{25}	N_{25}	7	No. 32

Rectifier data

D_3, D_4, D_5, D_6 — 6 plates, selenium, 1 in. square, doubler-connected

D_7, D_8, D_9, D_{10} — 6 plates, selenium, 1 in. square

$D_1, D_2, D_{11}, D_{12}, D_{13}, D_{14}$ — 6 plates, selenium, $1/2$ in. square

Resistor data

$R_{13}, R_{14}, R_{17}, R_{18}, R_{19}, R_{20}$ — 56,000 ohms, $1/2$ watt

R_{15}, R_{16} — 100,000 ohms, $1/2$ watt

R_{21} — 150 ohms, 5 watt

R_1 through $R_{12}, R_{14}, R_{15}, R_{17}, R_{18}, R_{19}, R_{20}$ — vary depending on amount of compensation

Capacitor data

C_3 — 0.1 μ f, 400 v, paper

C_1 and C_2 — vary depending on amount of compensation

Transformer data

T_1 — Westinghouse, Hypersil cut core, No. 6H

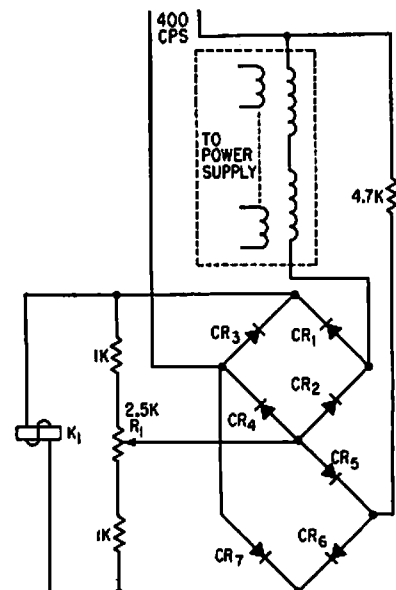
Windings 1,200 turns No. 30 wire

Tap at 400 turns

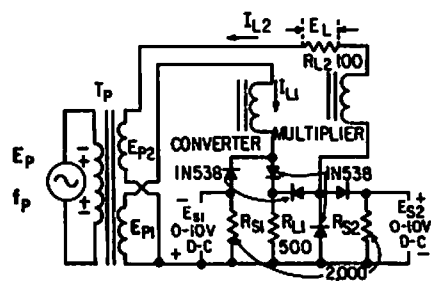
600 turns

800 turns

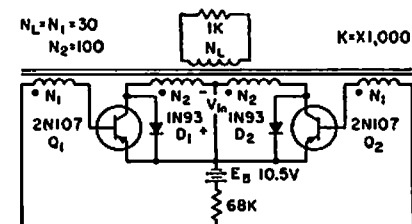
T_2 — 115/2.5-v transformer for stickoff voltage



DOUBLE-BRIDGE OPEN-FUSE DETECTOR—Used with magnetic amplifier to disconnect d-c voltage from section protected by fuse that opens. Output of lower bridge is balanced against output of upper bridge by adjusting R_1 . Gives greatly increased sensitivity.—J. Maroz, *Magnetic Amplifier Detects Open Fuses*, *Electronics*, 31:29, p 86-92.



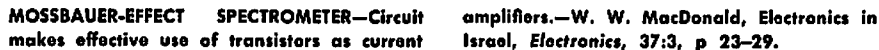
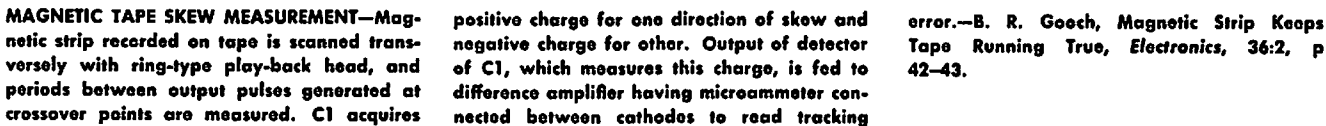
BASIC ANALOG MULTIPLIER—Multiplies d-c voltages E_{S1} and E_{S2} in two-stage arrangement in which converter is input stage controlled by one signal voltage and multiplier is output stage controlled by other signal voltage.—J. Markus, *Handbook of Electronic Control Circuits*, McGraw-Hill, N.Y., 1959, p 103.

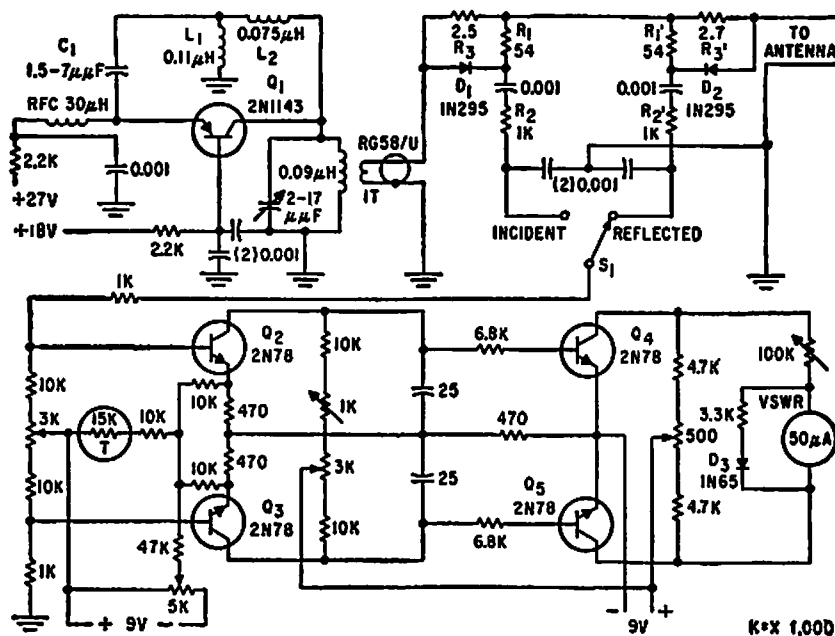


NONDIFFERENTIAL MAGNETIC INVERTER—Is analogous to free-running capacitor-coupled mvbr. Frequency and output amplitude are both directly proportional to input voltage. Chief drawback is need to increase input voltage to get higher frequency, which in turn increases all winding voltages.—J. Markus, *Handbook of Electronic Control Circuits*, McGraw-Hill, N.Y., 1959, p 102.

TWO-SPEED SERVO MOTOR DRIVE—Consists of two conventional half-wave bridge-type stages driving full-wave slave-type output stage. Designed to replace former electron-

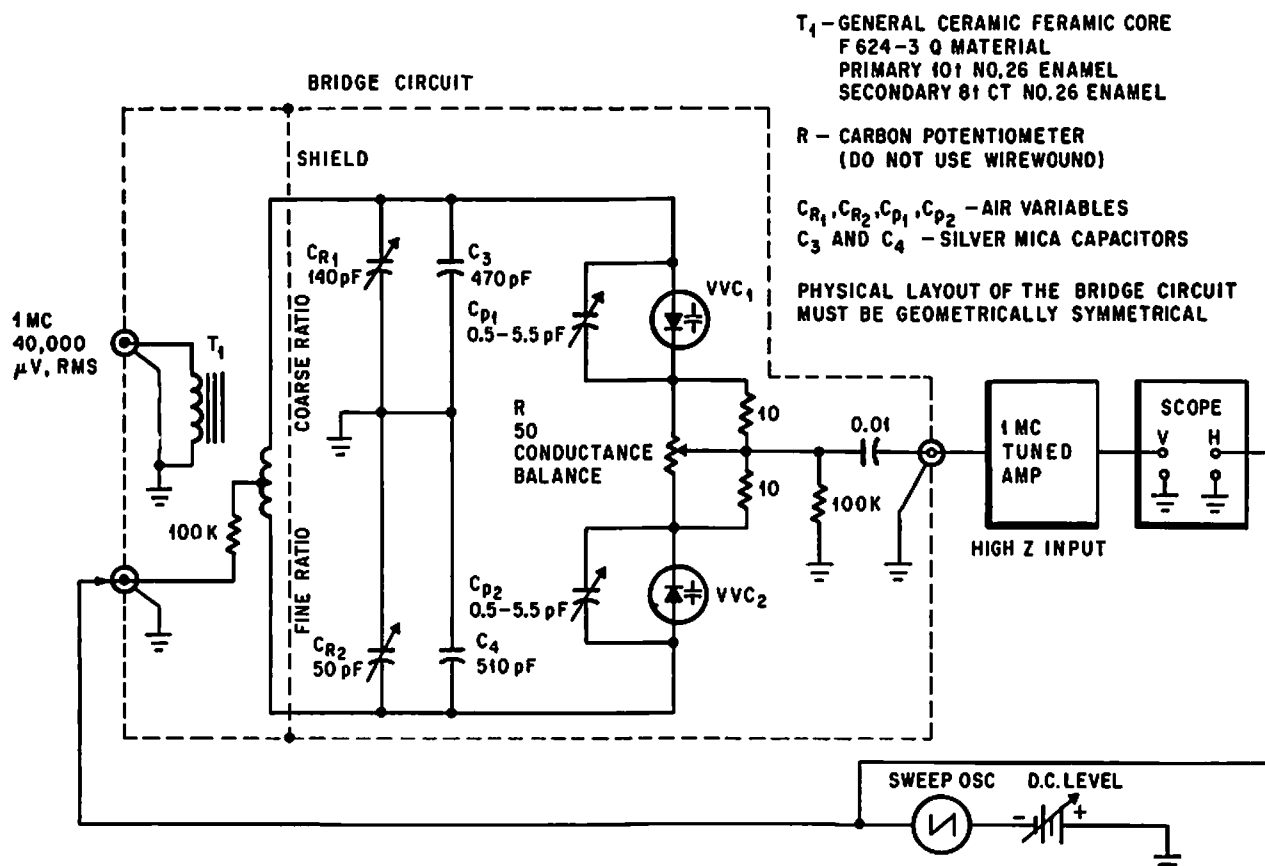
tube amplifier of two-phase servo system using fine and coarse control transformers.—J. Markus, *Handbook of Electronic Control Circuits*, McGraw-Hill, N.Y., 1959, p 112.





R-C DIRECTIONAL COUPLER—Couples variable-frequency 150–175 Mc oscillator to vhf antenna and furnishes incident and reflected power samples to d-c amplifier for meas-

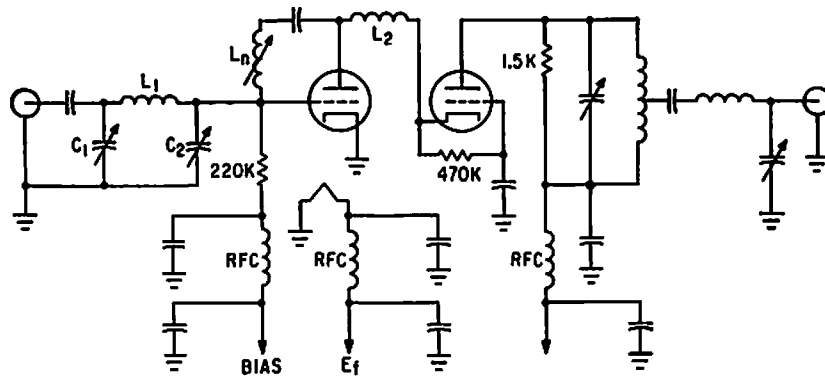
uring voltage-standing wave ratio.—J. Hanson, *Unconventional Technique for Measuring VSWR*, *Electronics*, 32:43, p 120–121.



CAPACITANCE-TRACKING TEST SET—Output signal voltage of capacitance bridge, proportional to capacitance unbalance, is applied to vertical input of scope through

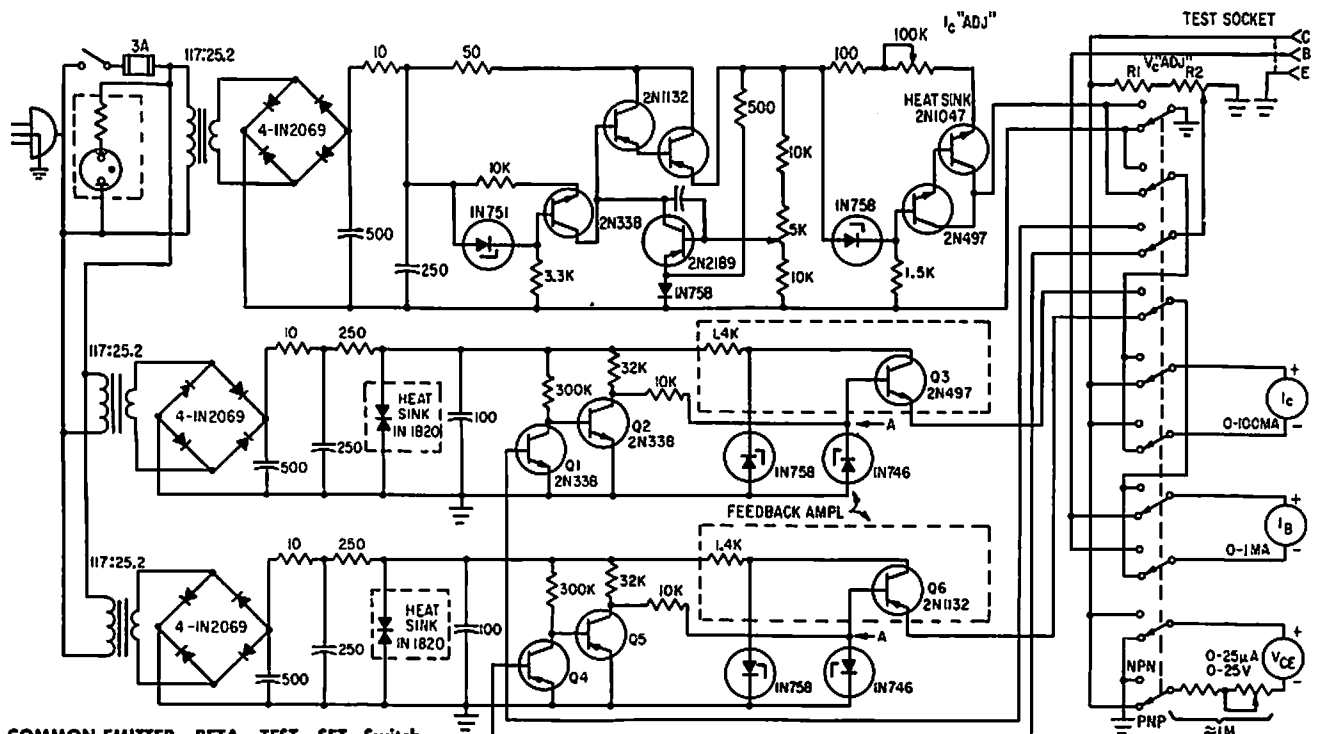
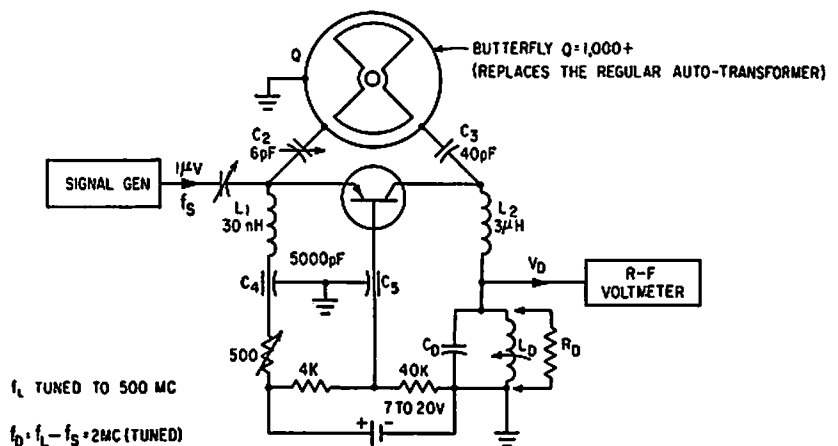
1-Mc preamp. Horizontal sweep voltage of scope is also applied as bias to voltage-variable capacitors VVC whose tracking is being measured, so scope display shows

diode capacitance-tracking as function of bias voltage.—L. A. Weldon and R. L. Kopski, *Boost for Electronic Tuning*, *Electronics*, 37:14, p 61–63.



NOISE-FIGURE MEASUREMENT OF R-F TUBES—Standardized EIA Committee circuit measures noise-figure of cascode r-f amplifiers with 95% repeatability. Jig circuit for tube under test has 200-Mc center frequency and 10-Mc bandwidth.—T. E. Gausman, *Standardizing Noise-Figure Measurement*, *Electronics*, 36:1, p 124-129.

MEASURING CONVERSION GAIN—Used for measuring input impedance characteristics of high-frequency transistor for operation beyond cutoff in special converter circuits.—V. W. Vadicka and R. Zuleeg, *Transistor Operation Beyond Cutoff Frequency*, *Electronics*, 33:35, p 56-60.



COMMON-EMITTER BETA TEST SET—Switch permits testing both npn and pnp transistors over wide current and voltage ranges.

Two feedback amplifiers are used, one for npn and the other for pnp.—R. M. Mann,

Fresh Approach to Measuring Transistor Beta, *Electronics*, 36:30, p 47-49.

LOCAL OSCILLATOR INPUT

47K

33K

0.1

2.2K

+300

TO CHANNEL FILTERS

6BE6

SIGNAL INPUT

UTC A-24

47

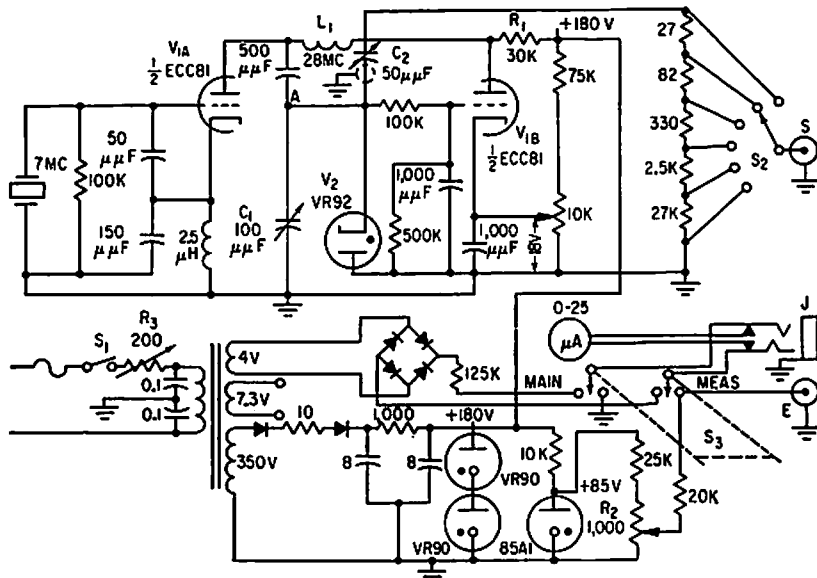
10K

0.01

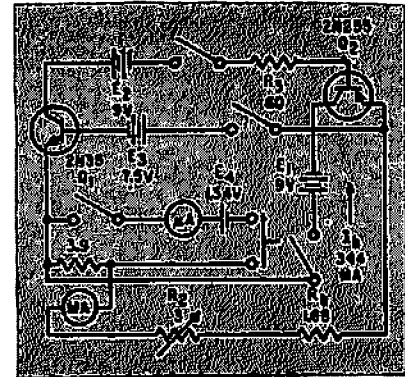
220

0.01

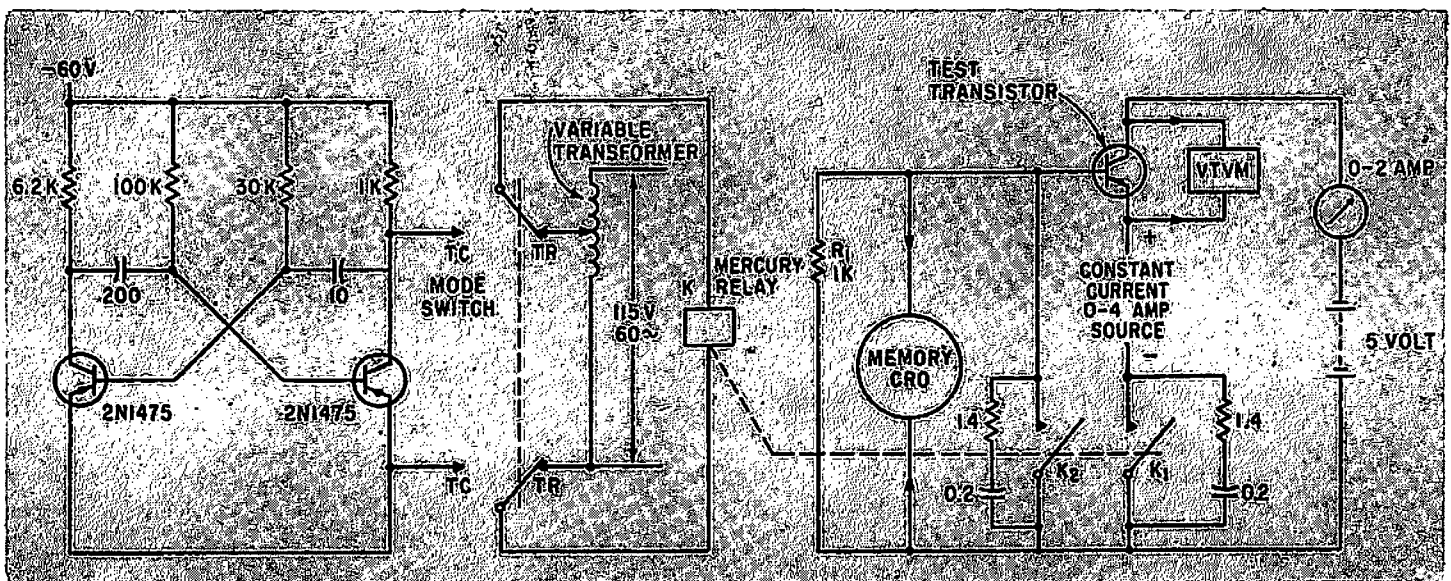
47



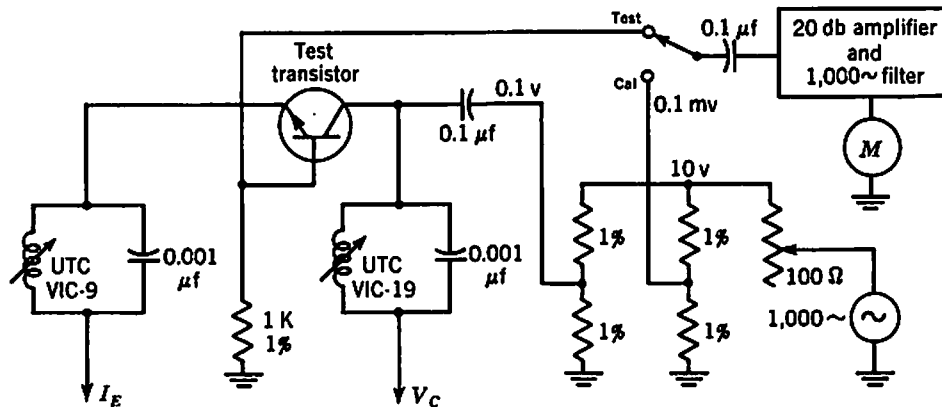
Maximum admittance indicates end of setting process.—J. M. Tobio, Electronics Determines Cement Setting Time, *Electronics*, 31:41, p 88-90.



MOISTURE METER—Maintains constant current for 20 minutes of heavy loading of conventional zinc-carbon dry cells when measuring moisture in pulverized coal or other powders, through use of inverse voltage feedback in two-stage direct-coupled amplifier.—G. E. Fasching, Inverse Feedback Stabilizes Dry Cell Current Sources, *Electronics*, 32:41, p 78.

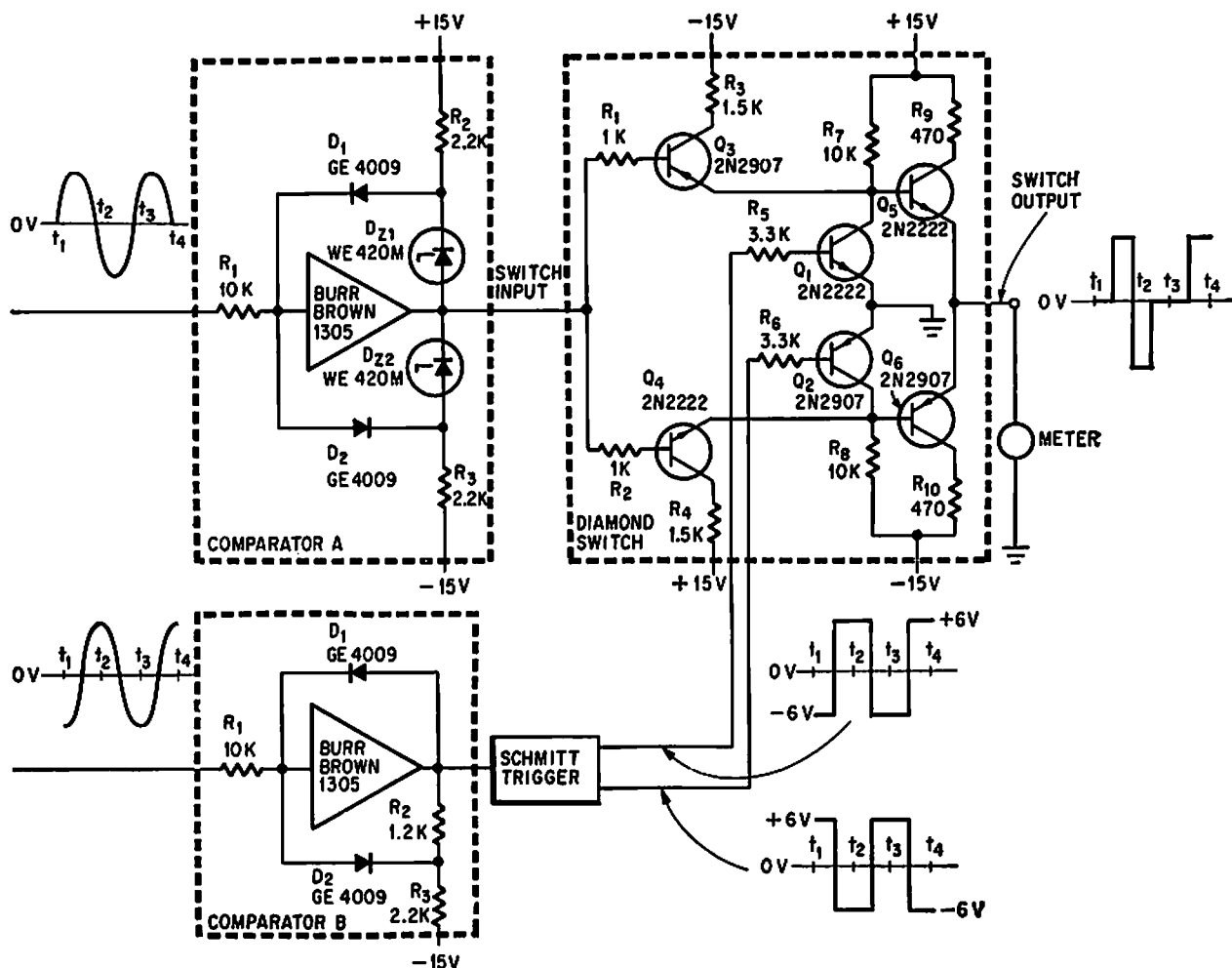


supply drives relay.—H. Bauman, Practical Way to Measure Transistor Thermal Resistance, *Electronics*, 36:7, p 66-67.



MEASURING TRANSISTOR OPEN-CIRCUIT OUTPUT ADMITTANCE—Test set gives small-signal value h_{ob} of open-circuit output admittance of transistor for common-base connection. Input voltage is held constant at 1 v and current is read as voltage drop

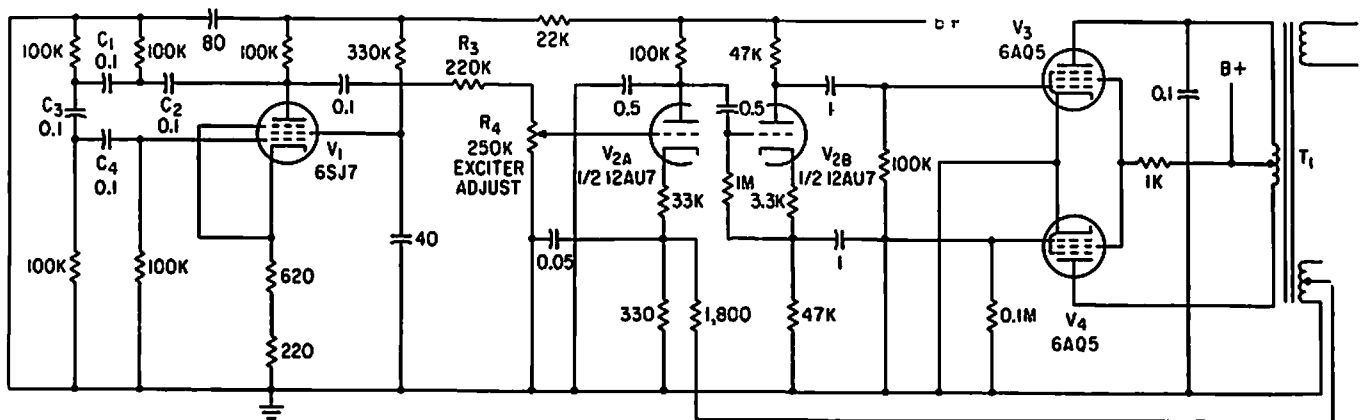
across 1K resistor. Voltage in mv can then be read directly as admittance of 0.1 to 1 micromho on 10-mv scale of meter.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 71.

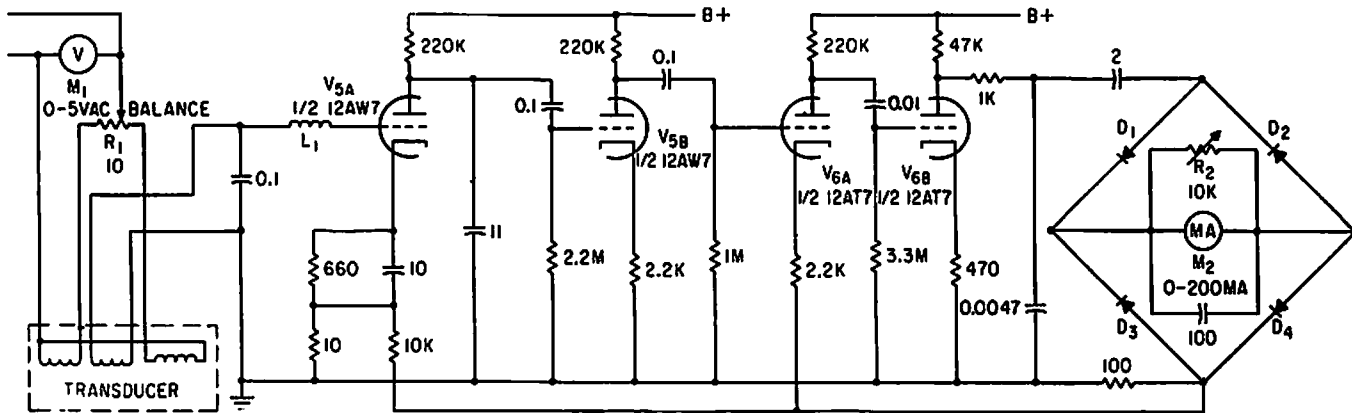


DIAMOND CIRCUIT MEASURES A-F PHASE SHIFT—Accuracy is 1% up to 2,000 cps. Used in computers and for high-speed analog

instrumentation. Zero-center d-c ammeter indicates negative value for zero phase shift, zero for 90° phase shift, and some maximum

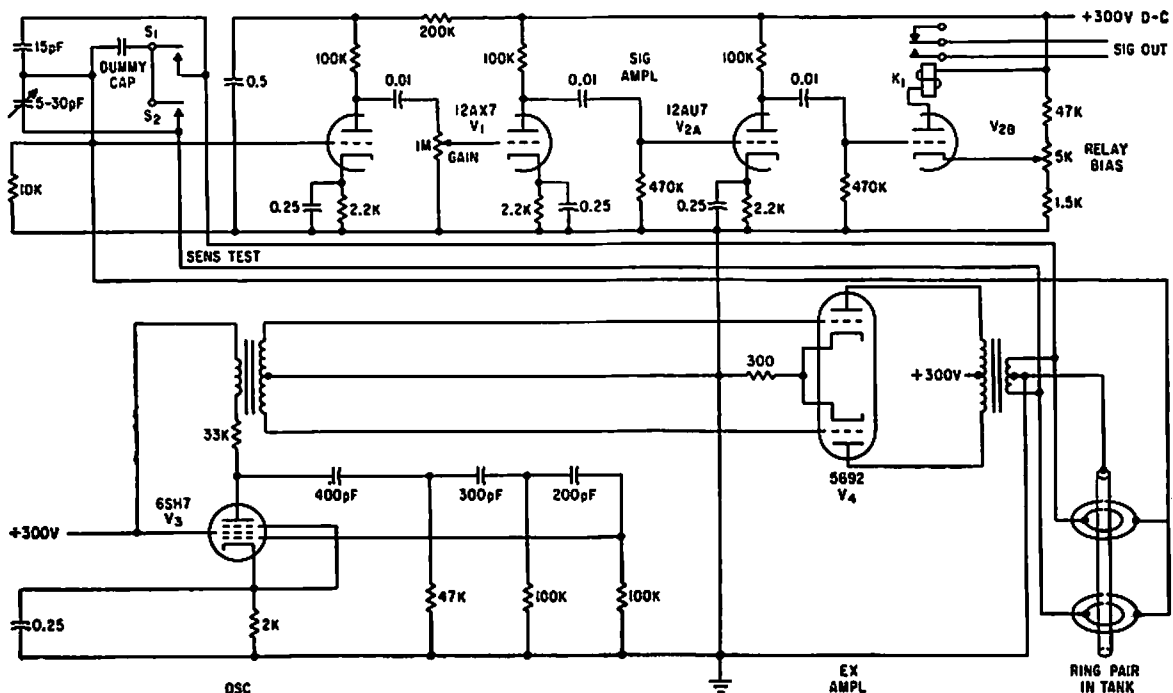
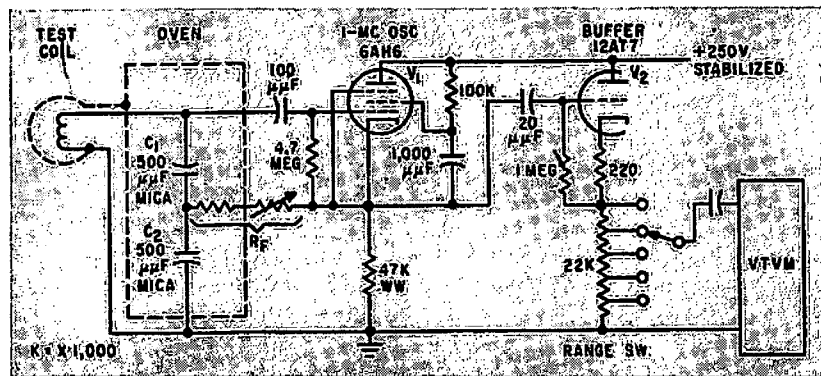
value for 180° phase shift.—M. R. Deveraux, Diamond Circuit Measures Phase Shift, *Electronics*, 37:25, p 74-75.





After amplification in V5-V6, this voltage is rectified for d-c milliammeter.—P. Dick, Measuring Thickness of Paramagnetic Coatings, *Electronics*, 34:8, p 48-50.

WIRE THICKNESS GAGE—Uses principle of proximity detectors for nondestructive measurement of moving copper wire thickness during drawing operation or on coil-winding machine. Wire passing through test coil acts like shorted turn of transformer, lowering Q of coil. Stable 1-Mc oscillator and buffer drive vtvm that indicates variations in wire diameter for sizes down to AWG 46. —K. H. Jaensch, Wire Gage Provides Continuous Measurement, *Electronics*, 33:7, p 109-111.

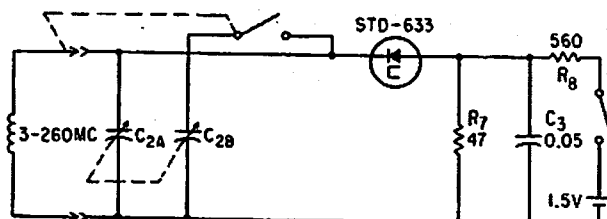
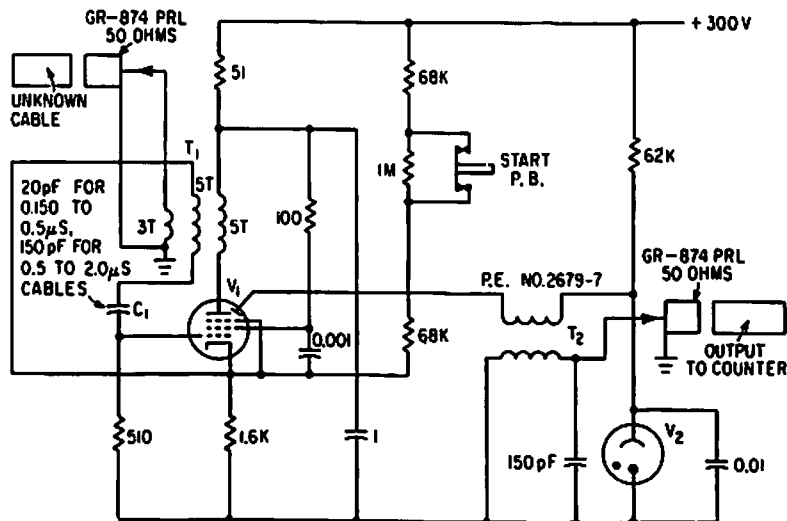


LIQUID LEVEL—Nonconducting liquids change electrostatic capacitance of ring electrodes in tank. Amplified error signal from electrode

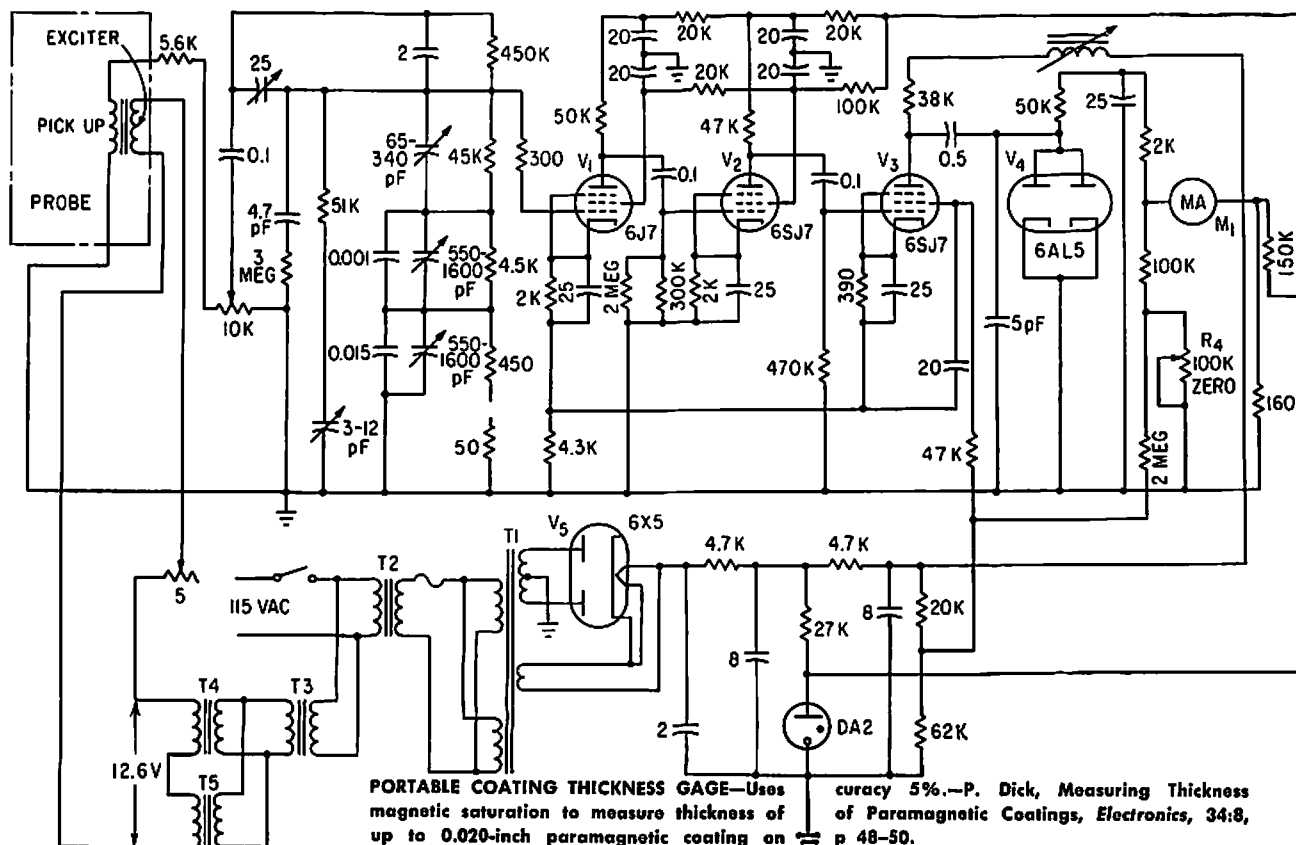
bridge operates recording galvanometer or indicator lamps to provide measurements of level accurate to 0.01 inch.—T. L. Greenwood,

Capacitance Change Indicates Liquid Levels, *Electronics*, 33:34, p 66-67.

3-260 MC TUNNEL-DIODE DIP METER—Oscillator is tunable in six ranges, using plug-in circuits.—E. Gottlieb and J. Giorgis, Tunnel Diodes—Using Them as Sinusoidal Generators, *Electronics*, 36:24, p 36-42.

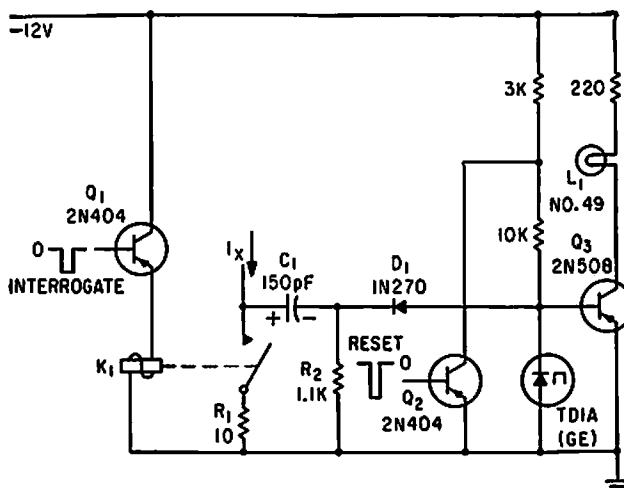
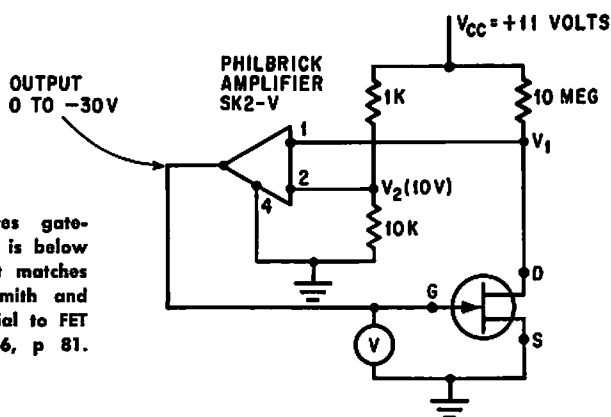
**BLOCKING OSC****VOLTAGE REG**

CHRONOTRON CIRCUIT MEASURES COAX DELAY—Secondary-emission tube V1 (EPP-60) in blocking oscillator generates millimicrosec pulses that are fed to unknown cable through anode transformer. Pulse reflected back from open-circuited end of cable is coupled into grid through same transformer to initiate new pulse. Resulting pulse repetition rate, proportional to cable delay, is measured with 10-Mc digital counter.—E. F. Laine, Getting Subnanosecond Precision in Coax Cable Delay Measurements, *Electronics*, 36:5, p 39-41.

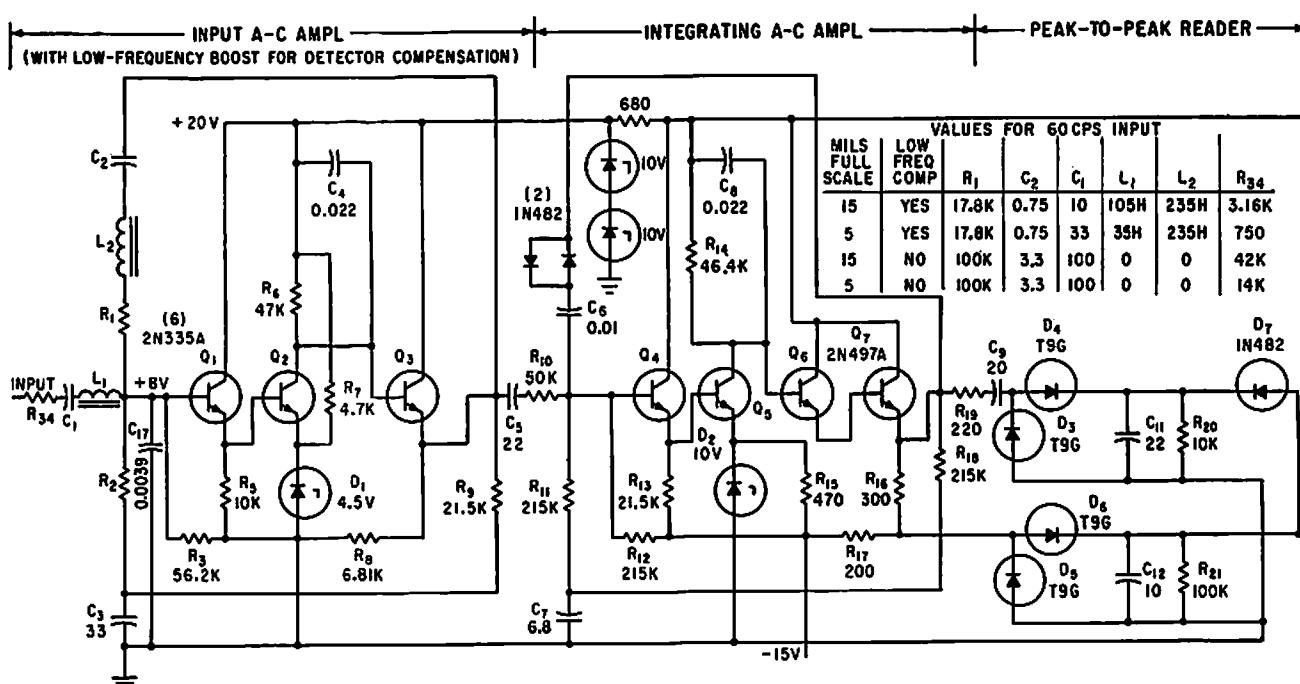


PORTABLE COATING THICKNESS GAGE—Uses magnetic saturation to measure thickness of up to 0.020-inch paramagnetic coating on nonmagnetic base. Weight is 40 lb, and accuracy 5%.—P. Dick, Measuring Thickness of Paramagnetic Coatings, *Electronics*, 34:8, p 48-50.

FET PINCHOFF VOLTAGE—Measures gate-source voltage while drain current is below 0.1 microamp, to give value that matches pinchoff voltage of fet.—B. R. Smith and I. C. Chase, Matching Gate Potential to FET Pinchoff Voltage, *Electronics*, 38:16, p 81.



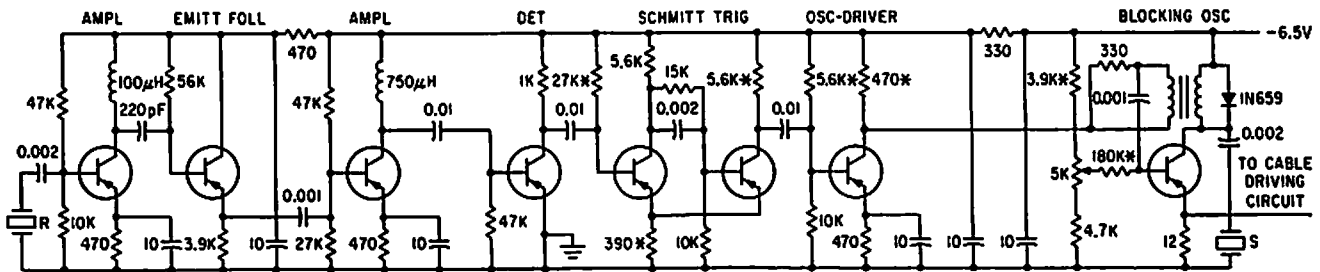
LOW-LEVEL CURRENT DETECTOR AND MEMORY—Unknown current I_x charges C_1 . Operation of relay K_1 by interrogate pulse discharges C_1 through tunnel diode, initiating switching of diode if in low-voltage state and unknown current is correct polarity. Lamp in transistor amplifier glows when tunnel diode is in high-voltage state. Currents of one picoampere can be measured.—C. D. Todd, Tunnel Diode Detects Currents Down to 100 Femtoamperes, *Electronics*, 36:14, p 33-37.



VIBRATION DETECTOR—Low-frequency boost compensates for characteristics of velocity-type vibration detector for turbines. Detector voltage is proportional to both displacement

and frequency, so integrating action by capacitance feedback around high-gain amplifier stage makes output proportional to displacement only.—H. A. Harriman and

W. M. Trenholm, Vibration Measurements with Peak-Reading Circuit, *Electronics*, 35:20, p 57-59.



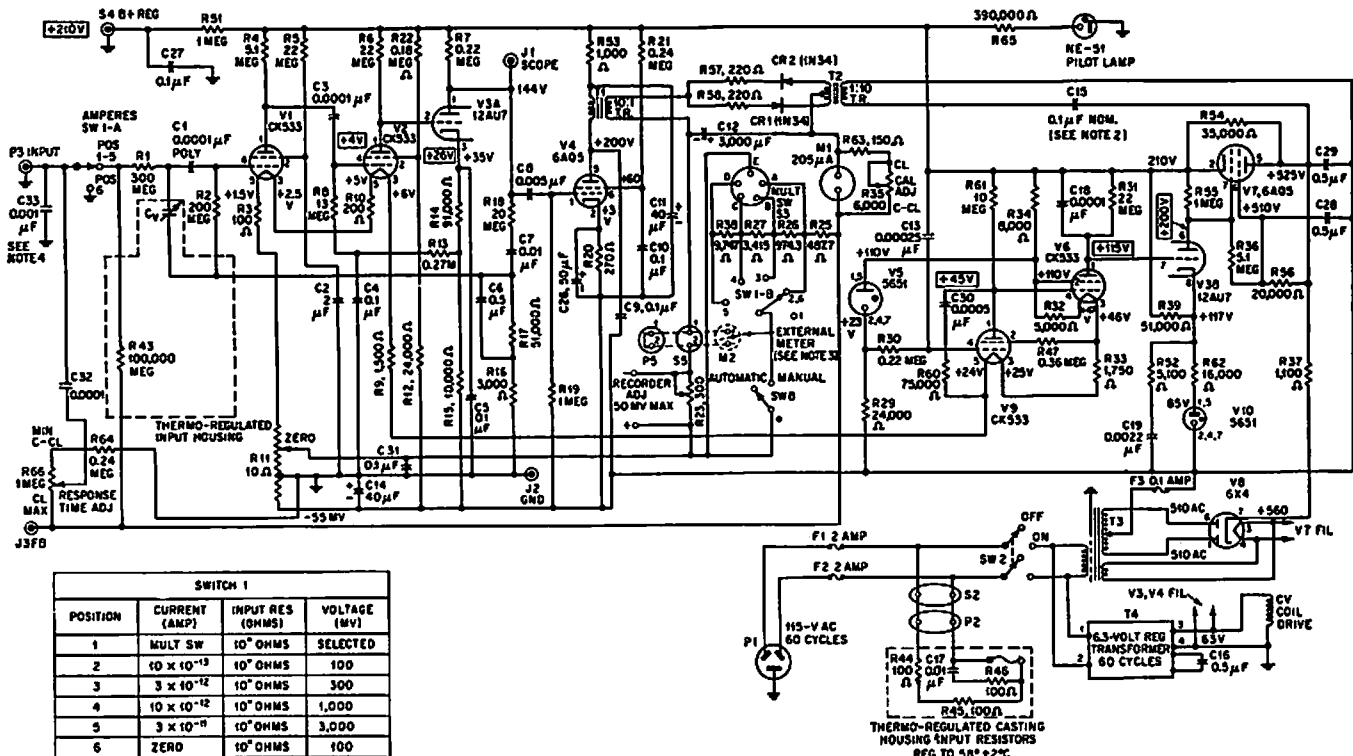
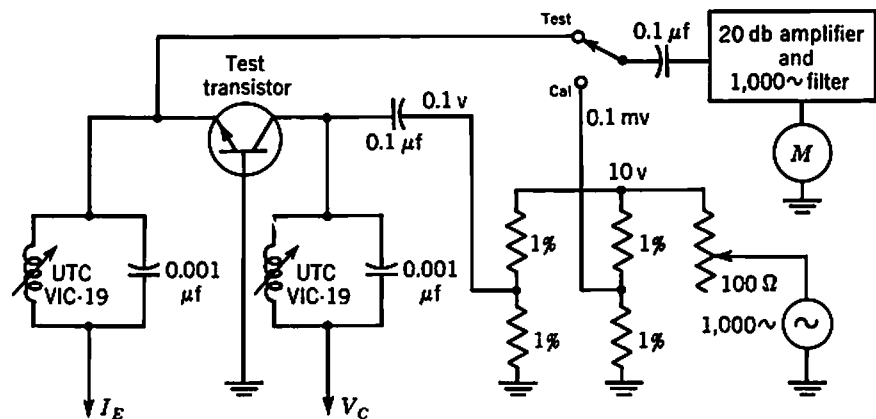
ALL TRANSISTORS TYPE 2N393

DEEP-OCEAN VELOCIMETER—Sonic pulse produced by transducer S is sent along fixed-length water path at any depth and picked up by receiving transducer R. Received sig-

nal is then amplified and fed back to blocking oscillator to synchronize it at repetition rate determined by delay time of pulse in water. Pulses then go to cable-driving cir-

* VALUE DEPENDS UPON SOUND PATH
cuit, and through cable to surface vessel for counting.—L. Dulberger, Deep-Ocean Velocimeter Aids Sonar Systems Design, *Electronics*, 34:22, p 41-43.

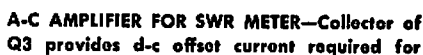
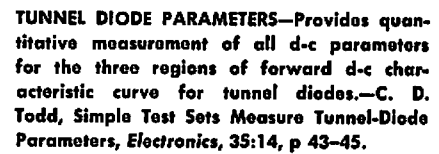
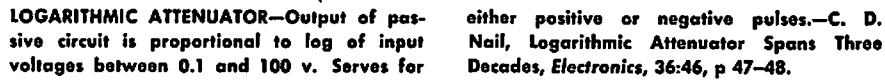
MEASURING TRANSISTOR REVERSE-VOLTAGE TRANSFER RATIO—When output voltage is held constant, 10-mv scale of output meter gives direct readings of small-signal value of open-circuit reverse-voltage transfer ratio h_{rb} over range of 0.0001 to 0.001.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 72.



MICROMICROAMMETER—Uses vibrating capacitor, electrometer amplifier, and electronic servo in which amplifier attempts to main-

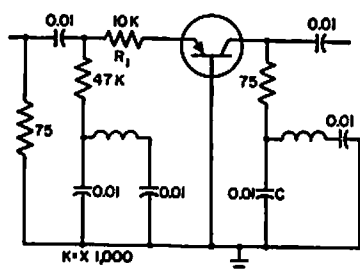
tain input at ground potential regardless of magnitude of voltage developed across input resistor by very small signal current being

measured.—G. C. Carroll, "Industrial Instrument Servicing Handbook," McGraw-Hill, N.Y., 1960, p 6-6.

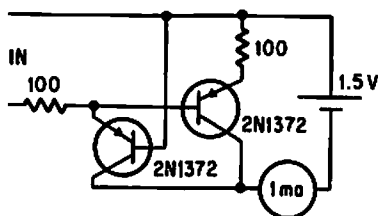


expanding a segment of the 10-db scale of the meter. Gain adjustment permits any of

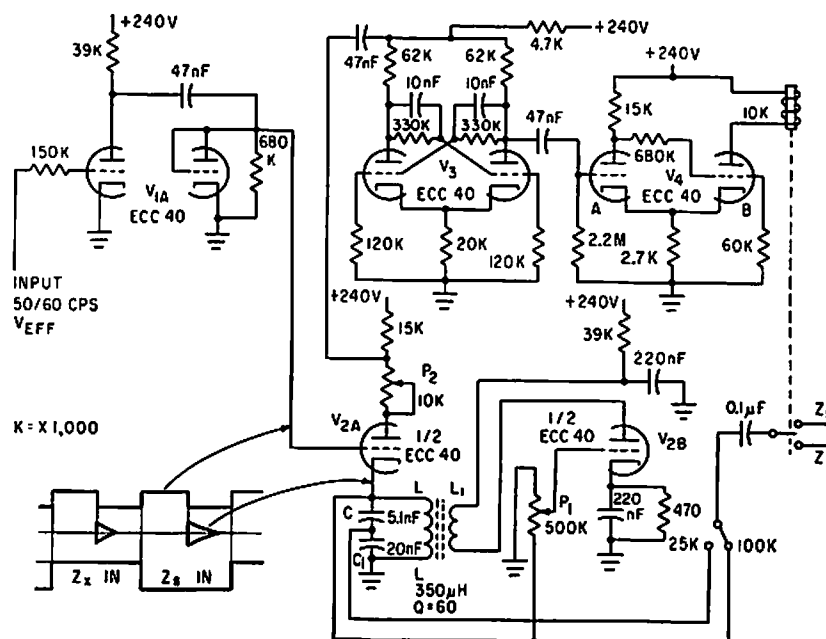
the four 2.5-db expand ranges to give a full-scale reading.—D. I. Howard, Drift Control Allows Expansion Scales for SWR Meter, Electronics, 35:21, p 45-47.



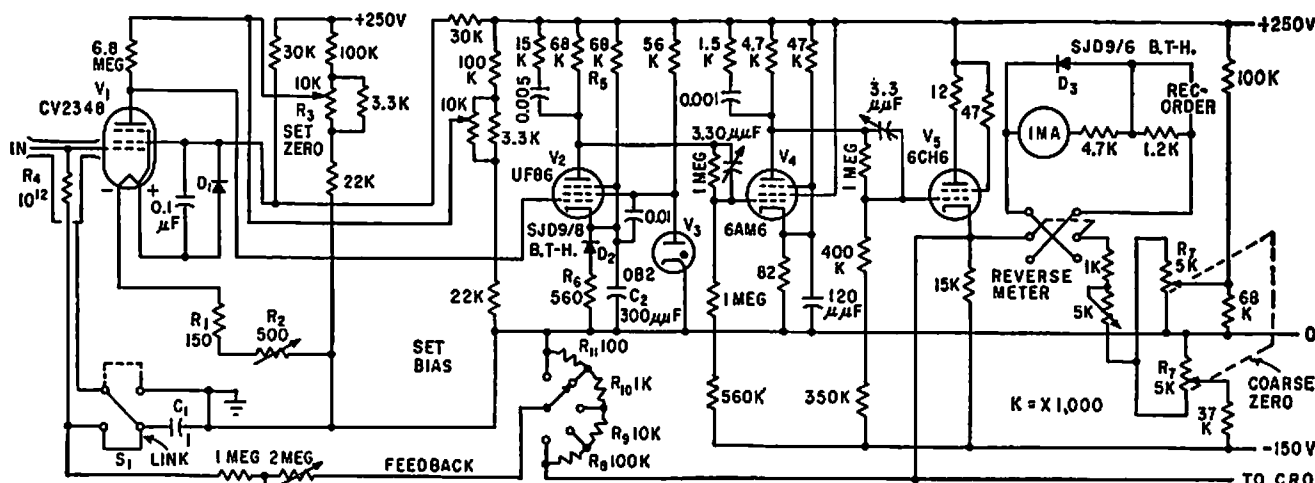
ALPHA CUTOFF—Measured with 3% accuracy up to 30 Mc and 5% up to 100 Mc. Method compares transistor to short-circuit. —G. I. Turner, *Measuring Transistor Alpha Cutoff*, *Electronics*, 32:1, p 54.



MILLIOHMETER—Substitution of transistors for diodes in rectifier circuit of a-c milliohm-meter gives significant increase in sensitivity and linearity. Uses inexpensive milliammeter.—P. Leferts, **Transistors Replace Diodes in Milliohm-meter Circuit**, *Electronics*, 39:18, p 97.



RESISTANCE COMPARATOR—Known and unknown resistances are connected alternately across shock-excited oscillator by flip-flop-driven relay, and damping effect is observed on cro.—A. Kislovsky, Comparing Resistances with Oscillator and Oscilloscope, *Electronics*, 34:23, p 118.



ELECTROMETER FEEDBACK AMPLIFIER—Measures currents in range of 10^{-11} to 10^{-13} amp by passing current through high-value precision resistor and amplifying voltage drop

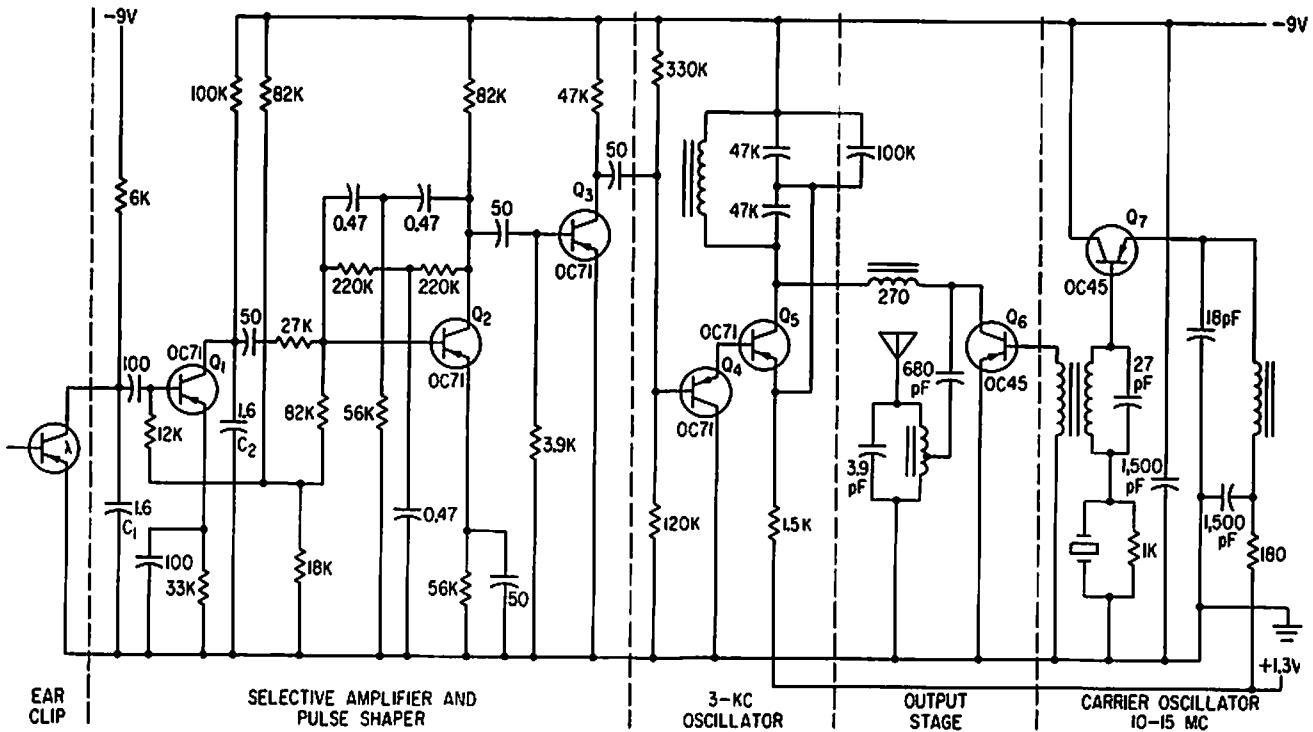
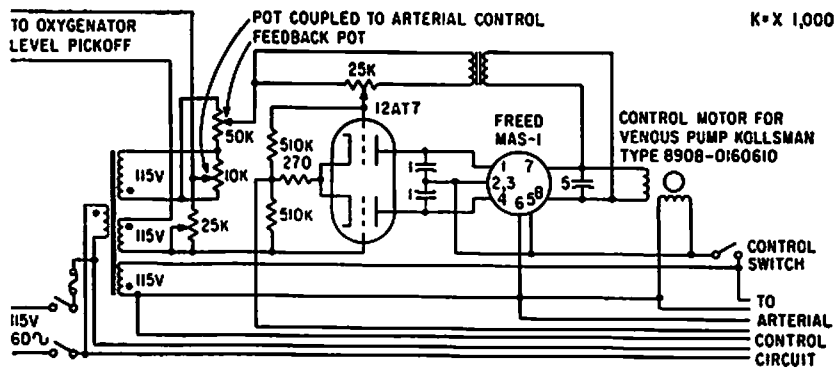
across resistor with direct-coupled amplifier of electrometer. British CV2348 is similar to CK5886. Bandwidth is 7.5 Mc. Zener diode D3 provides meter overload protection

by clamping at about 20% overload.—D. Allenden, Using Feedback in Electrometer Design, *Electronics*, 32:41, p 71-73.

CHAPTER 50

Medical Circuits

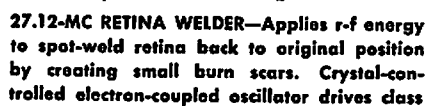
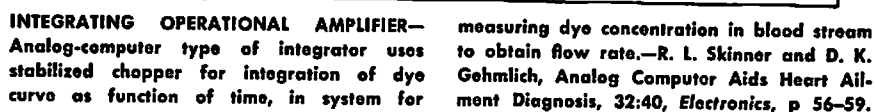
BLOOD FLOW CONTROL—Used to correct long-term differences in flow rates of venous and arterial pumps. Venous control portion of heart-lung amplifier has two inputs, a signal derived from oxygenator level error and an input from potentiometer connected to arterial transmission control lever.—R. Roberts and J. Loeffler, *Automatically Controlled Heart-Lung Machine, Electronics*, 33:31, p 91-93.



HEARTBEAT TRANSMITTER—Self-contained device worn by patient transmits his pulse to radio receiver for remote monitoring or recording. Photo-transistor, fed separately,

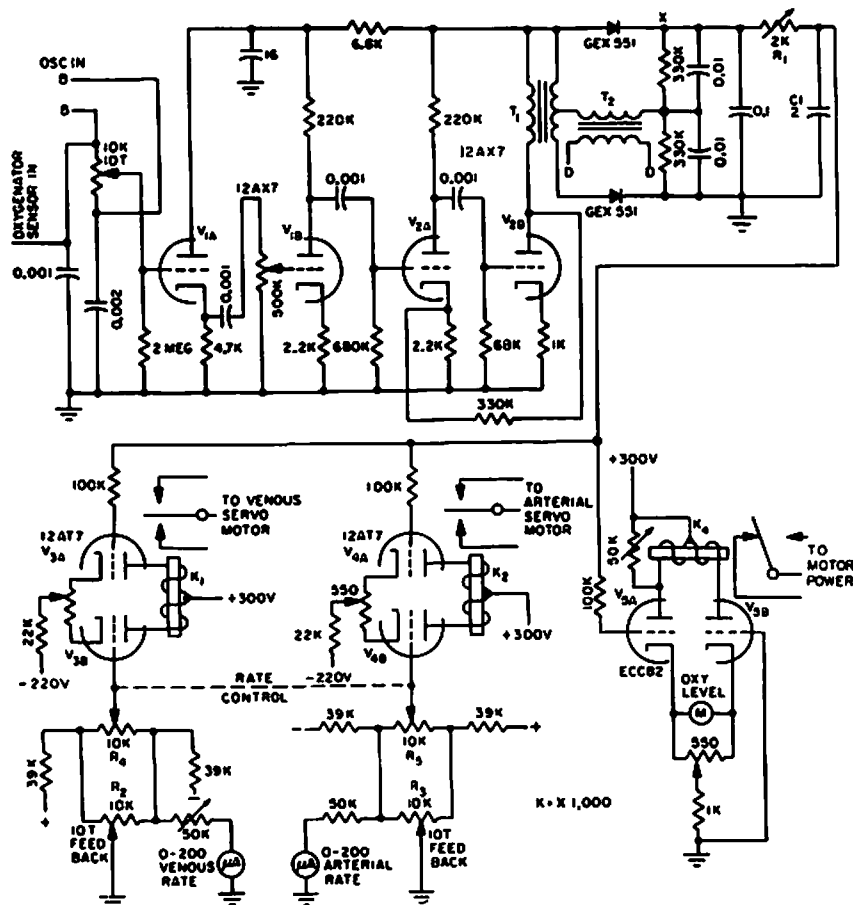
measures changes in light transmitted through earlobe as heart pulses change blood density and volume of lobe.—G. A. Harten and A. K. Koroncai, Radio Transmitter for Remote Heart-

beat Measurements, *Electronics*, 33:52, p 54-55.

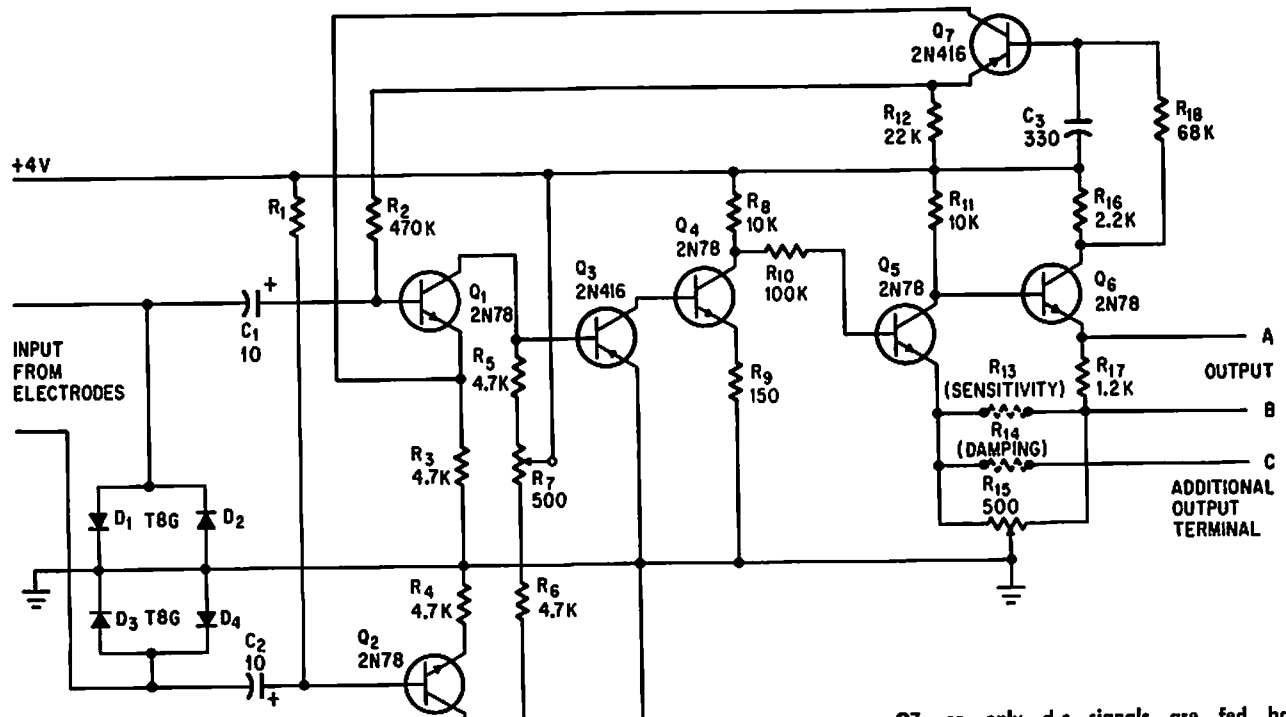


C power amplifier V2. Sample of output is taken through C3, detected in V8, and applied to grid of V5, which amplifies output changes and applies them to grid of

clamp tube V3 to restore output of V2 to desired level.—O. Rich, Jr. and R. V. Hill, R-F Spot Welder Reattaches Retina of Human Eye, *Electronics*, 34:32, p 160-163.



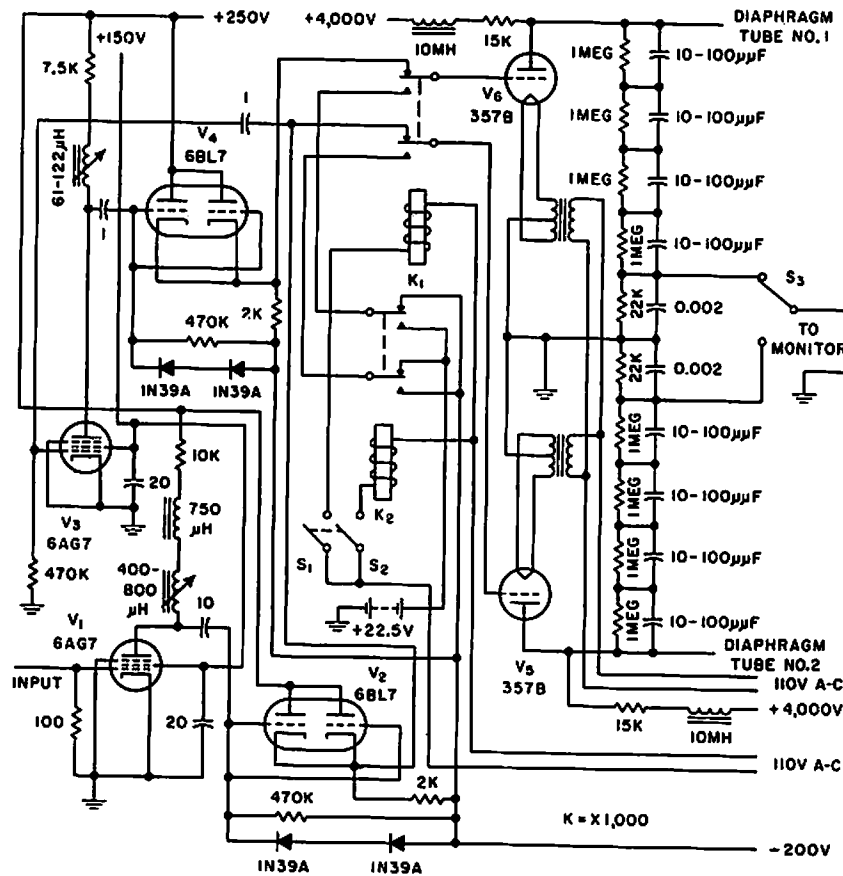
BLOOD-VOLUME SERVO—Servo-controlled pump with variable stroke drives blood from venous system of patient into artificial lung and after oxygenation returns it to arterial system. Control circuit insures that volume of blood is constant. Sensor is brass disk forming capacitance with pool of blood in oxygenator at spacing of 1 mm. Error signal derived from capacitance change unbalances bridge that is energized at 3 kc (points B-B). Amplified error signal is applied to phase-sensitive demodulator. Unbalance energizes center-stable relays K1 and K2 of arterial and venous servo motors, so stroke output of arterial pump is decreased while that of venous pump is increased, or vice versa, to restore preselcted volume of blood.—R. Schild and N. Wesson, *Servo Circuit Controls Artificial Heart*, *Electronics*, 31:15, p 73-75.



TEMPERATURE COMPENSATION IN ECG AMPLIFIER—Circuit has common-mode rejection ratio of 10,000, with adjustable cancella-

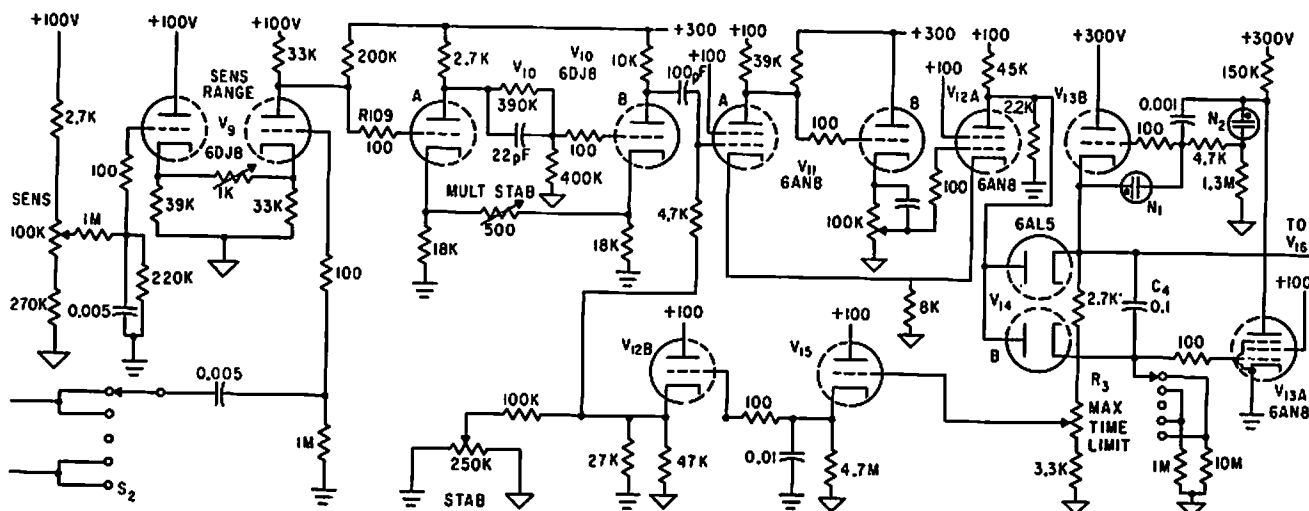
tion of unbalanced noise at input, for electrocardiograph. For temperature compensation, C3 bypasses a-c signals from base of

Q7, so only d-c signals are fed back through this transistor to Q1.—J. R. Smith, Jr., *Amplifier Can be Adjusted to Cancel Unbalanced Noise*, *Electronics*, 37:23, p 60-61.



X-RAY TUBE PULSER—Supplies 3,600 v peak-to-peak pulses, swinging from 400 to 4,000 v. Input signal comes from square-wave generator having adjustable duty cycle of 10 to 90% from 35 to 100,000 cps. Uses two pulsating x-ray tubes, each controlled by applying low-voltage square-wave to special

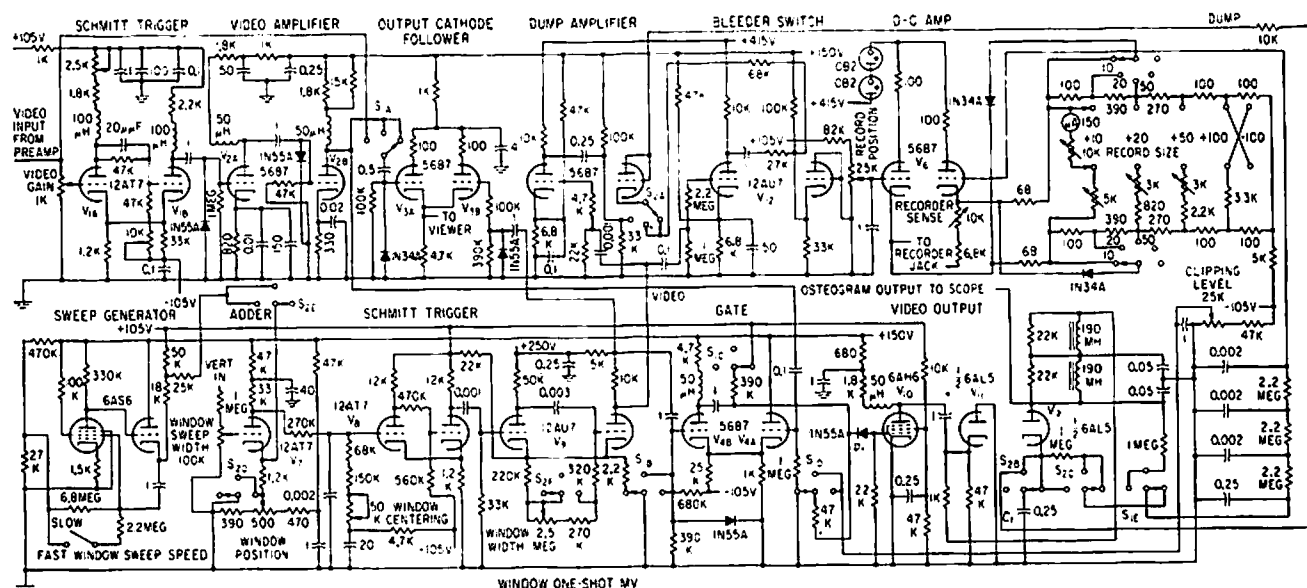
diaphragm element. Anode current is maintained constant by switching alternately between tubes. Used for delivering therapeutic dose levels.—E. F. Weller, Roof-Top-Target Tubes Pulse X-Rays, *Electronics*, 31:11, p 138-139.



TRIGGER SHAPING FOR RETINA WELDER—Trigger pulse, selected by S2, is compared to fixed bias on one half of comparator tube V9. Trigger shaping by V10 provides

strong, sharp pulse for gating mvbr V11-V12A, which turns off diodes V14, allowing Miller integrator V13A and cathode follower V13B to start time base runup that drives

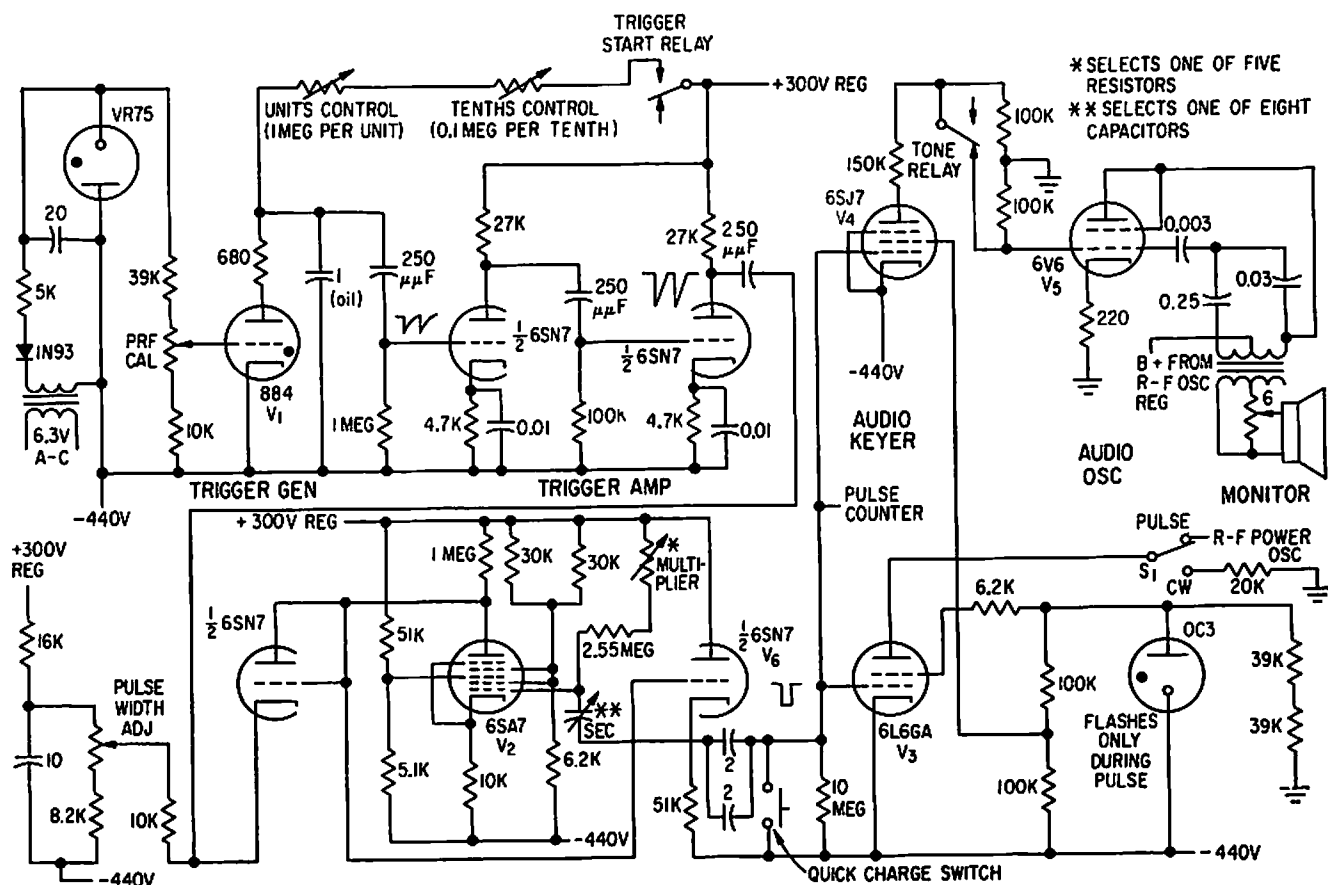
C4 to 150 v. Circuit then reverts to normal.
—O. Rich, Jr. and R. V. Hill, R-F Spot Welder
Reattaches Retina of Human Eye, *Electronics*,
34:32, p 160-163.



OSTEOGRAPH DETECTS BONE DISEASE—Electronic scanner using television flying-spot microscope measures irregular microscopic tissue areas of spongy bone, for early

diagnosis of bone disease. Television monitor receiver shows enlarged picture of bone section as aid in centering area to be scanned. Recorder plots ratio between bone

area and marrow area.—O. W. Jones III, R. V. Vreeland, and C. C. Collins, *Video Microplanimeter Detects Bone Disease*, *Electronics*, 31:43, p 85-87.



K=X1,000

ULTRASONIC OSCILLATOR KEYER—Generates keying pulses up to 2 sec wide at prf down to 0.1 pps. Oscillator cutoff bias is gated off during pulse operation and switched off during c-w operation. Pulse repetition gener-

PULSE GENERATOR

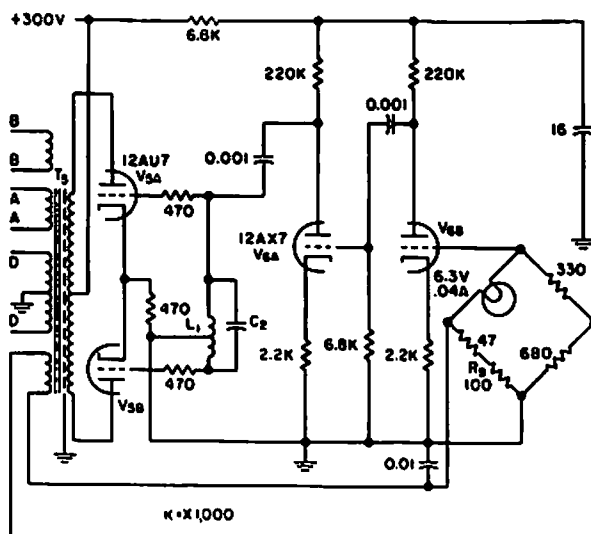
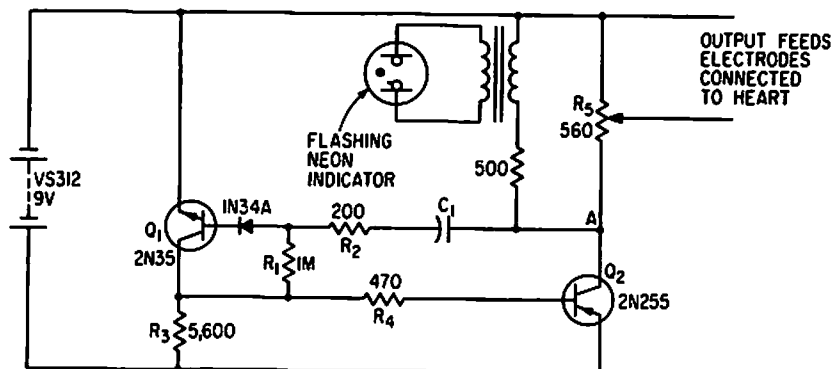
ator V1 is relaxation oscillator with trigger period variable in 0.1-sec steps from 0.1 to 10.9 sec. Fast-recovery phantastron pulse generator V2 allows precise pulsing up to 90% duty cycle, with pulse lengths from

0.005 to 2 sec.—B. J. Cosman and T. F.

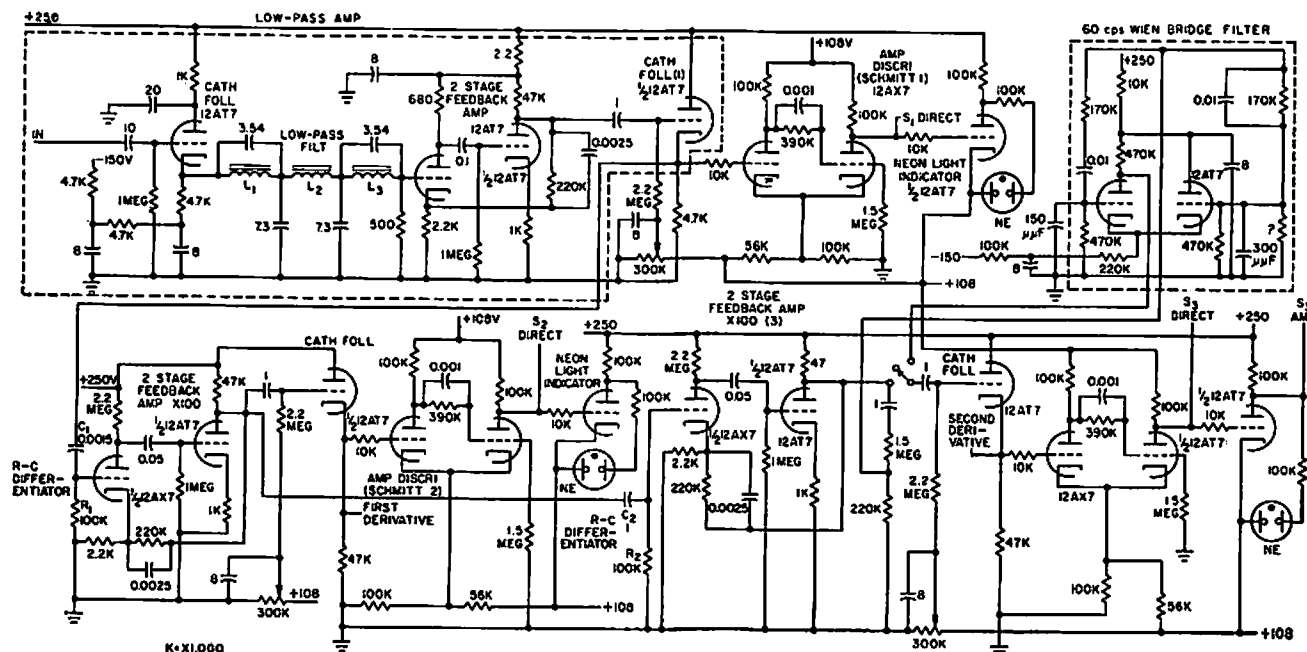
Hueter, *Instrumentation for Ultrasonic Neurosurgery*, *Electronics*, 32:20, p 53-57.

TWO-TRANSISTOR CARDIAC PACEMAKER—

PRODUCES triggering pulses that stimulate heartbeats during surgery. Repetition rate is determined by C1 and R1. Pulse duration is 4 millisecc, with 8-v peak that sends 16 ma through 500-ohm load.—W. E. Gilson and H. F. Klinge, Cardiac Pacemaker Triggers Heartbeats, *Electronics*, 34:40, p 80.



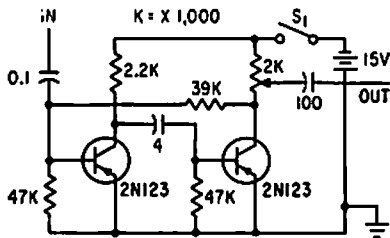
THREE-OUTPUT 3-KC OSCILLATOR—L-C oscillator provides carrier voltages of 4 v rms at 3 kc to blood-volume servo amplifier and to venous and arterial pressure indicator. Amplitude stabilization is achieved by bridge feedback network using filament-type lamp as nonlinear element in one bridge arm.—R. Schild and N. Wesson, *Servo Circuit Controls Artificial Heart, Electronics*, 31:15, p 73-75.



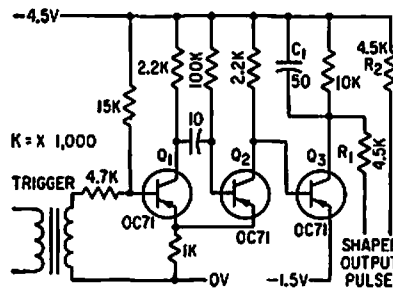
EEG WAVEFORM ZERO DETECTOR—Uses Schmitt triggers to produce output of one value when input signal exceeds preset

reference, and produces output of one other value when input signal is less than reference value.—C. J. Zaandor, Computer Ang-

lyzes Brain Waveforms, *Electronics*, 31:29, p 68-72.

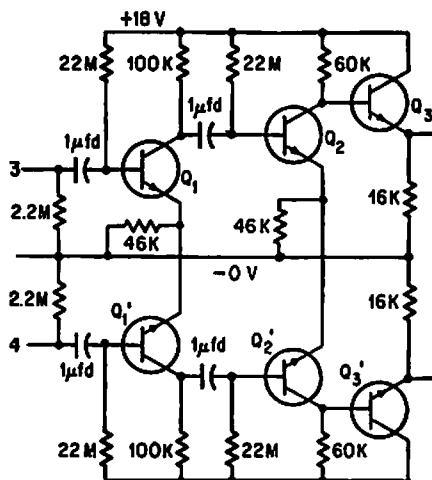
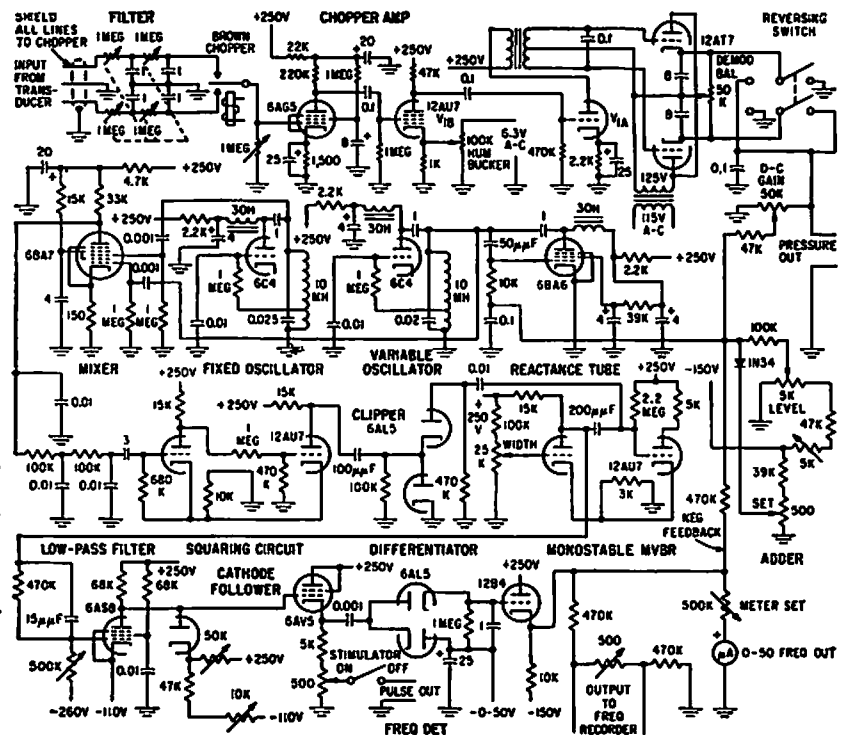


HEART-STIMULATING PULSE OUTPUT STAGE—Used in conjunction with pulse amplifier to increase stimulating voltage when scar tissue develops under electrodes sewn to auricle and ventricle of heart, introducing excessive load resistance. Produces constant-voltage positive-going pulse having maximum amplitude of 15 v and 0.1-sec duration, when triggered by positive pulse.—G. F. Vanderschmidt, Two-Transistor Amplifier Corrects Heart Block, *Electronics*, 31:47, p 80-81.

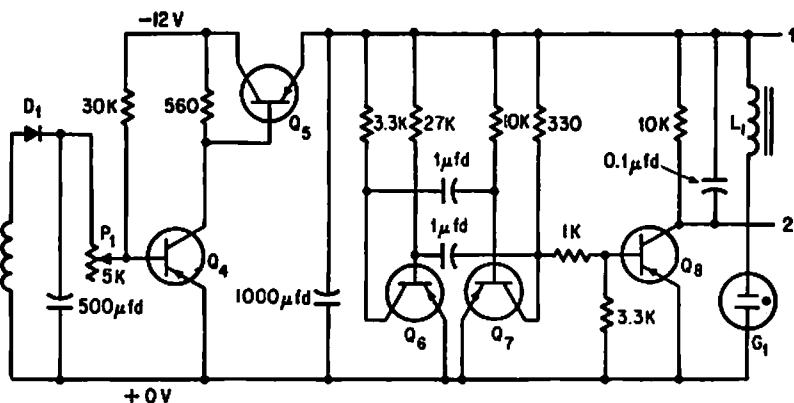


RETINA STIMULATOR—Generated pulse is applied to skin near eyes, to act on nerve cells of retina and give same effect as slight flash of extremely short duration.—P. Scott, Microflash and Pulse Stimulator Tests Human Optical Response, *Electronics*, 34:27, p 48-51.

BLOOD PRESSURE REGULATOR—Chopper amplifier delivers d-c voltage to adder that is proportional to mean blood pressure. Potentiometers in adder permit introducing negative voltages corresponding to desired blood pressure level and maximum safe level.—R. L. Skinner, D. K. Gehmlich, and F. W. Longson, Blood Pressure and Heart Regulator, *Electronics*, 32:1, p 38-41.



MYOELECTRIC STIMULATOR—Six-transistor amplifier having high-impedance differential input for commercial aeg or emg electrodes and gain of 10,000 from 5 to 10,000 cps drives modulator Q4-Q5 from decoupling transformer. Modulator makes stimulator (astable mvbr Q6-Q7) apply pulsating voltages to muscles of hand, to make hand open in response to signals picked up by

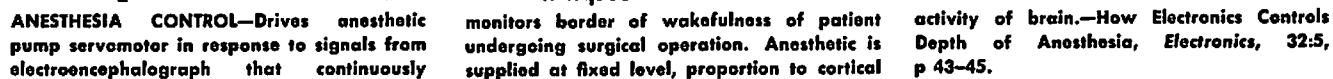
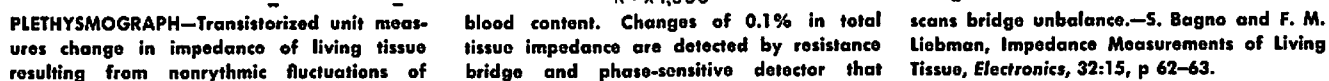
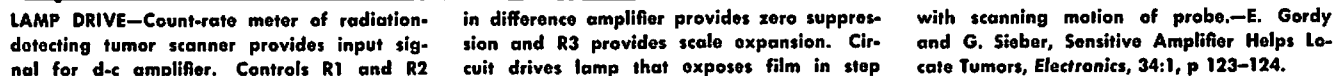


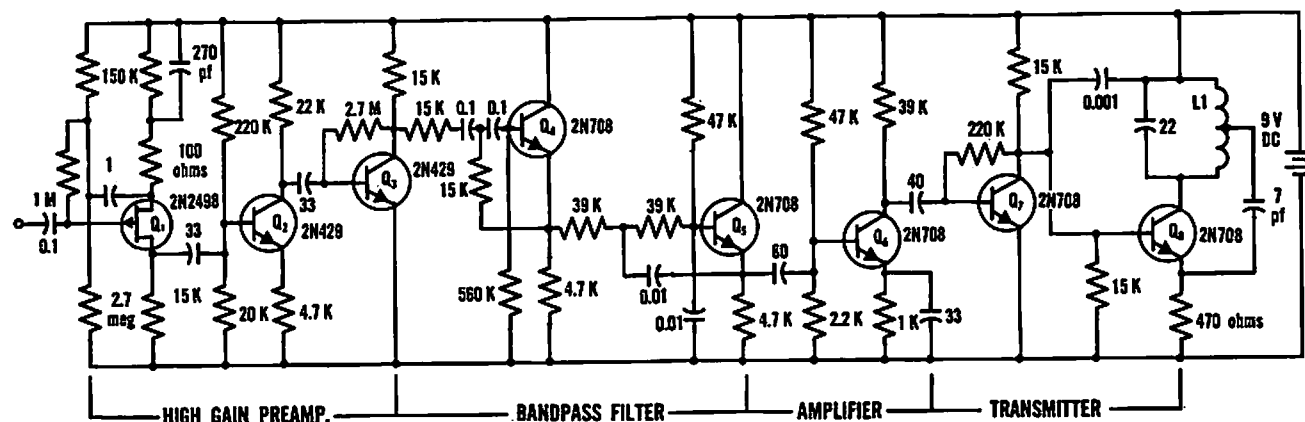
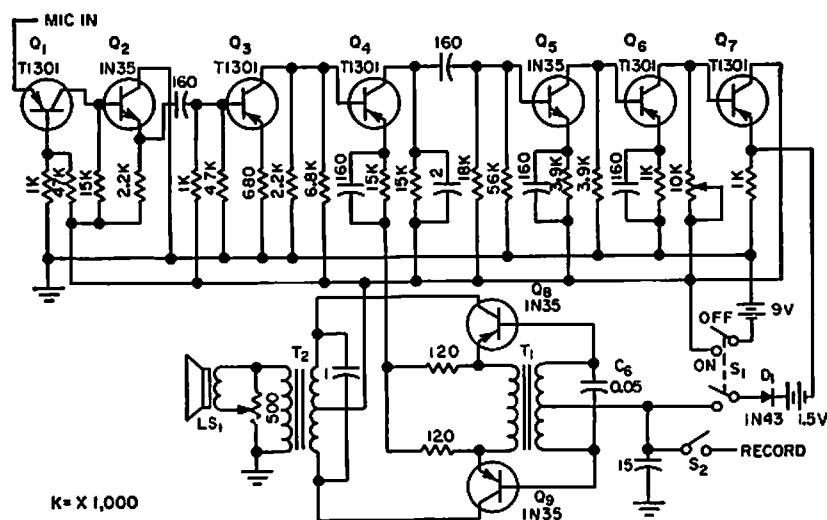
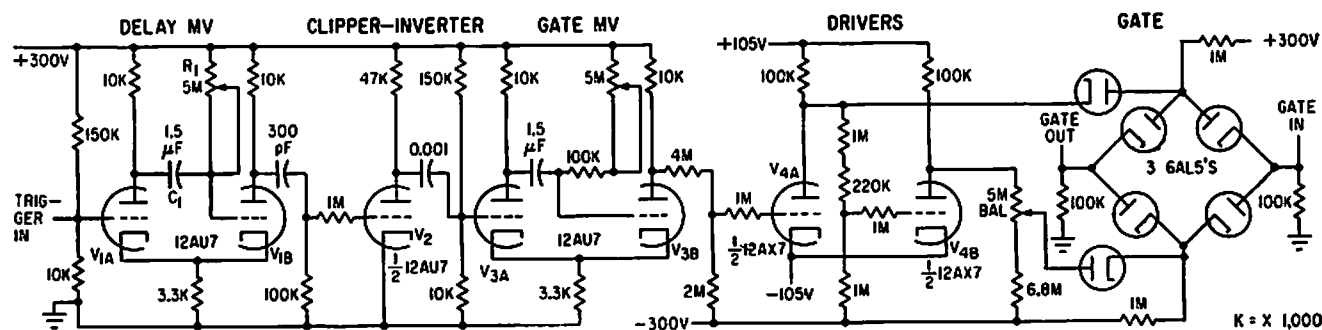
T IS LAFAYETTE T-230
D₁ IS IN34A
L₁ IS UTC-HQE-5 200 mH
G₁ IS NE-2

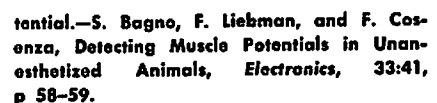
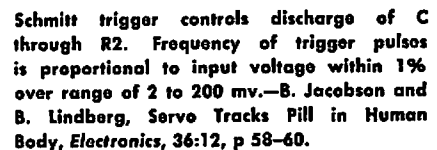
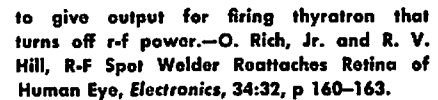
Q₁ - Q₁', Q₂ - Q₂' ARE 2N2644
Q₃ - Q₃' IS 2N2223
Q₄ IS 2N1305
Q₅ IS 2N1038
Q₆, Q₇ ARE 2N404
Q₈ IS 2N398

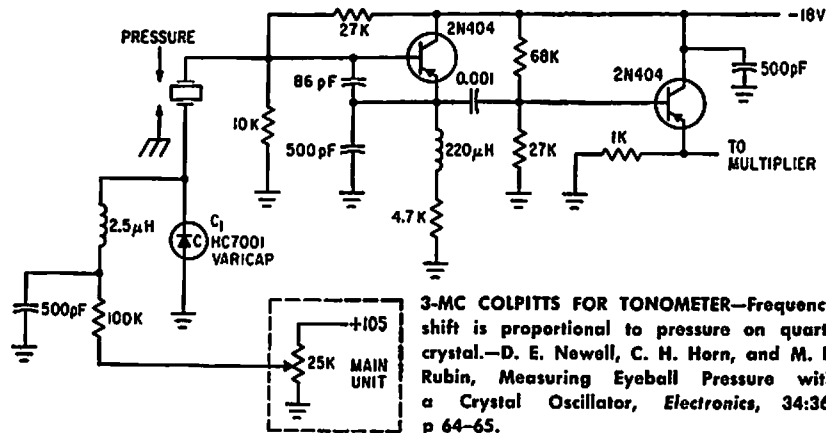
electrodes over shoulder muscles, thereby bridging severed arm nerves.—L. Vodovnik and W. D. McLeod, Electronic Detours of

Broken Nerve Paths, *Electronics*, 38:19, p 110-116.

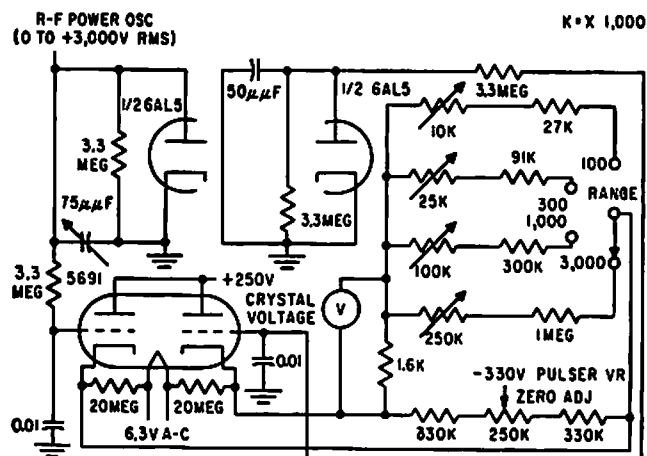
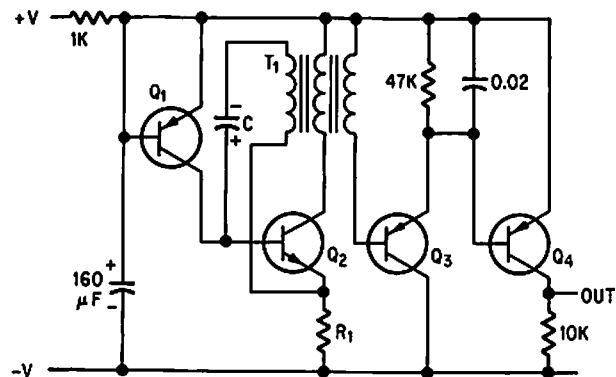








BATTERYLESS CARDIAC PACEMAKER—Body fluids are electrolyte for implanted silver and zinc electrodes that provide d-c power for four-transistor pacemaker. Secondary of T1 provides feedback for ringing-choke oscillator Q1, which charges C until Q2 is cut off. C then discharges until Q1 can again conduct.—O. Z. Roy and R. W. Wehnert, *Keeping the Heart Alive with a Biological Battery*, *Electronics*, 39:6, p 105-107.



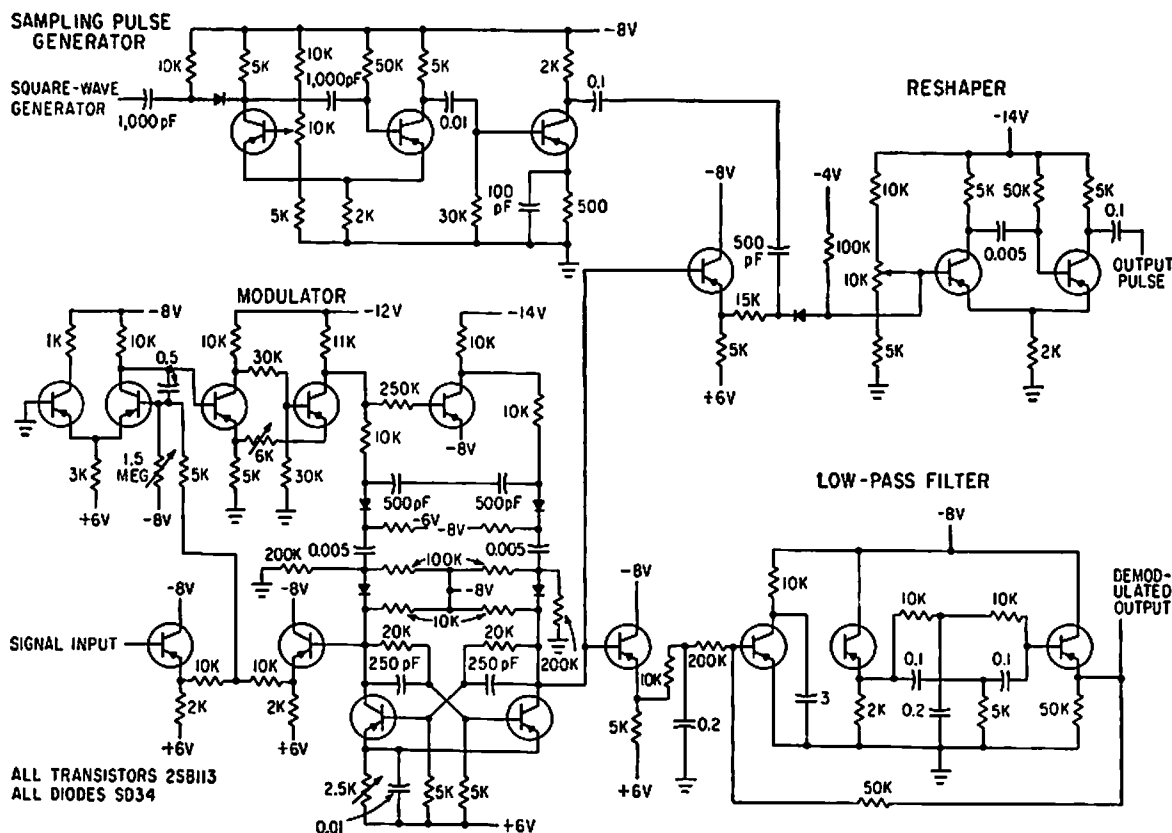
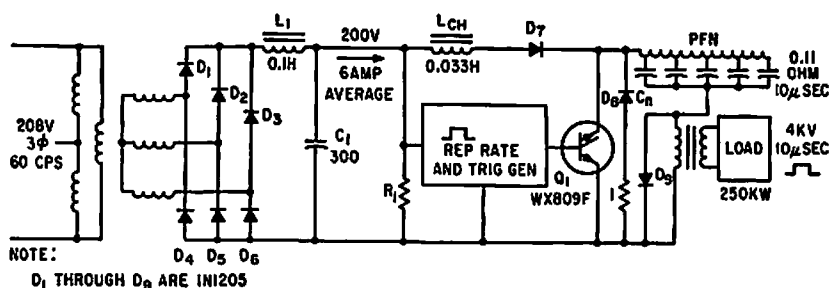
FOUR-RANGE VTVM—Measures r-f power oscillator output, for establishing irradiation and calibration procedures and positioning techniques when using focused ultrasound

for therapeutic treatment of deep-seated brain structures.—B. J. Cosman and T. F. Hueter, *Instrumentation for Ultrasonic Neurosurgery*, *Electronics*, 32:20, p 53-57.

CHAPTER 51

Modulator Circuits

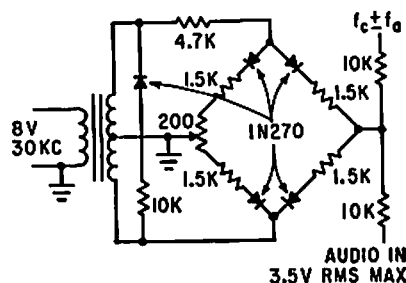
250 KW PEAK FROM SCR—Line-type modulator uses silicon diodes for high-voltage rectifiers, backswing, holdoff, and inverse-diode circuits. Trigger generator uses two-layer and four-layer diodes to provide pulse burst repetition rates up to 25 kc.—H. G. Heard, *Controlled Rectifier Produces Quarter-Megawatt Pulse Power*, *Electronics*, 34:25, p 54-55.



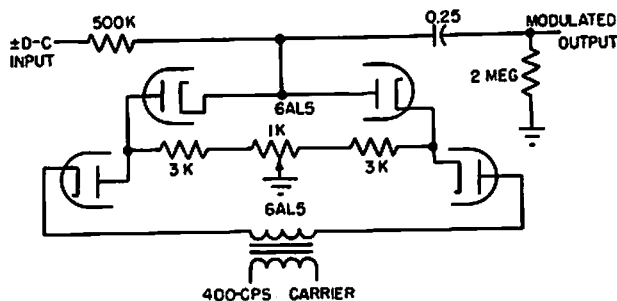
DELTA-SIGMA MODULATION FOR DIGITAL COMMUNICATIONS—Flip-flop sampling pulse generator supplies 5-microsec pulses at 3-kc ppr to modulator that also has analog signal

input. Integrated difference signal fires Schmitt trigger to provide positive output that opens gate, passing square-wave pulse that sets flip-flop. Output of flip-flop is fed

to emitter-follower and demodulated by active low-pass filter having 50-cps cutoff.—H. Inose et al, *New Modulation Technique Simplifies Circuits*, *Electronics*, 36:4, p 52-55.

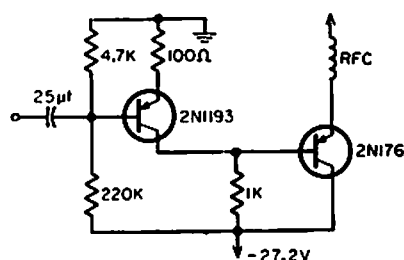


MODULATOR TRANSFORMER—Circuit provides best possible balance between halves of center-tapped secondary, as required for precise phase splitting, in Boynton-Scholt modulator shown.—Wide-Band Transformer Covers 3 Kc to 22 Mc, *Electronics*, 35:25, p 66.



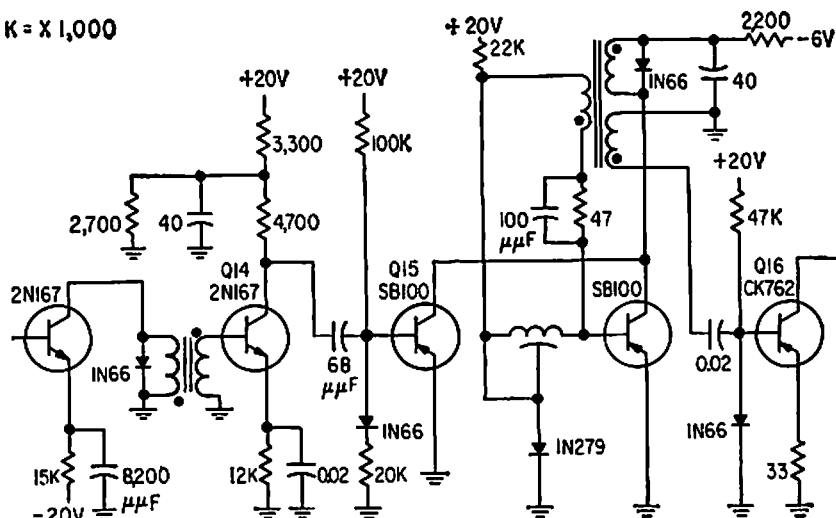
DIAMOND MODULATOR—Carrier signal turns electron tube or equivalent crystal diode on and off to modulate d-c input signal. Signal-noise ratio is about 1,500 to 1, with carrier

voltage of 19 v rms. Output is linear up to 3 v rms for d-c inputs up to 10 v.—L. S. Klivans, *Modulators for Automatic Control Systems*, *Electronics*, 31:1, p 82-84.



DIRECT R-C COUPLED COMMON-EMITTER MODULATOR—Performance is considerably better than that of conventional transformer-coupled collector-modulated 27-Mc CB modulator. Total current drain is 83 ma. 0.14 v gives full modulation. Power gain is nearly 20 db for 2N1193. Requires no audio transformers.—B. Rhinolder, *Modulation Techniques for Transistorized A-M Transmitters*, *EEE*, 11:7, p 54-57.

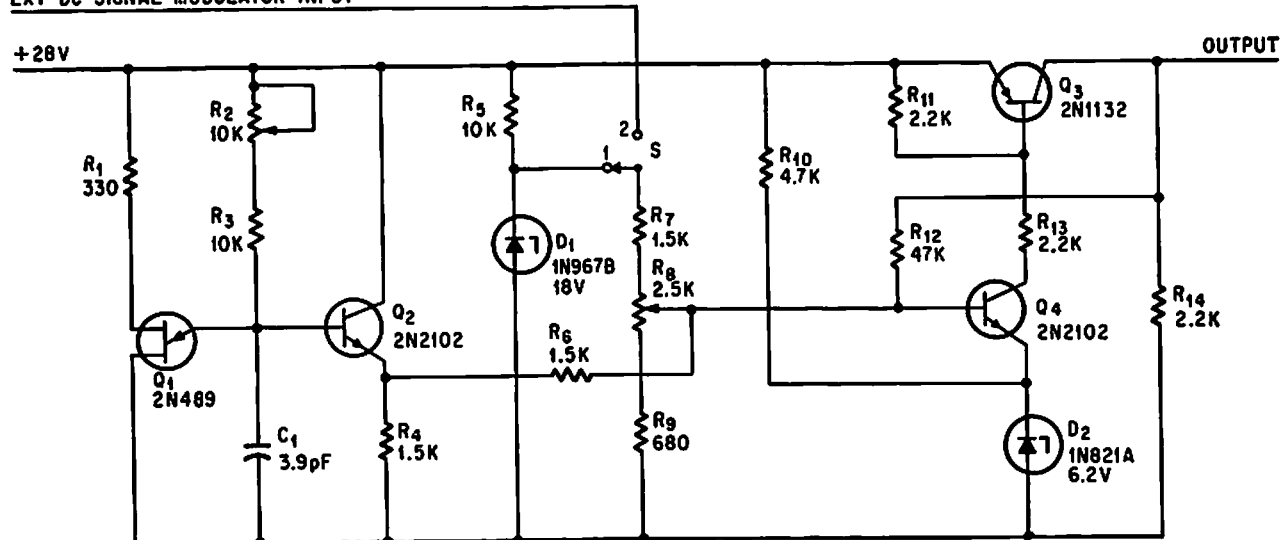
K = X 1,000



MODEM PULSE SHAPER—Removes audio component from modulator output to prevent crosstalk, and shapes pulse to required rise and fall time and width. Used in four-channel

ppm microwave multiplex unit.—P. W. Kiesling, Jr., *Portable Multiplexer for Telephone Communications*, *Electronics*, 32:2, p 60-62.

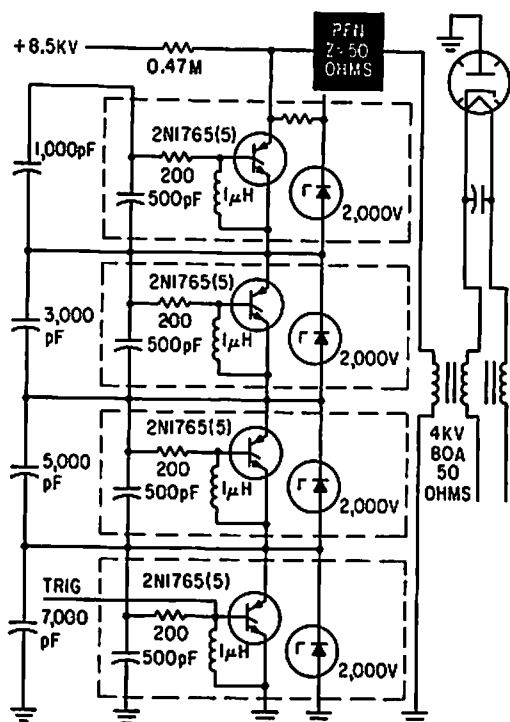
EXT DC SIGNAL MODULATOR INPUT



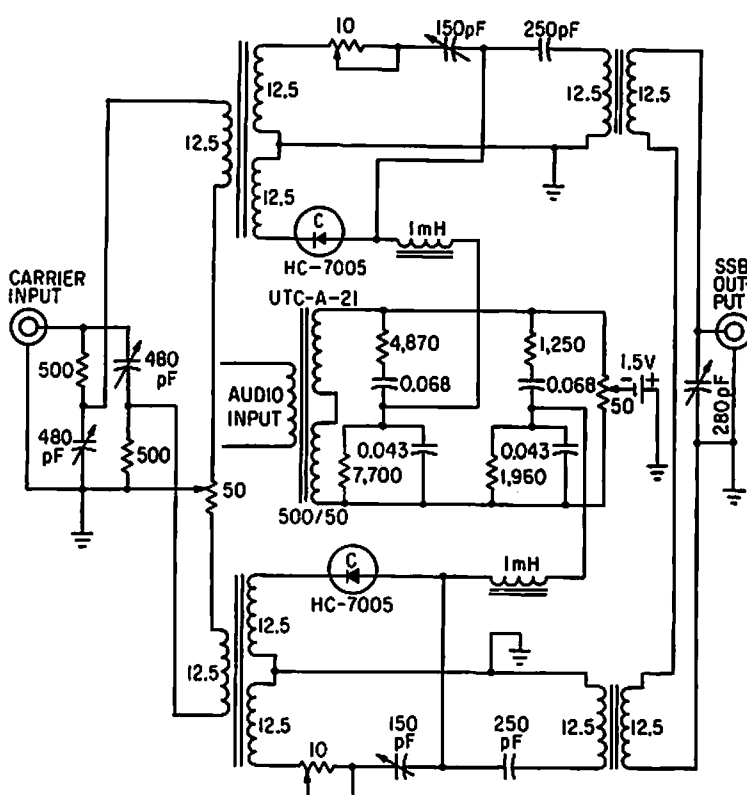
PWM—With S1 at 1, circuit is unijunction-transistor pulse generator with prf variable from 13 to 25 kc, and duty cycle variable

from 0 to 100%. With S1 at 2, circuit is modulator whose output pulse width varies

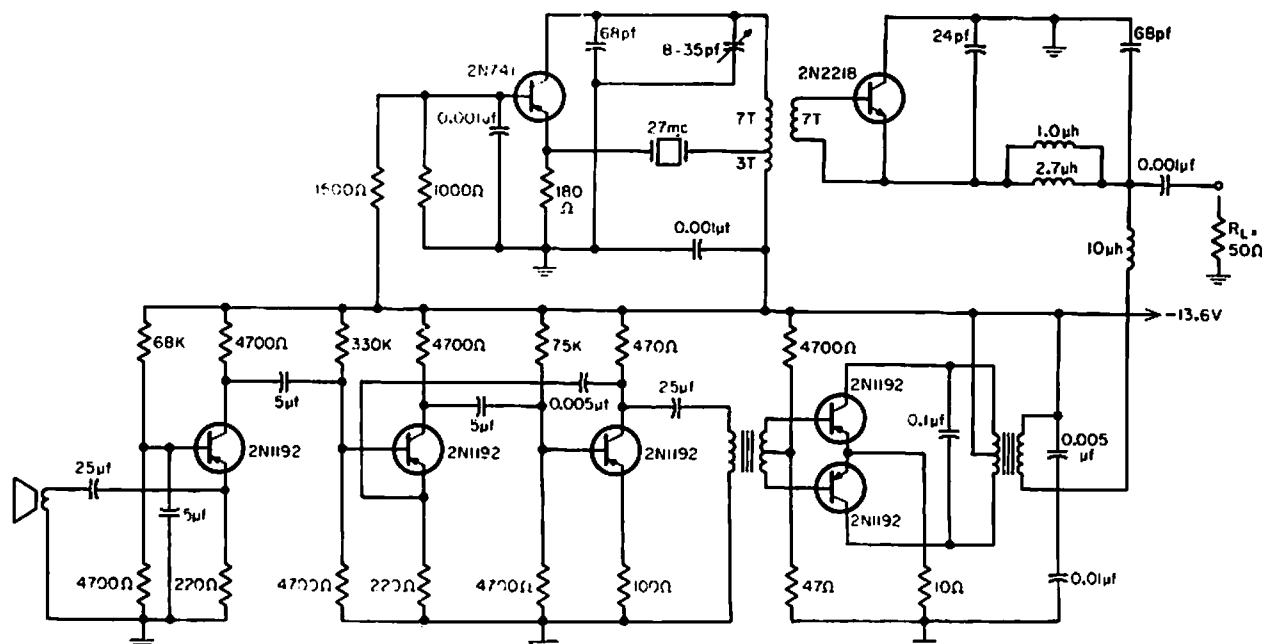
with level of external d-c signal.—D. L. Patillo, *Pulse Generator Circuit Doubles as Modulator*, *Electronics*, 38:8, p 91-92.



300-KW MAGNETRON MODULATOR—Trigger is applied to first of four 2-kv switch modules arranged in series, for simultaneous triggering of other modules to furnish 16-kv 20-amp pulse to type 7208 Ku-band coaxial magnetron through standard 50-ohm pulse-forming network PFN.—F. A. Gateka and M. L. Embree, Semiconductor Modulators for Modern Magnetrons, *Electronics*, 35:37, p 42-45.



CAPACITOR-BALANCED SSB—Output varies linearly with input over signal range of 0 to 4.5 v. Undesired sideband is suppressed 26 db at balance.—A. C. Todd, R. P. Schuck, and H. M. Sachs, Using Voltage-Variable Capacitors in Modulator Design, *Electronics*, 34:3, p 56-59.

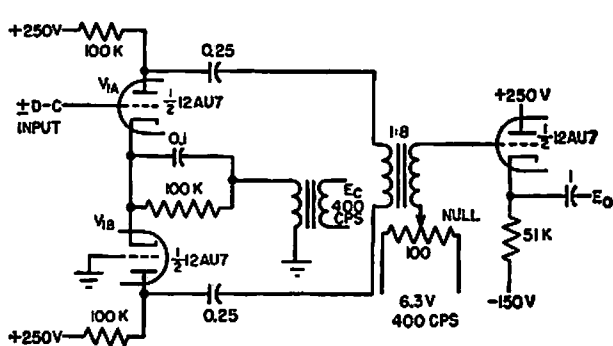
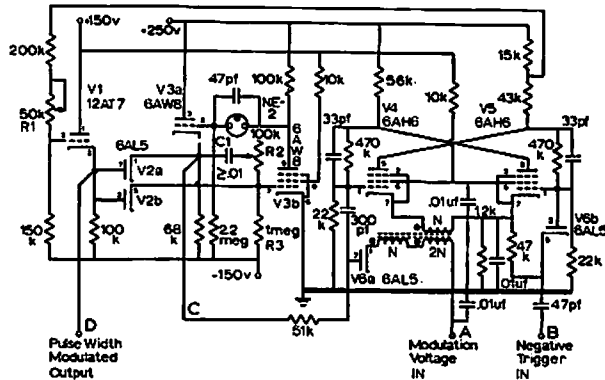


TRANSFORMER-COUPLED COLLECTOR-MODULATED TRANSMITTER—Widely used in a-citizen's band (27-mc) transmitters. Modulation amplifier should be adjusted for clip-

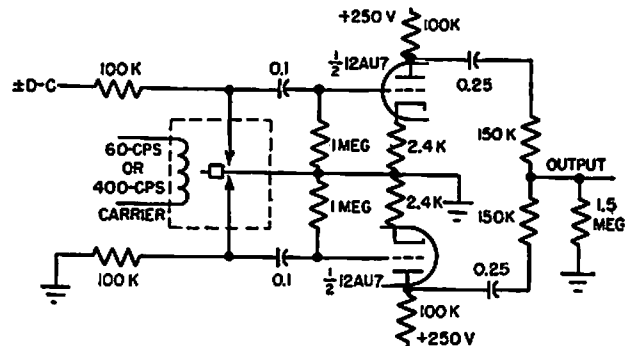
ping at 300-mw level, with passband from 300 to 3,000 cps. Input of 0.6 mv will provide rated output of 780 mw at 70 db gain. Large modulation transformer is required.—

B. Rheinfelder, Modulation Techniques for Transistorized A-M Transmitters, *EEE*, 11:7, p 54-57.

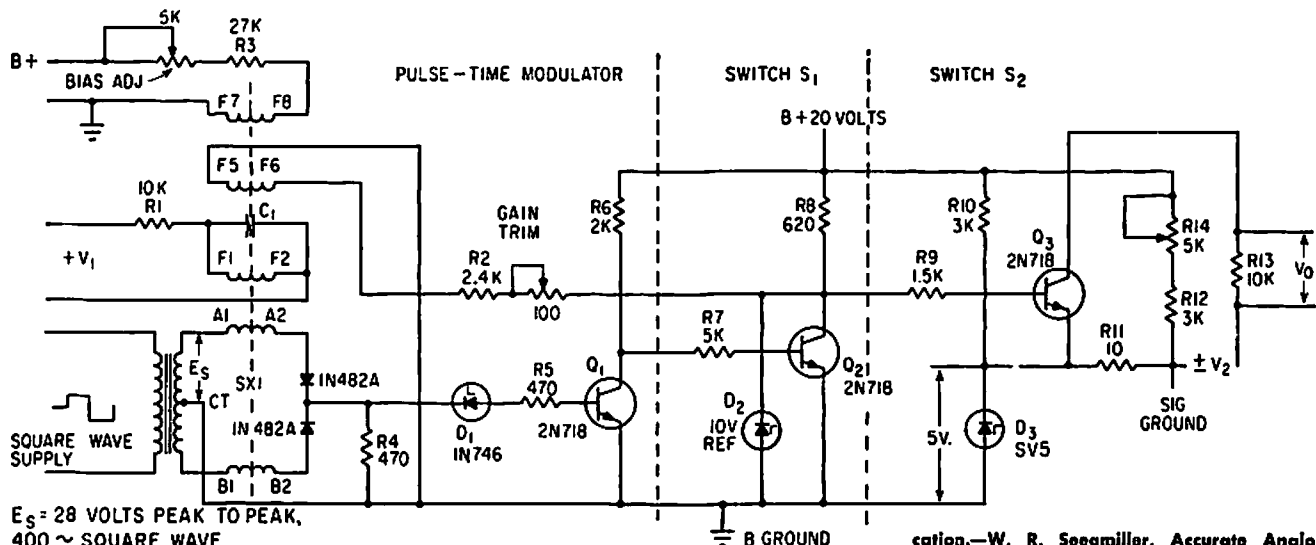
LINEAR PWM FOR 0.5 TO 175 V—Tubes V4 and V5 form bistable mvbr that will accept modulation voltage range of 350:1, from 0.5 v to 175 v, at point A. V3 is Miller integrator. With 0.05 mfd for C1, pulse lengths are 61 and 173 millsec for limits of modulation voltage. Linearity is nearly perfect.—J. E. Frecker, A Pulse Width Modulator, *EEE*, 10:12, p 28-30.



BALANCED-TRIODE SINE-WAVE MODULATOR—Gives sinusoidal output without filtering. Can be used for either open-loop or error-signal modulation when high input impedance and low-distortion sinusoidal output are required. Long-term drift stability is less than 1 mv per hour referred to output.—L. S. Klivans, Modulators for Automatic Control Systems, *Electronics*, 31:1, p 82-84.



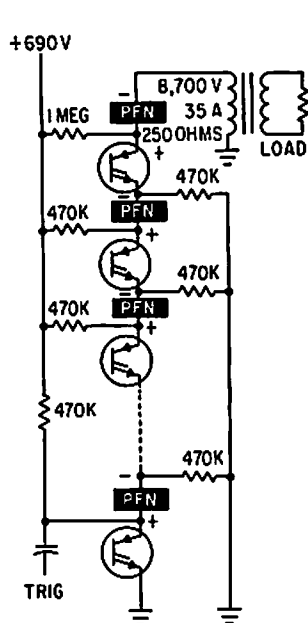
CHOPPER MODULATOR—Reduces output signal null level by balancing out zero input signal. Null levels can be maintained in microvolt region by proper shielding. Long-time drift stability is less than 1 mv referred to output. Used with strain gage and other low-level transducer signals.—L. S. Klivans, Modulators for Automatic Control Systems, *Electronics*, 31:1, p 82-84.



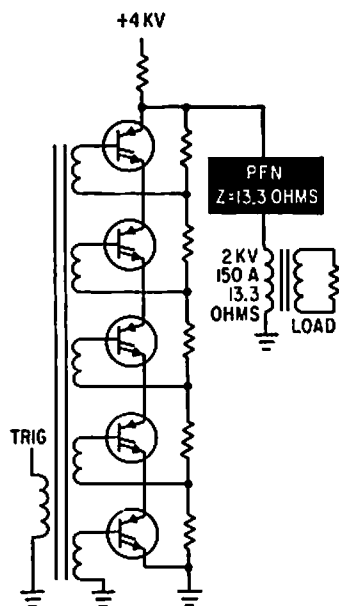
$E_s = 28$ VOLTS PEAK TO PEAK,
400 ~ SQUARE WAVE
PULSE-TIME MODULATOR USES TWO-QUADRANT MULTIPLIER—Output transistor Q3 serves

as series switch driven by Q2 and magnetic modulator to give accurate analog multipli-

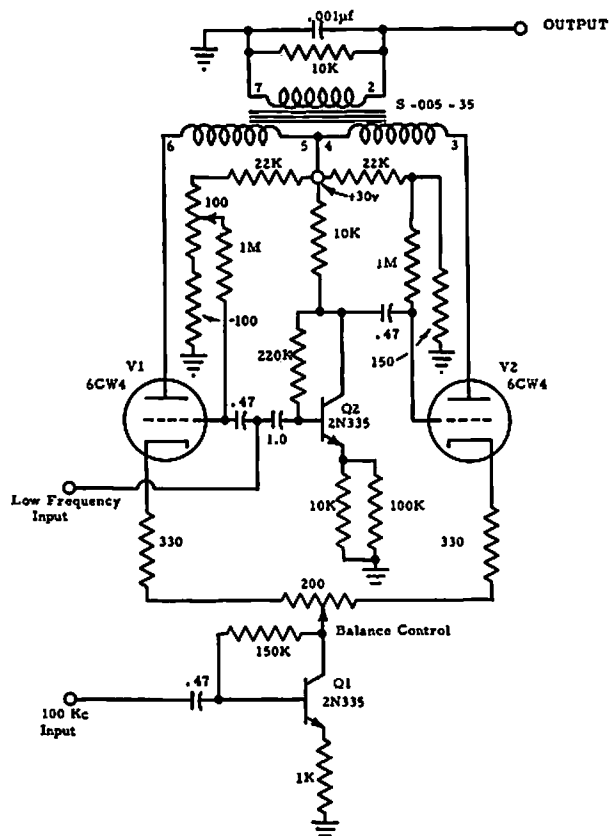
cation.—W. R. Seegmiller, Accurate Analog Computation with Pulse-Time Modulation, *Electronics*, 35:13, p 54-57.



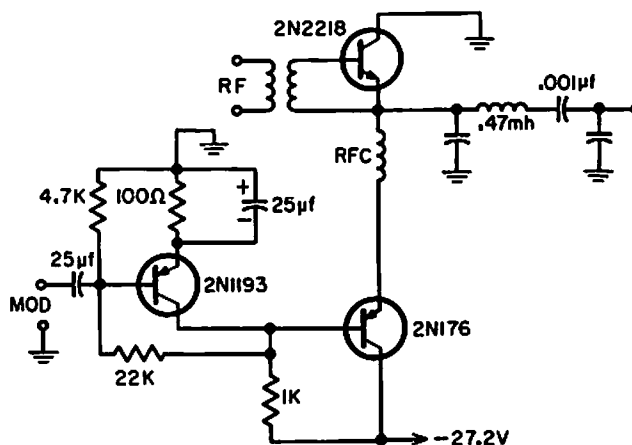
SERIES-DIODE MAGNETRON MODULATOR—In variation of spark-gap modulator, 25 pnpn diodes in series with pulse-forming networks (PFN) are switched by trigger and resulting voltage transient to supply 8,700 v at 35 amp to load. Success depends on availability of 700-v diodes.—F. A. Gateka and M. L. Embree, *Semiconductor Modulators for Modern Magnetrons*, *Electronics*, 35:37, p 42-45.



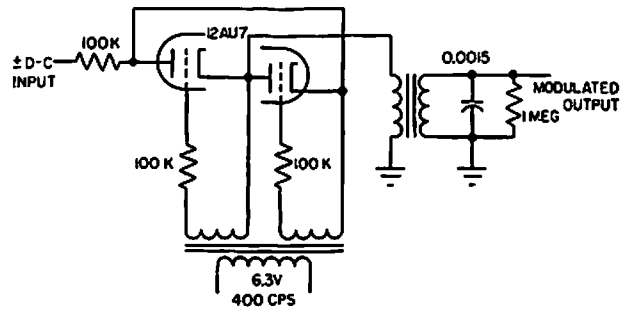
TRANSFORMER-TRIGGERED MAGNETRON MODULATOR—Success of circuit depends on availability of five pnpn transistors with breakover voltages of at least 800 v, in order to deliver 300-kw pulses to load through pulse-forming network PFN.—F. A. Gateka and M. L. Embree, *Semiconductor Modulators for Modern Magnetrons*, *Electronics*, 35:37, p 42-45.



100-KC HYBRID BALANCED MODULATOR—Circuit is essentially balanced bridge, with carrier injected by Q1. Balance is maintained by balance control and by bias adjustment on V1. Modulation is achieved by unbalancing bridge in accordance with low-frequency input signal, using phase inverter Q2 to make both tubes unbalance bridge in same direction.—J. Chirnitch, *Hybrid Balanced Modulator for 100 Kc*, *EEE*, 10:10, p 30.

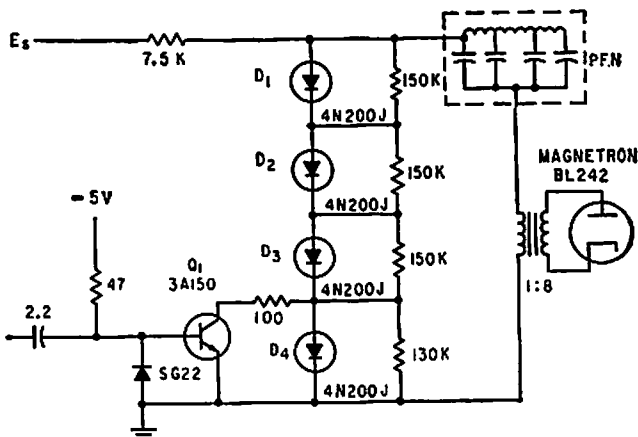


TRANSFORMERLESS-COLLECTOR MODULATOR—Provides 950 mw modulated power for CB transmitter, but 100% modulation can be reached only by using double modulation.—B. Rheinfelder, *Modulation Techniques for Transistorized A-M Transmitters*, *EEE*, 11:7, p 54-57.

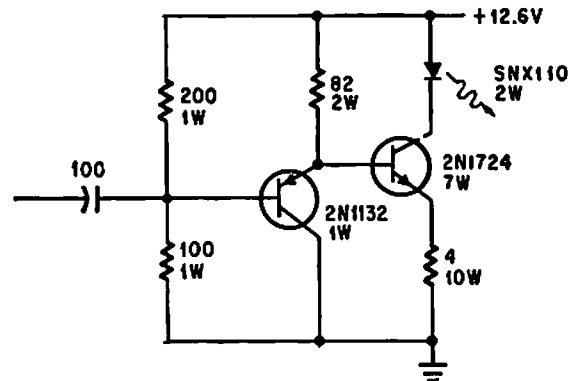


TRIODE CLAMP MODULATOR—Dual-triode performs chopper function. With 6.3 v rms carrier, output is linear to 2 v rms for d-c inputs up to 25 v. Null level is 100 mv, but can be reduced by filtering. Long-time drift

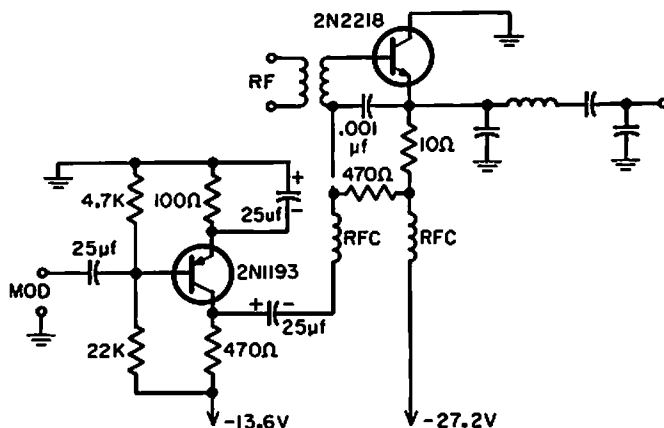
stability is excellent. Output signal is normally square-wave, but tuned circuit shown converts this to sinusoidal signal.—L. S. Klivans, *Modulators for Automatic Control Systems*, *Electronics*, 31:1, p 82-84.



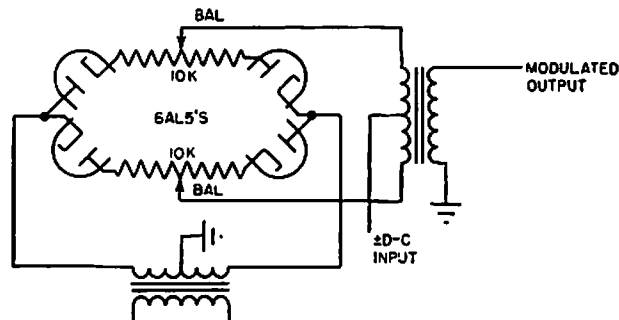
MAGNETRON MODULATOR—Uses four Shockley diodes in series, triggered by avalanche triode transistor, to give action similar to that of conventional line-type pulser using hydrogen thyatron, but requires no heater power or warmup.—L. Diven, *Solid-State Modulator Feeds Subminiature Transponder*, *Electronics*, 33:27, p 48-51.



TWO-TRANSISTOR MODULATOR FOR LIGHT-EMITTING DIODE—Linear range of 80% modulation for bandwidth of 30 cps to 250 kc, with only 3% distortion at 1 kc, permits good voice transmission over light beams generated by SNX110 light-emitting diode.—E. L. Bonin, *Drivers for Optical Diodes*, *Electronics*, 37:22, p 77-82.

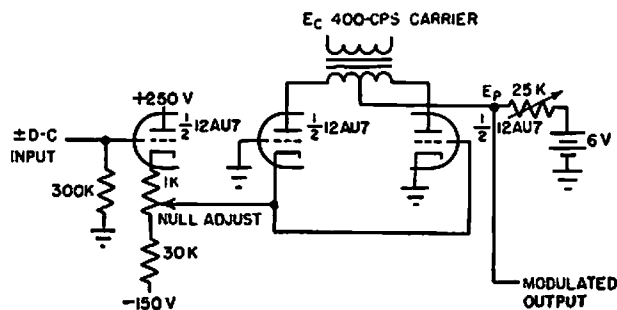


TRANSFORMERLESS BASE MODULATOR—Requires only one audio transistor, and readily provides 100% modulation. Modulated output power is 660 mw for CB transmitter. Audio quality is excellent.—B. Rheinfelder, *Modulation Techniques for Transistorized A-M Transmitters*, *EEE*, 11:7, p 54-57.

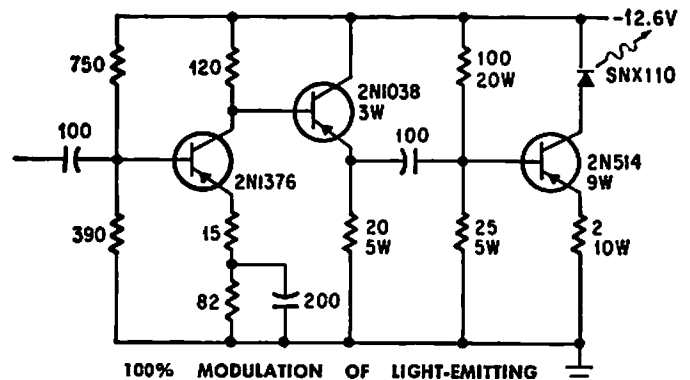


RING MODULATOR—Can be operated with either input or output ungrounded. With 100-v rms carrier and d-c input of 30 v, output is linear up to 0.2 v rms. Null level is less than 1 mv, but drift stability is poor

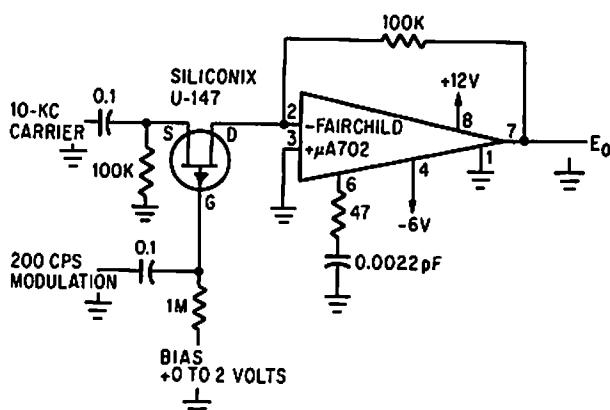
and balance is critical. Used in applications where modulation of error signal is required.—L. S. Klivans, *Modulators for Automatic Control Systems*, *Electronics*, 31:1, p 82-84.



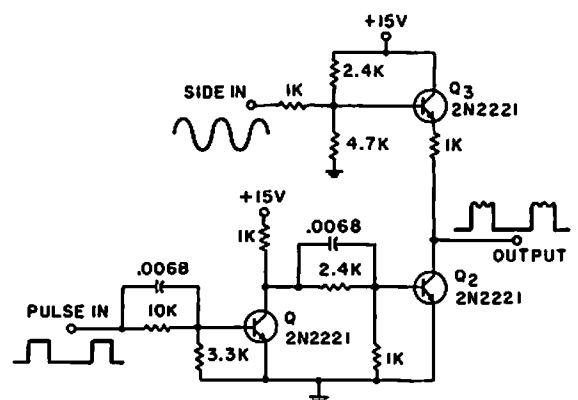
VARIABLE PLATE RESISTANCE MODULATOR—Increasing magnitude of carrier voltage increases modulated output. Used in amplification of d-c signals for automatic control systems.—L. S. Klivans, *Modulators for Automatic Control Systems*, *Electronics*, 31:1, p 82-84.



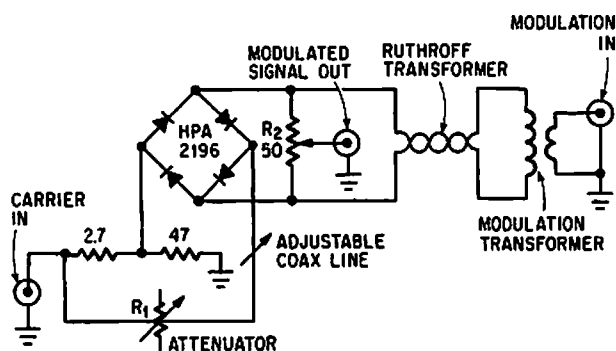
100% MODULATION OF LIGHT-EMITTING DIODE—Inpt of 0.35 v rms at 1 kc gives 100% modulation. Useful operating range of circuit is 30 cps to 25 kc for light-beam communication.—E. L. Bonin, *Drivers for Optical Diodes*, *Electronics*, 37:22, p 77-82.



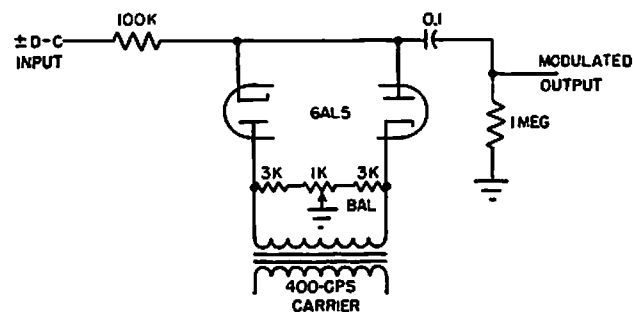
TRANSFORMERLESS LINEAR MODULATOR—Integrated-circuit operational amplifier and field effect transistor in single-ended linear configuration eliminate need for filter and transformer. Carrier level is adjusted by changing d-c bias, which sets gate midway between zero bias and pinchoff.—J. Althouse, *Linear Amplifier Circuit Eliminates Transformers*, *Electronics*, 39:6, p 99.



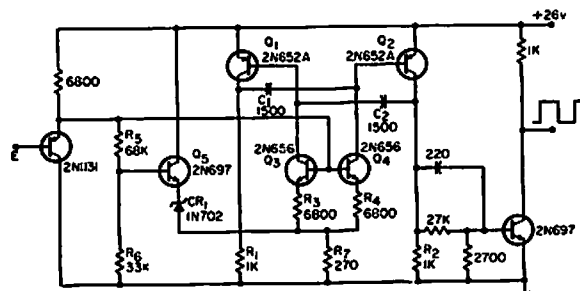
PULSE TRAIN AMPLITUDE MODULATOR—Provides amplitude modulation of pulse train with audio signal or other input, such as noise, over range of 0 to 200 kc with input pulses over 1 microsec wide. 80% modulation is available up to 3 kc, decreasing to 30% at input of 200 kc.—J. F. McCormick, Jr., *Pulse Amplitude Modulator*, *EEE*, 13:7, p 44.



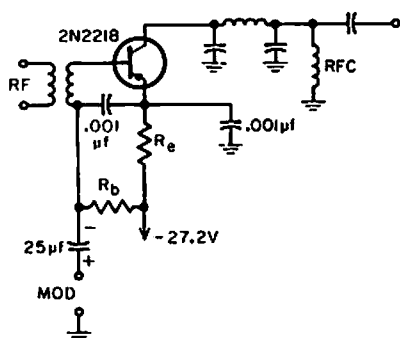
BALANCED MODULATOR-DEMODULATOR—Achieves high carrier and modulation suppression by using closely matched diodes and providing R1 for amplitude adjustment and coaxial line for phase adjustment. R2 provides slight amplitude adjustment.—W. H. Ellis, Diode Quad Modulator Suppresses Carrier 65 Db, *Electronics*, 39:8, p 97.



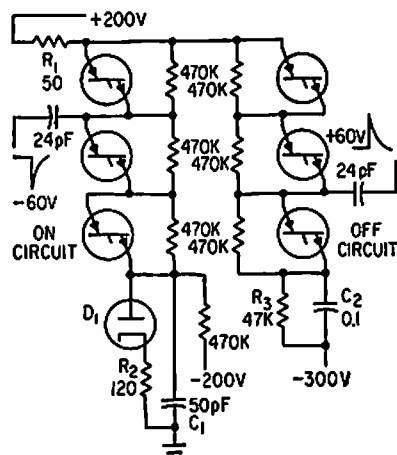
DUO-DIODE HALF-WAVE SWITCH MODULATOR—Tube serves in place of chopper. Carrier voltage turns diodes on and off, transferring d-c input signal to output when diodes are not conducting. With 10-v rms carrier voltage, output is linear up to 2 v for inputs up to 5 v d-c.—L. S. Klivans, Modulators for Automatic Control Systems, *Electronics*, 31:1, p 82-84.



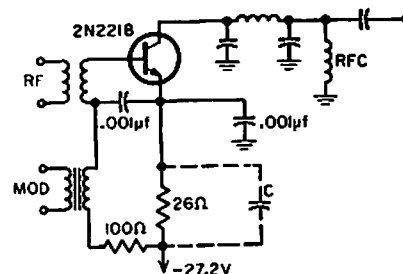
LINEAR F-M MODULATOR—Adding emitter-follower to astable mvbr makes output frequency linear function (within 0.01%) of input voltage for 50% modulation above and below center frequency.—G. Richwell, Linear FM Modulator, *EEE*, 12:10, p 59-60.



R-C COUPLED BASE MODULATION—Modulation signal is injected by using two resistors, values of which are determined by available r-f drive power; higher power is needed for larger resistance values. Excellent linearization of waveform is obtained with Re from 10 to 30 ohms. Rb should be in range from 100 to 2,000 ohms, with 470 as good compromise value. Modulation of 100% is easily achieved for CB transmitter.—B. Rheinfelder, Modulation Techniques for Transistorized A-M Transmitters, *EEE*, 11:7, p 54-57.



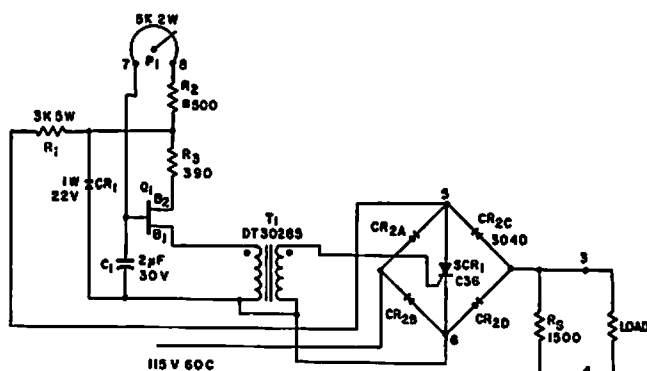
SERIES-RESISTOR PULSE MODULATOR—Four-layer diodes with series resistors reduce 350-v, 1-amp pulses to modulate twt and for other applications requiring fast-rise, variable-width, high-current flat-top pulses at repetition rates up to 200 pps.—E. H. Heckman, Three New Approaches to Pulse Modulation, *Electronics*, 36:18, p 62-64.



TRANSFORMER-COUPLED BASE MODULATION—Modulation is in series with r-f signal, so transistor operates in common-emitter configuration for both r-f and audio. Waveform is good, but modulation power is only 0.7 mw into 900 ohms when audio bypass C is used. Without bypass, modulation power is 1 mw into 200 ohms.—B. Rheinfelder, Modulation Techniques for Transistorized A-M Transmitters, *EEE*, 11:7, p 54-57.

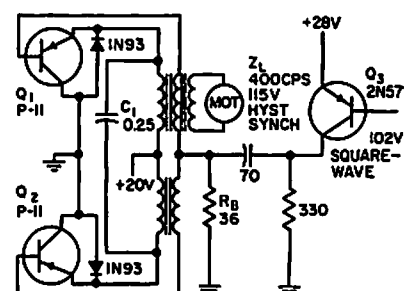
CHAPTER 52

Motor Control Circuits

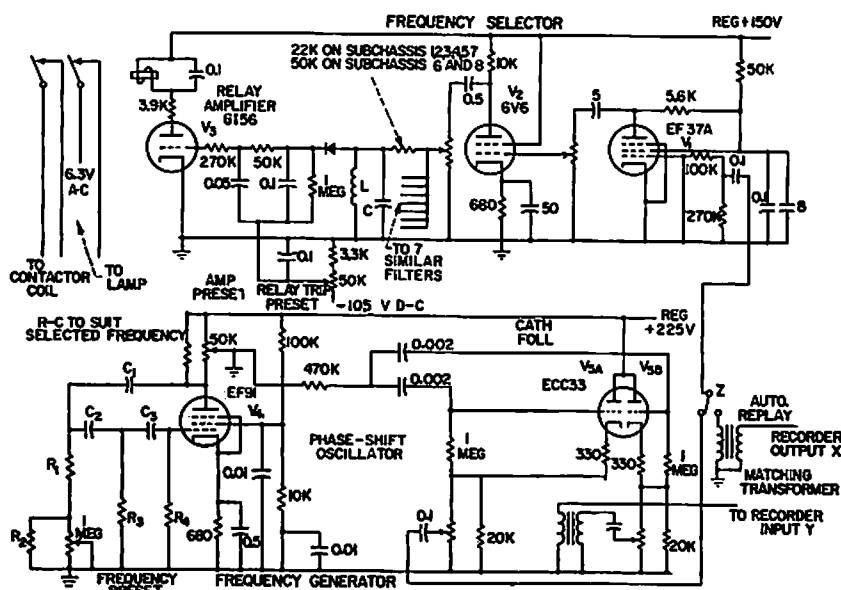


FULL-WAVE CONTROL—Uses only one control rectifier and one single-ended trigger to obtain continuously variable a-c or d-c full-wave output. May be designed for any standard service power voltage. Trigger is always synchronized with power bridge because both obtain power from same source. Used to drive and adjust speed of single-

phase induction motor, drive and adjust speed of universal motors in machine tools, and vary light output of high-power incandescent lamp.—Full-Wave Control with One Trigger and One Control Rectifier, "Electronic Circuit Design Handbook," Macfater Pub. Corp., N.Y., 1965, p 187.



CONSTANT MOTOR SPEED—Precise control of instantaneous voltage and current for power transistors gives 90% operating efficiency in driving 400-cps synchronous motor of portable tape transport from 28 v d-c. Negative 800-cps synchronizing pulses from precision oscillator are applied to base of Q3 to produce positive pulses at bases of Q1 and Q2, cutting them off quickly.—J. W. Caldwell and T. C. G. Wagner, Boosting Power Transistor Efficiency, *Electronics*, 31:47, p 86-88.



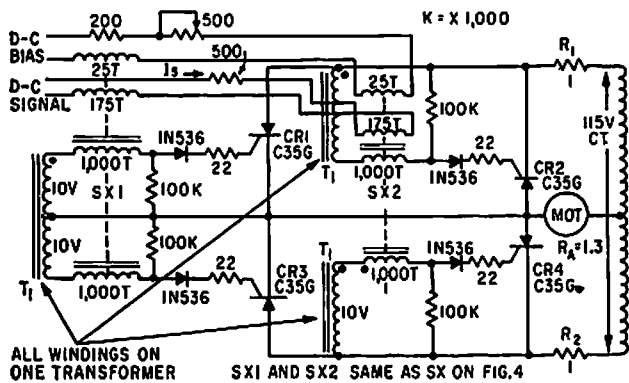
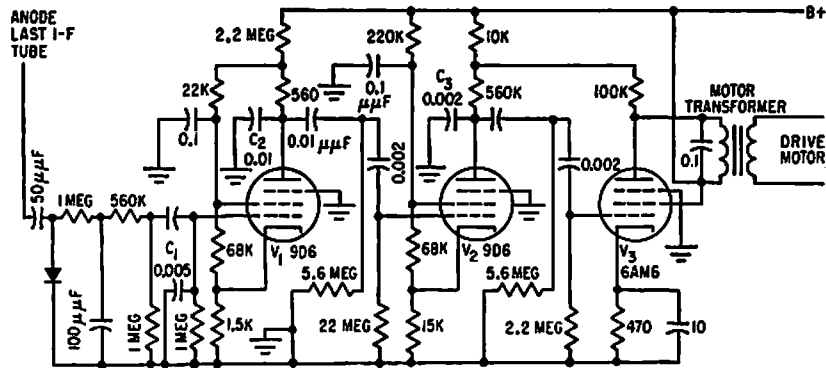
EIGHT-TONE CRANE MOTOR CONTROL—Eight preset frequencies or tones activate collec-

tor relays that operate crane motor contactors. Sequence of preselected operations.

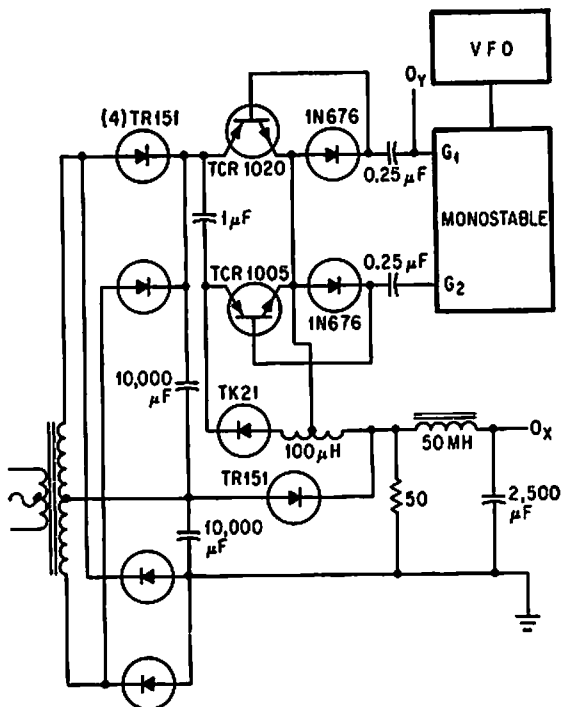
Chassis	Frequency	C_1	C_2	C_3	R_1	R_2	R_3	R_4
No	in cps	in μf			$\times 1,000$ ohms			
1	270	460	460	390	135	270	560	1,000
2	1,600	220	220	100	135	100	270	270
3	450	390	303	270	195	560	560	1,000
4	2,600	127	100	68	135	100	270	270
5	700	460	290	330	135	270	270	270
6	3,800	68	68	78	100	100	270	270
7	1,080	330	270	270	100	270	270	270
8	4,500	100	68	25	160	47	270	270

recorded on magnetic tape, is repeated by traveling crane during playback, to give positioning accuracies better than 1/8th inch. Table gives values of R-C network components in grid circuit of phase-shift oscillator to provide the eight tones.—G. V. Sadler, Taped Tones Control Overhead Crane, *Electronics*, 31:1, p 63-65.

GONIOMETER MOTOR AMPLIFIER—Portion of adf receiver output is separately rectified and applied to selective amplifier V1-V2-V3. C1, C2, and C3 develop 90° phase shift required between two coils of goniometer drive motor and serve also as low-pass filter with sharp cutoff above 150 cps. Overall gain is high enough so motor will exert full torque when goniometer is only 3° off true null.—J. F. Hatch and D. W. G. Byatt, *Direction Finder with Automatic Readout*, *Electronics*, 32:16, p 62-64.

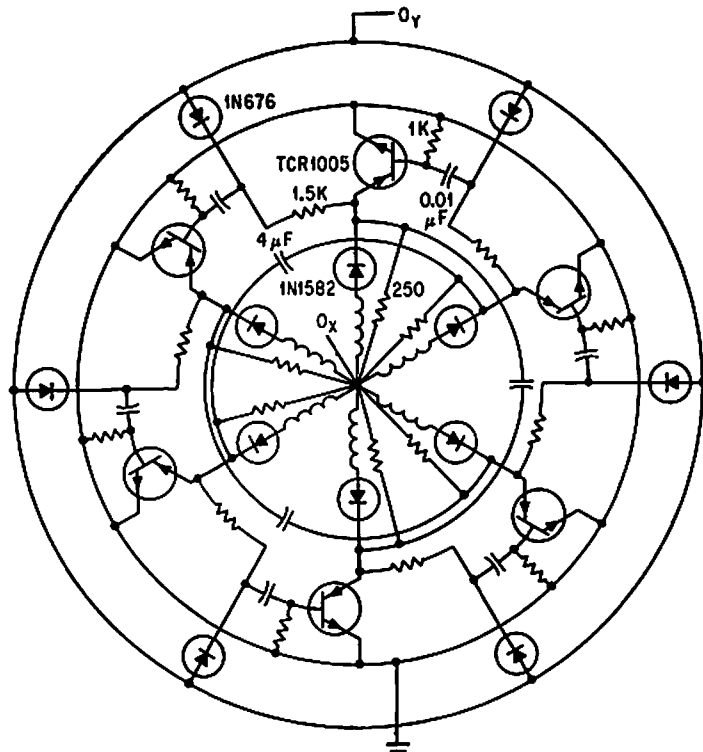


FULL-WAVE DRIVE FOR D-C MOTOR—Requires four controlled rectifiers and center-tapped transformer. Four magnetic cores are required for full-wave push-pull action.—W. R. Seegmiller, *Controlled Rectifiers Drive A-C and D-C Motors*, *Electronics*, 32:46, p 73-75.

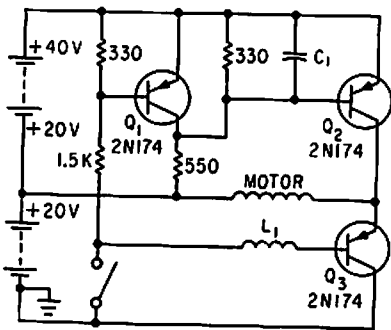


SCR RING COUNTER DRIVES HYSTERESIS MOTOR—Speed range of 1,200 to 18,000 rpm is obtained with 400-cps, six-pole fractional-hp hysteresis motor by modifying scr

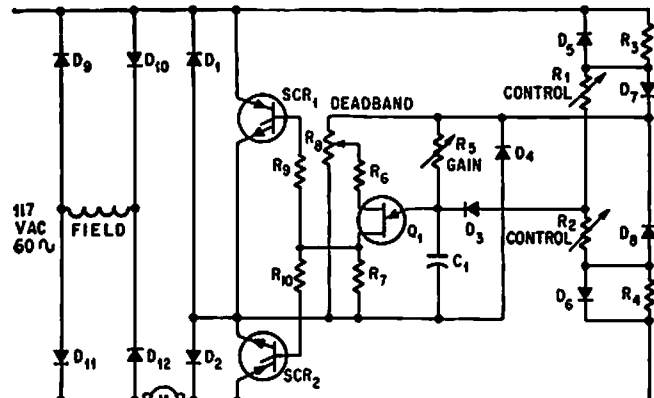
ring counter to work in switching mode. Series rectifiers prevent spurious modes during commutation. Output OX of circuit at left goes to center of circular configuration,



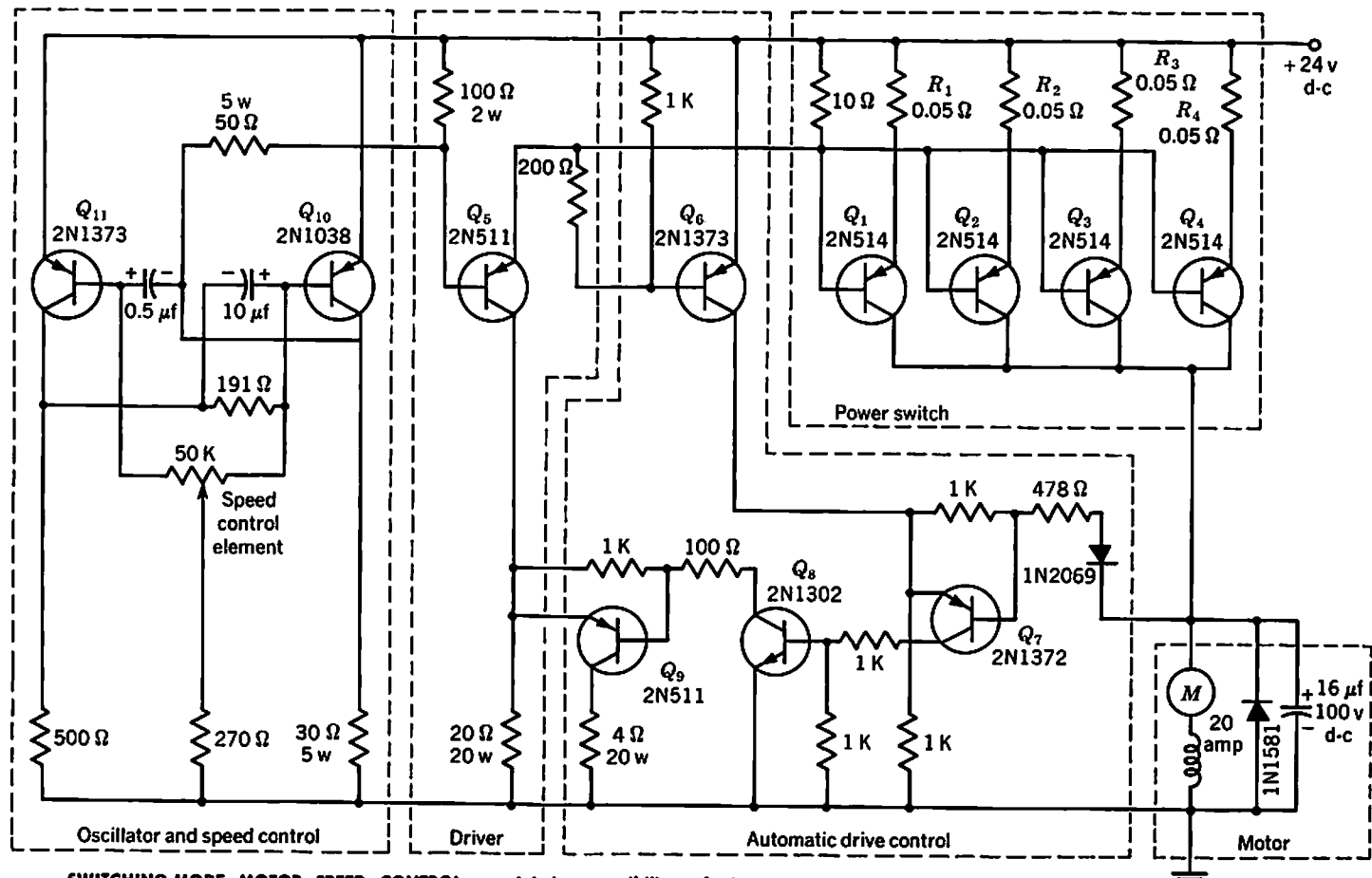
and OY goes to outer circle.—R. H. Murphy, *Static Alternator Controls Three-Phase Motor*, *Electronics*, 37:5, p 30-33.



TWO-SOURCE CONTROL—Acts as bidirectional current switch that selects one of two oppositely polarized current sources for d-c motor of gyro or accelerometer. Switch is operated by opposing forces of motor torsion and acceleration. Motor torsion opens switch, reducing speed and therefore torsion of motor, and acceleration forces then close switch. Shaft speed is therefore proportional to acceleration.—F. W. Kear, D-C Motor Controls Improve System Accuracy, *Electronics*, 33:41, p 76.



REVERSING DRIVE FOR SHUNT-WOUND MOTOR—Silicon controlled rectifiers in half-wave circuit act with unijunction transistor and two rheostats to adjust speed in either direction.—J. C. Hey, *The Widening World of the SCR*, *Electronics*, 37:25, p 78-85.

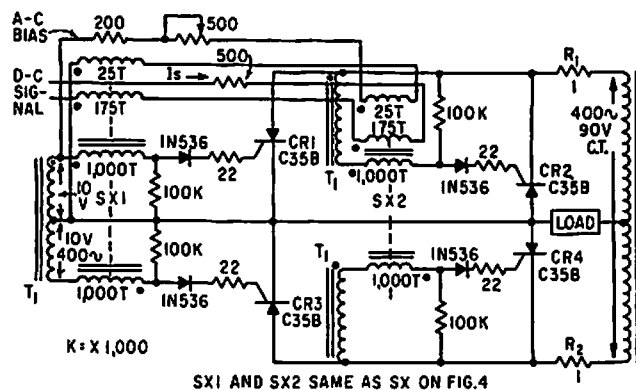
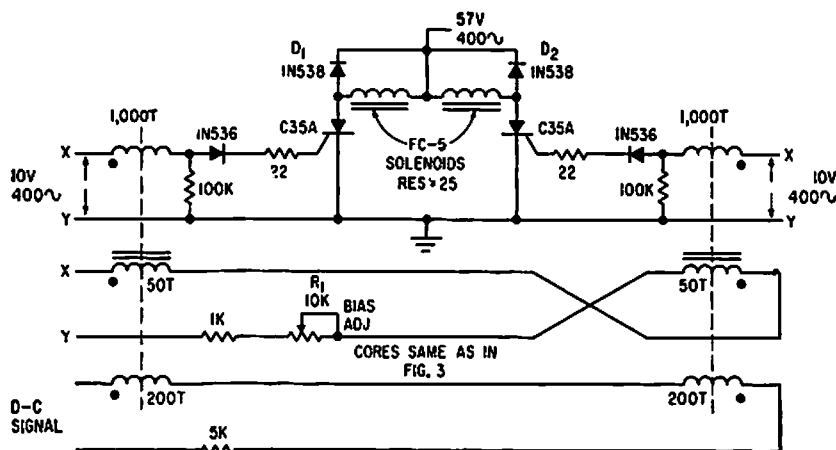


SWITCHING-MODE MOTOR SPEED CONTROL—Power-switch stage consists of four 2N514 transistors in parallel, to handle starting or stalled motor current approaching 100 amp. Rectifier and capacitor in parallel with motor

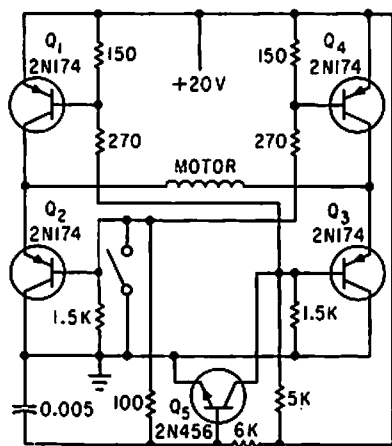
minimize possibility of damage to power transistors when they switch off heavily inductive motor load. Variation of time duration of on and off portions of power transistor cycle, controlled by 50K potentiometer

in mvbr, provides control of motor speed while giving high starting torque.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 477.

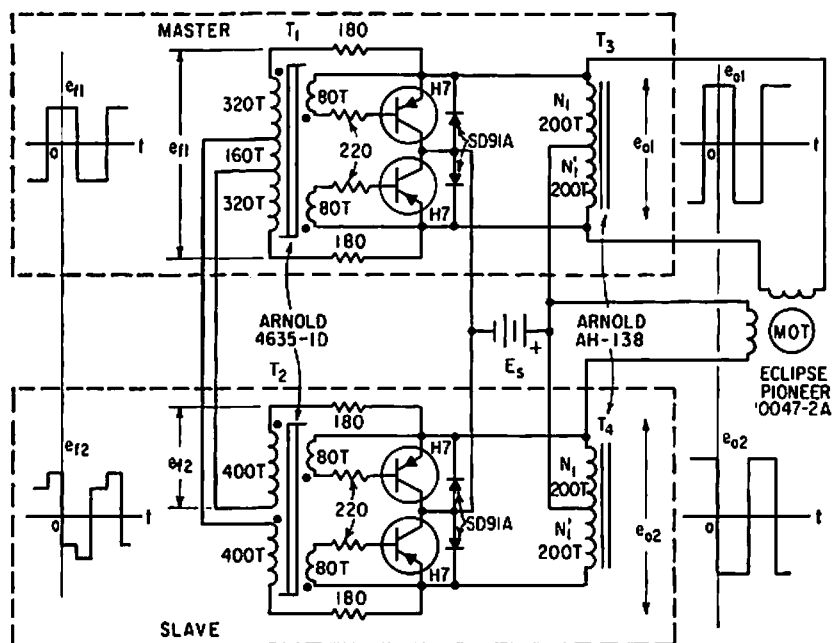
SERIES ACTUATOR SOLENOID DRIVE—Consists of controlled rectifier in series with each solenoid, and saturable magnetic core firing circuit. Each magnetic core has two control windings, one for adjustment and one for signal. Can also be used to drive d-c split-series motors. Windings of motor then replace solenoids.—W. R. Seegmiller, *Controlled Rectifiers Drive A-C and D-C Motors, Electronics*, 32:46, p 73-75.



FULL-WAVE PUSH-PULL FOR A-C SERVO-MOTOR—Circuit is identical to full-wave push-pull d-c shunt motor drive except for different arrangement of firing circuit. Limiting resistors R1 and R2 determine standby current.—W. R. Seegmiller, Controlled Rectifiers Drive A-C and D-C Motors, *Electronics*, 32:46, p 73-75.

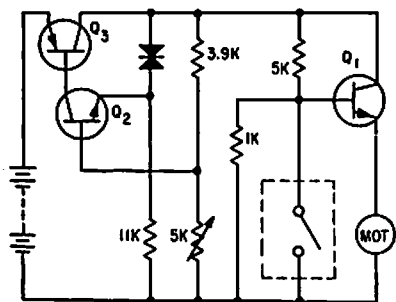


ONE-SOURCE CONTROL—Q5 determines direction of current flow through motor winding, which in turn depends on position of motor control switch. Motor torsion opens switch, and acceleration during slowdown closes switch, to make motor speed proportional to acceleration.—F. W. Kear, D-C Motor Controls Improve System Accuracy, *Electronics*, 33:41, p 76.

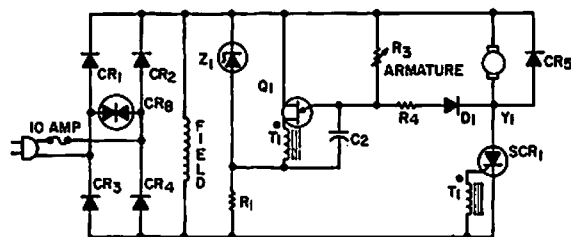


TWO-PHASE INDUCTION MOTOR DRIVE—Transistors used as controlled switches in inverter provide two-phase square-wave output from single d-c source. May also

be used with hysteresis-synchronous motors to provide speed under load.—W. H. Card, *Four Transistor Inverter Drives Induction Motor, Electronics*, 32:8, p 60-61.



ACCELERATION-SENSING SWITCH—Sensing switch controls Q1, which provides power for accelerometer motor. Q2 and Q3 provide damping by current limiting, to increase accuracy by one order of magnitude.—F. W. Kear, *Dynamic Fluid Switch Senses Acceleration*, *Electronics*, 34:38, p 64-67.

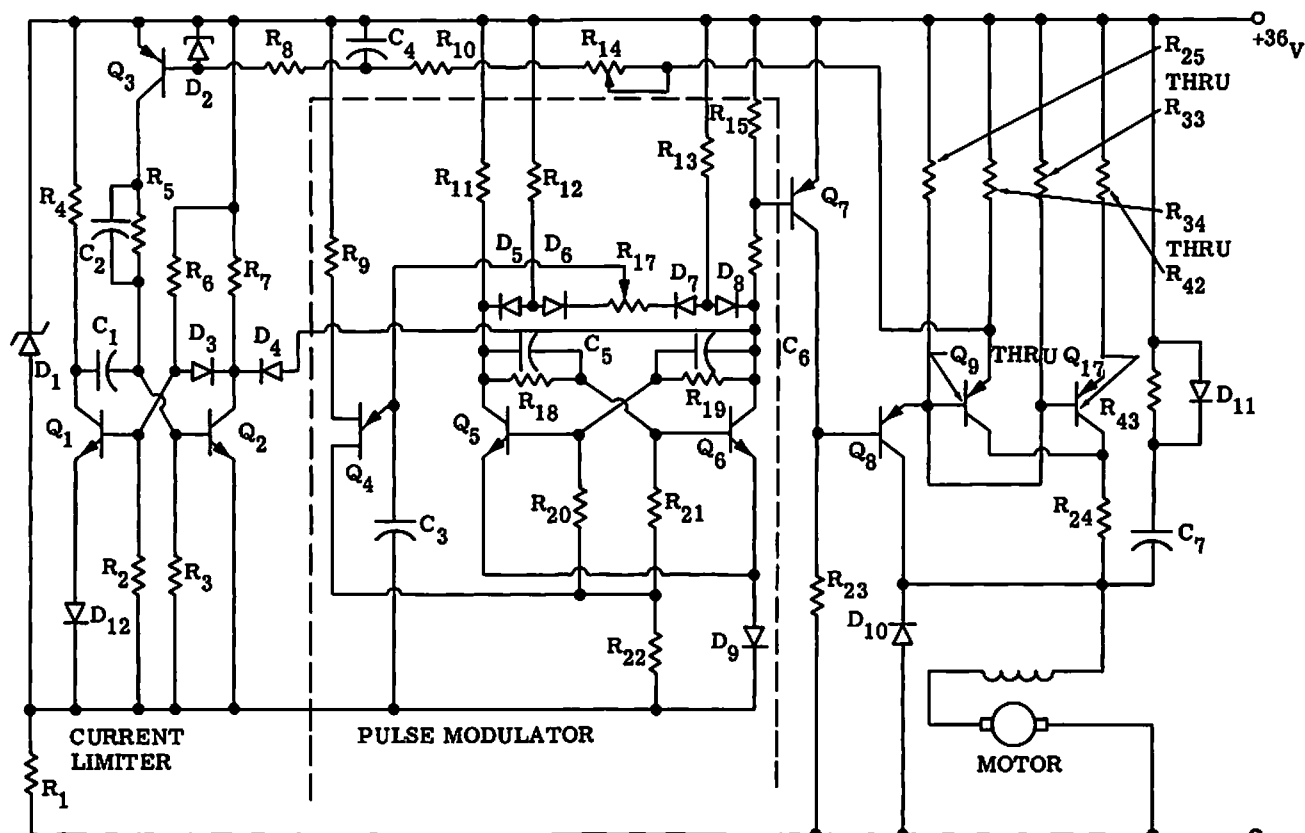


R1 - 4.7K, 10%
R2 - 100K VARIABLE - SPEED CONTROL
R3 - 100K, 1/2W, 10%
R4 - 0.5 μ F, 50V
D1 - GE IN536
Z1 - 25V ZENER DIODE, 1W
T1 - PULSE TRANSFORMER, SPRAGUE 93220 OR EQUIVALENT

CR1-CR5 GE A408
SCR1-GE C358
Q1 - GE 2N2846
CR6-GE 6RS2ISA05
THYRECTOR (OPTIONAL-
TRANSIENT PROTECTION)

FULL-WAVE SPEED REGULATOR—Features closed-loop feedback armature control to regulate speed of 0.5-hp shunt-wound d-c motor

over 6:1 range.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 145.



C1 - 0.6
C2 - 0.03
C3 - 0.1
C4 - 1.0
C5 - 2200 μ F
C6 - 470 μ F
C7 - 1000 50V

R1 - 100 Ω , 10 W

R2, R4 - 5.1K
R3 - 2.7 K
R5 - 2 K
R6 - 51K
R7, R12, R13, R21 - 10 K
R8 - 20 Ω
R9 - 470 Ω
R10 - 51 Ω
R11 - 2.4 K

R14 - 100 Ω POT
R15, R22 - 100 Ω
R16 - 510 Ω
R17 - 100 K POT
R18 - 6.8 K
R19 - 27 K
R20 - 39 K
R23 - 120 Ω , 20 W
R24 - 0.004 Ω , 50 W

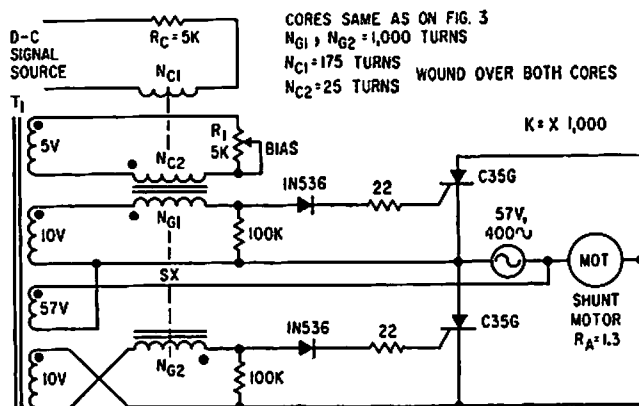
R25 Thru R33 - 12 Ω
R34 Thru R42 - 0.03 Ω , 10 W
R43 - 0.5 Ω , 50 W
D1 - 1N2976
D2 - 1N3128
D3 Thru D9 - 1N4001
D10, D11 - MR1210
D12 - 1N4001

Q1, Q2, Q5, Q6 - 2N2218
Q3 - 2N1187
Q4 - 2N2160
Q7 - 2N1188
Q8 - 2N1559
Q9 Thru Q17 - MP506

GOLF CART TRACTION DRIVE—Pulse-width-modulating series motor control was designed to operate motor in Cushman golf cart from 36-v battery supply. Provides 200 amp for climbing steep inclines and up to 300 amp

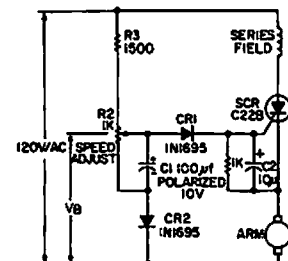
for starting. Eight MP506 transistors in parallel are used to switch peak motor load. Speed is changed by varying width of pulse that is applied to motor at constant rate, to vary average motor voltage.—H. F. Weber,

"Solid-State DC Motor Control for Traction Drive Vehicles," Motorola Application Note AN-189, Mar. 1966.

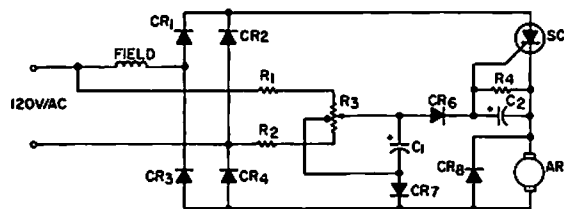


HALF-WAVE DRIVE FOR D-C MOTOR—Uses controlled rectifiers to control armature of d-c shunt motor or d-c torque, for applications requiring push-pull output for reversible drive. Saturable reactor control windings are wound over both cores together. Maxi-

mum current during reversal from top speed in one direction to top speed in opposite direction is approximately 20 amp, with current dropping to 10 amp in 0.1 sec.—W. R. Seegmiller, Controlled Rectifiers Drive A-C and D-C Motors, *Electronics*, 32:46, p 73-75.



UNIVERSAL-MOTOR SPEED CONTROL—Regulated speed control is achieved by varying conduction angle of scr placed in series with armature and field of universal a-c/d-c motor. Makes use of motor residual field to induce counter emf in armature proportion to speed, for use as feedback signal. Provides stable operation at low speeds for sewing machines and small appliances.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 143.

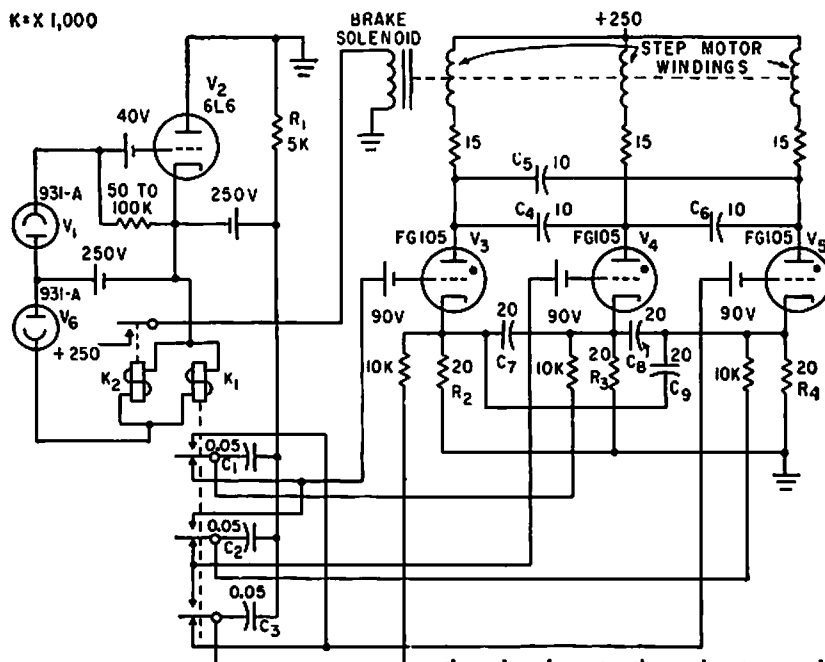


REVERSIBLE HALF-WAVE SPEED CONTROL—Simple circuit is adequate for majority of universal series-wound motor drive applications. Direction of rotation depends on which

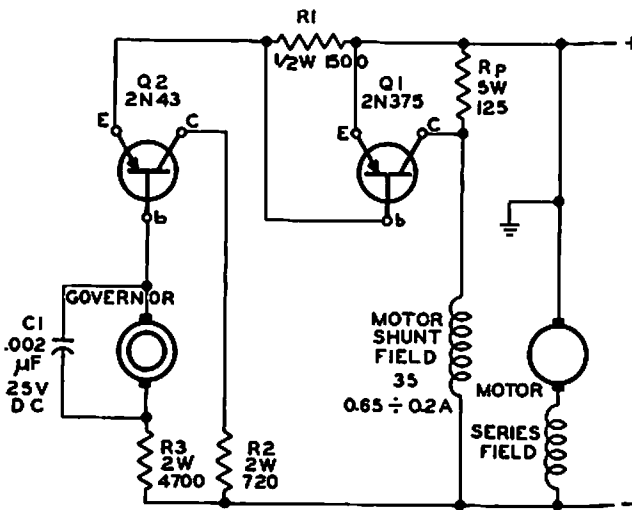
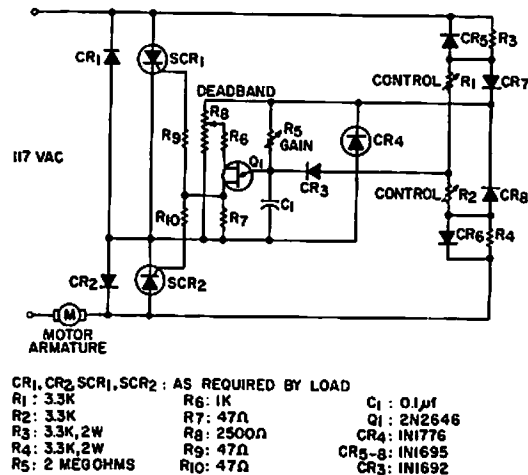
half-cycle scr conducts, since series field is in a-c leg of bridge rectifier.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 144.

C_1 : 200 μ f
 C_2 : 5 μ f
 CR_1-4 : A40B
 CR_6-7 : 1N694
 R_1 : 1500
 R_2 : 1500
 R_3 : 3000 CT
 R_4 : 1000
 SCR : C20B
 CR_8 : A40B
 MOTOR: SERIES, REVERSIBLE

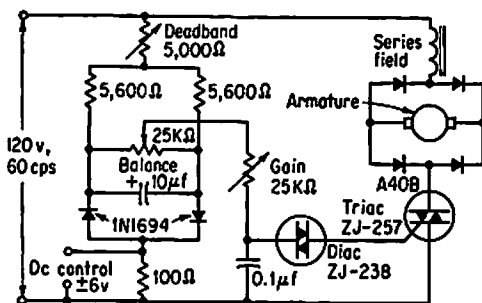
$K = X 1,000$



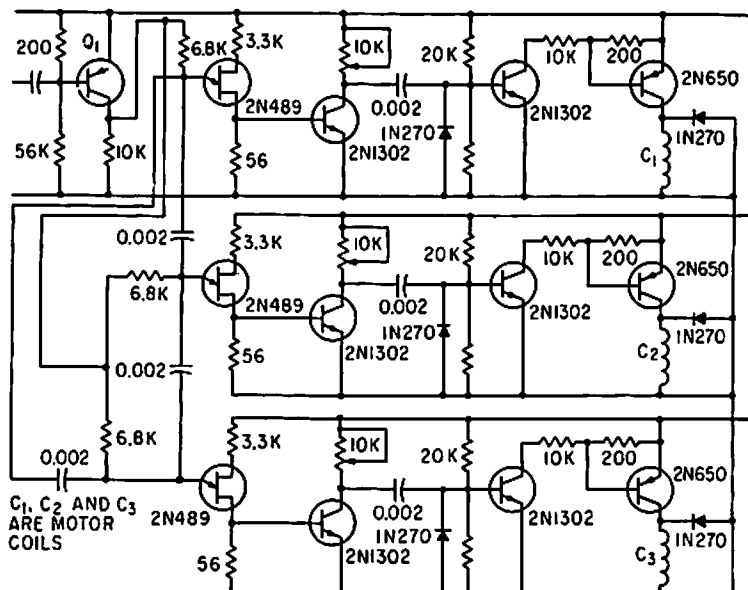
BALANCED-BRIDGE REVERSING DRIVE—Phase-sensitive servo drive supplies reversible half-wave power to armature of small permanent magnet or to shunt motor. Power circuit consists of two half-wave circuits back-to-back (SCR1-CR1 and SCR2-CR2) fired by ujt Q1 on either positive or negative line half-cycle depending on direction of unbalance of reference bridge containing sensing element R1, which can be photoresistor, thermistor, potentiometer, or output from control amplifier.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 142.



GOVERNOR-TRANSISTOR SPEED REGULATOR—Centrifugal governor is used as error detector, with contacts handling only a few microwatts. Two-transistor amplifier actuated by governor is connected across motor field resistor, with power being obtained from 24 v d-c motor bus. Maintains 0.5-hp motor speed at 6,000 rpm over input voltage range of 20 to 30 v.—Transistorized Speed Regulator, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 30.

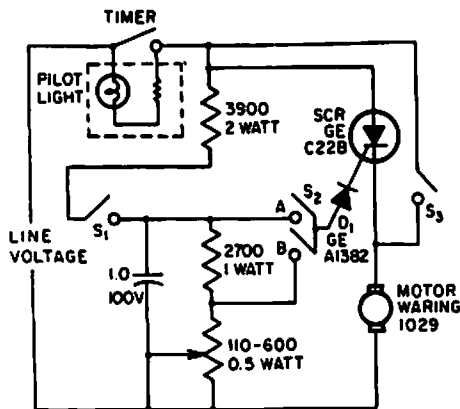


TRIAC-DIAC REVERSING SERVO CONTROL—Varies speed and direction of 5-amp reversible series a-c motor in accordance with d-c control signal. Polarity of control signal determines direction of rotation. Gain potentiometer adjusts slope of speed versus control voltage curve.—M. P. Southworth, Bidirectional Static Switch Simplifies Ac Control, *Control Engineering*, March 1964, p 75-76.

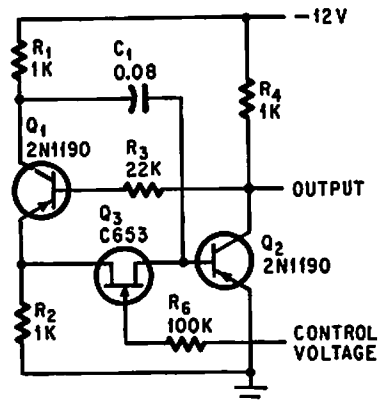


STEPPER-MOTOR PULSE GENERATOR—Unijunction ring counter energizes windings of stepper motor sequentially.—F. W. Kear, *Digi-*

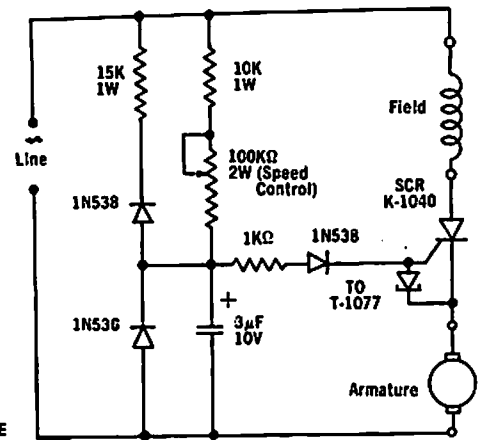
tal Control Uses Unijunction Transistors., *Electronics*, 34:18, p 79-80.



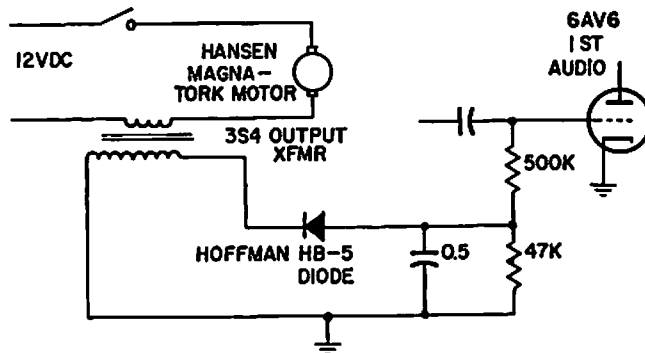
THREE-SPEED BLENDER CONTROL—Single scr safely handles 7.5-amp current of 1/2-hp motor. Feedback is used to change firing angle of scr as load increases, to maintain constant blending speed.—J. Eimbinder, SCRs In The Consumer Market, *EEE*, 14:8, p 100-103.



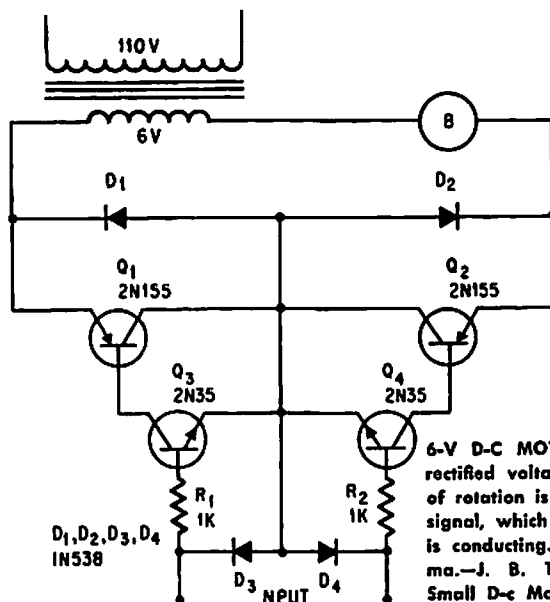
FIELD-EFFECT TRANSISTOR CONTROLS PULSE OSCILLATOR—C653 transistor serves as voltage-controlled nonlinear resistor that varies time constant of oscillator. Can generate narrow output pulses at rates up to several Mc, to drive stepping motor.—T. C. Ross, Field-effect Transistor Controls Pulse Oscillator, *Electronics*, 37:18, p 80-81.



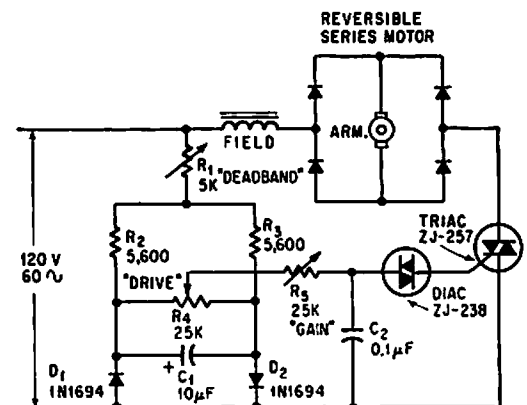
SPEED FEEDBACK—Introduction of speed feedback signal into firing circuit helps maintain constant torque regardless of speed. Tunnel diodes provide excellent stabilizing action at low speeds.—TD/SDR Combos for Sale, *EEE*, 12:3, p 62-64.



MOTOR NOISE ACTUATES MUTING SYSTEM—Noise pulses from commutator-type tuning motor in receiver are rectified and used to bias audio stage to cutoff for as long as tuning motor is running. Audio amplifier remains cut off for about 0.25 sec after motor stops.—Muting System for Motor-Tuned Receivers, "Electronic Circuit Design Handbook," MacIvor Pub. Corp., N.Y., 1965, p 51.



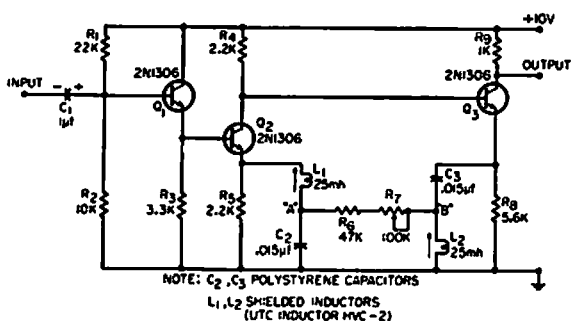
6-V D-C MOTOR—Motor B is energized by rectified voltage from transformer. Direction of rotation is controlled by polarity of input signal, which determines whether Q1 or Q2 is conducting. Motor draws several hundred ma.—J. B. Tiedemann, Transistors Control Small D-c Motor, *Electronics*, 39:7, p 93.



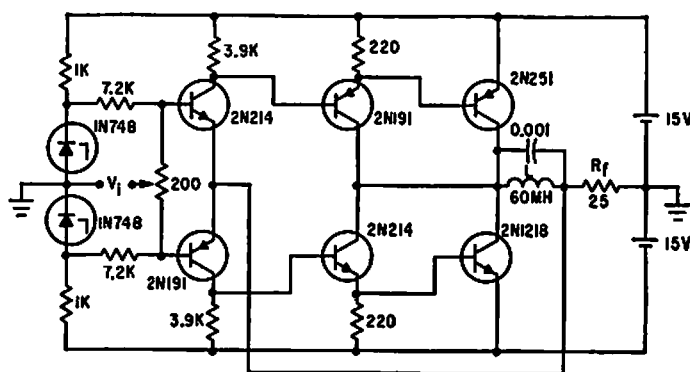
REVERSING DRIVE FOR SERIES D-C MOTOR—Triode and diode a-c switch components can be triggered into conducting in either direction by applying positive or negative gate current signal. Transducers can be used in place of potentiometer and rheostats to give automatic speed control.—J. C. Hey, The Widening World of the SCR, *Electronics*, 37:25, p 78-85.

CHAPTER 53

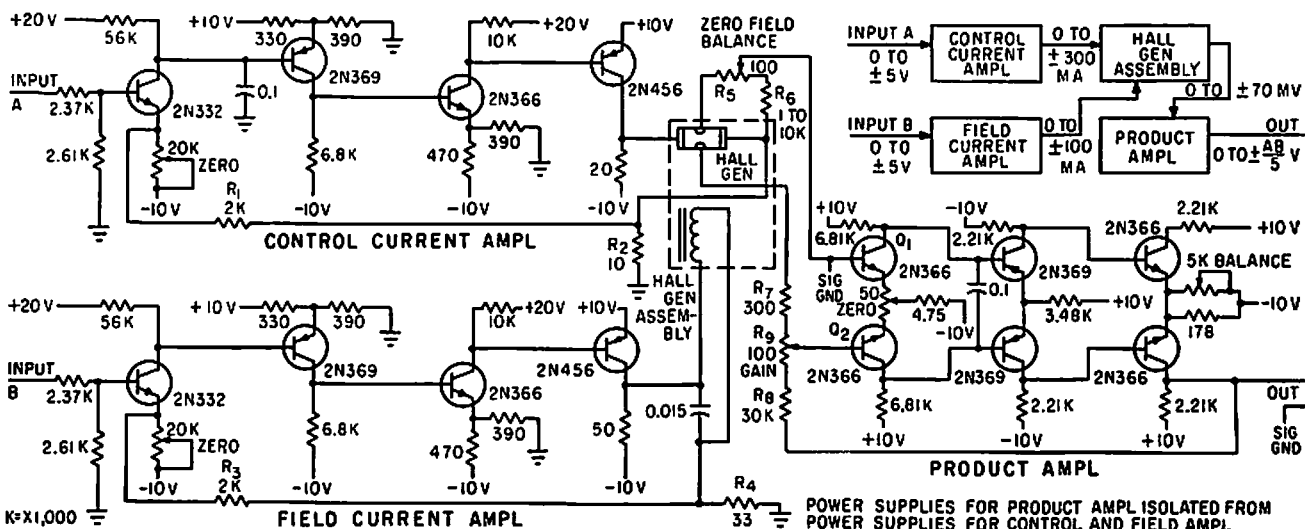
Multiplier Circuits



Q MULTIPLIER FOR TONE FILTER—Provides stable multiplication over wide temperature range, as required for high-selectivity single-frequency telemetry tone filter. Center frequency is 8 kc and bandwidth is 40 cps. Q is about 200.—W. New, Jr., Stable Q Multiplier, *EEE*, 13:4, p 41.



HALL-MULTIPLIER FIELD-COIL DRIVE—Feedback amplifier drives field coil current in phase with input signal over range of 0 cps to 7 kc, with less than 1.5% distortion.—R. A. Greiner, Feedback Amplification Improves Hall-Effect Multipliers, *Electronics*, 34:34, p 52-55.

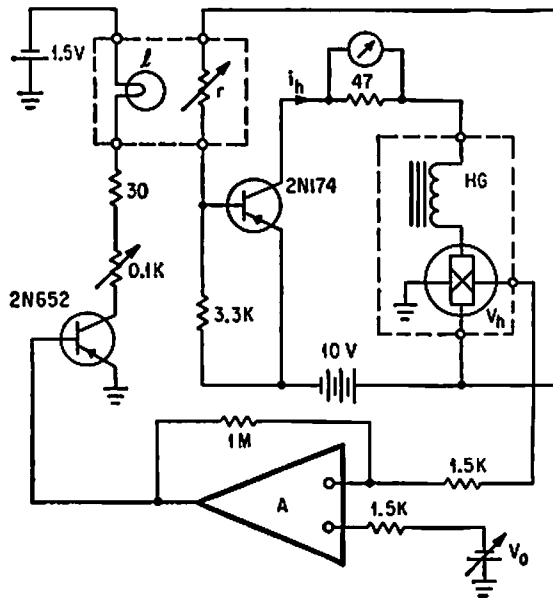
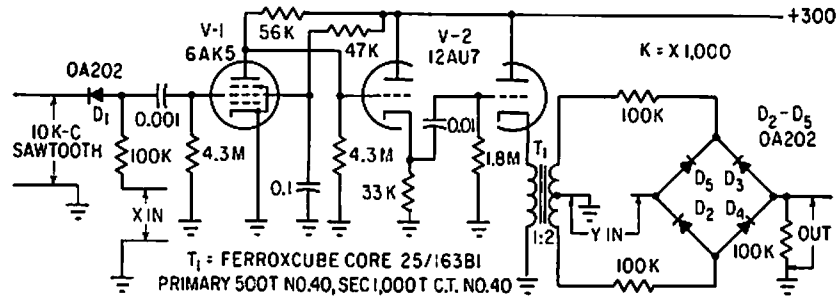


HALL-EFFECT MULTIPLIER—Circuit gives algebraic product of two inputs. Accuracy is only 0.5%, but cost is low. Control current is applied through long dimension of Hall semi-

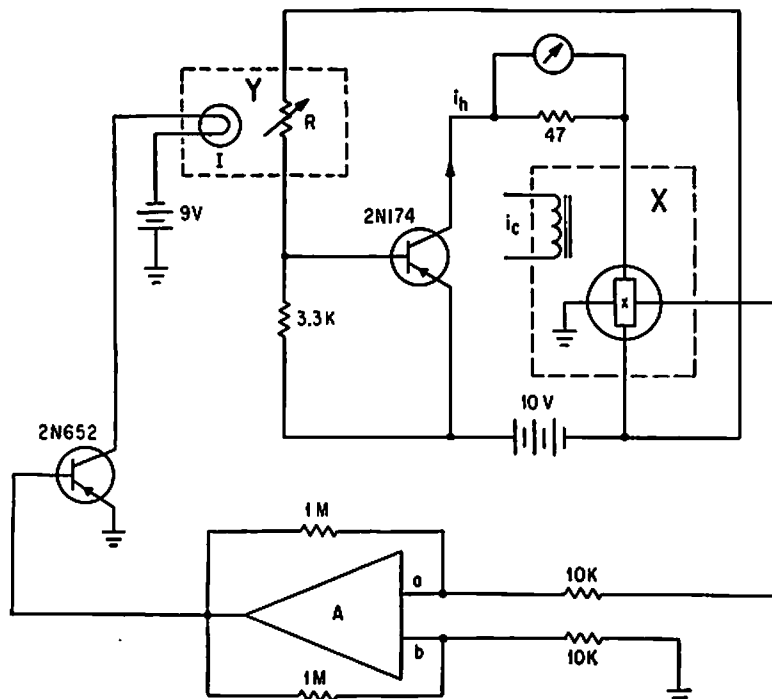
conductor element, to produce voltage across width of element that is proportional to product of control current and magnetic flux density.—W. A. Seanga, A. R. Hilbinger, and

C. M. Barrack, Hall-Effect Multipliers, *Electronics*, 33:29, p 64-67.

ANALOG VOLTAGE MULTIPLIER—Pulse-width modulator and push-pull rectangular pulse generator driving diode-bridge switch give product of two input voltages X and Y , which must be in range of -10 v to $+10$ v. Input circuit of pulse-width modulator D1-V1 is supplied by 10-kc negative-slope sawtooth and input variable X .—J. Ash and Y. J. Fokkinga, Inexpensive Multiplier for Analog Computers, *Electronics*, 35:18, p 37.



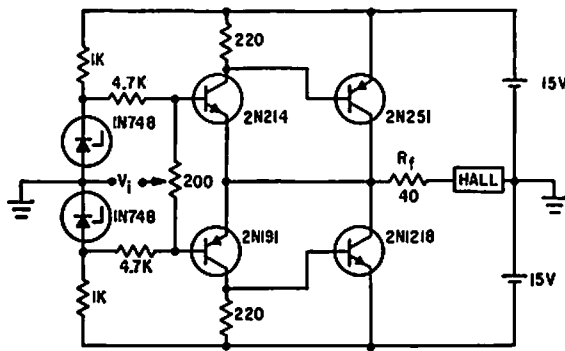
SQUARE ROOT OF SUM OF SQUARES OF THREE VARIABLES—Output signal of Hall-effect squaring multiplier HG is fed to differential amplifier A. Amplified difference controls current through lamp that determines resistance of photoresistor r which, with 2N174, controls Hall current derived from voltage source.—H. H. Wieder, Square-Root Computer Uses Hall Multiplier, *Electronics*, 37:4, p 30-31.



HALL MULTIPLIER FOR ANALOG RATIO COMPUTER—Indium arsenide Hall plate serves as analog multiplier in circuit with photoresistor

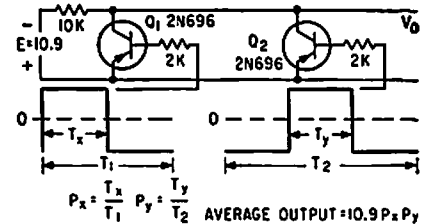
R and 2N174 grounded-emitter power amplifier that controls drive current of Hall generator. Differential amplifier A in feedback

loop including lamp I controls Hall current.—H. H. Wieder, Analog Ratio Computer Uses Hall Multiplier, *Electronics*, 36:45, p 46-47.



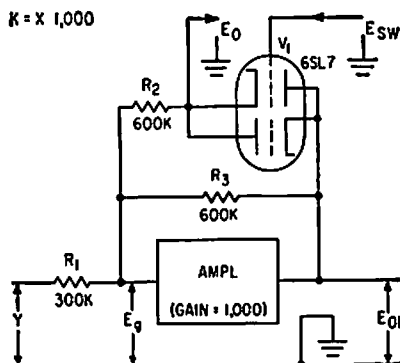
HALL-MULTIPLIER PLATE DRIVE—Current remains in phase with signal from 0 cps to over 20 kc. Distortion is less than 1% over

entire range.—R. A. Greiner, *Feedback Amplification Improves Hall-Effect Multipliers*, *Electronics*, 34:34, p 52-55.

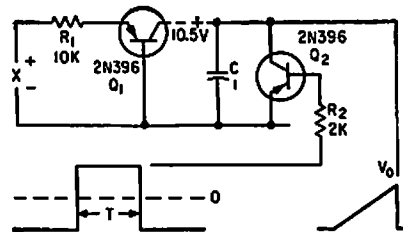


PROBABILITY MULTIPLIER—Based on converting two analog factors to duty cycles of pulse trains of uncorrelated repetition rate. Pulse-train control of and gate is such that there is no output unless both trains are simultaneously positive, and then average value of gate output is proportional to product.—T. R. Hoffman, *Analog Multiplication Using Time as One Variable*, *Electronics*, 33:33, p 136-138.

$K = X \cdot 1,000$

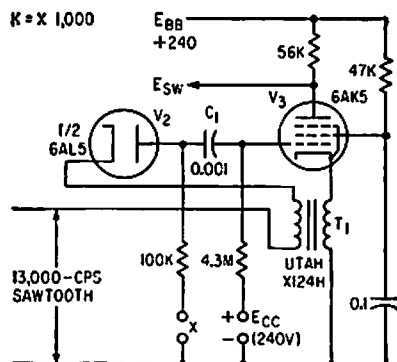


PULSE-AMPLITUDE MODULATOR—Used in multiplier that acts with one of operational amplifiers of analog computer. Double-triode V1 here provides pulse-amplitude modulation, for use with separate pulse-width modulator to form desired product of two input variables.—A. J. Ferraro, *Multiplier for Analog Computers*, *Electronics*, 33:45, p 73-74.

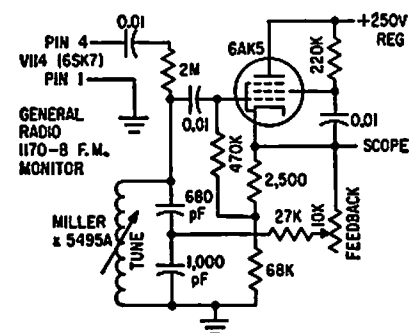


TRIANGLE MULTIPLIER—Electronic multiplication is achieved by making slope of sawtooth wave proportional to one factor and duration to other factor. Peak height of triangle will then be proportional to product. Triangle is generated by charging C with

collector current of constant-current generator Q1 during time interval in which Q2 is cut off.—T. R. Hoffman, *Analog Multiplication Using Time as One Variable*, *Electronics*, 33:33, p 136-138.



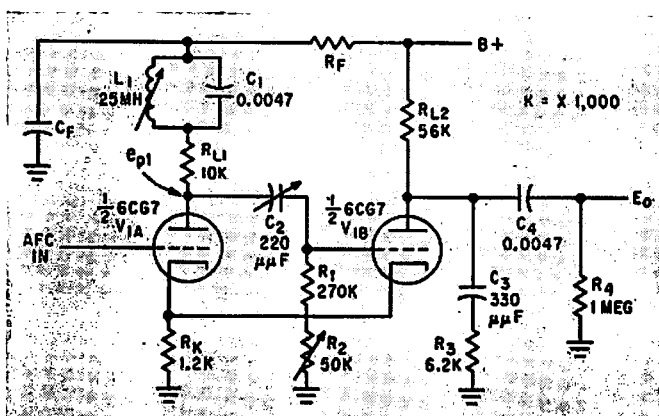
PULSE-WIDTH MODULATOR—Combines functions of rectangular pulse generator and width modulator for analog multiplier having error less than 2% of full-scale output.—A. J. Ferraro, *Multiplier for Analog Computers*, *Electronics*, 33:45, p 73-74.



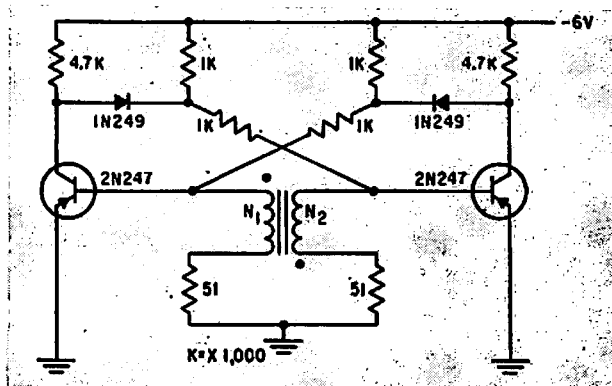
Q MULTIPLIER FOR F-M MONITOR—Checks calibration of f-m and television transmitter percentage-of-modulation monitors by using Q multiplier with monitor to make Bessel function measurements.—D. S. Henry, *Calibrating Broadcast Modulation Meters*, *Electronics*, 33:16, p 67.

CHAPTER 54

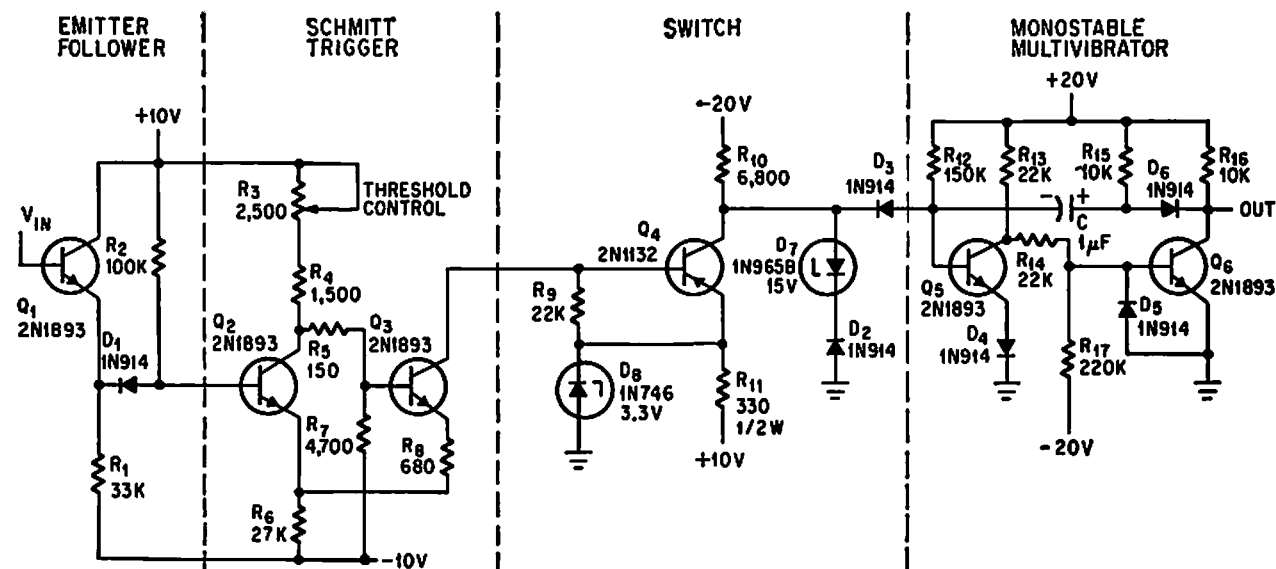
Multivibrator Circuits



TV HORIZONTAL-SWEEP OSCILLATOR—Cathode-coupled multivibrator includes noise-immunizing tuned circuit in plate circuit of triode.—C. L. Barsony, Graphical Checkout of Multivibrator Design, *Electronics*, 33:8, p 55-57.



TRANSFORMER-CONTROLLED MVBR—Gives sharper trailing edges and tighter control over ratio of on times of the two sides, as compared to choke control for same free-running mvbr.—W. M. Carey, Using Inductive Control in Computer Circuits, *Electronics*, 32:38, p 31-33.

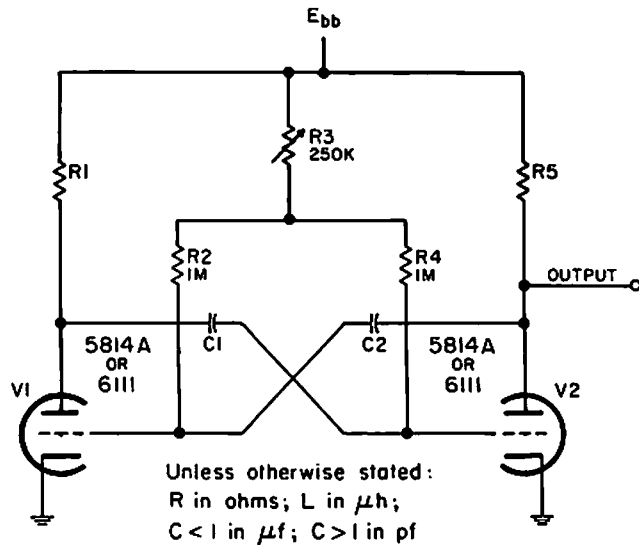


96-MILLISEC PULSE STRETCHER—Monostable multivibrator stays on for 96 millisec after

Schmitt trigger goes off, while C discharges through R12.—J. R. Giroux, Multi's Output

Duration Controlled by Input, *Electronics*, 38:4, p 88-89.

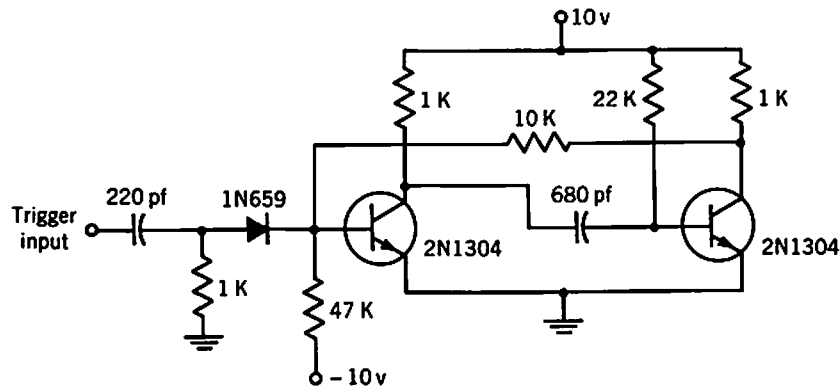
PREFERRED PRF GENERATOR—Astable plate-to-grid coupled mvbr serves as moderately stable repetition-rate generator having greater frequency stability than blocking oscillator and fewer components than Wien-bridge oscillator. One drawback is that output impedance for positive pulses equals plate load resistance, which must be relatively high for good frequency stability. Output is 260 v for 5814A and 125 v for 6111. Maximum prf is 8,000 pps.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 40, p 40-2.



Components:

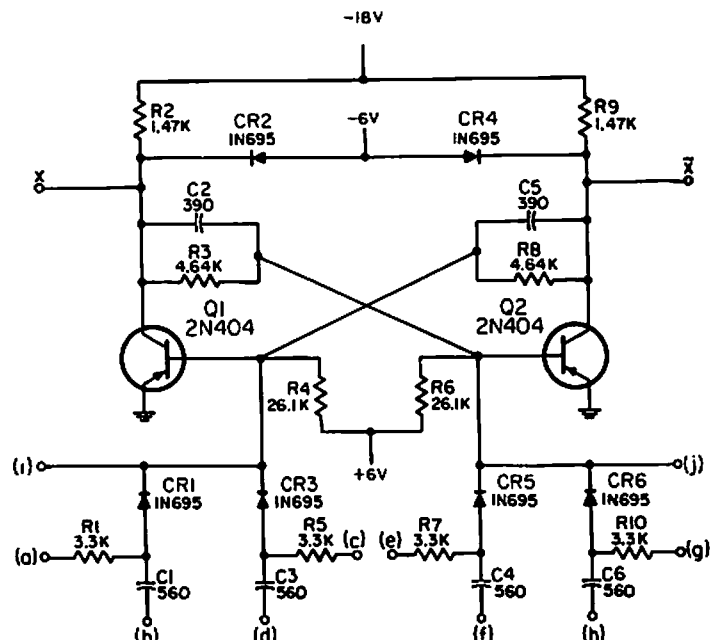
R1, R5: 68K Ω (5814A); 39K Ω (6111).

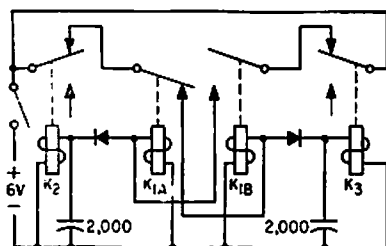
C1=C2= $\frac{0.79}{\text{prf}} \times 10^6$ pf, where prf=pulse repetition frequency in pulses per second.



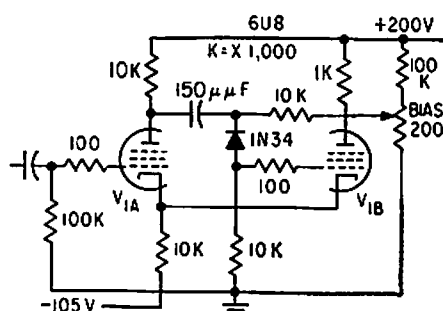
10-MICROSEC MONOSTABLE MVBR—Output pulse width is approximately 10 microsec with values shown for basic one-shot.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 381.

150-KC BISTABLE—Designed as storage element in digital logic circuits for computer, control, and communication equipment. Can be used as counter and as serial or parallel shift register at operating rates up to 150 kc under maximum load. Article gives connections of lettered terminals for various circuit functions and performance characteristics.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 9 (originally PC 212), p 9-2.

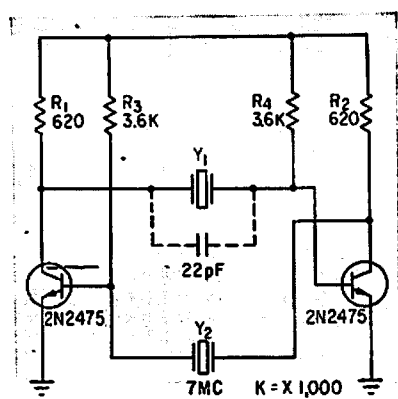




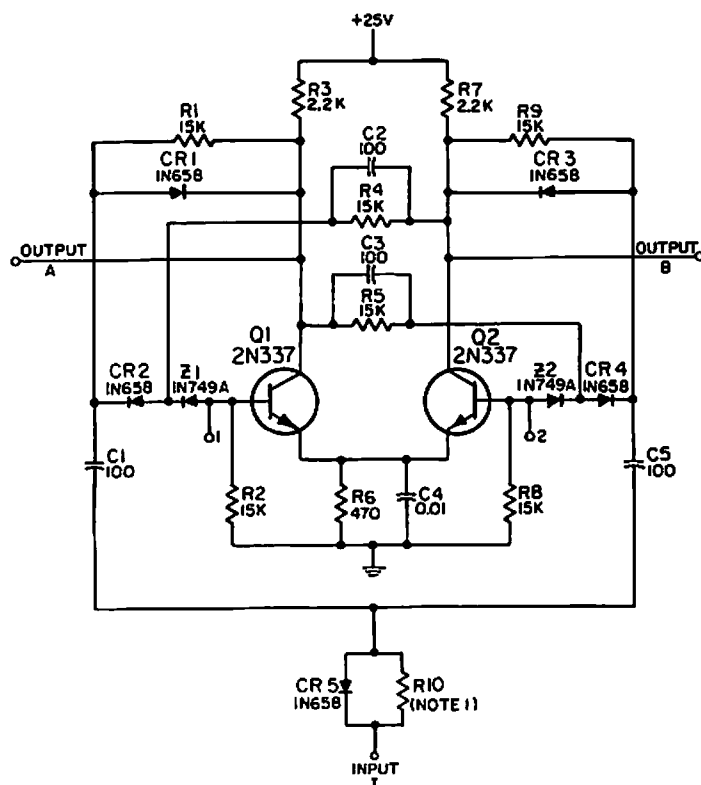
RELAY MVBR WITH ISOLATOR DIODES—Use of diodes to isolate capacitors reduces capacitance requirements for low frequencies. High-resistance relays for K2 and K3 cut costs.—R. L. Ives, *Multivibrator for Low Frequencies Uses Relays*, *Electronics*, 34:32, p 166-169.



CATHODE-COUPLED TRIGGER—Series diode improves sensitivity for cathode-coupled monostable mvbr while giving stability of 5% for threshold levels of several mv. Second tube can be triode, permitting use of 6U8 triode-pentode in compact assembly.—M. M. Vojinovic, *Series Diode Increases Multivibrator Sensitivity*, *Electronics*, 32:17, p 90-91.

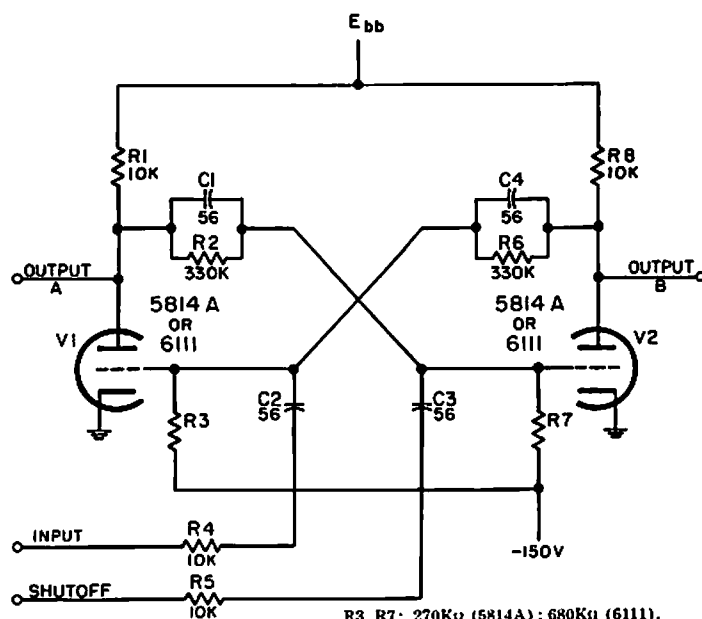


CRYSTAL CONTROL IMPROVES STABILITY—Use of 7-Mc crystal in place of feedback capacitors in conventional mvbr improves stability and waveform while still permitting operation down to 750 kc. Circuit also operates with one crystal; variable 7-47 pf capacitor in noncrystal-controlled side permits varying pulse width on this side over wide range.—H. R. Newhoff, *Crystal-Controlled Multivibrator has Better Stability*, *Electronics*, 36:15, p 60-61.



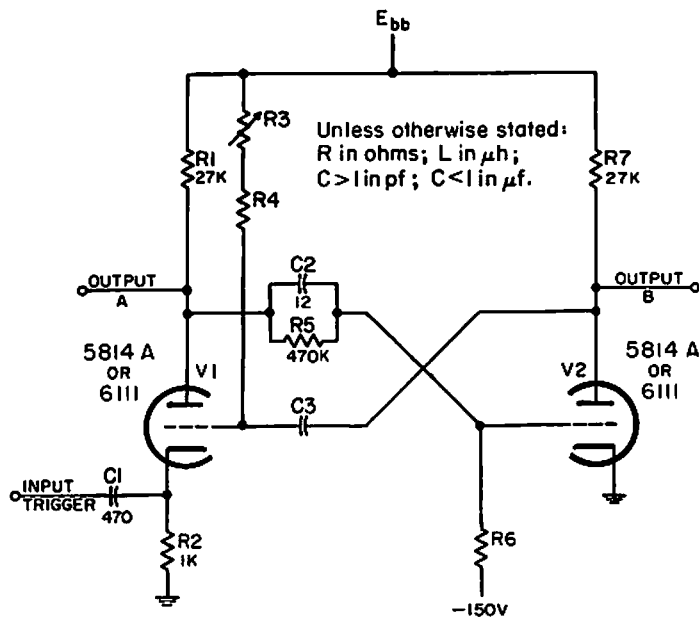
PREFERRED NONSATURATING BISTABLE—Used for frequency division of pulse trains when high stability is required. Cascade connection with appropriate feedback can provide any desired ratio. Also useful for coding, gating,

and synchronizing. Maximum operating rate is up to 1 Mc.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 15 (originally PC 253), p 15-2.



PREFERRED BISTABLE—In response to negative trigger, generates voltage steps of opposite polarity at the two outputs. Has no timing function, so second trigger is needed to restore circuit to initial state. Used as

radar gate. Requires 150 v plate supply for 6111 and 300 v for 5814A.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 42, p 42-2.

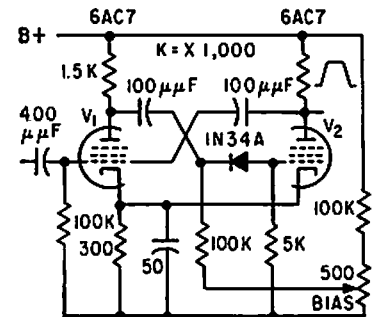


Components:

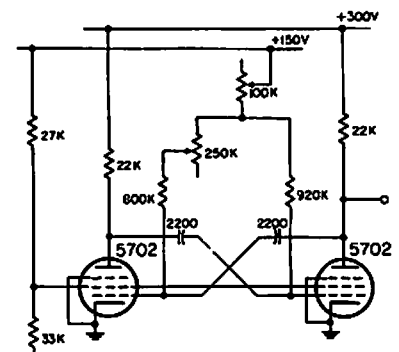
R6: 470K Ω (5814A); 680K Ω (6111).

	Gate duration (μ sec)		
	25-100	100-2500	>2500
R3.....	250K Ω	500K Ω	1M Ω
R4.....	470K Ω	1M Ω	1.5M Ω
C3 (pf).....	4000T	2000T	1000T

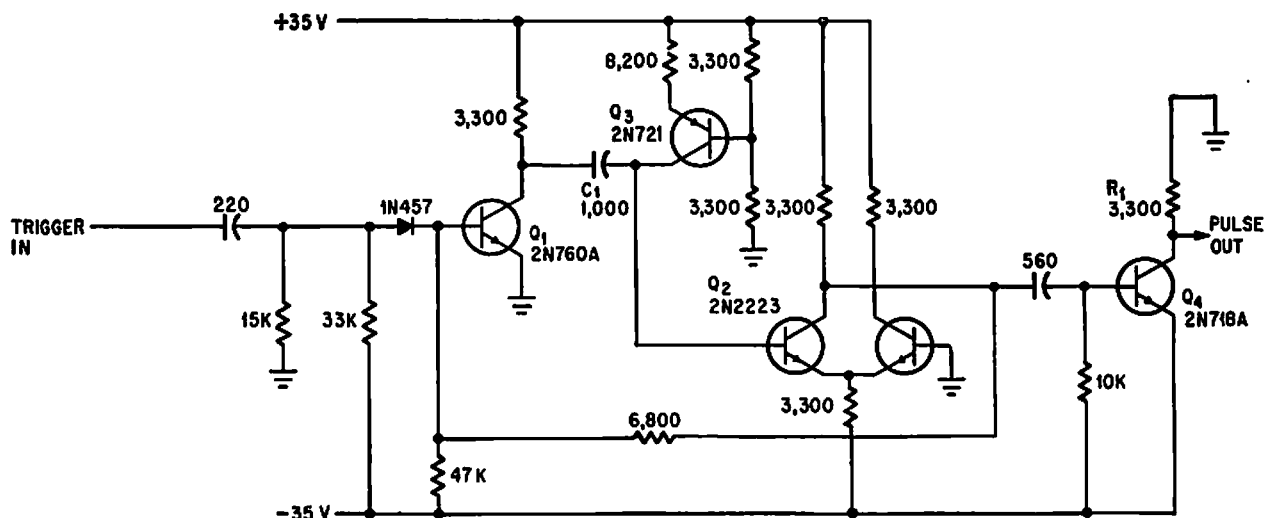
PREFERRED MONOSTABLE—Generates positive or negative rectangular gate in response to positive input trigger. Duration of gate will remain within 5% of initial preset value, and amplitude will be constant within 30%. Circuit is self-timing, with output gate duration determined by configuration and values of R3, R4, and C3. Used in radar to establish period during which main display sweep is presented, to provide crt unblanking gate, and to provide enabling gate for distance mark generator.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 41, p 41-2.



HIGH TRIGGERING SENSITIVITY—Semiconductor diode is used as series nonlinear element in feedback loop of monostable mvbr, to give good stability along with improved triggering sensitivity for nuclear event registration, pulse analysis, counting, and time modulation.—M. M. Vojinovic, Series Diode Increase Multivibrator Sensitivity, *Electronics*, 32:17, p 90-91.



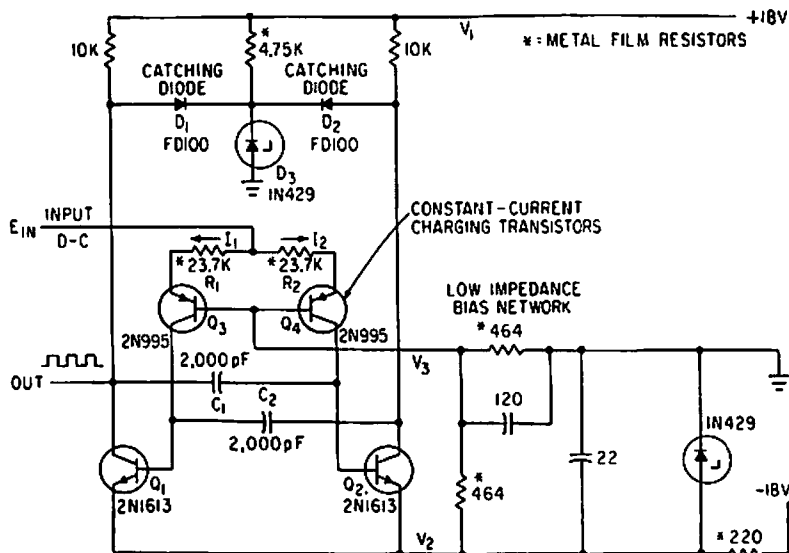
PRF GENERATOR—Provides frequency stability of 3% as repetition rate generator in airborne radar. Free-running connections are shown, but may also be triggered externally.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N5-1.



CONSTANT-PULSE-WIDTH ONE-SHOT—Monostable circuit holds pulse width constant within 0.5% from -65°C to 110°C . Stages Q1 and Q2 form conventional one-shot. Use

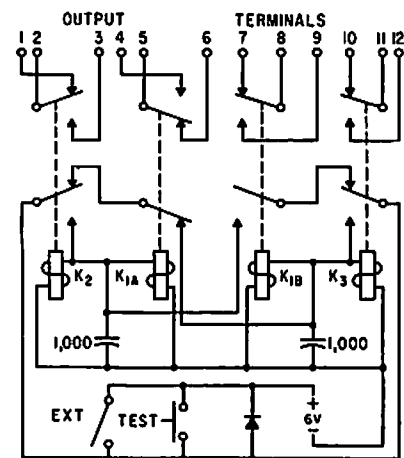
of differential amplifier for Q2 stabilizes base voltage for turn-on of Q2 near ground. Constant-current transistor Q3 minimizes effects of small voltage variations, and switching

transistor Q4 provides 35-v output pulse with base line at ground.—R. Stevens, One-Shot Multi Produces Constant Pulse Width, *Electronics*, 34:13, p 74-75.

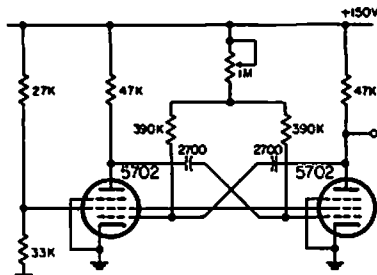


LINEAR VOLTAGE-FREQUENCY CONVERTER—Addition of two transistors to conventional astable mvbr gives constant-current charging of cross-coupling capacitors C1 and C2. Output frequency then varies linearly from

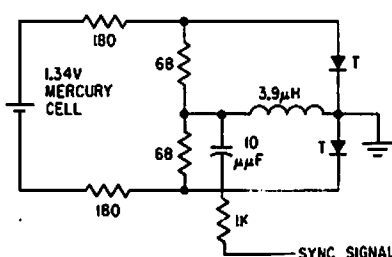
2,000 to 7,000 cps as d-c input rises from 0 to 5 v.—R. W. Biddlecomb, Latest Multivibrator Improvement: Linear Voltage-to-Frequency Converter, *Electronics*, 36:17, p 64-65.



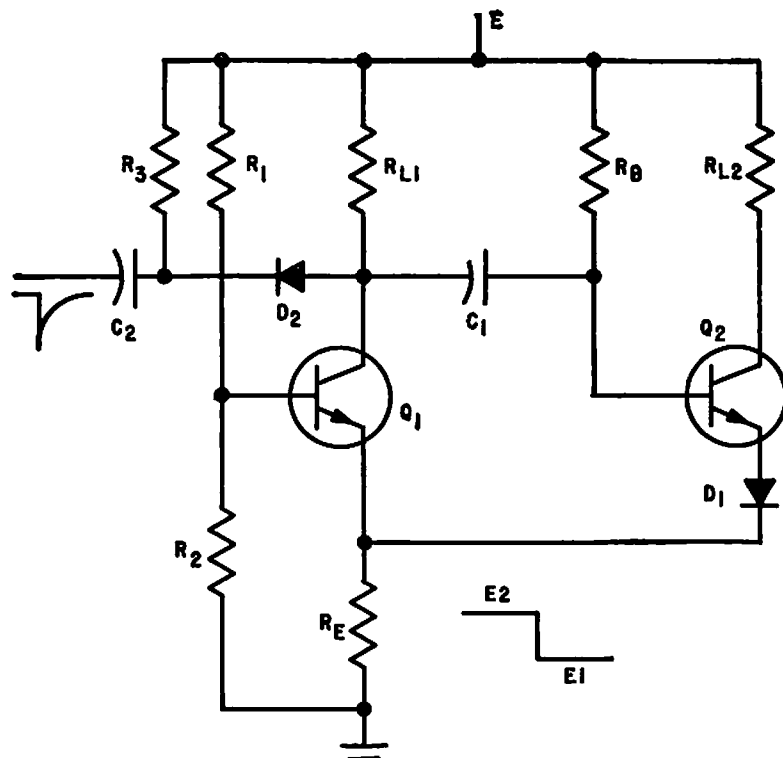
RELAY-ONLY MVBR—Consumes power only during switching. Can provide bistable, monostable, or astable operation at frequencies from a few operations per second to a few operations per hour.—R. L. Ives, Multivibrator for Low Frequencies Uses Relays, *Electronics*, 34:32, p 166-169.



PRF GENERATOR—Used as repetition rate generator in airborne radar. Gives greater frequency stability than blocking oscillator and greater economy of components than Wien-bridge oscillator.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N5-1.

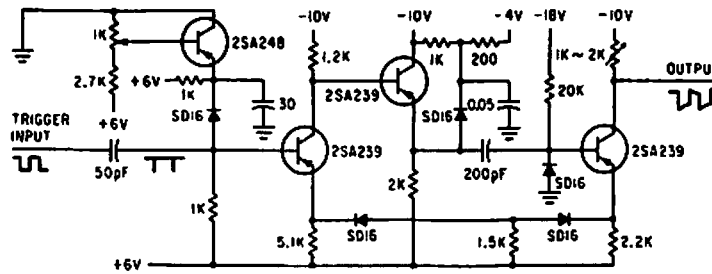


10-MC TD MVBR—Uses two 1-mA, 0.01-ohm tunnel diodes.—I. A. Lesk, N. Holonyak, Jr., and U. S. Davidson, The Tunnel Diode—Circuits and Applications, *Electronics*, 32:48, p 60-64.

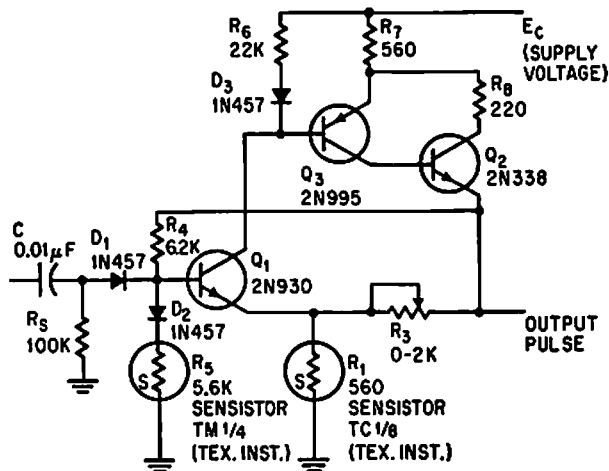


MONO DESIGN—Simplified equations are given for designing emitter-coupled monostable mvbr. With 20-v supply, component values for use with 2N388 transistors to provide 25-microsec pulse width, for E1 = 4 v and E2 = 7 v are RE = 1K, RL2 = 1,800

ohms, RL1 = 3,900 ohms, R1 = 56,000 units, R2 = 27,000 ohms, and C1 = 620 pf.—L. I. Kleinberg, Designing Emitter-Coupled Monostable Multivibrators, *Electronics*, 34:39, p 86.

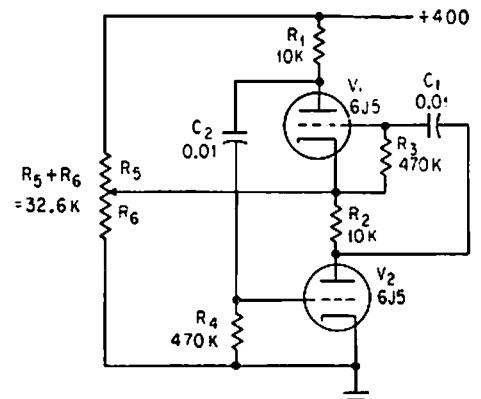


NONCUTOFF MONOSTABLE—Optimum operating conditions are achieved by keeping amplifiers connected to current sources continuously. With constant input voltage, triggering makes threshold level shift. Diodes in feedback loop improve switching speed. —H. Inose, Y. Yoshida, and H. Tada, *Noncutoff Circuits Improve Trigger Switching*, *Electronics*, 35:30, p. 36–39.

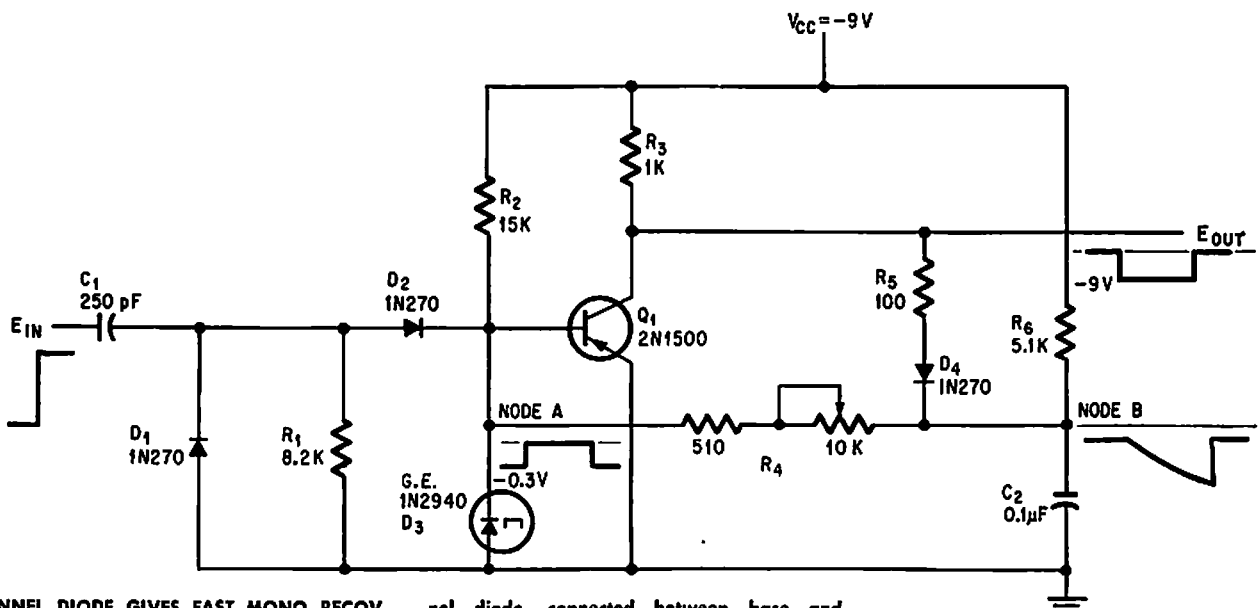


THERMAL MONO—Accurate pulse periods of 15 sec to 2 minutes are produced by thermal mono using silicon resistor (Sensistor) whose resistance varies with temperature,

power, and time. Used as timer to turn on plate supply 30 sec after filament supply. —L. L. Kleinberg, *Sensistor Produces Long, Reliable Pulses*, *Electronics*, 37:31, p. 51–52.



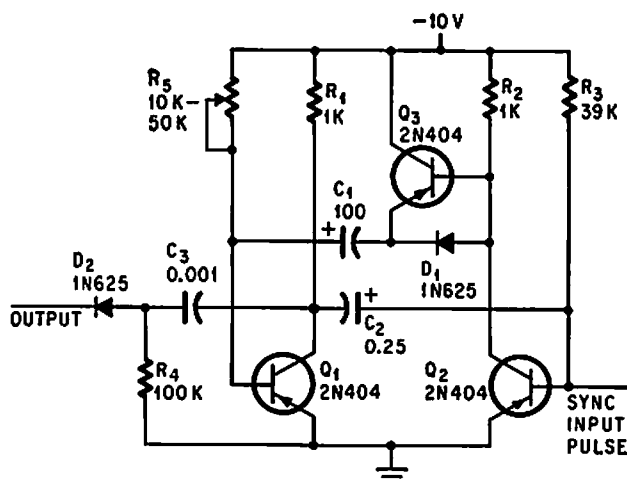
FREE-RUNNING CASCODE MULTIVIBRATOR—Output signal at cathode of V1 is nearly perfect square wave, either positive or negative depending on setting of potentiometer. —C. Sing, *Advantages of Free-Running Cascode Multivibrators*, *Electronics*, 37:5, p. 28–29.



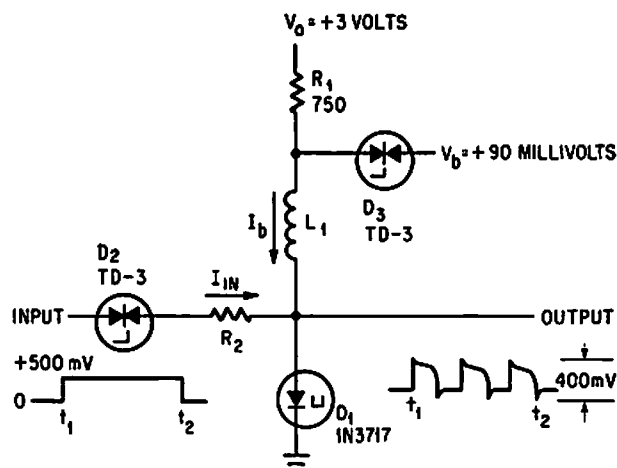
TUNNEL DIODE GIVES FAST MONO RECOVERY—Time delay can be varied continuously over 100-to-1 range. Duty cycle is 0.9. Tunnel

diode, connected between base and emitter of transistor switch, acts as current-controlled threshold detector. —P. Heffner,

Tunnel Diode Multi Recovers Quickly, *Electronics*, 37:25, p. 75–77.

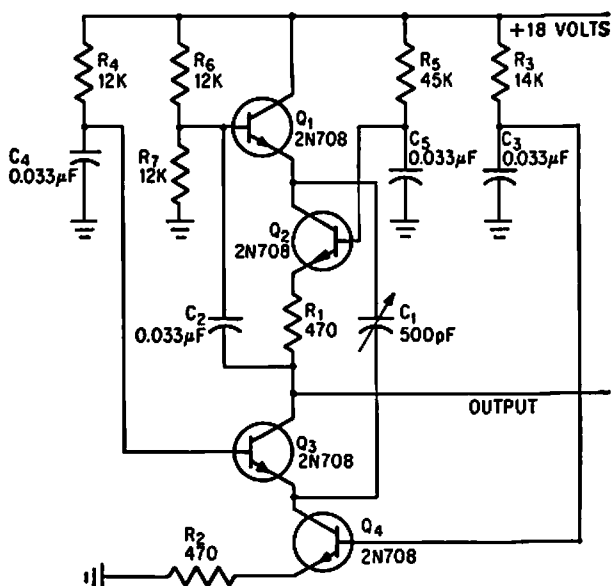
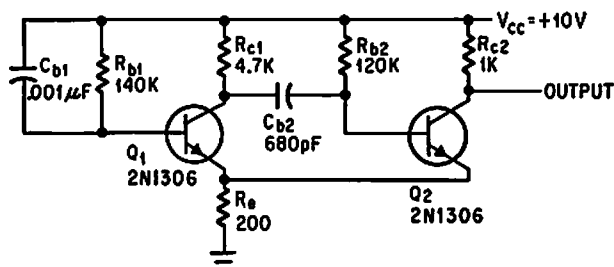


TRANSISTOR DECREASES RESET TIME—Time for recharging C_1 is reduced by factor of 30 when Q_3 is added to conventional astable mvbr.—S. A. Bell, Added Transistor Decreases Multivibrator Reset Time, *Electronics*, 37:21, p 72-73.



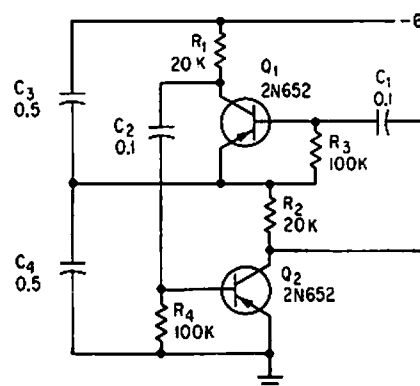
CLOCK PULSES—Tunnel-diode one-shot produces number of pulses in series, proportional to value of L_1 and width of input pulse.—C. A. Buddo, Pulse Width Converted to Pulse Sequence, *Electronics*, 38:4, p 86-87.

WIDE MARK-SPACE RATIO—Pulse width and interpulse period are independently adjustable from tenths of microsecond to several seconds by varying C_{b1} - R_{b1} .—S. Tesic, Pulses with Variable Mark-To-Space-Ratio, *Electronics*, 38:14, p 78-79.

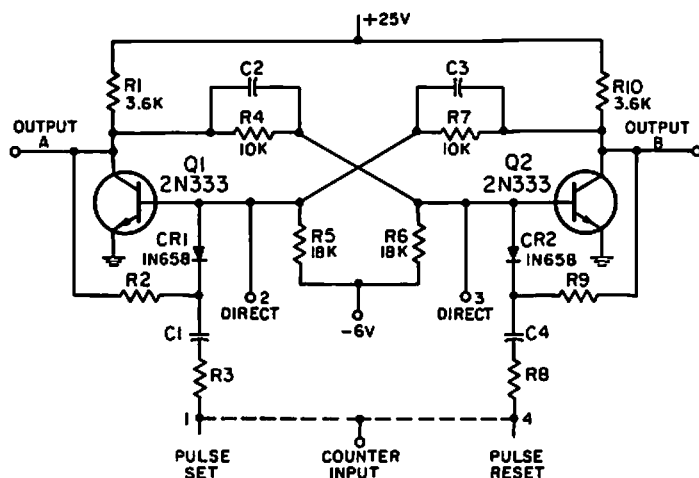


5-MC MVBR—Constant-current generators Q_1 and Q_2 conduct continuously. Timing capacitor C_1 charges through Q_1 and Q_4 and discharges in next half-period through Q_2 and

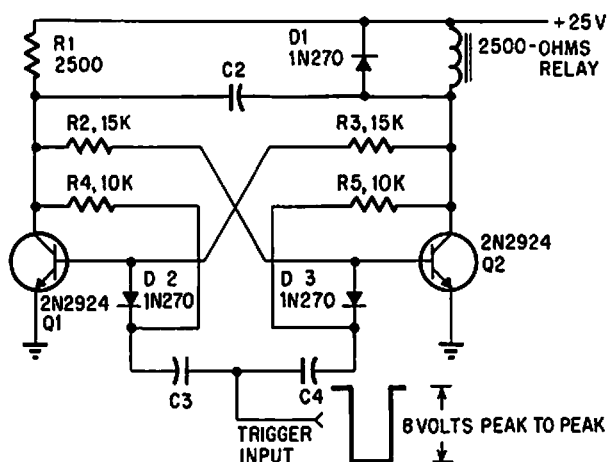
Q_3 , all in saturated states. Output pulse amplitude is 4 v at up to 5 Mc.—V. M. Ristic, Simple Multivibrator Operates at 5 Mc, *Electronics*, 38:17, p 86-87.



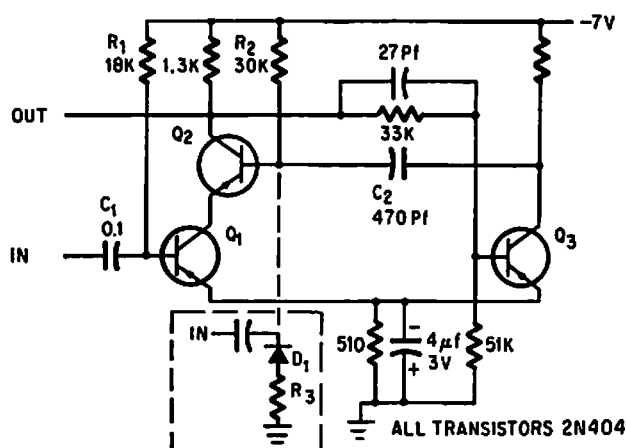
TWO-TRANSISTOR CASCODE MULTIVIBRATOR—Two capacitors in voltage-divider storage circuit control transistors to give choice of rectangular or sawtooth waveforms at output of Q_1 , depending on time constants C_1 - R_3 and C_2 - R_4 .—C. Sing, Advantages of Free-Running Cascode Multivibrators, *Electronics*, 37:5, p 28-29.



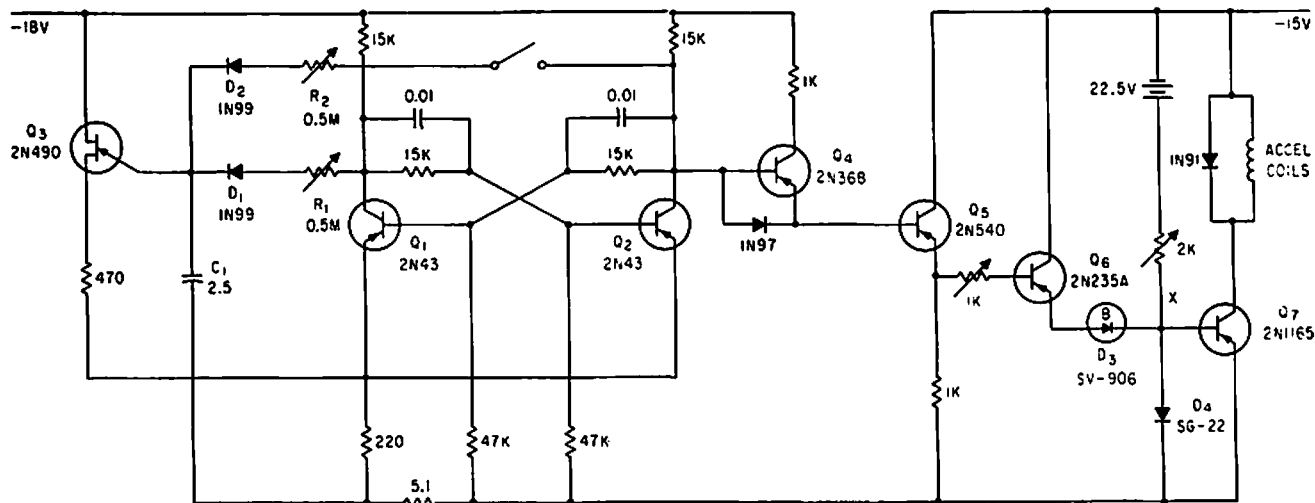
PREFERRED SATURATING BISTABLE—Slow-speed mvbr may be used as counter, shift register, gator, or switch. Provides transitional stages between electromechanical readout and higher-speed nonsaturating bistable counters. Maximum prf is 40 kc, delay time is 2 to 5 microsec, and output levels are both 18 v. 2N333 has been dropped from Preferred List, but 2N335 can be used if operating point is adjusted for its higher beta. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 14 (originally PC 250), p 14-2.



NOISE-IMMUNE BISTABLE—C2 gives immunity to accidental triggering by noise, though with some reduction in switching speed. When C2 is 0.1 mfd, upper frequency limit is 400 cps, and is 100 cps for 0.47 mfd.—R. W. Simister, Bistable Multivibrator Immune to Noise, *Electronics*, 39:6, p 97.



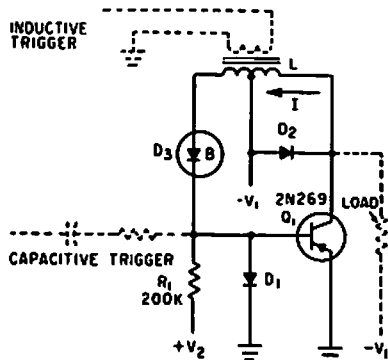
EXTRA TRANSISTOR STABILIZES ONE-SHOT—Q1 isolates triggering circuit from timing elements R2-C2, making duration of output pulse independent of input pulse amplitude and reducing minimum triggering voltage from 0.25 v to 0.1 v.—J. Kalisz, Isolating Transistor Improves One-Shot, *Electronics*, 38:25, p 76.



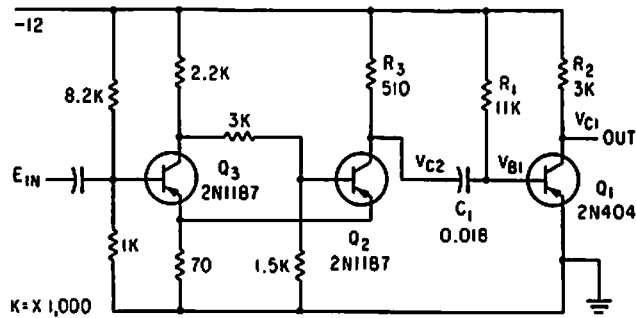
UNSYMMETRICAL PULSE GENERATOR—2N490 unijunction transistor serves as timer and trigger for flip-flop Q1-Q2, to provide 750-

milli-sec rectangular pulses spaced 250 milli-sec apart, for energizing falling-sphere accelerometer.—C. H. Price, Jr., High-Current

Solid-State Switches, *Electronics*, 33:38, p 72-73.

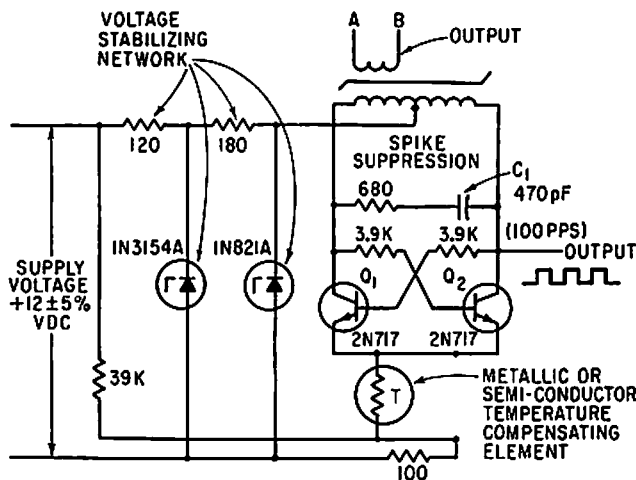


SINGLE-TRANSISTOR ONE-SHOT—Requires fewer components and gives higher reliability.—T. F. Heiting, One-Transistor Single-Shot, *Electronics*, 34:16, p 66.



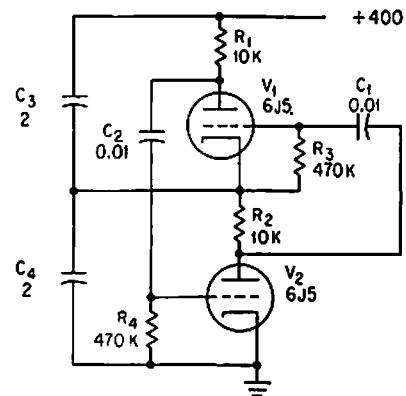
MONOSTABLE PULSE FORMER—Output stage of driver serves also as first stage of monostable multivibrator, with saving in components. Here Q2 is output of Schmitt trigger

and first stage of monostable mvbr Q2-Q1.—R. L. Paul and A. S. Ottonstein, Eliminating the First State of a Monostable Multivibrator, *Electronics*, 35:36, p 54-55.

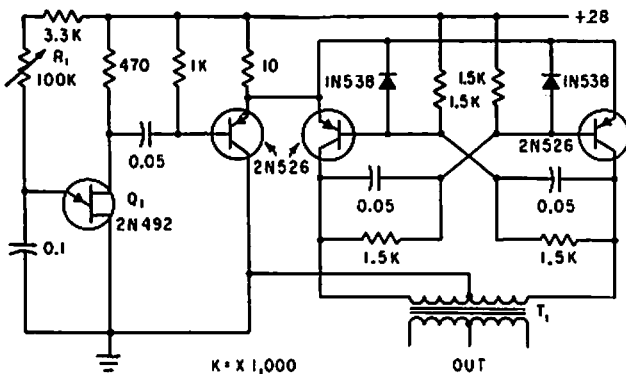


MAGNETICALLY COUPLED MVBR—Nonlinear element T in common-emitter lead stabilizes against temperature variation to within 0.1%

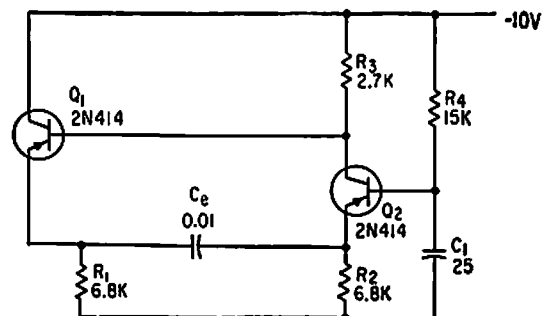
over 150°C range. Output is 100 pps.—M. Ingenito, Magnetically Coupled Multivibrators, *Electronics*, 36:13, p 42-43.



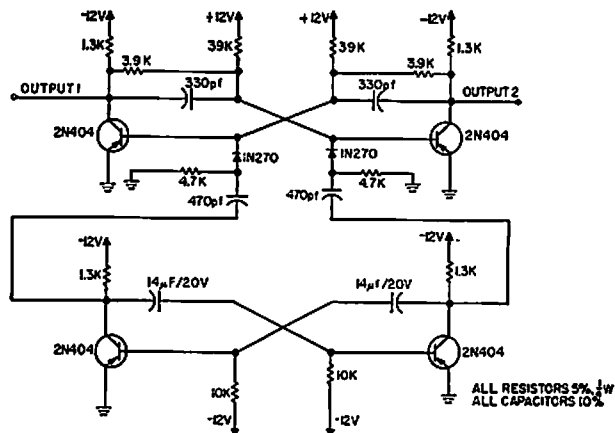
TWO-TRIODE CASCODE MULTIVIBRATOR—Two capacitors in voltage-divider storage circuit control dual-triode multivibrator to produce linear sawtooth waveform, square wave, sine wave, or pulse.—C. Sing, Advantages of Free-Running Cascode Multivibrators, *Electronics*, 37:5, p 28-29.



UNIUNCTION CONTROL OF MVBR—Transistor mvbr trigger for scr inverter is controlled by unijunction relaxation oscillator Q1. Square-wave output of T1 is required for triggering some inverter circuits.—D. V. Jones, Turn-Off Circuits for Controlled Rectifiers, *Electronics*, 33:32, p 52-55.

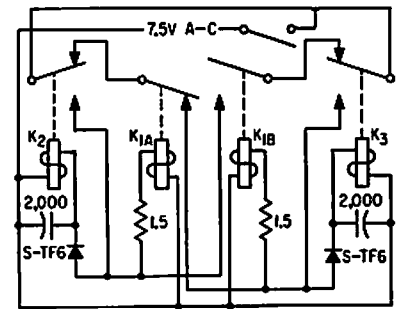


EMITTER-COUPLED MVBR—When Q1 conducts, Q2 is cut off and conversely. Duration of both quasi-stable states is controlled by C_e. Q2 should saturate when conducting, to prevent distortion in flat tops of rectangular output pulses.—B. Rakovic, One More Transistor makes a Linear Sawtooth, *Electronics*, 35:49, p 50-51.

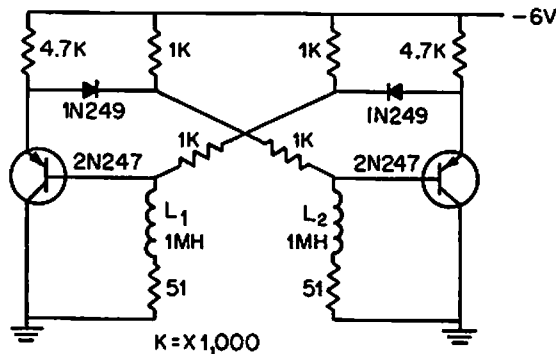


MICROSEC FALL TIME—Fast rise time of astable mvbr is used to set and reset bistable flip-flop, whose output waveform follows that of astable with important exception that

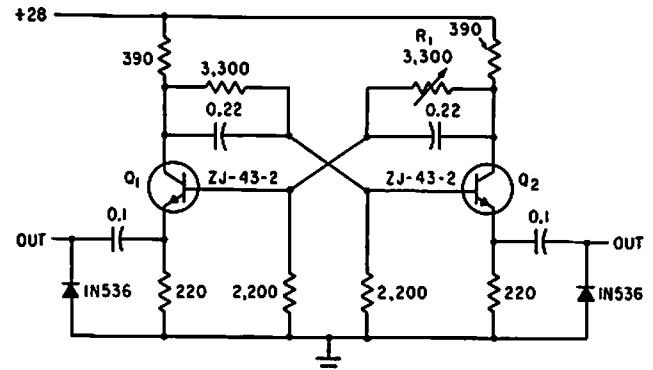
now both rise and fall times are very fast, of the order of few microsec for 5-cps square wave.—M. I. Neidich, Astable Multi has Microsecond Fall, *EEE*, 11:7, p 28.



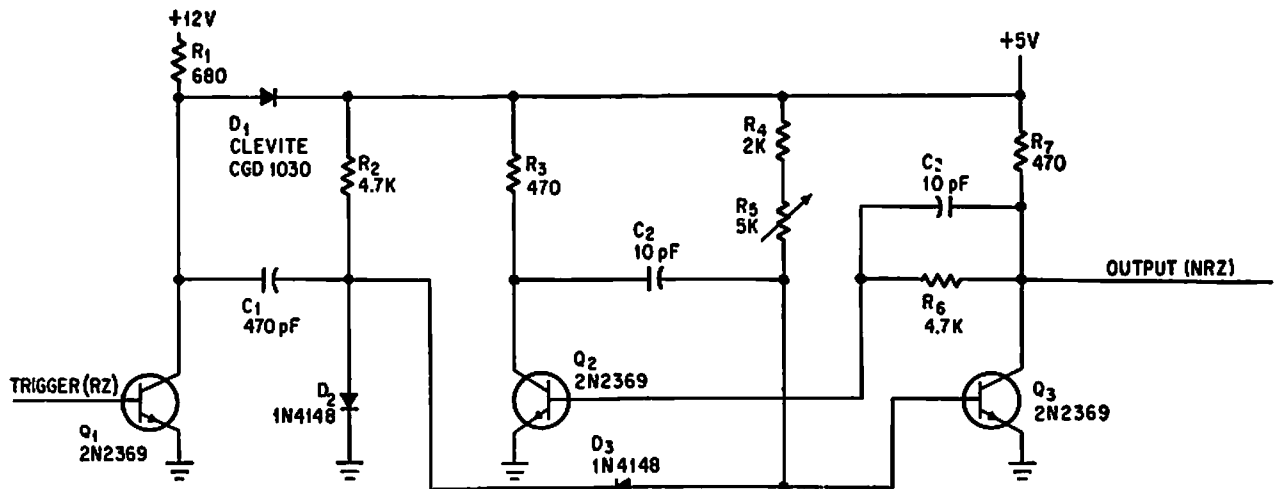
RELAY-ONLY MVBR FOR A-C—Use of a-c latching relay and rectifier diodes permits operation of low-frequency relay-type mvbr from a-c source.—R. L. Ives, Multivibrator for Low Frequencies Uses Relays, *Electronics*, 34:32, p 166-169.



CHOKE-CONTROLLED FREE-RUNNING MVBR—Small resistors between chokes and ground bias transistors initially into active region to insure self-starting. Crossover resistors insure that chokes recover rapidly.—W. M. Carey, Using Inductive Control in Computer Circuits, *Electronics*, 32:38, p 31-33.



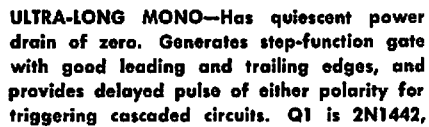
3-KC SCR TRIGGER—Two outputs from mvbr give alternating trigger pulses to each rectifier. R1 is adjusted for symmetrical operation.—D. V. Jones, Turn-Off Circuits for Controlled Rectifiers, *Electronics*, 33:32, p 52-55.



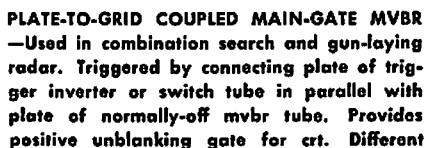
RZ TO NRZ ONE-SHOT—Zero recovery time of monostable multivibrator arrangement, achieved by using energy of input capacitor

to recharge timing capacitor C2, makes circuit useful for converting digital data from

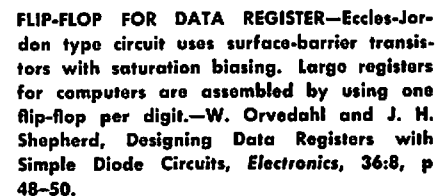
return-to-zero format to non-return-to-zero.—P. T. Rux, One-Shot Multivibrator with Zero Recovery Time, *Electronics*, 39:2, p 75-76.

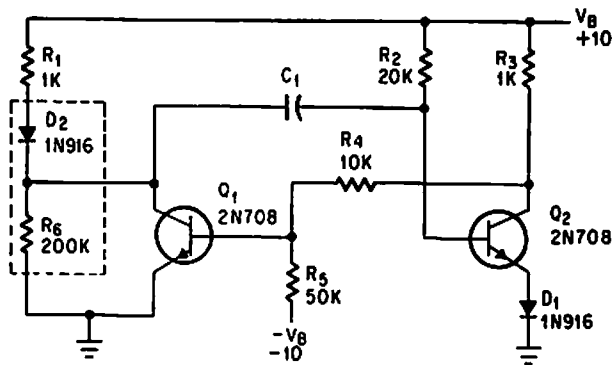


scr Q2 is 2N1595 or 3A31, and ujt Q3 is 2N489. Values for R3 and C2 give 50-sec pulse duration.—Ultra-Long Monostable Multivibrator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 66.

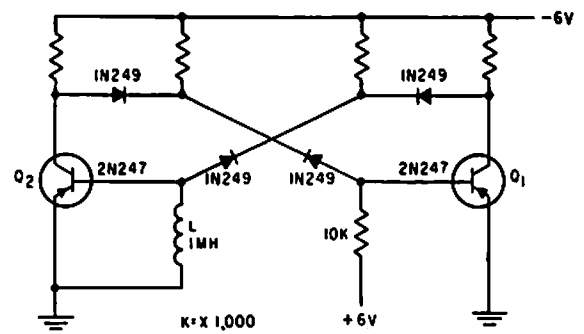
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gate lengths are obtained by switching mvbr capacitors.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-2.

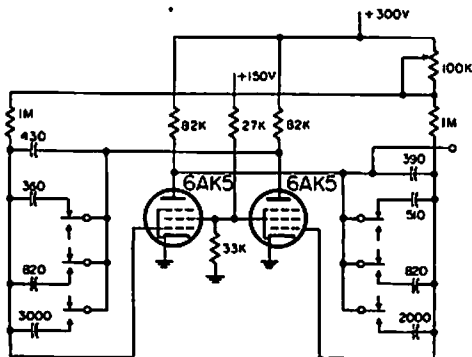




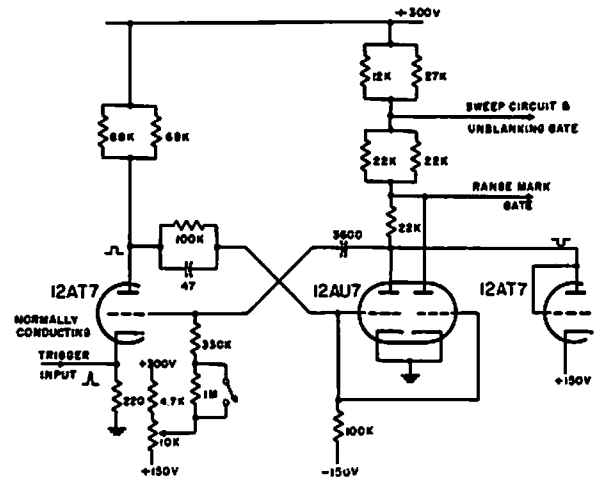
NOISE SUPPRESSION—Diode in collector circuit makes monostable mvbr immune to most noise pulses.—B. D. Simmonds, *Diode Quiets Input to Monostable Multi*, *Electronics*, 38:19, p 99-100.



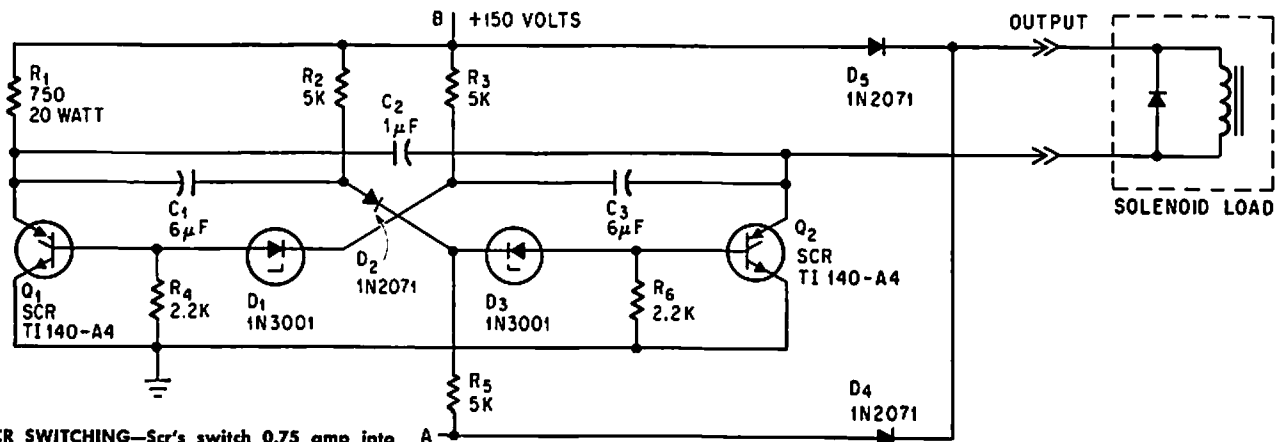
CHOKE-CONTROLLED ONE-SHOT—Provides output pulses longer in duration than input trigger. Can be triggered by either negative or positive pulse.—W. M. Carey, *Using Inductive Control in Computer Circuits*, *Electronics*, 32:38, p 31-33.



200, 400, AND 800 PPS PRF GENERATOR—Used in airborne radar. Frequency stability is 3% for 200 pps and 8% for higher frequencies. One drawback of mvbr here is that output impedance equals plate load resistance, which must be relatively high for good frequency stability.—NBS, *Handbook Preferred Circuits Navy Aeronautical Electronic Equipment*, Vol. 1, *Electron Tube Circuits*, 1963, p N5-1.



MAIN-GATE MVBR WITH DIODE LIMITER—Diode-connected triode in parallel with output tube plate limits positive swing at this point. Circuit is triggered by blocking-oscillator pulse through normally-on tube cathode resistor.—NBS, *Handbook Preferred Circuits Navy Aeronautical Electronic Equipment*, Vol. 1, *Electron Tube Circuits*, 1963, p N10-4.

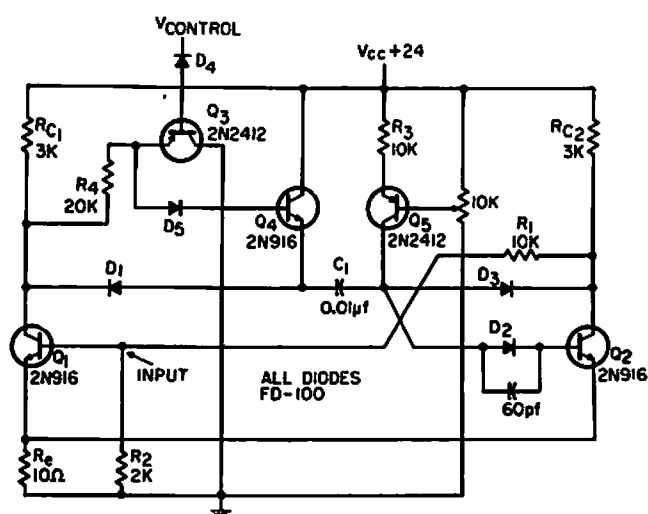
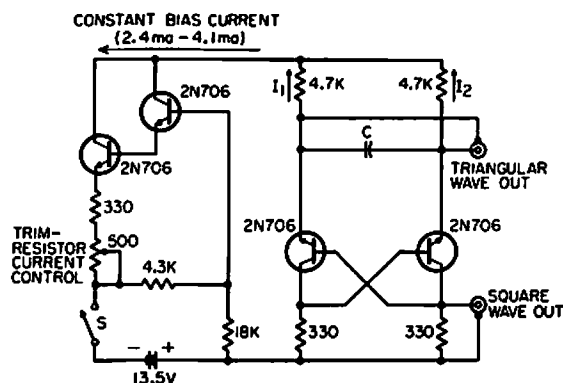


SCR SWITCHING—Scr's switch 0.75 amp into solenoid load at 20 cps with high reliability, simplicity, and low cost. For monostable

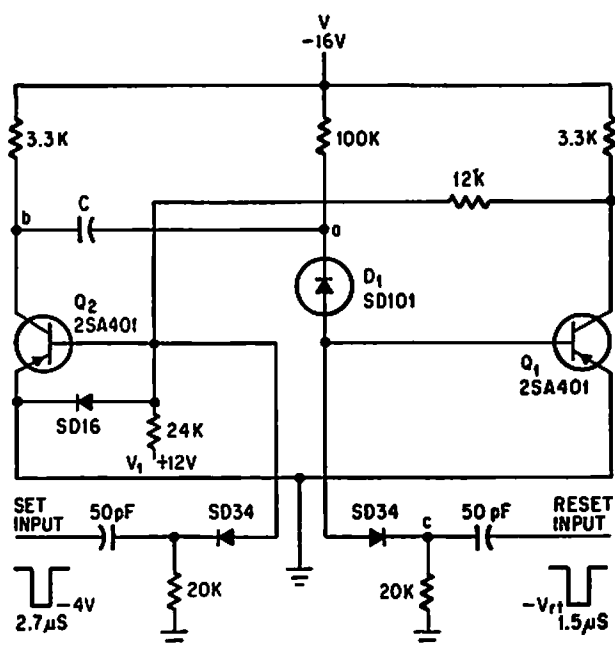
mode, remove supply voltage from B and apply single +150-v pulse to A.—H. D. Valli-

ant, *Scr Multivibrator Switches Reliably*, *Electronics*, 38:5, p 95.

CONTINUOUSLY VARIABLE PRR—When basic mvbr is biased from constant-current source, tops of square wave become flat across collector resistor, and triangular wave across capacitor becomes linear. Pulse repetition rate then varies directly with magnitude of constant biasing current, over range of 5.6 cps to 2.68 Mc, by using only seven different capacitance values for C (from 330 pf to 100 mfd).—J. H. Bayne, Jr., and R. J. Haislmaier, Improved Multi with Continuously Variable Rep Rate, *EEE*, 13:5, p 44-45.

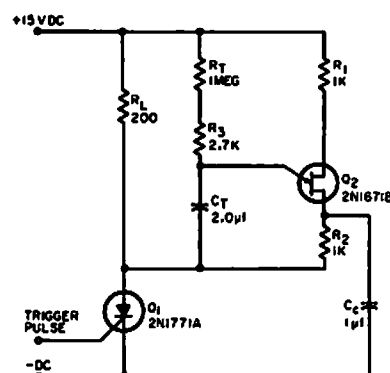


FAST-RECOVERY ONE-SHOT—Dynamic period can be varied linearly over range of 20 to 425 microsec. Retrigger time is only 3 microsec for short periods and 14 microsec for longest periods. Circuit is conventional except for d-c isolation diode D1 and drive resistor R4.—R. S. Hughes, A Linear, Voltage-Variable One-Shot With Fast Retrigger Time, *EEE*, 13:5, p 78-79.

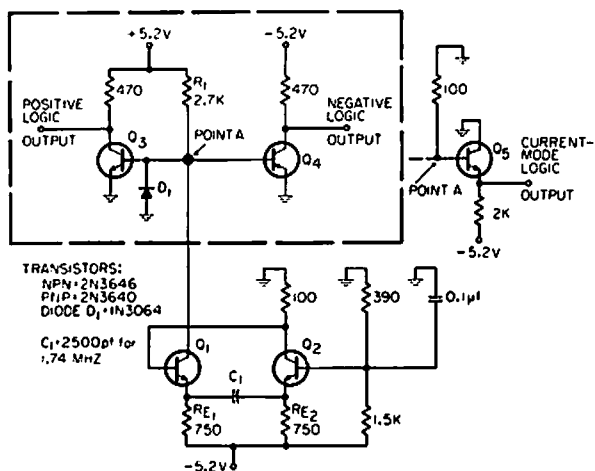


REDUCING RESET POWER LEVEL—Addition of diode D1 to conventional mvbr decreases required amplitude and duration of reset pulse.

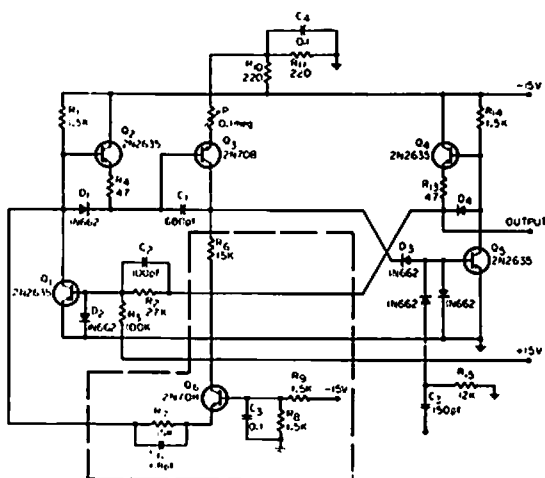
—H. Inose and T. Tomiyama, Diode Lowers Multi's Reset Power Level, *Electronics*, 39:13, p 76-77.



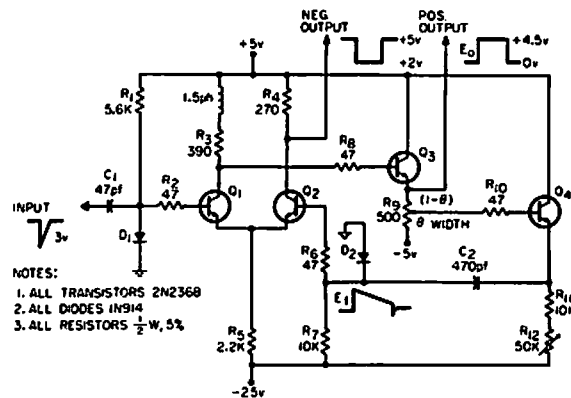
VARIABLE-POWER ONE-SHOT—Switches lead currents from few ma to over 1 amp for precise time interval ranging from few millisecond to one minute. Is excellent solid-state substitute for slug relays, dashpots, and thermal timers. Several stages can be cascaded to form sequence timer. With values shown, and RT at 680K, 1-v trigger pulse initiates 1-sec on period.—J. C. Rich and R. D. Turner, Variable Time, Power One-Shot Multivibrator, *EEE*, 12:7, p 25-26.



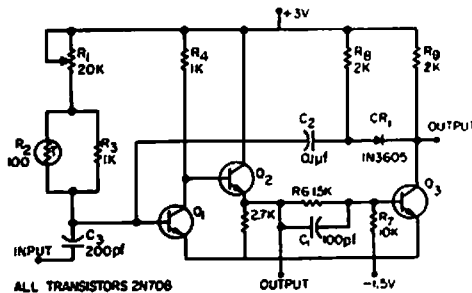
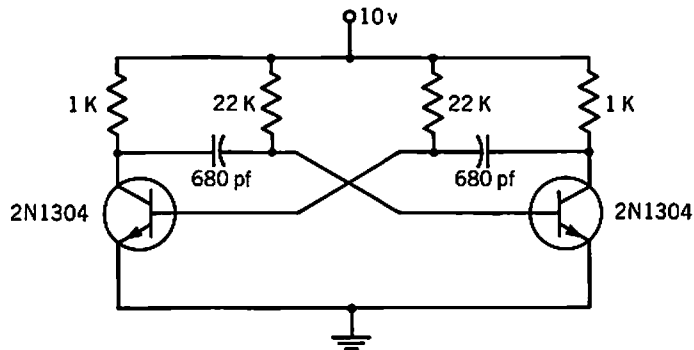
EMITTER-COUPLED ASTABLE LOGIC DRIVER—Self-starting design gives good frequency stability along with high-speed saturated positive and/or negative outputs. Current-mode logic output is optional, being obtained when circuit Q3-Q4 in dashed box is replaced by circuit of Q5. C1 determines operating frequency in range from 50 cps to 8.5 Mc.—D. R. Hoppe, *Emitter-Coupled Astable With Saturated Output*, *EEE*, 14:7, p 106.



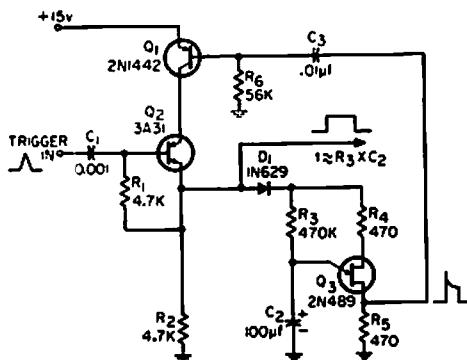
ADJUSTABLE-DUTY-CYCLE MONO—When ratio of pulse width to pulse spacing exceeds value set by R12, width of output pulse is automatically reduced to maintain duty cycle at preset maximum. Used as pulse driver for high-power amplifier when duty cycle must be limited to prevent overheating. If duty cycle is set for 50% and frequency is increased, output will be square wave for all frequencies up to maximum frequency limit for circuit. R9 adjusts output pulse width from 0.7 to 7 microsec.—D. N. Lee, Monostable Multi With Adjustable Duty Cycle, *EEE*, 13:9, p 92-94.



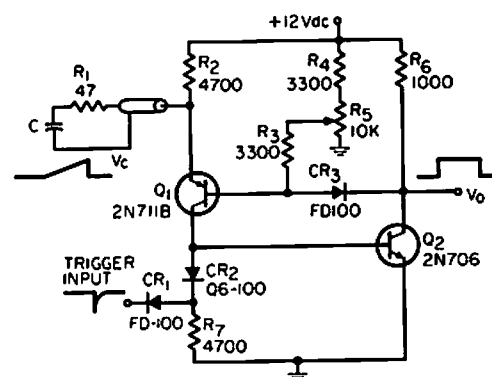
50-KC FREE-RUNNING MVBR—Uses 2N1304 transistors having base-emitter breakdown of -25 v, making emitter diodes unnecessary.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 380.



THERMISTOR COMPENSATED ONE-SHOT—Negative-temperature-coefficient thermistor in pulse width determining network keeps pulse width of mvbr constant within 0.6% over range of 25°C. Basic period with values shown is 357 microsec, increasing to 359 microsec at temperature extremes.—B. Hedlin, Temperature-Compensated One-Shot, *EEE*, 12:5, p 75.

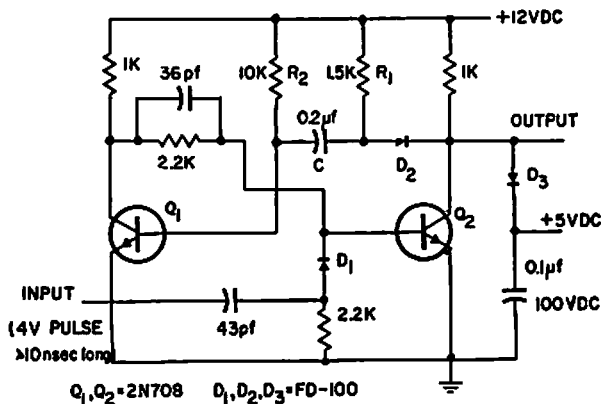
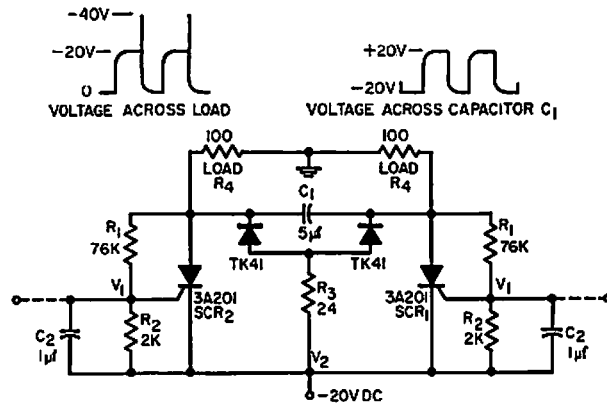


ULTRA-LONG MONO—For applications having only light loading. If required to drive heavy loads, standby efficiency is reduced, and C3 must be so large that circuit could be accidentally turned off by negative supply bus transients.—J. C. Schaeffert and N. F. Goldman, Improved Ultra-Long Monostable Multivibrator, *EEE*, 12:12, p 57-58.

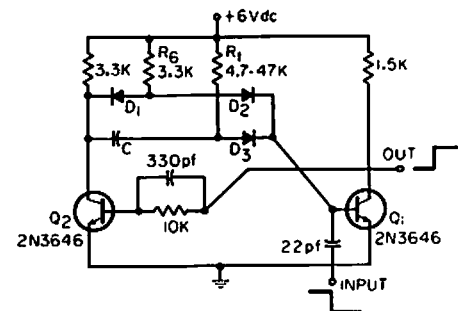


FAST-RECOVERY ONE-SHOT—Pulse width can be varied from 0.1 microsec to 10 millise in decade ranges by changing timing capacitors. Used in commercial radar range unit and in pulse analyzer.—J. Rogers, Fast-Recovery One-Shot Multi Gives 10:1 Width Control, *EEE*, 14:4, p 44-45.

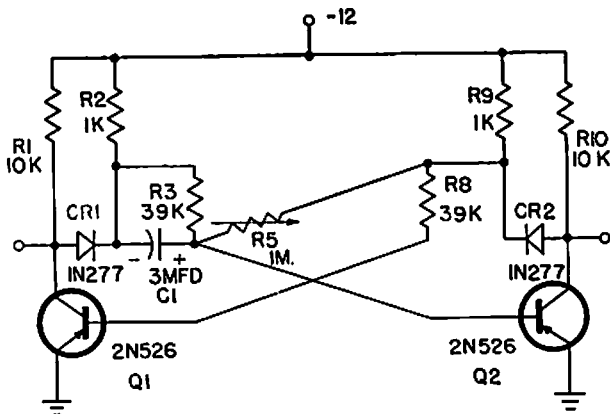
HIGH-POWER ASTABLE—Simple astable circuit design eliminates external trigger, minimizing number of components. Voltage dividers R_1 and R_2 provide gate voltage for scr's. Both dividers start charging associated capacitors C_2 until one scr breaks down, initiating oscillation. Used in converter power supplies.—W. B. McCartney and E. O. Uhrig, *Astable High Power Multivibrator*, *EEE*, 10:12, p 30-31.



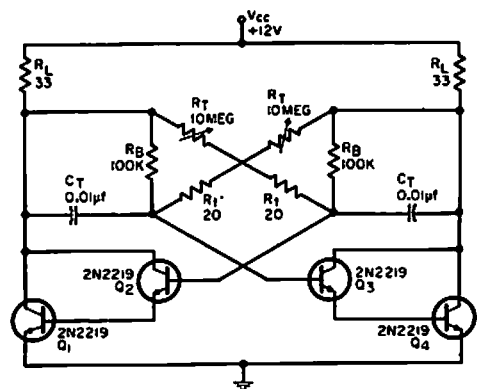
FAST-TURNOFF MONO—Has long delay time along with fast rise and fall times (each 30 nsec). Pulse amplitude is clamped at 5 v.—Fast Turnoff Monostable Multivibrator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 69.



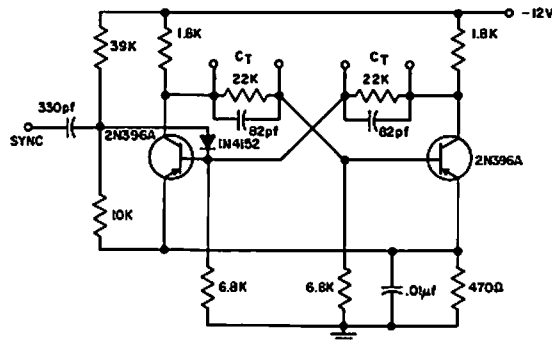
IMPROVED TRIGGERING OF MONO—Addition of three diodes and one resistor to conventional monostable mvbr permits increasing value of timing resistor R_t without making circuit susceptible to false triggering.—H. Cohen, *Eliminating False Triggering in Monostable Multis*, *EEE*, 14:8, p 168.



1,200:1 PULSE-WIDTH MONO—Output pulse width at collector of Q_2 can be varied from 0.5 to 300 millisec. With suitable trigger, serves as one-shot for variety of uses.—Wide Range Variable Multivibrator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 110.

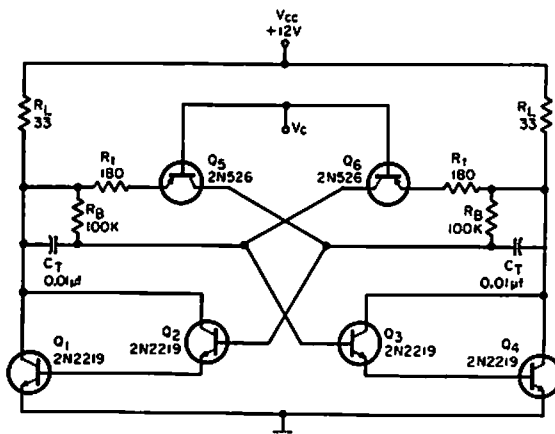


100 CPS TO 1 MC ASTABLE—Gives frequency change of 10,000 with reasonably good linearity over most of operating range. Two parts of timing cycle can be varied independently over wide range.—W. J. Mattox, *A Versatile, Very-Wide Range Multivibrator*, *EEE*, 13:7, p 59-61.

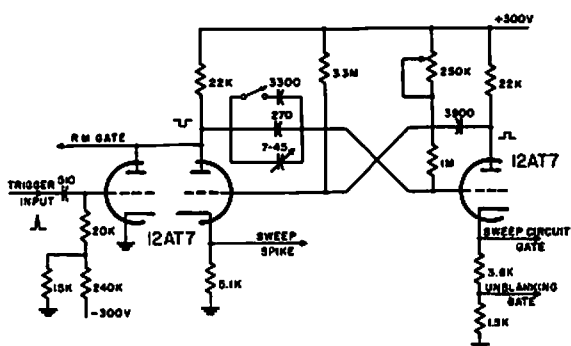
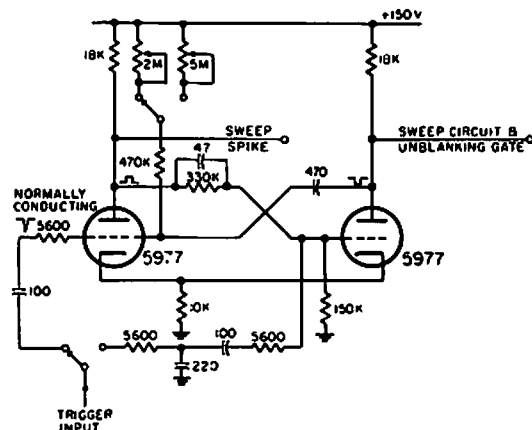


1 CPS-250 KC ASTABLE MVBR—Used as free-running oscillator for generating square waves and timing frequencies, and for frequency division. Synchronizing pulses permit

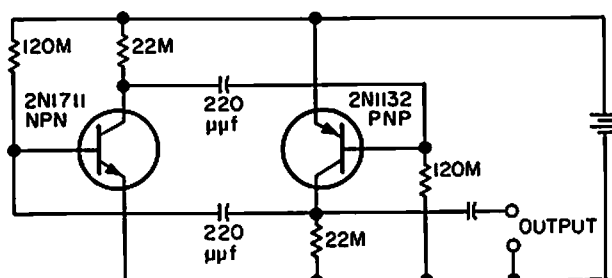
generation of subharmonics. Sync pulse amplitude must exceed +1.5 v with rise time less than 1 microsec.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 201.



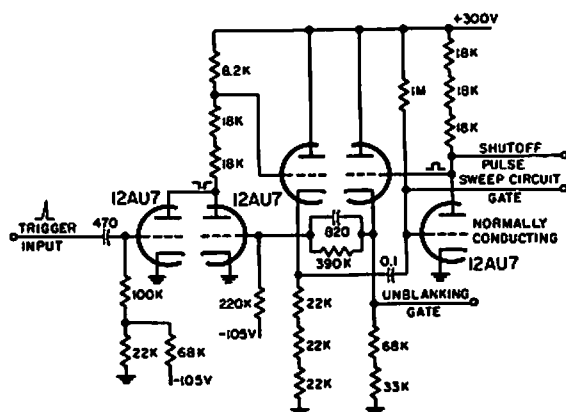
DOUBLE-COUPLED MAIN-GATE MVBR—Uses both cathode and plate-to-grid coupling, with gate length changed by switching of potentiometers. Differentiated negative gate from delay mvbr is applied as trigger to grid of normally conducting tube if undelayed range sweep is desired, or to grid of normally-off tube when delayed sweep is used.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-3.



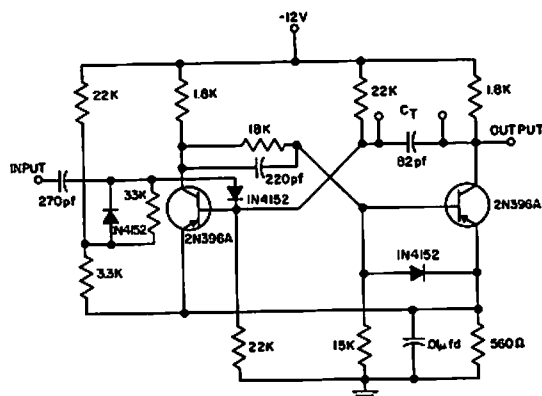
SYNCHRONIZED ASTABLE MAIN-GATE MVBR—Used in combination search and gun-laying radar. Different gate lengths are obtained by switching capacitors. Provides positive unblanking gate.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-4.



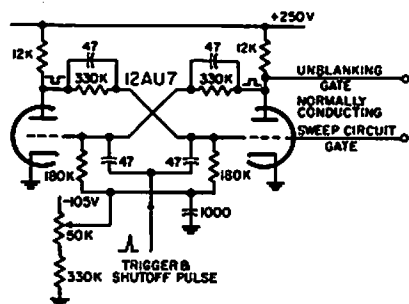
0.01-MICROWATT 40-CPS MVBR—Both npn and pnp transistors conduct at same time for part of cycle, and both are cut off for remainder of cycle, so average power consumed is much less than when one transistor always conducts. Frequency is 40 cps. With 0.6-v supply furnishing 0.015 microamp, total power consumption is 0.009 microwatt.—W. G. Shepard, A 0.01 Microwatt Multivibrator, EEE, 10:8, p 29.



CATHODE-FOLLOWER COUPLING FOR MAIN-GATE MVBR—Triode cathode-followers provide coupling from plate to grid of radar main-gate mvbr. Provides positive unblanking gate.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-2.

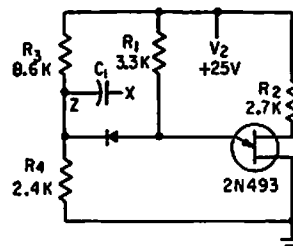


250-KC MONO—When triggered by input pulse up to 5 v, switches to unstable state and remains for predetermined time before returning to original stable state. Used for standardizing random-width pulses and generating time-delayed pulses. Output pulse duration range is 2 microsec to 1 sec. Maximum input frequency is 250 kc.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 201.

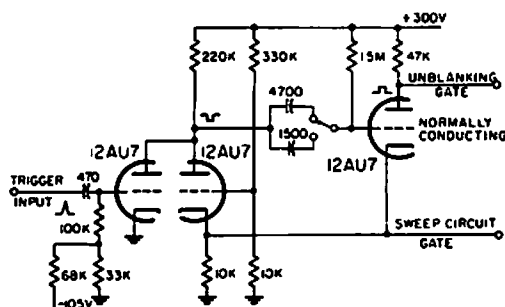


BISTABLE MAIN-GATE MVBR—Provides positive unblanking gate. Used in radar to provide gates during which displayed sweep is generated, along with gates for waveforms that must be coincident with display sweep,

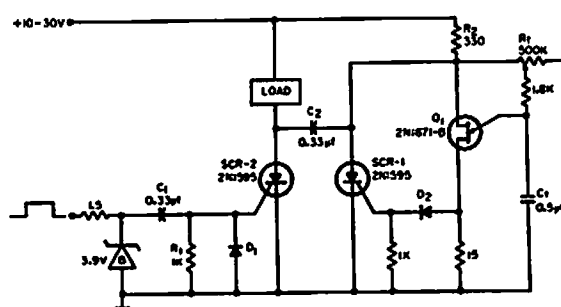
but monostable mvbr is generally preferred for this application.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-3.



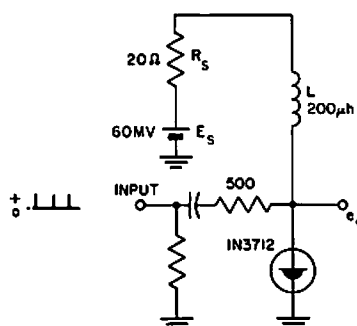
UJT BISTABLE WITH DIODE DECOUPLING—Needs only small negative trigger at X for turnoff.—T. P. Sylvan, Bistable Circuits Using Unijunction Transistors, *Electronics*, 31:51, p 89-91.



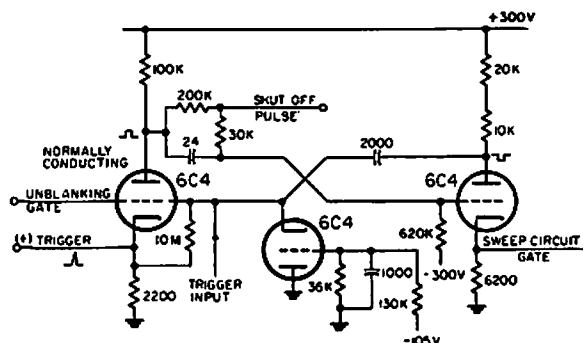
CATHODE-COUPLED MAIN-GATE MVBR—Provides positive unblanking gate. Used in radar to provide gate during which display sweep is generated. Capacitor switching provides choice of gate lengths.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-2.



HIGH-POWER ONE-SHOT—Operates from random-amplitude (10 to 28 v) square-wave input having 1 to 6.5 sec random duration, and switches 20-w load for adjustable period of 5 to 200 millise. Can easily be adapted for transient detection, pulse-width adjustment, and time delays.—G. T. Pennell, High-Power One-Shot Multi, *EEE*, 12:9, p 62.

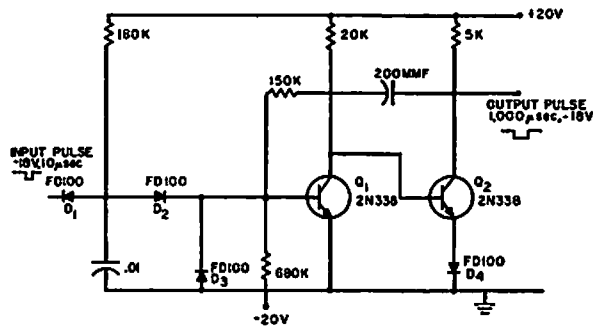


TUNNEL-DIODE MONOSTABLE—Used as pulse-controlled oscillator. Power consumption is very low.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 366.



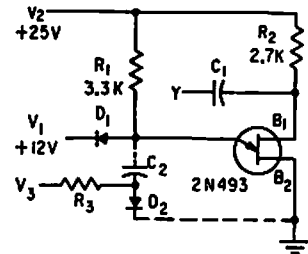
MAIN-GATE MVBR WITH TRIODE LIMITER—Triode connected to grid of normally-on tube limits swing of voltage. Plate of shutoff tube is tied to point in d-c coupling network between plate and grid, to give greater flex-

ibility in setting plate voltage level of shutoff tube.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N10-5.

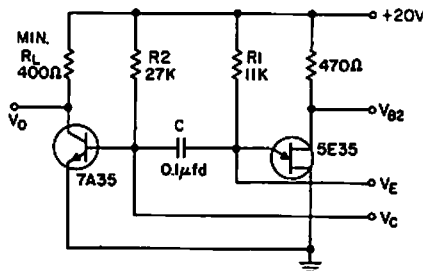


MONO WITH NEGATIVE RECOVERY TIME—Will respond to input pulses occurring even before end of output pulse, which in effect gives negative recovery time. If circuit begins normal 1,000-microsec cycle and another trigger pulse arrives in 500 microsec, output pulse will last 1,500 microsec, or 500 microsec longer than usual. In other words, out-

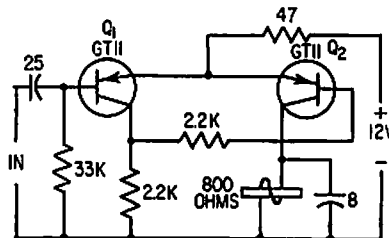
put pulse continues for 100 microsec after last trigger pulse. Input pulses should be of standardized voltage and long enough to discharge 0.01-mfd capacitor.—Monostable Circuit with Negative Recovery Time, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 72.



UJT BISTABLE WITH CLAMP—Clamping diode holds emitter voltage below peak-point voltage. When negative trigger at base B2 turns on transistor, D2 is back-biased and R1 becomes emitter load. Operation is stable if capacitance between emitter and base B1 is kept below critical value.—T. P. Sylvan, Bistable Circuits Using Unijunction Transistors, *Electronics*, 31:51, p 89-91.

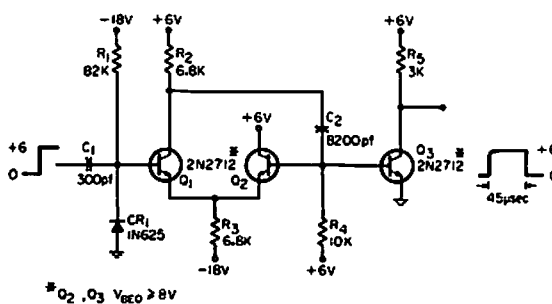


TRANSISTOR-UJT MVBR—Uses low-cost 7A35 silicon mesa transistor.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 341.

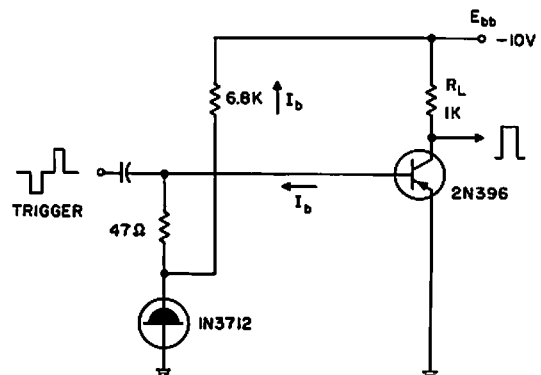


MULTIVIBRATOR-CONTROLLED RELAY—Control signal is used to trigger mvbr that operates relay when input signal is greater than predetermined level (about 10 mv), and

releases relay immediately when signal falls below this level.—G. B. Miller, Multivibrator Operates Relay, *Electronics*, 31:49, p 106-112.



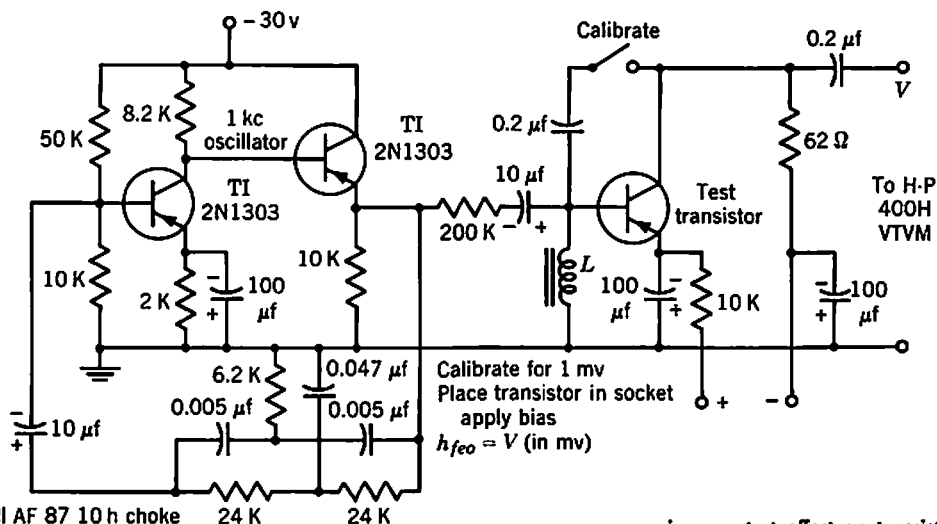
IMPROVED ONE-SHOT—Improvement of basic circuit uses less power and fewer components, while providing higher timing accuracy.—T. G. Ellestad, Improved One-Shot Output Circuits, *EEF*, 13:8, p 67.



BISTABLE TD-TRANSISTOR—Fast switching speed of tunnel diode contributes to output waveform.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 367.

CHAPTER 55

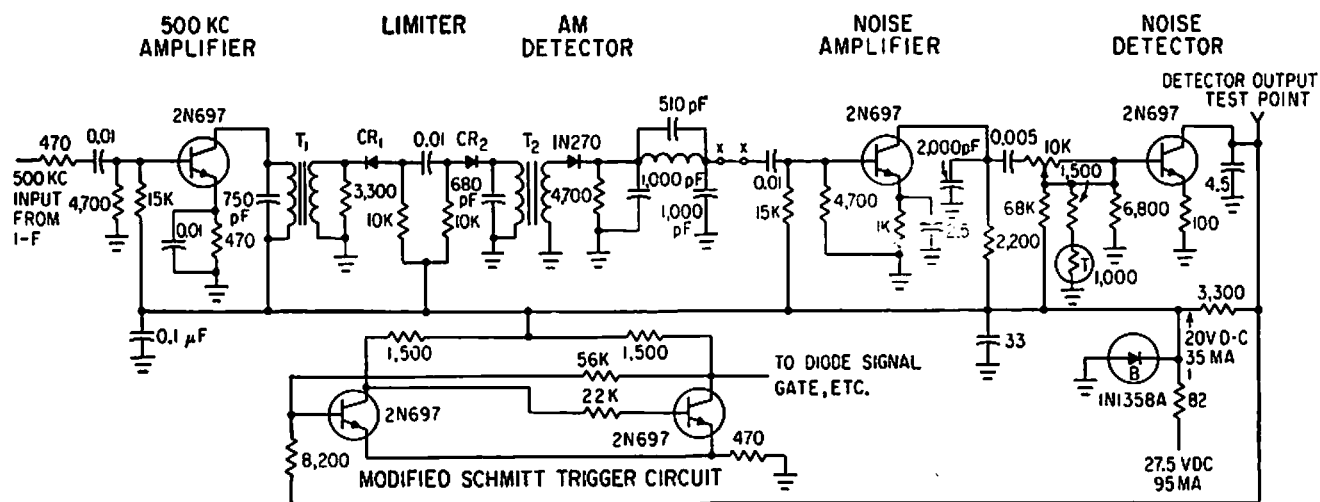
Noise Circuits



1-KC NOISE FIGURE TEST SET—Used in measuring low-frequency common-emitter

short-circuit forward current transfer ratio h_{fe} , which is one of the parameters hav-

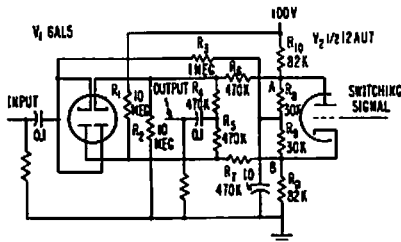
ing greatest effect on transistor noise figure. —Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 304.



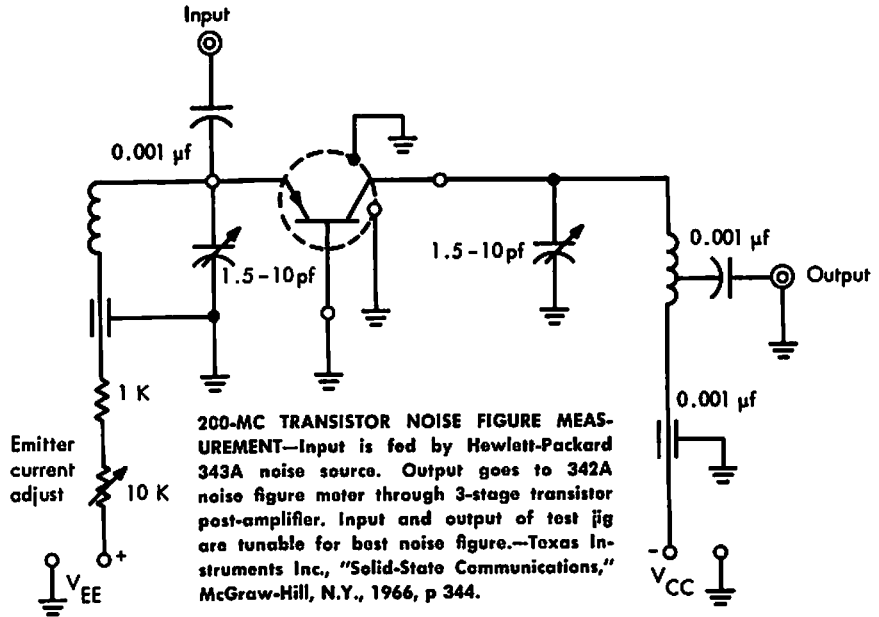
FAIL-SAFE COMMUNICATIONS SQUELCH CIRCUIT—Circuit adjusts automatically to changing noise levels in point-to-point vhf and uhf receivers while suppressing noise when no carrier is present, but stays open when re-

ceiver gain is below threshold level of 0.7 microvolt over range of 108.95 to 135.95 Mc. Schmitt trigger gives fast turn-on and turn-off of receiver audio output at predetermined carrier-to-noise ratios.—H. G. Mi-

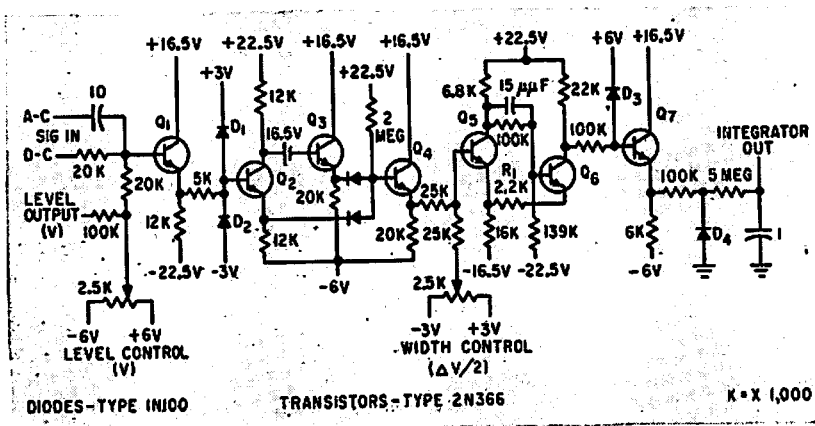
chael, Fail-Safe Squelch Circuit Adapts to Changing Noise Levels, *Electronics*, 36:1, p 88-91.



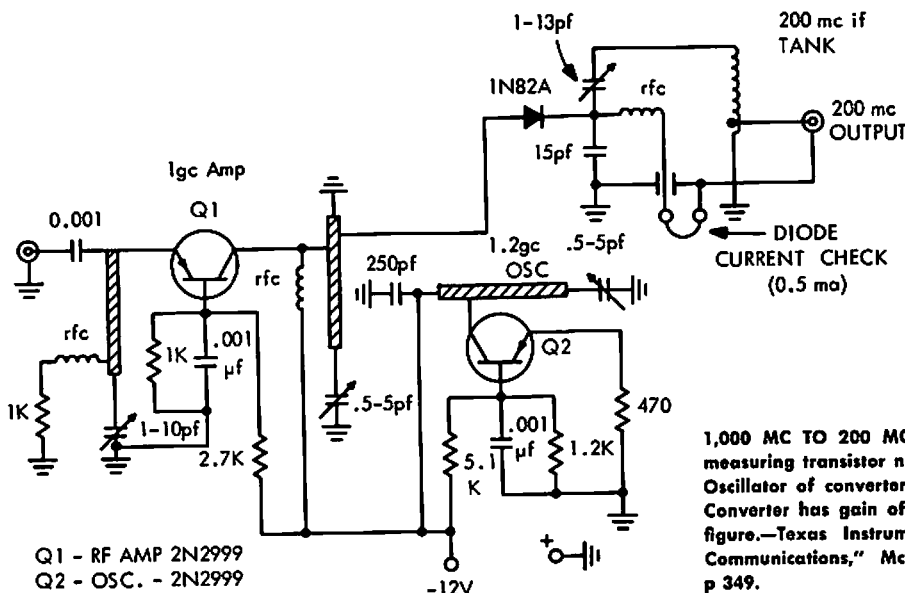
TRIODE VARIABLE-RESISTANCE THRESHOLD CONTROL SWITCH—Passes only signals above predetermined positive and negative threshold value, for suppression of audio background noise. When V2 is cut off, threshold is at highest value, corresponding to off position of switch. With V2 conducting, threshold will be low and practically all signals appear unclipped at output, corresponding to on position of switch. Provides stable, nontransient switching, independent of changes in tube characteristics.—W. E. Earle, A-C Threshold Converts to Switch, *Electronics*, 31:1, p 96-99.



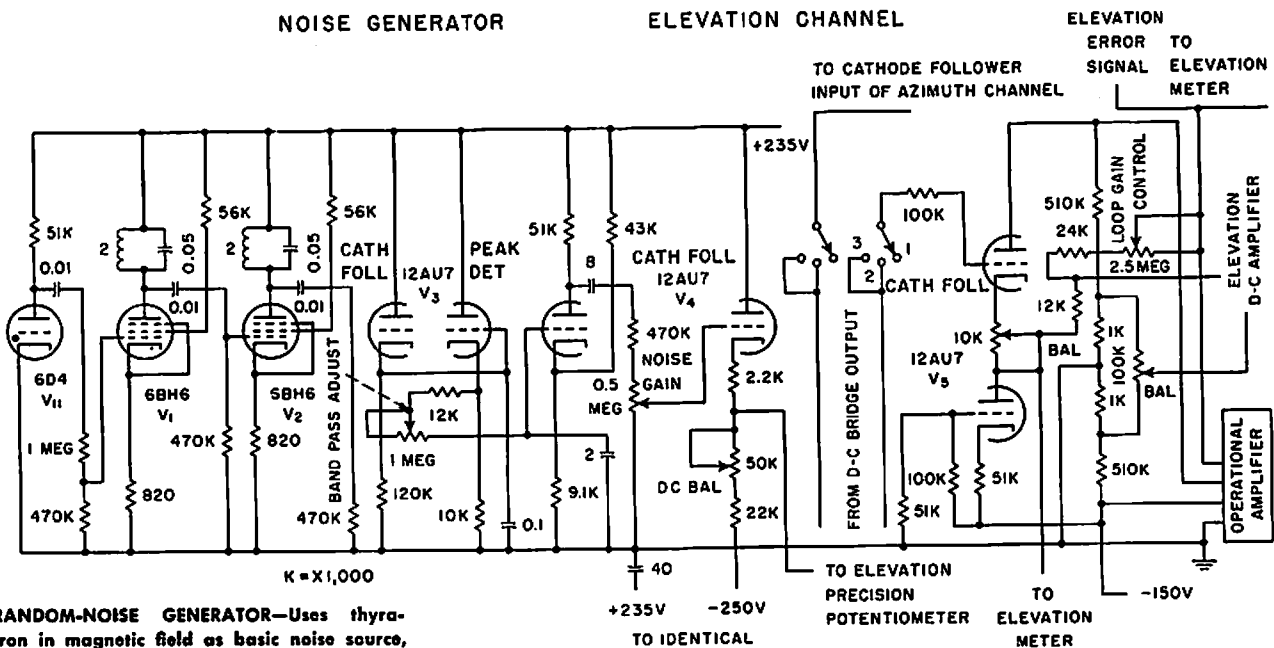
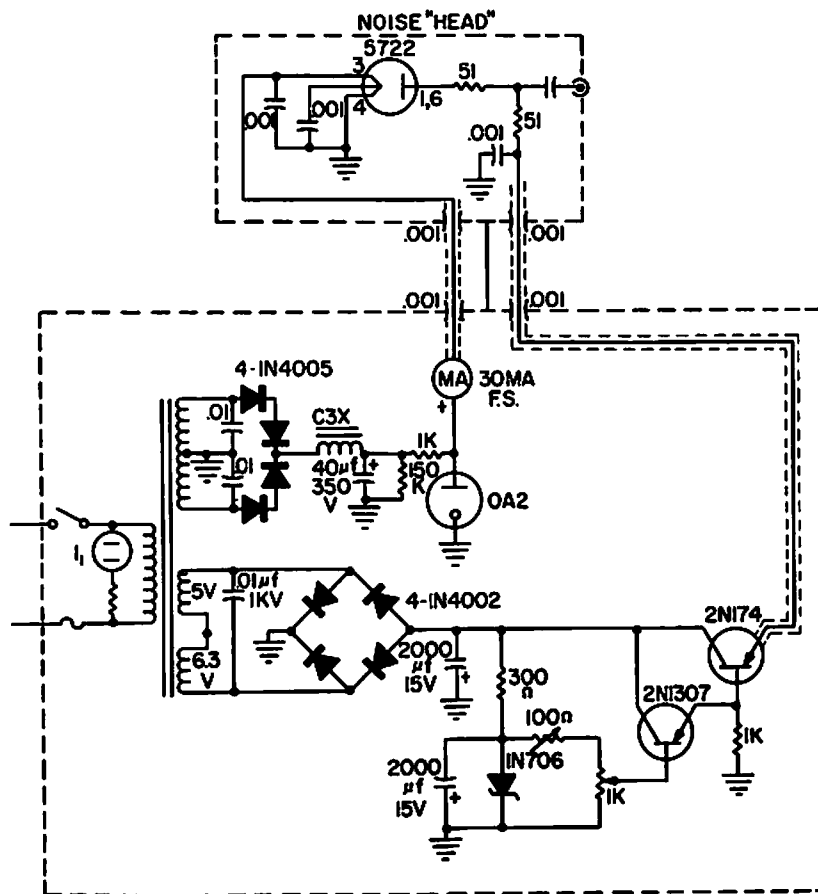
200-MC TRANSISTOR NOISE FIGURE MEASUREMENT—Input is fed by Hewlett-Packard 343A noise source. Output goes to 342A noise figure meter through 3-stage transistor post-amplifier. Input and output of test rig are tunable for best noise figure.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 344.



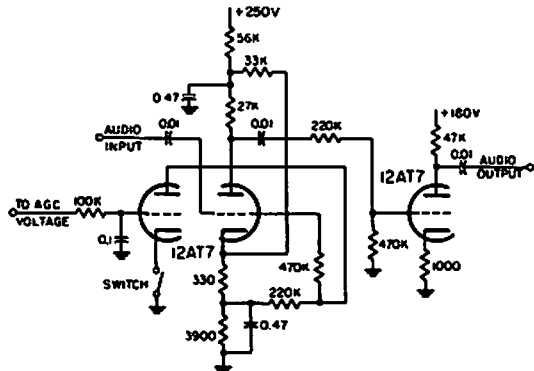
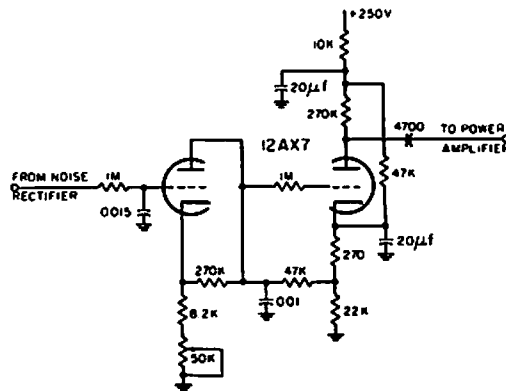
AMPLITUDE PROBABILITY DENSITY FUNCTION—Width of output pulse is proportional to time that input signal is between specified voltage levels. Used in statistical measurements of signals and noise.—B. M. Rosenheck, Detecting Signals By Polarity Coincidence, *Electronics*, 33:5, p 67-69.



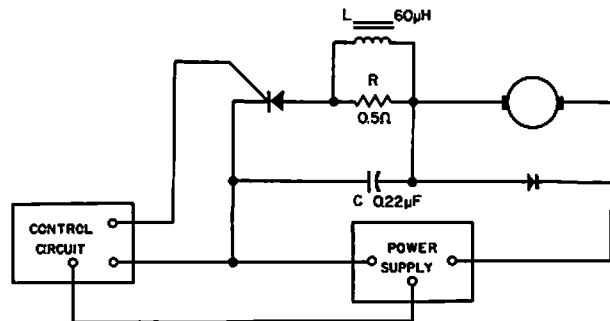
1,000 MC TO 200 MC CONVERTER—Used in measuring transistor noise figure at 1,000 Mc. Oscillator of converter operates at 1,200 Mc. Converter has gain of 10 db and 5 db noise figure.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 349.



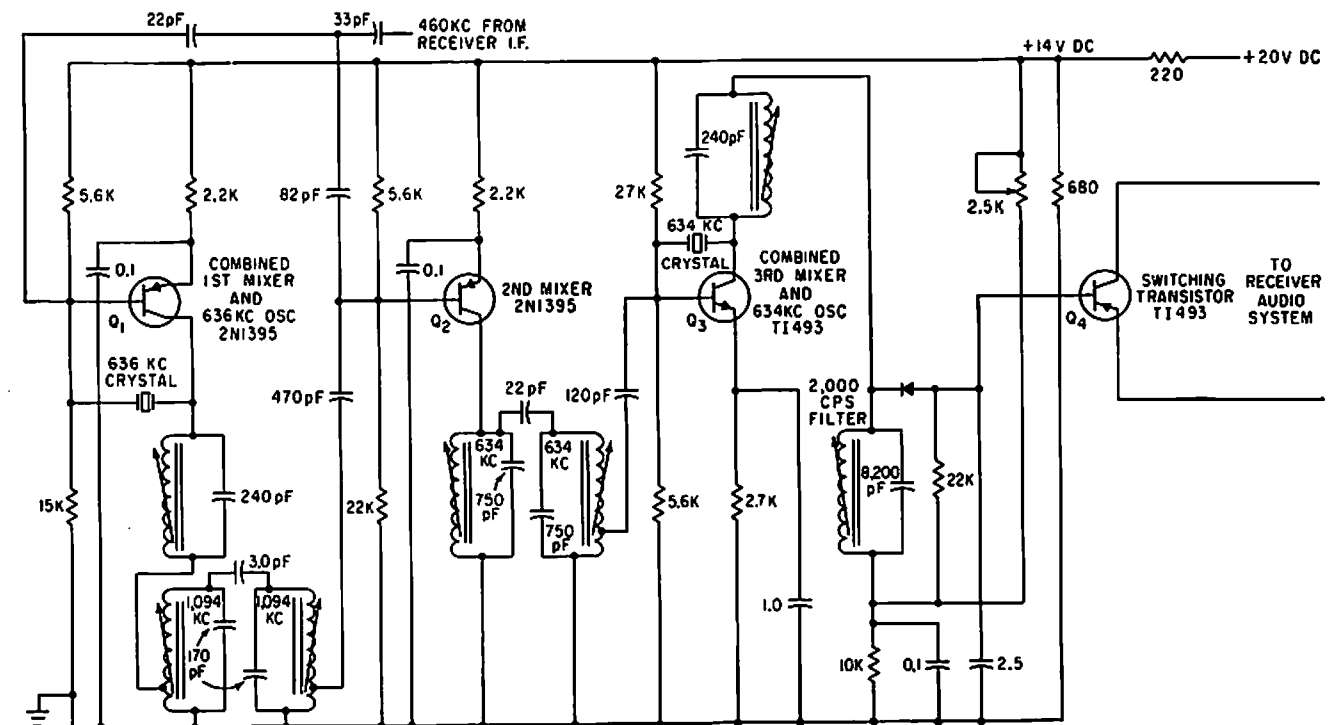
A-F AMPLIFIER WITH SQUELCH—Input is obtained from noise rectifier and amplifier of communication receiver. Squelch is used to make first audio stage inoperative during no-signal condition.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N12-2.



A-F AMPLIFIER WITH SQUELCH—Used to make first audio stage inoperative during no-signal condition in communication receiver.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N12-2.



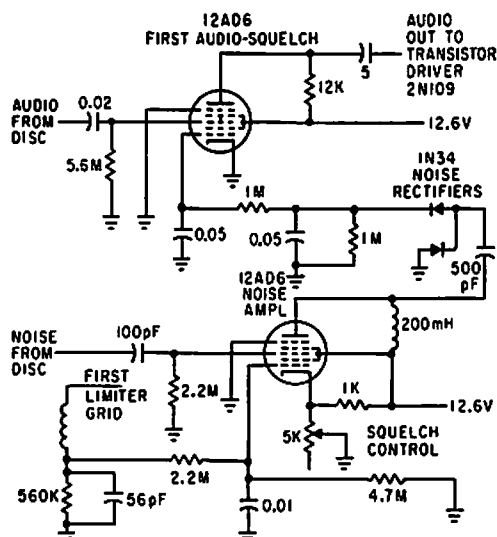
SCR NOISE SUPPRESSOR—Used to suppress circuit noise generated when scr is switched on, without materially affecting power-handling capacity or efficiency. Load shown is d-c series motor.—Noise Reducer for SCR, EEE, 10:10, p 94.



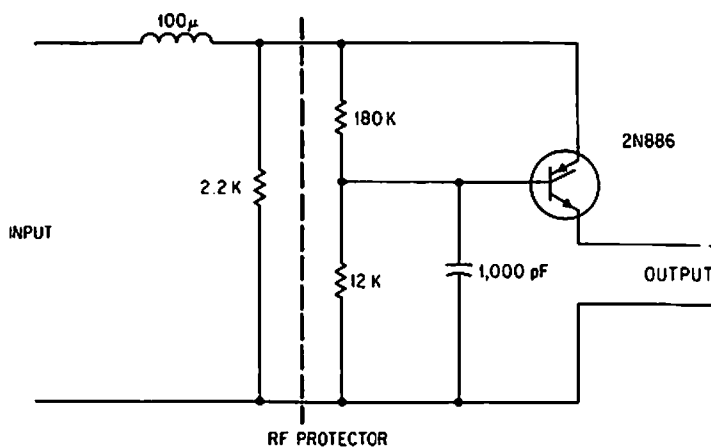
SQUELCH FOR DOUBLE SUPERHET—Carrier-operated squelch level is automatically adjusted to best compromise for incoming

signal. Squelch can be adjusted to open on 0.5-microvolt input signal while remaining closed when subject to full output of noise

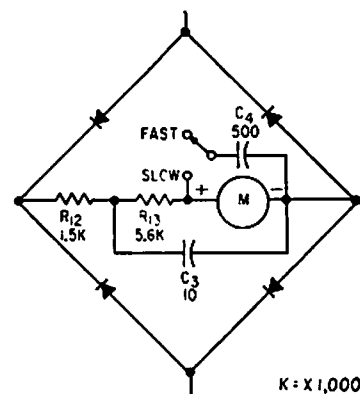
generator.—J. M. Towksbury, Receiver Squelch Control Uses Double Superheterodyne, Electronics, 35:3, p 44-46.



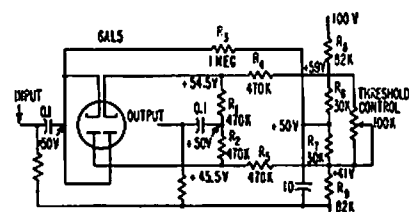
SQUELCH FOR MOBILE—Reduces background noise, to prevent operator fatigue during no-signal periods. Double-action squelch is obtained by using negative voltage at first limiter grid during signal periods to cut off 12AD6 noise amplifier.—C. Gonzalez and R. J. Nelson, *Design of Mobile Receivers with Low-Plate-Potential Tubes*, *Electronics*, 33:34, p 62-65.



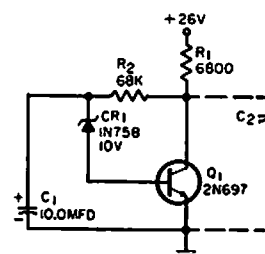
GATE CAPACITOR PROTECTS SCS AGAINST RFI—Capacitor between gate and cathode provides shunt path at higher frequencies to prevent firing of silicon controlled switch by r-f interference. Values in circuit will give protection from 32-v signals between 100 kc and 30 Mc.—R. J. Sanford, *Can RFI Control Prevent Weapons Failures?*, *Electronics*, 36:45, p 43-45.



SOUND METER RECTIFIER—Used with sound level meter to provide indication proportional to rms value of wideband noise, in two-segment linear approximation to required square-law characteristic.—W. V. Richings and B. J. White, *Transistorized Sound Level Meter*, *Electronics*, 33:25, p 64-66.



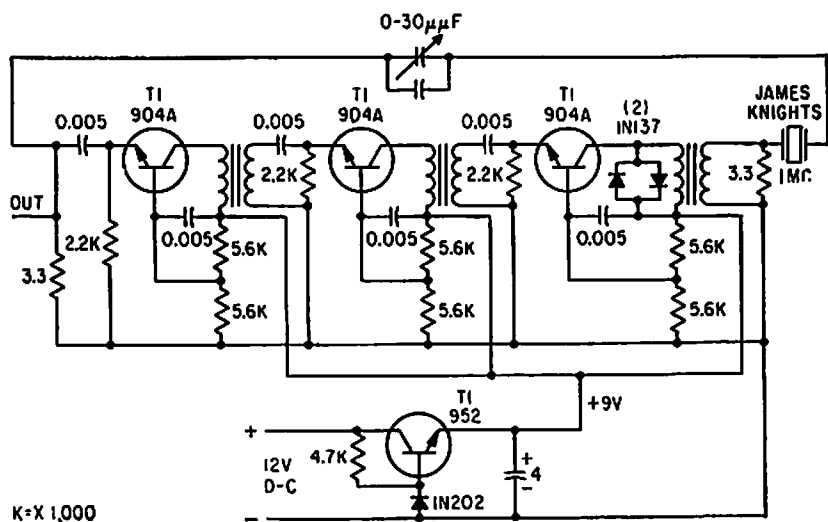
BACKGROUND NOISE SUPPRESSOR—With control set for maximum resistance, only desired a-c voltage peaks above 4.5 v are passed by diodes. Balanced variable-threshold circuit achieves this suppression of weak background noise without affecting a-c balance of signals.—W. E. Earle, *A-C Threshold Converts to Switch*, *Electronics*, 31:1, p 96-99.



ZENER NOISE GENERATOR—Amplifies noise voltage developed across conducting zener diode. Zener current is fed to base of transistor, which has nominal current gain of 75.—G. Richwell, *One-Stage Semiconductor Noise Generator*, *EEE*, 12:7, p 26-28.

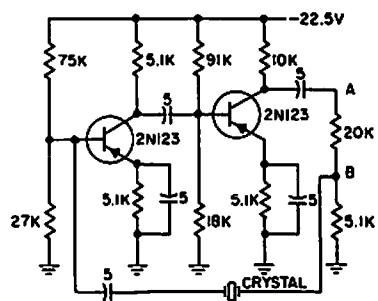
CHAPTER 56

Oscillator Circuits

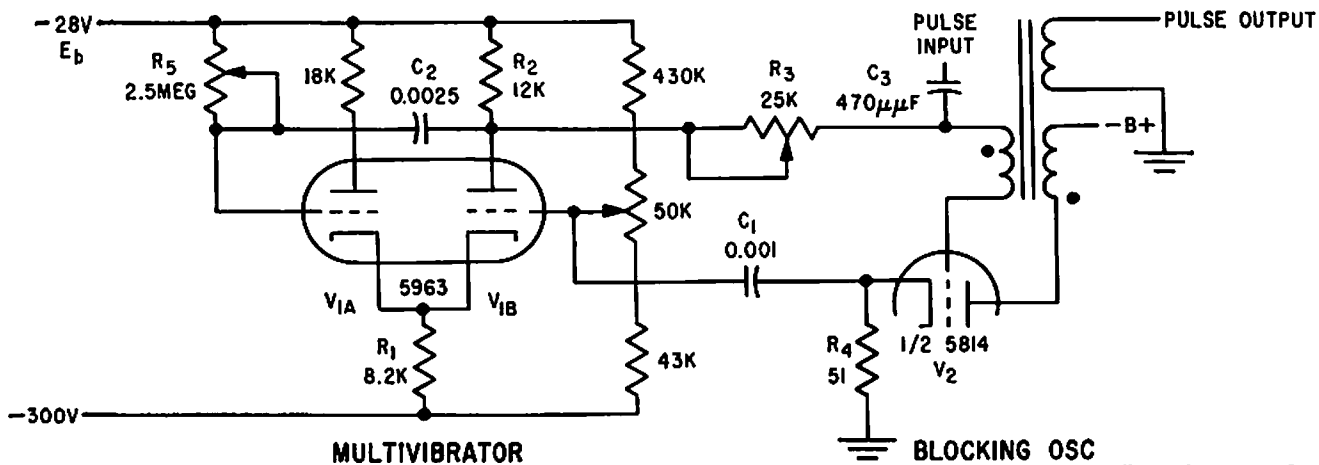


STABLE 1-MC OSCILLATOR—Gives frequency stability of one part in 1 billion per day at normal room temperature, at which a 12-lb, 45-v battery can furnish crystal oven and

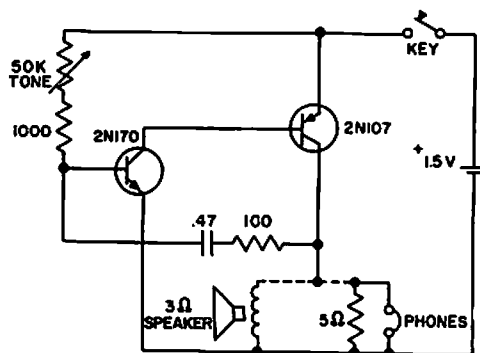
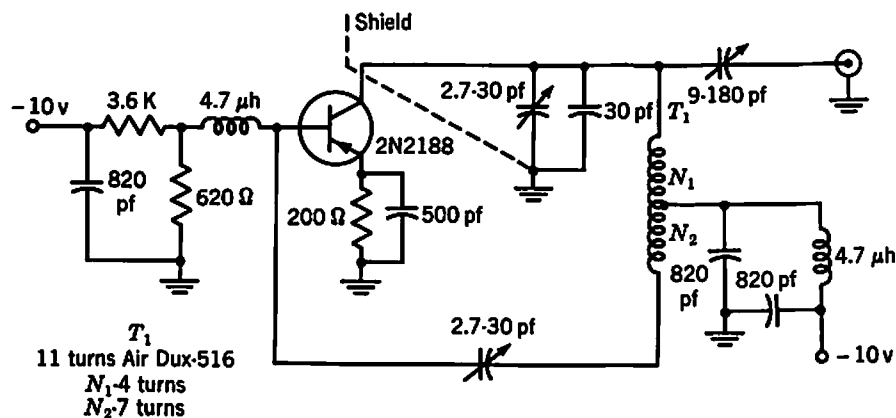
circuit power for 72 hours.—J. F. Mercurio, Jr., Stable, Low-Cost One-Mc Oscillator, Electronics, 32:6, p 50-51.



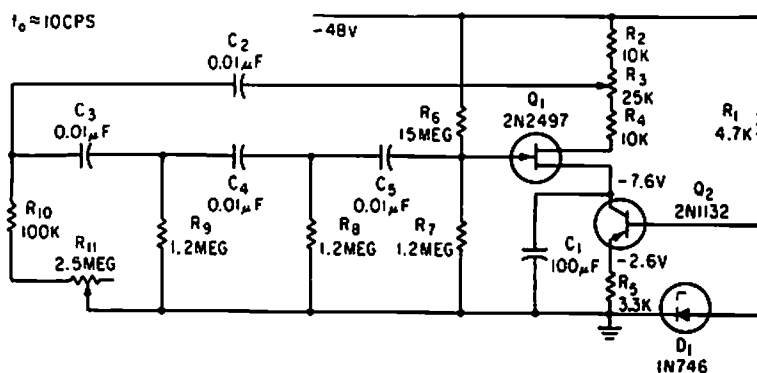
TWO-STAGE VARIABLE-FREQUENCY CRYSTAL OSCILLATOR—Operates at 9.1 kc with long-term frequency stability of a few parts per million. Frequency can be pulled up to 5 cps off resonance by adjusting trimmer capacitor in series with crystal. Used in analog and digital systems to achieve calibration by deviating carrier frequency a small but accurately known amount.—G. A. Gedney and G. M. Davidson, *Crystal Oscillator has Variable Frequency*, *Electronics*, 31:7, p 118-119.



Whatley, Blocking Oscillator for Ten-to-One Synchronization, *Electronics*, 32:48, p 58-59.

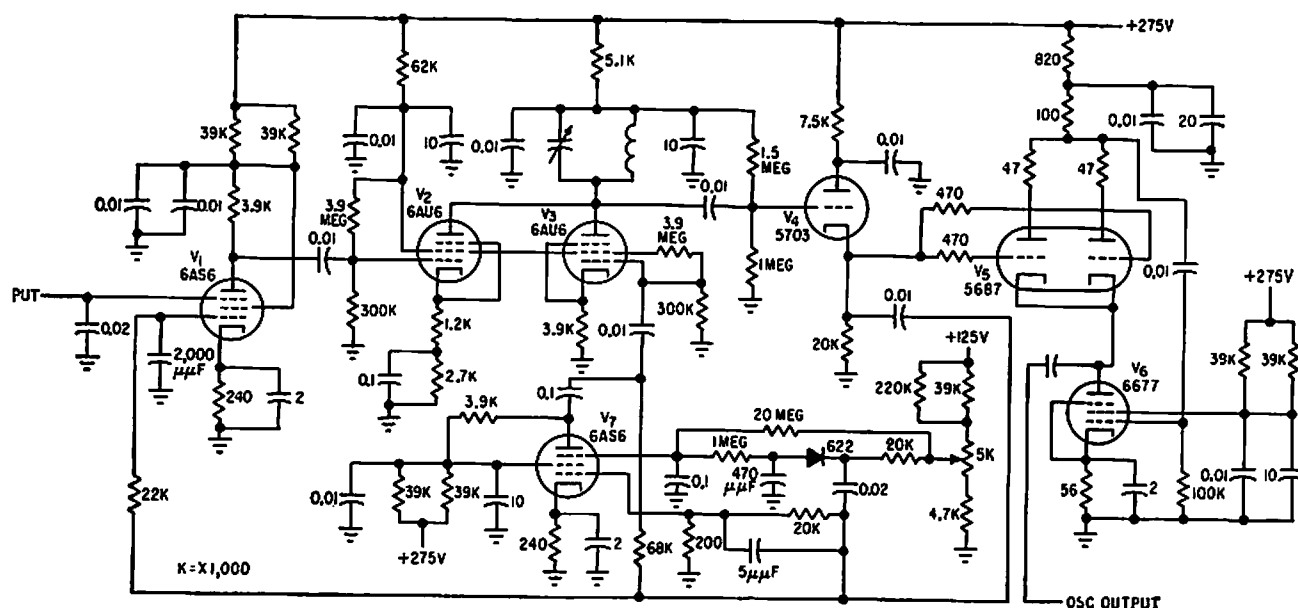


CODE PRACTICE OSCILLATOR—Requires only single flashlight cell and two transistors.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 378.



10-CPS PHASE-SHIFT FET OSCILLATOR—Uses four-mesh feedback network to provide attenuation of 18.36, without use of lamps.—

V. Glover, Using a New Device: Field-Effect Transistor Oscillators, *Electronics*, 35:51, p 44-46.

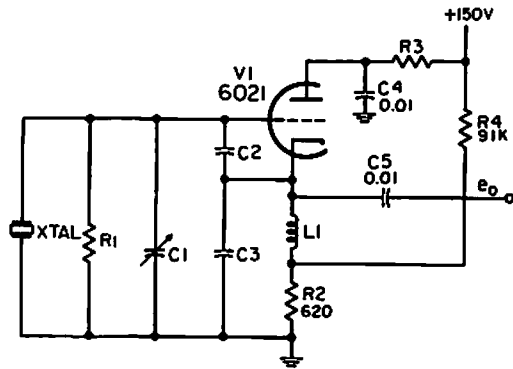


CONSTANT-OUTPUT OSCILLATOR—Used with automatic doppler cycle counter to determine position and velocity of missiles and satel-

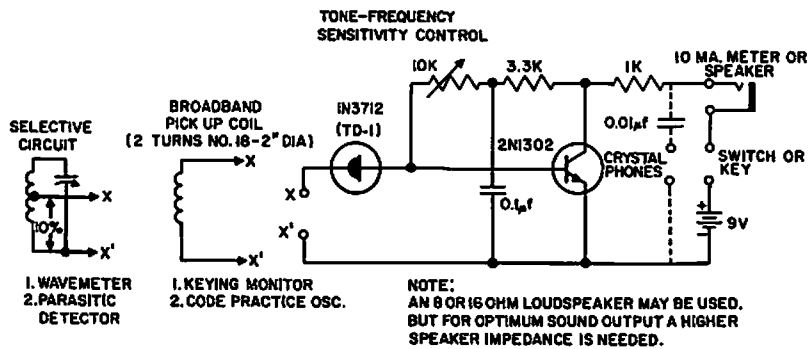
ites. Output signal amplitude is maintained constant over wide range of frequencies.—B. E. Keiser, Digital-Counter Techniques In-

crease Doppler Uses, *Electronics*, 32:21, p 46-50.

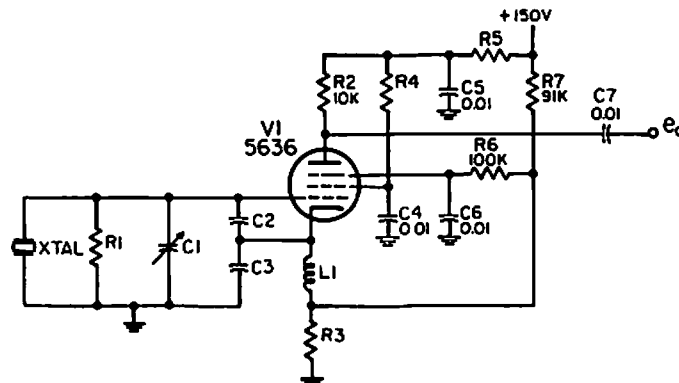
PREFERRED 0.8-20 MC COLPITTS CRYSTAL—Frequency is changed by substituting plug-in crystals. Component values depend on frequency range. Serves as simple and stable frequency source.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 101, p 101-2.



Frequency range mc	R1 Ω	R3 Ω	C1 pf	C2 pf	C3 pf	Distributed "C" pf			L1 mh
						C1'	C2'	C3'	
0.8-5	50K	33K	10.7	15	100	6.3	2	12.5	7.0
3-11	47K	33K	12.4	15	33	6.3	2	12.5	0.8
5-20	33K	33K	8.4	24	24	6.3	2	12.5	0.3



CODE PRACTICE OSCILLATOR—Basic tunnel-diode oscillator with single-transistor amplifier stage can be used as code practice oscillator, sensitive broadband c-w keying monitor, sensitive aural-visual parasitic detector, or as wavemeter.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 362.



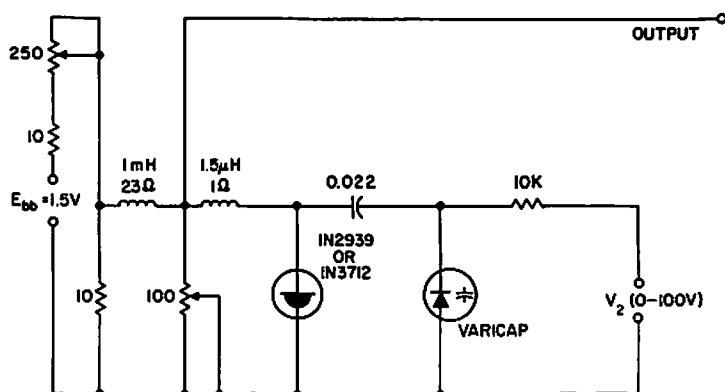
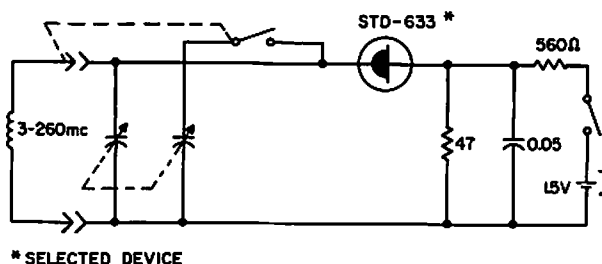
Frequency range mc	R1 Ω	R3 Ω	R4 Ω	R5 Ω	C1 pf	C2 pf	C3 pf	Distributed "C" pf			L1 mh
								C1'	C2'	C3'	
0.8-5	330K	620	47K	12K	10.7	15	150	6.3	2	12.5	7.0
3-11	100K	620	68K	6.8K	8.7	18	100	6.3	2	12.5	0.8
5-20	47K	470	110K	150	8.6	22	47	6.3	2	12.5	0.3

PREFERRED 0.8-20 MC ELECTRON-COUPLED COLPITTS CRYSTAL—Provides higher output, greater harmonic content, better frequency

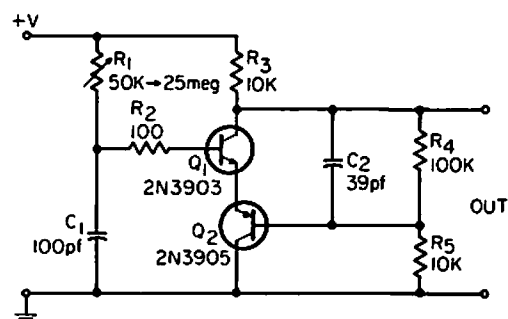
correlation, and more immunity from effects of load changes than simpler Colpitts version.—NBS, "Handbook Preferred Circuits Navy

Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 102, p 102-2.

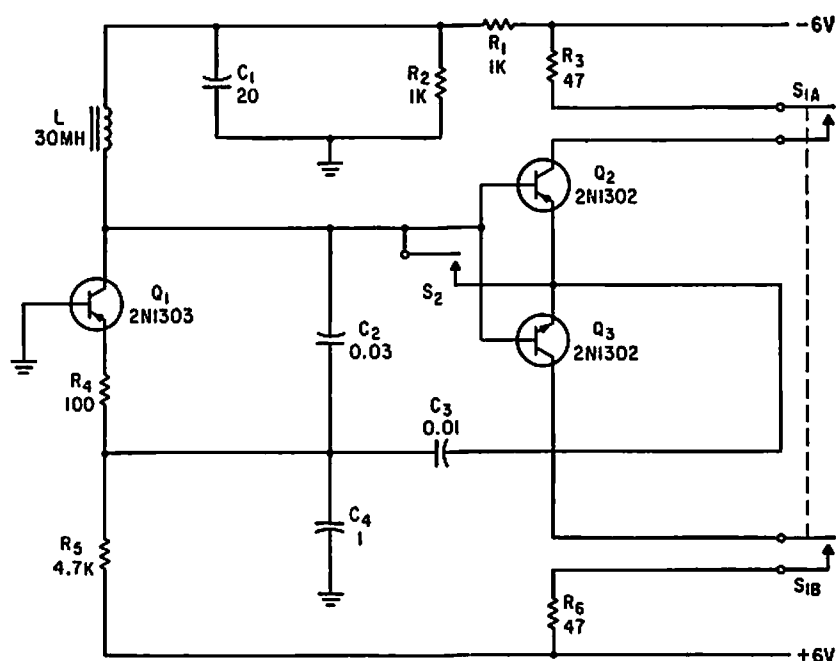
3-260 MC TUNNEL-DIODE OSCILLATOR—Uses plug-in coils to generate sine-wave output over wide frequency range.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 352.



12-22 Mc VOLTAGE-CONTROLLED OSCILLATOR—Voltage-variable capacitor tunes tunnel-diode oscillator electronically.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 350.



RAMP GENERATOR DRIVES TRIGGER—Circuit is relaxation oscillator providing short, fast pulses for triggering mvbr. Upper operating frequency is about 1 Mc for values shown. Efficiency is high yet total component cost is under \$2.—C. F. Andren, High Efficiency Relaxation Oscillator, *EEE*, 14:4, p 43.



K=X 1,000

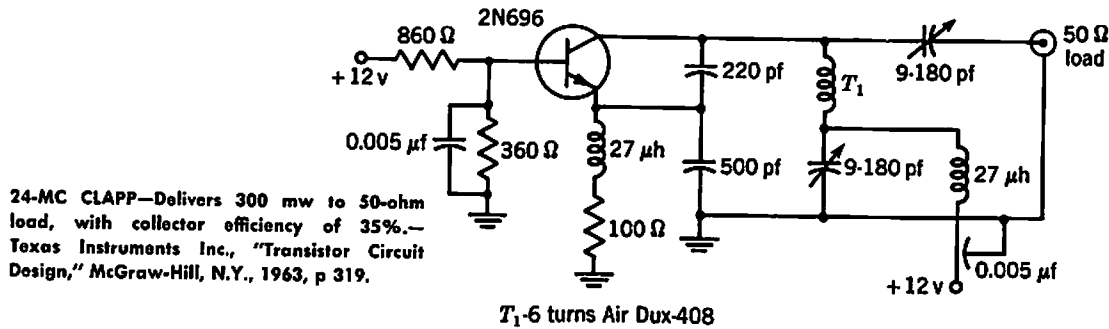
$f_1 = 4,342 \text{ CPS}$ $f_2 = 5,059 \text{ CPS}$

EITHER S_1 OR S_2 MAY BE USED FOR KEYING, AS DESIRED

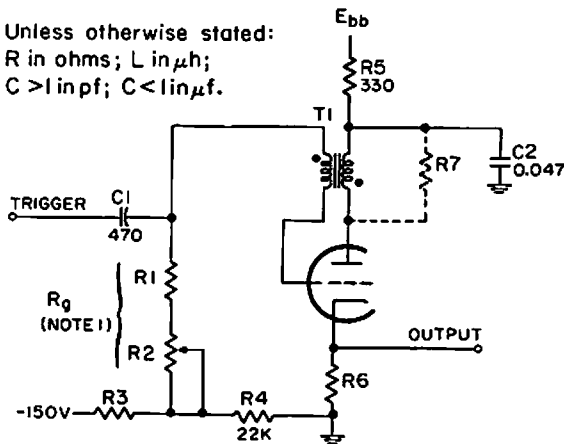
FREQUENCY-SHIFT-KEYED OSCILLATOR—Q1 is Colpitts oscillator at 5 kc and Q2-Q3 is push-pull complementary-emitter amplifier with

unity voltage gain. Either switch shorts amplifier, thereby increasing tuning capacitance enough to shift frequency 1 kc.—N. C.

Hekimian, Getting Rid of Transients in Frequency-Shift Keying, *Electronics*, 35:45, p 58-59.



Unless otherwise stated:
R in ohms; L in μ h;
C > 1 in pf; C < 1 in μ f.



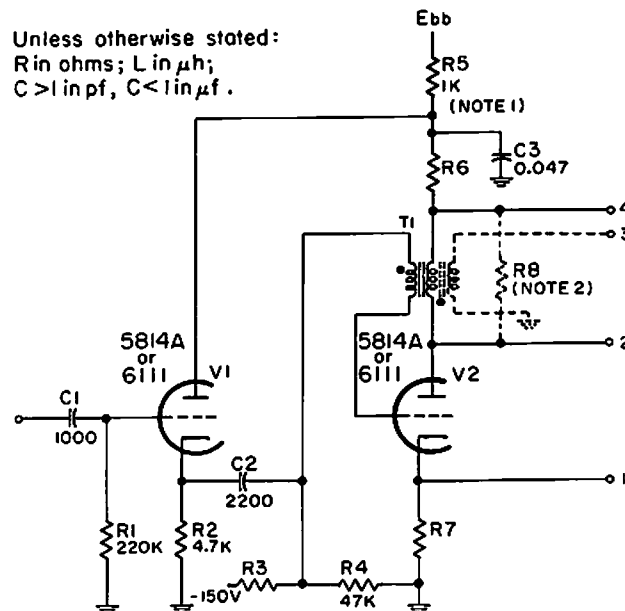
Components:

Input pulse spacing μ sec	Division by	Output pulse spacing μ sec	R_6 ohms
12.2	5	61	250K
61	2	122	250K
61	5	305	1M
122	2	244	1M
122	5	610	2.5M

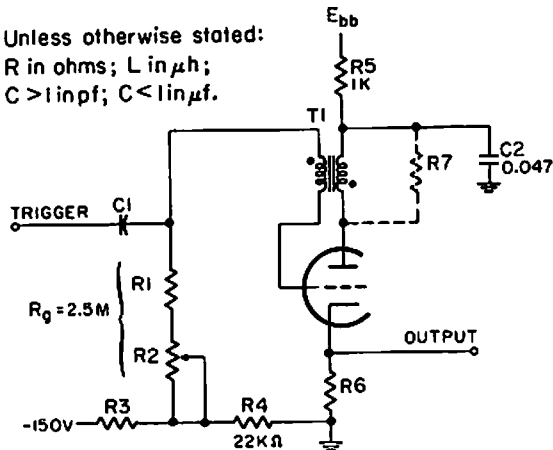
R_2 : 120K Ω (5814A); 270K Ω (6111).

Unless otherwise stated:
R in ohms; L in μ h;
C > 1 in pf, C < 1 in μ f.

PREFERRED SERIES-TRIGGERED BLOCKING OSCILLATOR—Responds to more slowly rising trigger than parallel-triggered version. Cathode follower V1 is included to provide required low driving impedance and minimize reaction of oscillator on trigger source. Designed for repetition rates up to 2,000 pps. Four terminals give choice of positive or negative output from positive input. Plate voltage is 300 v for 5814A and 150 v for 6111.—NBS, "Handbook Preferred Circuits, Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 49, p 49-2.



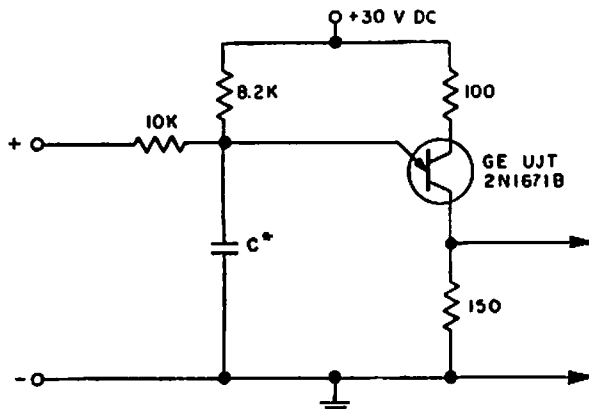
Unless otherwise stated:
R in ohms; L in μ h;
C > 10 pF; C < 10 μ f.



Components:
Output prf (pps) 200-600 600-1200 1200-2000
C1 (picofarads) 2200 1000 470
R3: 120K Ω (5814A); 270K Ω (6111).

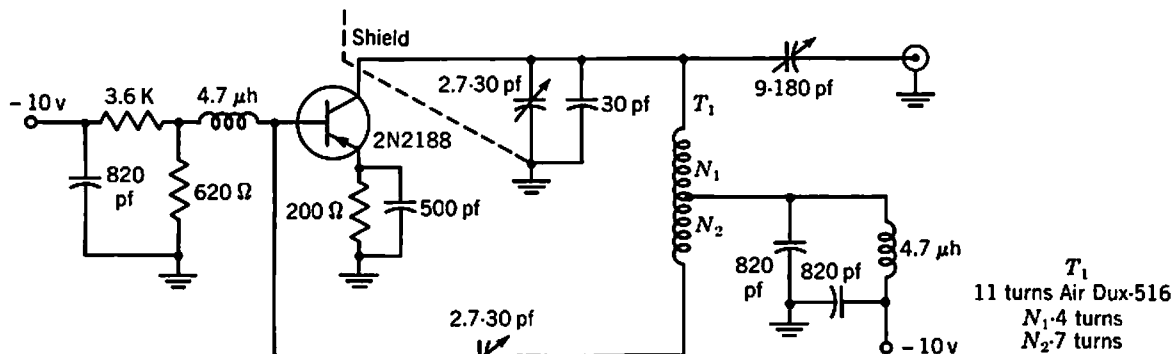
PREFERRED PULSE-FREQUENCY DIVIDER—Blocking oscillator design produces equally spaced pulses at submultiple of 2 to 5 of trigger frequency. Maximum prf is 2,000 pps. Input and output are both positive. Plate voltage is 300 v with 5814A tube and 150 v

with 6111. R6 is 100 or 150 ohms depending on tube. R7 should be maximum that will just suppress ringing.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 50, p 50-2.



VOLTAGE-CONTROLLED VFO—Adding 10K resistor to basic ujt oscillator gives voltage-controlled variable-frequency oscillator. With 0.68 mfd for C, d-c input voltage range of 0 to 30 v gives 670 to 4,550 pps. With 0.2

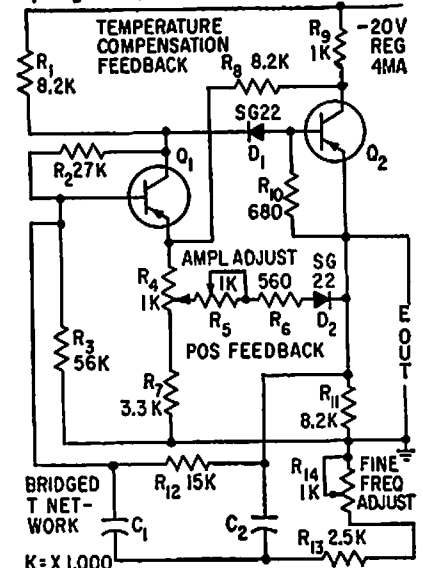
mfd for C, same input range gives 220 to 1,400 pps. Not intended for use where linearity is important.—B. Strunk, Voltage-Controlled Variable-Frequency Oscillator, EEE, 10:12, p 28-30.



30-MC WIDE TEMPERATURE RANGE—Operates over range of -40 to $+60^{\circ}\text{C}$. Typical power output is 23 mw at lowest tempera-

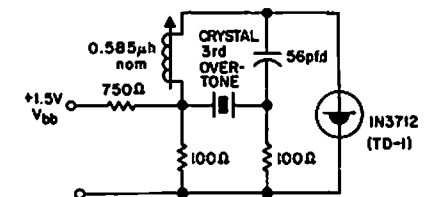
ture and 20 mw at highest. Collector efficiency is 30%.—Texas Instruments Inc.,

OSC FREQ	<10KC	TO 100KC	AT 350 KC
Q_1 & Q_2 USED	2N270	2N247	2N384



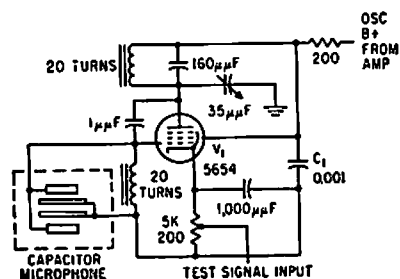
FREQ IN CPS	$C_1 = C_2$ VALUES	FREQ IN KC	$C_1 = C_2$ VALUES
4	6.8 μ F	1	0.025 μ F
100	0.25 μ F	10	2300 μ F
400	0.068 μ F	100	200 μ F

MULTIPLE-FEEDBACK R-C OSCILLATOR—Gives excellent amplitude stability and low distortion. Uses vibration and shockproof version of Sulzer bridged-T configuration to provide single-frequency operation in 4-cps to 350-kc range.—L. H. Dulberger, Improved R-C Oscillator, Electronics, 32:10, p 62.

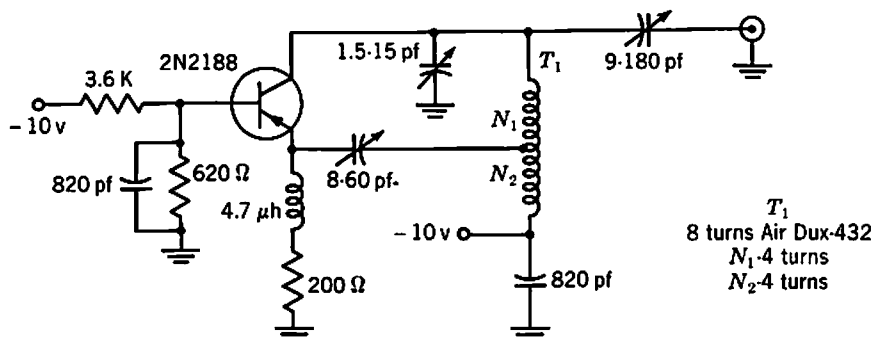


27.255-MC TUNNEL DIODE-CRYSTAL OSCILLATOR—Operates within tolerance of quartz crystal from -55 to $+85^{\circ}\text{C}$ and bias range of 110 to 150 mv for Citizens Band service.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 353.

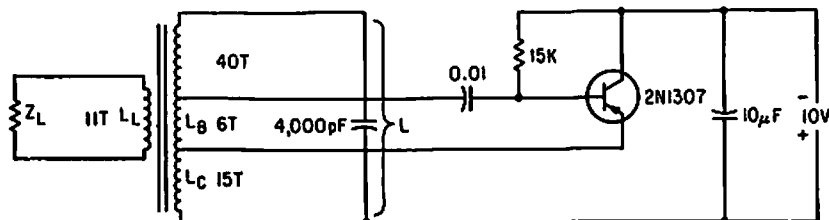
"Solid-State Communications," McGraw-Hill, N.Y., 1966, p 239.



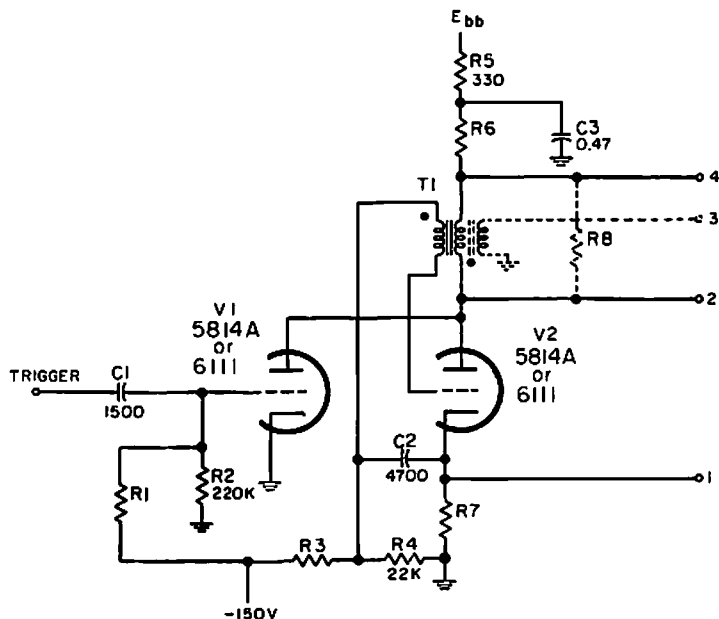
OSCILLATOR-DETECTOR—Capacitor microphones form part of grid tank circuit of 6-Mc tuned-plate tuned-grid r-f oscillator that also detects 6.5-cps modulation by class-C operation during oscillation. Used in infrared analyzer for detecting leaks in automobile air-suspension systems.—P. G. Balko, *Infrared Finds Auto Suspension Leaks*, *Electronics*, 31:49, p 82-85.



60-MC COMMON-BASE—Delivers 10 mw to 50-ohm load at 25°C. Collector efficiency is 10%.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 239.



122-KC GROUNDED-COLLECTOR HARTLEY—Simple class-C L-C arrangement has many advantages for power oscillators and for d-c to a-c converters. One side of tank can be grounded.—P. Laakmann, *Designing Class-C Transistor L-C Oscillators*, *Electronics*, 35:30, p 42-45.

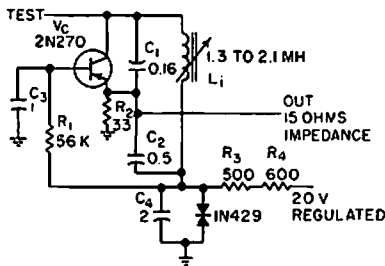


Components:
R1: 1M (5814A); 2.2M (6111).
R3: 100K (5814A); 220K (6111).

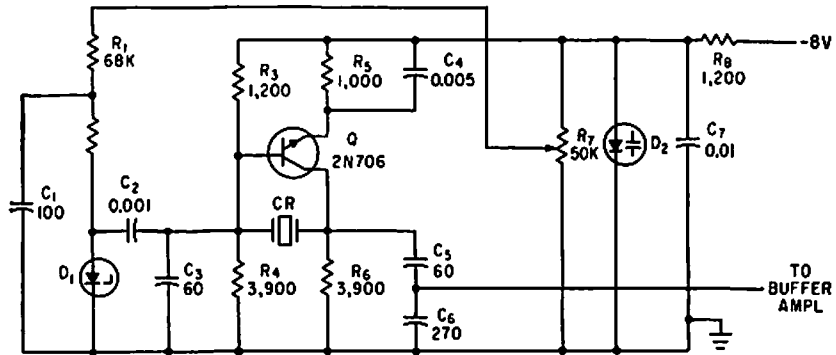
PREFERRED PARALLEL-TRIGGERED BLOCKING OSCILLATOR (BELOW 2,000 PPS)—Produces synchronizing impulses between 0.2 and 7 microsec wide at rates of 200 to 2,000 pps. One triode section is used as trigger am-

plifier to prevent triode blocking oscillator from reacting on trigger source. R6 and R7 are 100 ohms for 5814A and 150 ohms for 6111. R8 should be maximum that will just suppress ringing. Requires positive input

trigger and gives choice of output polarities at the four output terminals.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, *Electron Tube Circuits*, 1963, PC 46, p 46-2.

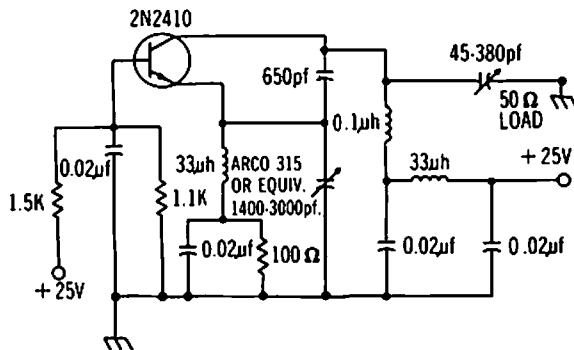


STABLE 10-KC COLPITTS—Provides constant-amplitude carrier for data reduction system, at 0.5 v rms with amplitude stability of 0.1% and frequency drift below 0.25% for temperature range of 30 to 50°C.—L. H. Dulberger, Transistor Oscillator Supplies Stable Signal, *Electronics*, 31:5, p 43.

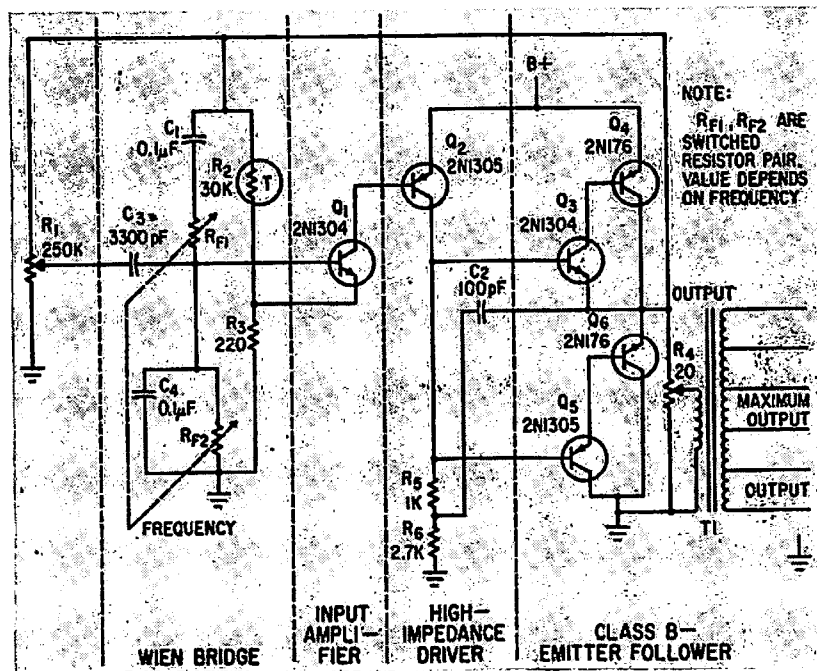


10-MC CRYSTAL—Collector voltage of transistor is kept low and is stabilized by zener diode D1 in microminiature oscillator using crystal in 10-Mc fundamental mode. Voltage-sensitive capacitor D2 and R7 serve for fine frequency adjustments.—M. Lysobey, Microminiature Crystal Oscillator Using Wafer Modules, *Electronics*, 35:15, p 60-61.

age-sensitive capacitor D2 and R7 serve for fine frequency adjustments.—M. Lysobey, Microminiature Crystal Oscillator Using Wafer Modules, *Electronics*, 35:15, p 60-61.



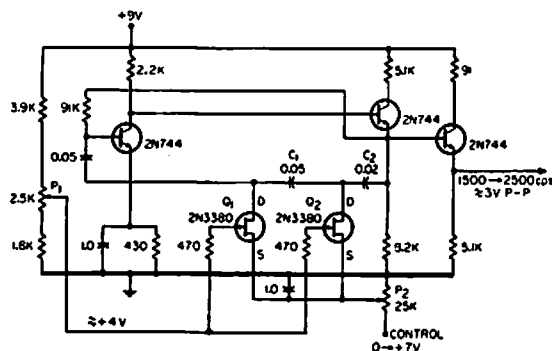
20-MC POWER OSCILLATOR—Colpitts-type common-base circuit gives power output of 500 mw to 50-ohm load, while dissipating 750 mw.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 300.



FEEDBACK LOOP STABILIZES A-F OSCILLATOR AMPLIFIER—Wien bridge determines frequency of oscillator, which is combined with amplifier stages to give single oscillator stage

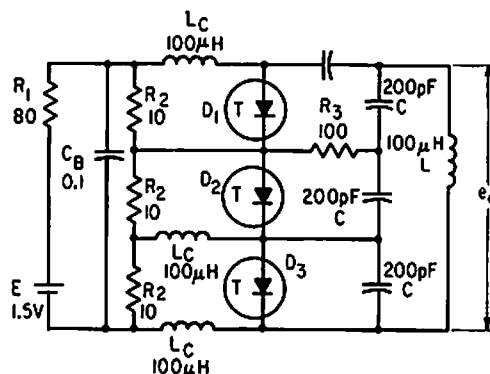
having sufficient output power to drive load directly. Thermistor R2 and resistor R3 provide negative feedback path around amplifier and oscillator, to make oscillator gain and

frequency independent of load variations.—R. G. Fulks, Novel Feedback Loop Stabilizes Audio Oscillator, *Electronics*, 36:5, p 42-43.

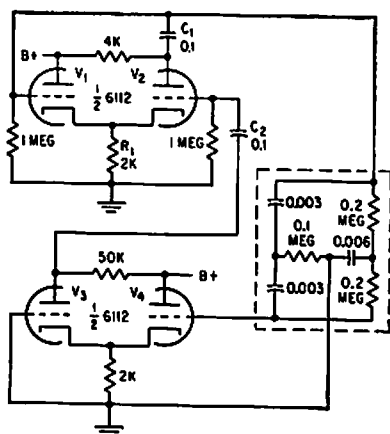


FET VOLTAGE-CONTROLLED OSCILLATOR—Produces excellent sine-wave output with good linearity over frequency range of 1,500 to 2,500 cps, for control voltage of 0 to 7 v

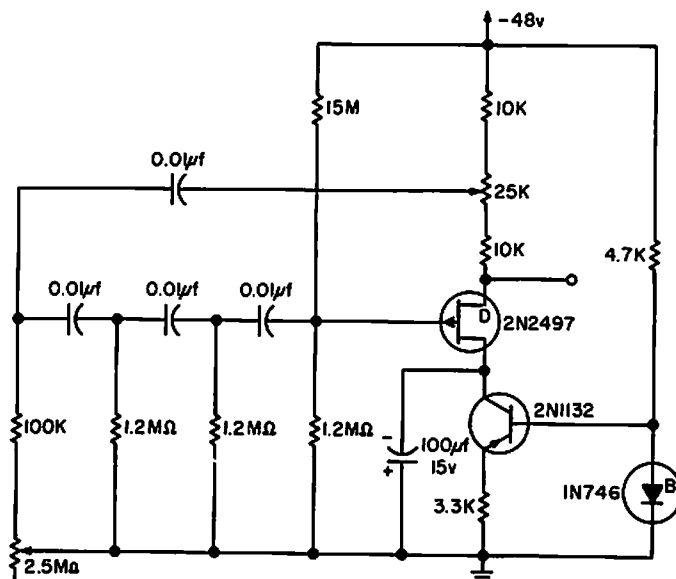
d-c. Circuit is resistance-controlled three-section phase-shift oscillator.—R. Solleck, *Voltage-Controlled Oscillators*, EEE, 13:3, p 47.



CASCADE TUNNEL DIODE—Voltage drops across resistors R2 serve as individual voltage sources in series for cascaded diodes that give three times sine-wave output voltage of single relaxation oscillator circuit.—Wen-Hsiung Ko, *Designing Tunnel Diode Oscillators*, Electronics, 34:6, p 68-72.

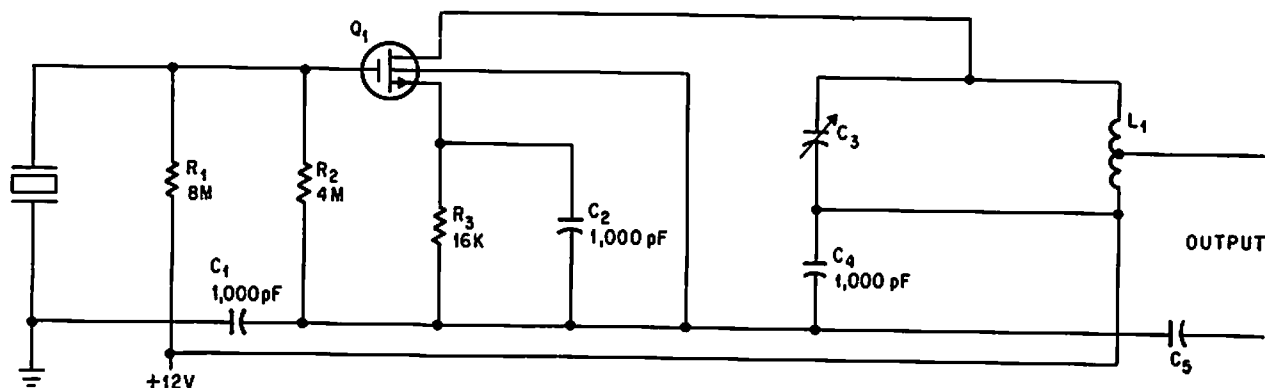


FEEDBACK OSCILLATOR—C1 provides positive feedback between amplifier V2 and cathode follower V1, causing oscillation at frequency and amplitude at which loop gain is unity. Twin-T network in negative feedback loop maintains pure sine wave, free of harmonics. Variable-gain negative-feedback amplifier V3-V4 stabilizes frequency and amplitude at prescribed values.—Oscillator Patent is Granted, Electronics, 31:37, p 108.



FET PHASE-SHIFT OSCILLATOR—Frequency of four-mesh phase-shift oscillator can be varied several cycles around 10 cps, using 2.5-meg pot. Attenuation of four-mesh feed-

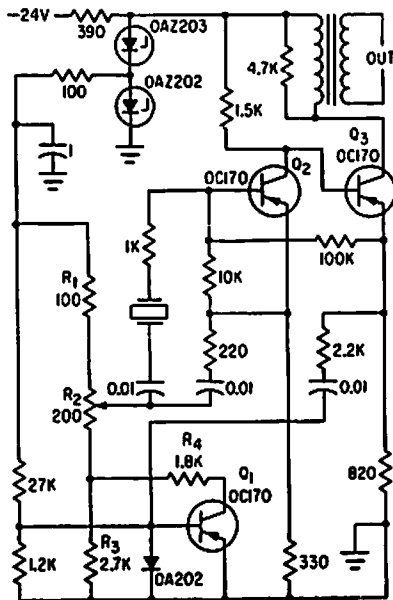
back network is 18.36.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 111.



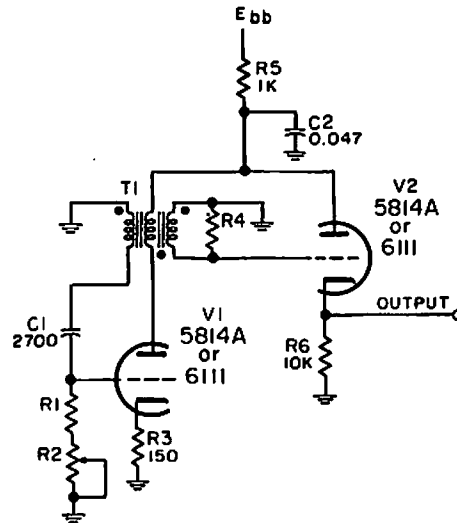
CRYSTAL MOS FET—Oscillation is maintained even with 100-microvolt oscillator output sig-

nal when using mos fet.—G. G. Luettgenau and S. H. Barnes, *Designing With Low-Noise*

MOS FETs: A Little Different But No Harder, Electronics, 37:31, p 53-58.

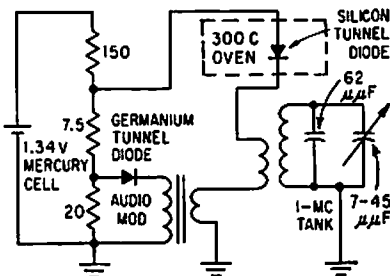


8 to 170 KC WITH PLUG-IN CRYSTALS—Oscillator frequency can be changed by replacing crystal and one capacitor in feedback loop. Stability is obtained from squaring transistor Q1 which feeds crystal.—R. Couvela, Oscillator Frequency is Changed by Plug-In Units, *Electronics*, 34:36, p 86-87.

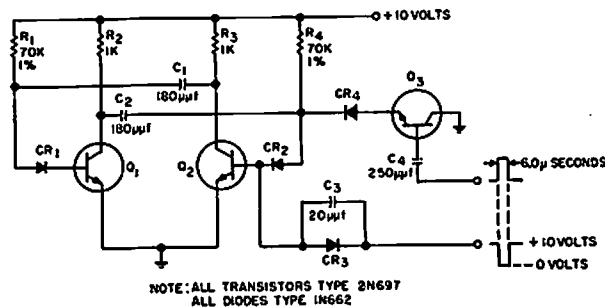


PREFERRED ASTABLE BLOCKING OSCILLATOR—Used as pulse generator when frequency stability is not important. Output can be used as trigger without further shaping. R4 should be maximum that will just suppress ringing. Design equations are given for R1

and R2, but final values must be determined experimentally. Range is 200 to 2,000 pps. Output is positive.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 48, p 48-2.

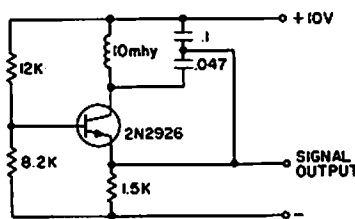


AUDIO-MODULATED 1-MC TUNED TO OSCILLATOR—Uses silicon tunnel diode that, with no surface protection, may be dipped in liquid nitrogen, placed in furnace, or immersed in acid, with only minor change in oscillator and modulation frequencies.—I. A. Lesk, N. Holonyak, Jr., and U. S. Davidsohn, The Tunnel Diode—Circuits and Applications, *Electronics*, 32:48, p 60-64.

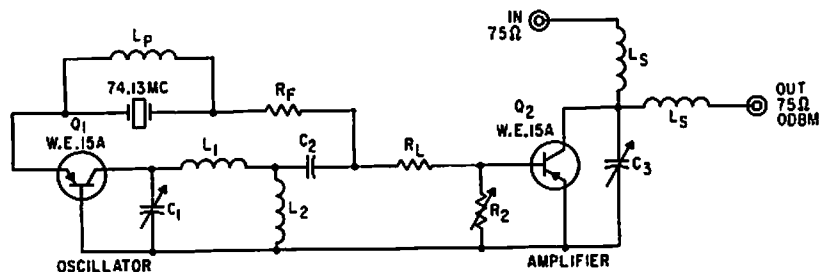


SYNCHRONIZED OSCILLATOR—Astable mvbr Q1-Q2 operating at 68.4 kc is synchronized by 400-cps signal having 6-microsec pulse width. Frequency stability can be one part in 4,000 if film resistors and other temperature-stable components are used. Synchron-

izing signal is variable, of the order of 1/170 of oscillator frequency but with no integral relationship between the signals.—G. Silverman, A Synchronized Oscillator Circuit, *EEE*, 10:7, p 29-30.

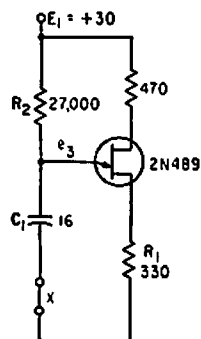


10-KC SINGLE-TRANSISTOR COLPITTS—Total temperature drift rate is only 0.035%/°C, determined by coil core material. For higher frequency stability, frequency-determining network should be buffered from amplifier.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 210.

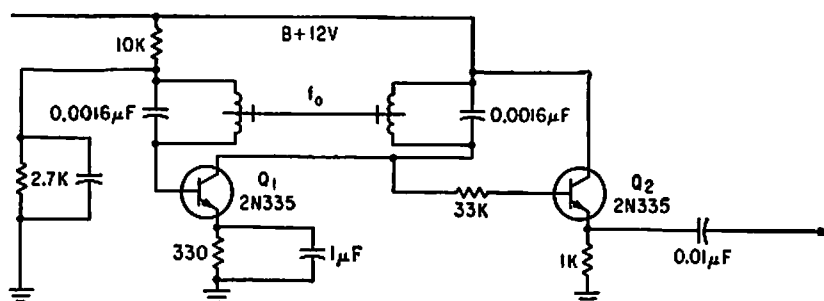


74.13-MC CRYSTAL REPLACES LOST MICRO-WAVE CARRIER—To prevent noise interference during signal losses due to fading, carrier resupplies oscillator, and amplifier replaces lost carrier, within 0.1 millise.

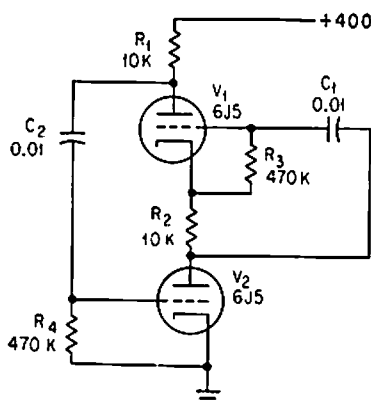
Q1 and Q2 are switched from cutoff when carrier is needed. Resistor RF in series with crystal lowers its Q to insure rapid starting.—Microwave Relay Designing with Traveling-Wave Tubes, *Electronics*, 35:3, p 40-43.



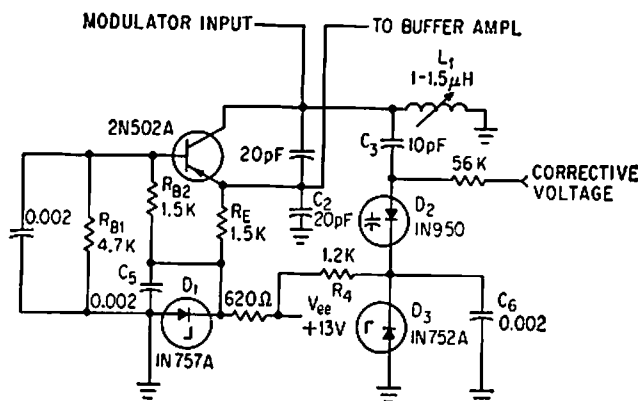
UNIUNCTION-TRANSISTOR OSCILLATOR—If circuit is broken at X, discharge current of C1 can be used to shut off transistor stage.—A. G. Lloyd, *Overload Protection for Transistor Voltage Regulators*, *Electronics*, 33:52, p 56-59.



100-KC MAGNETOSTRICTIVE-ROD CONTROL—Oscillator Q1 can be adjusted to within 0.1 cps of desired frequency by adjusting length and center thickness of rod made from modified Elinvar constant-modulus material positioned between coils. Emitter-follower Q2 minimizes pulling by variable load.—T. A. O. Gross, *New Magnetic Rods Simplify Circuits*, *Electronics*, 35:28, p 62-66.

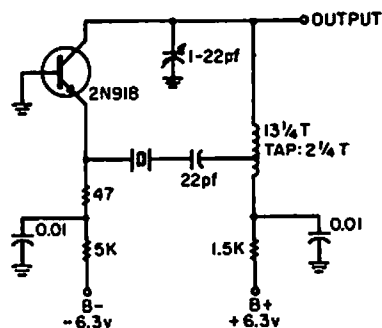


FREE-RUNNING CASCODE OSCILLATOR—Omission of voltage-divider capacitors from cascode multivibrator gives sine-wave oscillator if loop gain is equal to unity.—C. Sing, *Advantages of Free-Running Cascode Multivibrators*, *Electronics*, 37:5, p 28-29.

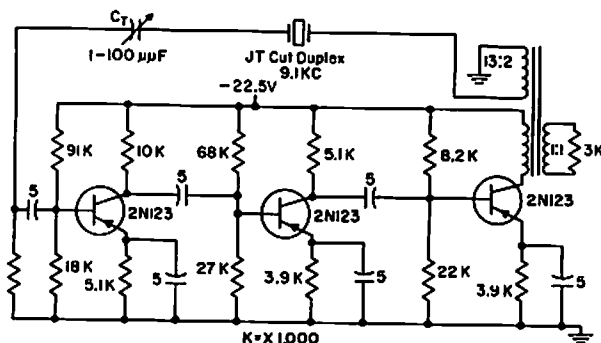


VOLTAGE-CONTROLLED 23-MC OSCILLATOR AND MODULATOR—Input signal voltage to transistor changes capacitance of tank circuit, to make oscillator frequency vary with input signal voltage. Variable-capacitance

diode requires fewer parts than transistor modulator. Zener diodes provide constant bias for variable-capacitance diode D2.—F. L. Carroll, *How to Achieve Stability in Space Telemetry*, *Electronics*, 37:4, p 32-35.

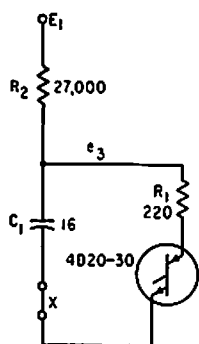


MEASURING OSCILLATOR STABILITY—Circuit is used as 90.3125-Mc reference oscillator in system for measuring short-term stability of 45-Mc stalo (stable local oscillator) of airborne radar under high vibration. Tape transformer in collector circuit of transistor controls crystal drive.—J. Coolican, *How to Measure STALO Short-Term Stability Under Vibration*, *EEE*, 13:5, p 96-98.

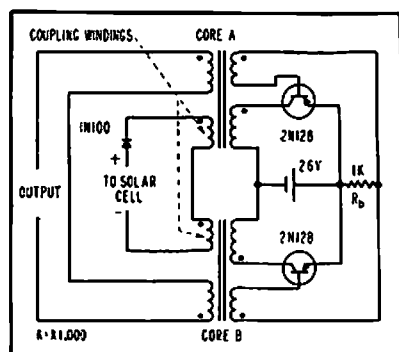


THREE-STAGE VARIABLE-FREQUENCY CRYSTAL OSCILLATOR—Provides loop transmission of 1, under maximum frequency pulloff of 5 cps from 9.1-kc crystal frequency, and has net phase shift around loop of 360° with crystal in circuit. Third stage provides extra circuit gain needed for larger power

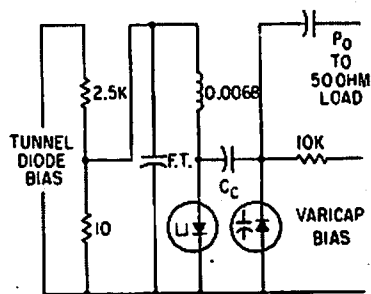
output or larger frequency deviations off resonance. Transformer provides phase reversal and reflects desired a-c load, to limit output swing of transistor.—G. A. Gedney and G. M. Davidson, *Crystal Oscillator has Variable Frequency*, *Electronics*, 31:7, p 118-119.



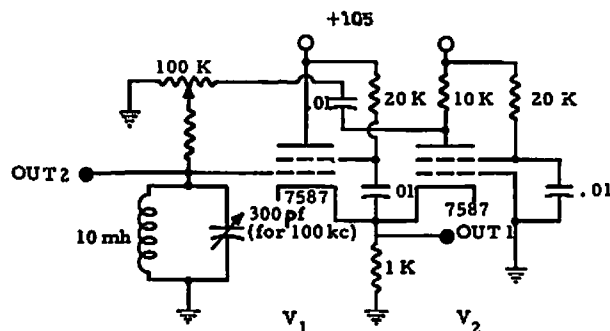
FOUR-LAYER DIODE OSCILLATOR—If circuit is broken at X, discharge current of C1 can be used to shut off transistor stage.—A. G. Lloyd, Overload Protection for Transistor Voltage Regulators, *Electronics*, 33:52, p 56-59.



TONE-BURST OSCILLATOR—Consists of variable-frequency magnetically coupled mvbr, with two magnetic cores driven by battery-powered transistors. Injection of current or voltage from solar cell or other transducer affects mvbr reset, to give frequency change over range of 5 to 15 kc.—R. W. Rochelle, Cyclops Cores Simplify Earth-Satellite Circuits, *Electronics*, 31:9, 56-63.

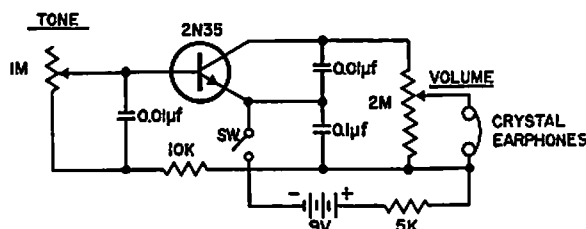


200-400 MC VARICAP-TUNED OSCILLATOR
—Tuning range is achieved by adjusting Vari-
cap bias voltage from 0.4 to 60 v.—E. Gott-
lieb and J. Giorgis, Tunnel Diodes—Using
Them as Sinusoidal Generators, *Electronics*,
36:24, p 36-42.



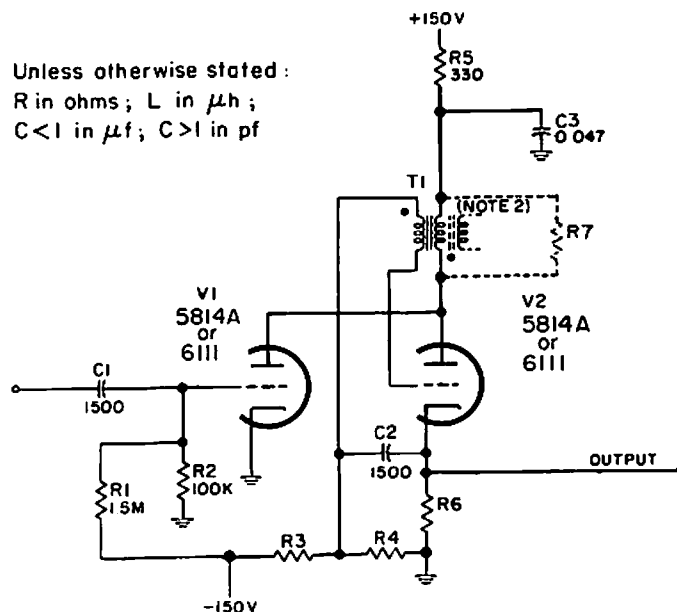
STABLE OSCILLATOR—Excellent frequency and amplitude stability is accomplished by eliminating all grid current in tank circuit and by isolating tank from driving tube by means of resistive degeneration. If very pure sine wave is required, grid of V1 should be cou-

pled to high-impedance load that is equivalent constant resistance, because either reactive or variable loads will impair stability.—J. C. Davis, *Stable Oscillator Circuit*, *EEE*, 11:2, p 26.



CHATTER JAMMER—Can be used to create pleasing tone at level that drowns out ambient noises, to permit concentration on prob-

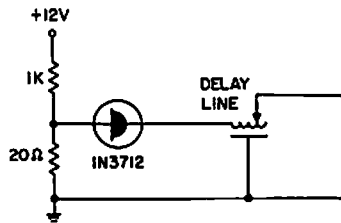
lem while others are talking in vicinity.—J. Leeb, A Chatter Jammer Circuit, *EEE*, 10:11, p 31.



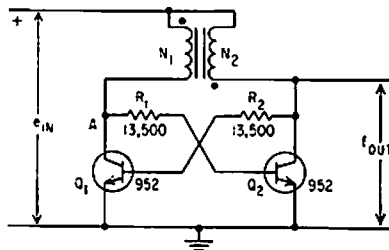
Components:	Pulse spacing (μ sec)	12 to 60	60 to 500
R3		47K Ω	150K Ω
R4		4.7K Ω	15K Ω

PREFERRED 2,000-83,000 PPS BLOCKING OSCILLATOR—Parallel-triggered circuit responds to trigger pulses separated by only few microsec, as required for distance-mark generators and pulse coding circuits. Input is positive, with minimum of 15 v, and output

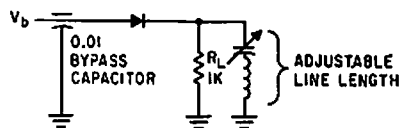
is positive. R6 is 220 ohms. R7 is maximum that will just suppress ringing.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 47, p 47-2.



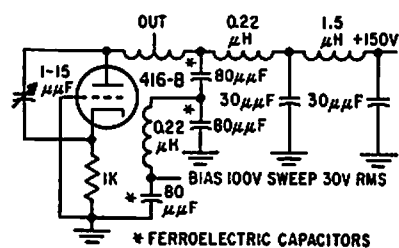
DELAY-LINE OSCILLATOR—Tunnel-diode oscillator with General Radio 314-586 delay line produces square-wave output in range of 0.5 to 20 Mc.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 352.



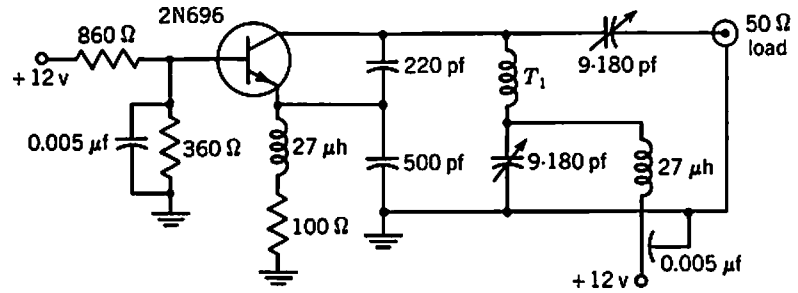
SATURABLE-REACTOR OSCILLATOR—Time-base integration of a variable is performed by counting cycles of saturable-reactor oscillator whose frequency is proportional to the variable. Linearity is within 1% of full scale.—L. W. Langley, *Saturable-Core Oscillator Integrates Gas-Flow Data*, *Electronics*, 32:4, p 42-43.



NR DIODE AS R-F OSCILLATOR—Simple negative-resistance diode circuit can develop several milliwatts at frequencies up to 300 Mc.—A. P. Schmid, Jr., *Negative-Resistance Diode Handles High Power*, *Electronics*, 34:34, p 44-46.

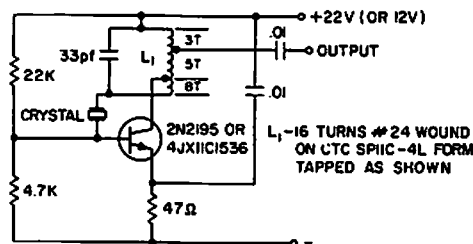


ELECTRIC TUNING FOR 600 TO 1,200 MC—Lumped-constant technique is used with voltage-tunable ferroelectric capacitors to provide 10 mw into 50 ohms.—T. W. Butler, Jr., *Ferroelectrics Tune Electronic Circuits*, *Electronics*, 32:3, p 52-55.



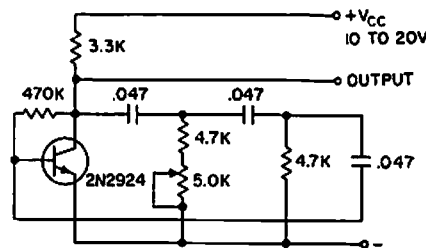
24-MC CLAPP—Delivers 300 mw into 50-ohm load. Typical collector efficiency is 35%.

—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 239.

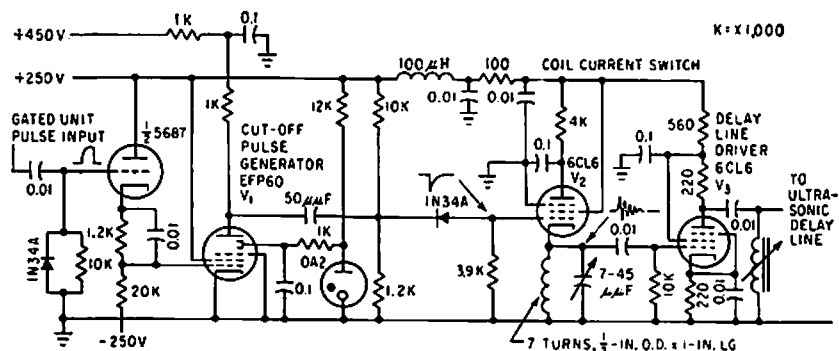


CB CRYSTAL OSCILLATOR—Uses low-cost crystal having high series resistance, up to 30 ohms. Provides adequate output to supply most master oscillator-power amplifier applications. Output tap is arranged to match

directly a companion 2N2195 grounded-base amplifier. Crystal is 3rd overtone type.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 211.



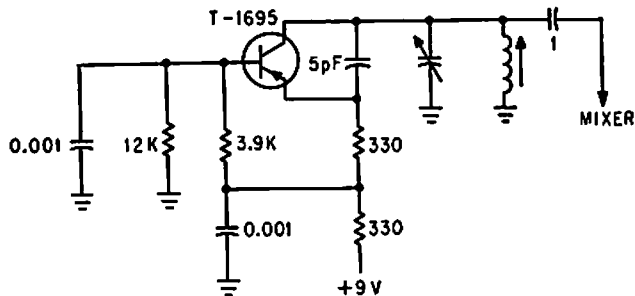
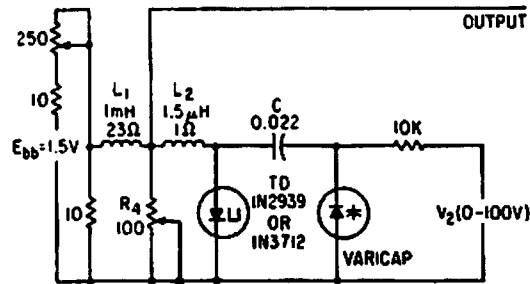
SIMPLE TRANSISTOR OSCILLATOR—Current gain is stabilized against transistor variation. Can be used over collector voltage range of 2 to 24 v. Oscillation occurs at frequency at which there is 360° total phase shift, 180° of which is furnished by grounded-emitter amplifier and 180° by high-pass network. 5K pot adjusts frequency from about 200 to 400 cps.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 206.



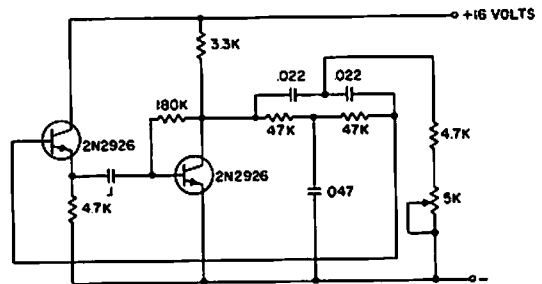
DAMPED 40-MC GENERATOR—Converts unit pulses, resulting from video screening in nuclear track scanner, to damped 40-Mc oscillations each 0.1 microsec long, which are in-

serted in quartz ultrasonic delay line that feeds counter.—P. V. C. Hough, J. A. Koenig, and W. Williams, *Scanner Recognizes Atomic Particle Tracks*, *Electronics*, 32:13, p 58-61.

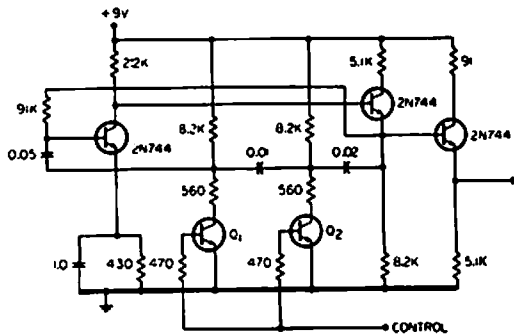
VARICAP TUNES TUNNEL-DIODE OSCILLATOR—Series oscillator circuit tunes electrically over range of 12 to 22 Mc.—E. Gottlieb and J. Giorgis, Tunnel Diodes—Using Them as Sinusoidal Generators, *Electronics*, 36:24, p 36-42.



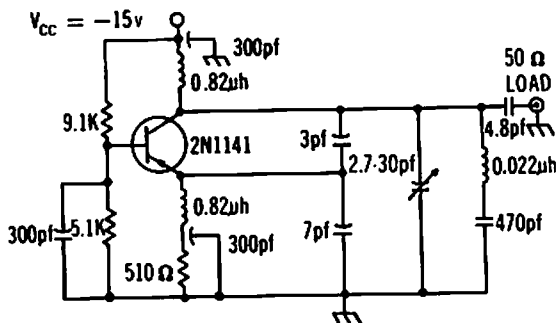
STABLE 40-MC OSCILLATOR—Frequency shifts less than 500 kc when supply voltage is changed from 5 to 12 v.—T. P. Prouty, Using Varactors to Extend Frequency-Control Range, *Electronics*, 36:45, p 48-49.



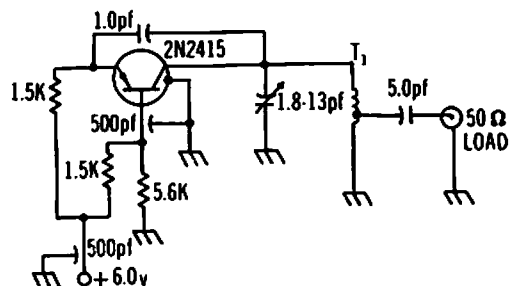
BRIDGED-T R-C PHASE-SHIFT OSCILLATOR—Emitter-follower eliminates loading variations, contributing to exceptional frequency stability (0.2%) over temperature range of -55 to 80°C.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 207.



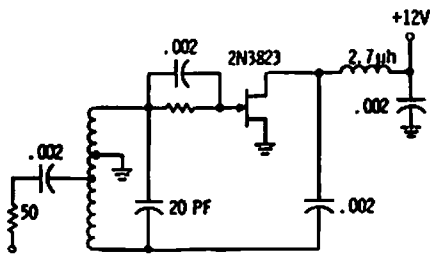
NONLINEAR VOLTAGE-CONTROLLED OSCILLATOR—Use of conventional transistors rather than fet's means that Q1 and Q2 operate in knee region, where frequency does not vary linearly with d-c control voltage. This is generally not a drawback when control voltage is servoed. Increasing the control voltage increases the frequency.—R. Selleck, Voltage-Controlled Oscillators, *EEE*, 13:3, p 47.



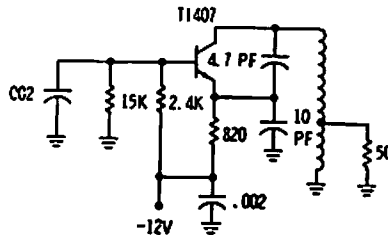
TEMPERATURE-STABLE 200-MC—Varies less than 2 Mc in frequency and less than 1.5 mw in power output over temperature range of 25 to 80°C. Normal power output is 22.5 mw.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 301.



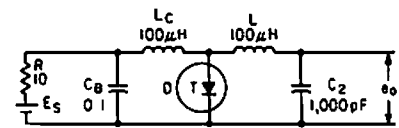
500-MC COLPITTS—Frequency varies less than 3 Mc from 25 to 75°C, and less than 1.5 Mc with bias change from 6 to 9 v. Output is 10 mw. T1 is 1.5" length of 3/8" brass rod with output tap 1/4" from bottom.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 301.



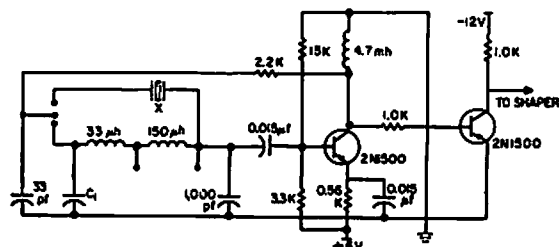
FET HARTLEY—Delivers 680 mv to 50-ohm load at 100 Mc. Coil is four 3/8-inch-diameter turns of No. 16 wire spaced to 0.5 inch.—Fets Come Alive: Clinic Unveils Practical Circuits, *EEE*, 14:4, p 16-18.



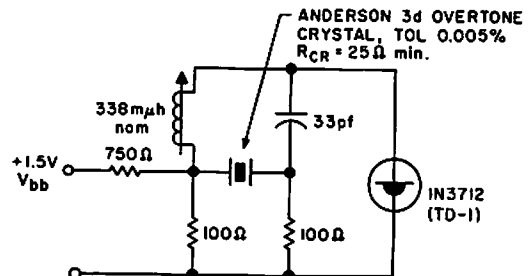
100-MC COLPITTS—Uses conventional bipolar transistor, which has low noise in operation from low-impedance voltage generator. Temperature drift is much greater than with fet.—Fets Come Alive: Clinic Unveils Practical Circuits, *EEE*, 14:4, p 16-18.



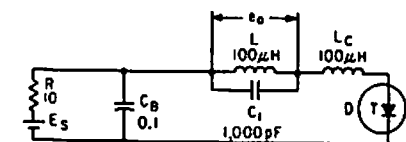
SINE-WAVE TUNNEL DIODE—Low-impedance capacitor in parallel with series-resonant circuit of tunnel-diode relaxation oscillator passes all frequencies except that for series resonance, giving sinusoidal voltage across output capacitor. Output frequency varies from 0.7 to 0.8 Mc over bias range of 100 to 400 mv.—Wen-Hsiung Ko, *Designing Tunnel Diode Oscillators*, *Electronics*, 34:6, p 68-72.



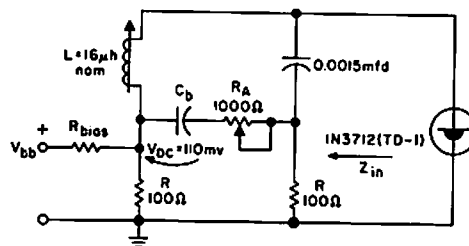
CRYSTAL-OR-CAPACITOR OSCILLATOR—Gives high stability from 800 kc to 3 Mc, from 0 to 65°C with either crystal or capacitor. Optimum operating frequency can be found and utilized by changing capacitor value C1 in range up to 500 pf, while awaiting delivery of CT-cut crystal at desired frequency.—T. Asai, *Crystal-or-Capacitor Oscillator*, *EEE*, 12:3, p 72.



47.1-MC TUNNEL DIODE-CRYSTAL OSCILLATOR—Used in Fire Department service. Operates within tolerance of quartz crystal from -55 to +85°C and bias range of 110 to 150 mv for Citizens Band service.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 353.



SINE-WAVE TUNNEL DIODE—Series filter selects desired frequency and rejects harmonics from pulse-shaped output of basic tunnel-diode relaxation oscillator. Values shown give 0.45 Mc, constant within 0.05 Mc over bias range of 100 to 400 mv.—Wen-Hsiung Ko, *Designing Tunnel Diode Oscillators*, *Electronics*, 34:6, p 68-72.



VARIABLE-AMPLITUDE TUNNEL-DIODE OSCILLATOR—Attenuating resistor RA varies magnitude of oscillator swing, so oscillator operates over limited highly linear portion of

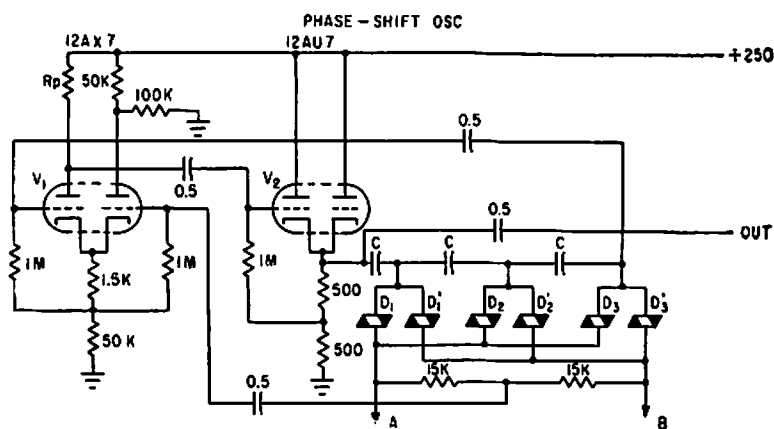
diode conductance curve.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 351.

$$C_b = \text{DC BLOCKING CAPACITOR}$$

$$R_A = \text{ATTENUATING RESISTOR}$$

$$Z_{in} \approx R \left(1 + \frac{R}{R_A} \right)$$

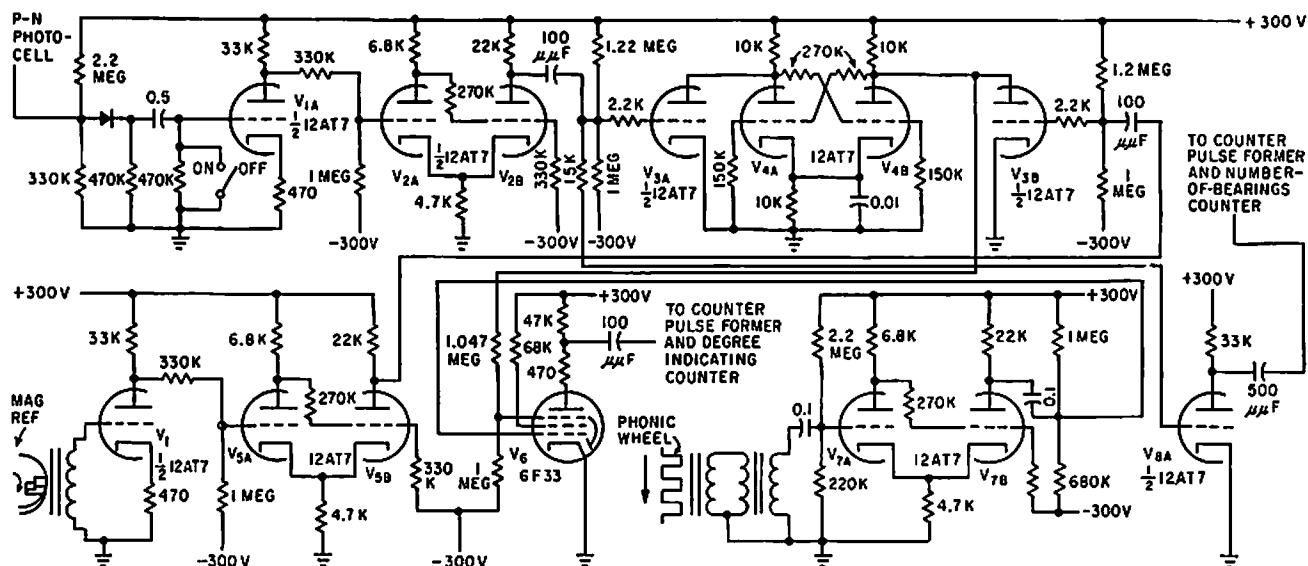
Phase Control Circuits



6-VARISTOR PHASE-SHIFT VTO—Range is ten times lowest frequency, with upper limit of several kc, depending on values of C. Triode

differential amplifier V1 is first stage of oscillator. One input grid, for a-c amplification, goes to output of phase-shift circuit. Other

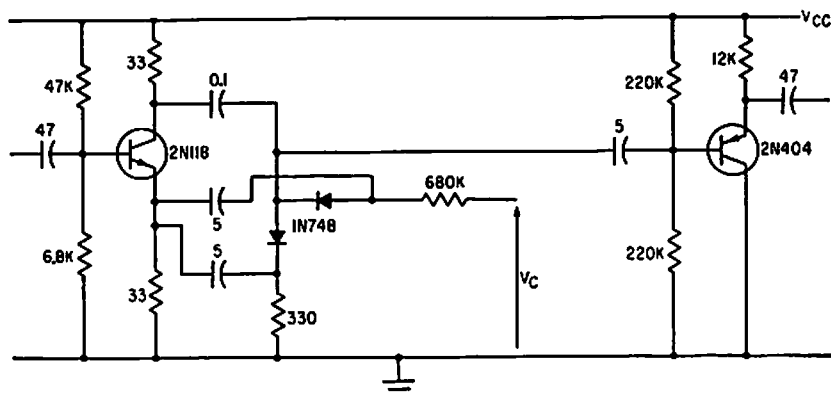
grid goes to centertap across SiC varistors. —M. Uno, Varistor Network Controls Voltage-Tuned Oscillator. *Electronics*, 34:30, p 44-47.



ADF PHASEMETER—Input signals are squared by Schmitt triggers, differentiated, and changed to unidirectional pulses that drive flip-flop V4 to produce pulse whose length is proportional to bearing of transmitter.

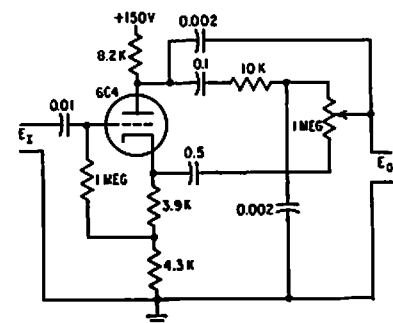
500-cps signal from phonic wheel is sharpened by Schmitt trigger V7 and used to indicate length of bearing pulse in degrees by modulating the pulse in V6. One output goes to decade counter chain that counts

total number of degrees.—J. F. Hatch and D. W. G. Byatt, Direction Finder with Automatic Readout, *Electronics*, 32:16, p 62-64.

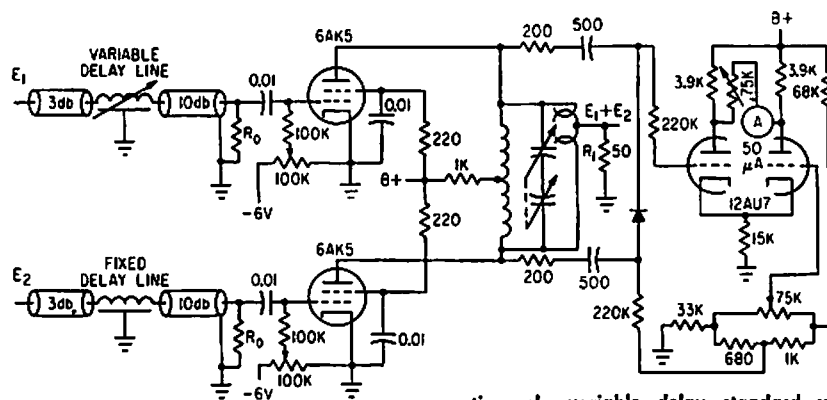


PHASE SHIFTER WITH LINEAR BIASING—Frequency range of more than two decades can be obtained from voltage-controlled oscillator when linear biasing is used for phase-shift

circuit.—R. A. Greiner and S. K. Morgan, Voltage Controlled Wide-Range Oscillator, *Electronics*, 34:51, p 31–35.

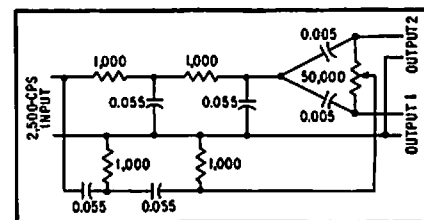


TUBE REPLACES 180° TRANSFORMER—Elimination of transformer resonance effects at higher frequencies permits phase shifter to handle wider band of frequencies, from 500 to 2,000 cps for component values shown.—W. G. Shepard, Phase Shifter Range Exceeds 180°, *Electronics*, 31:19, p 96–100.

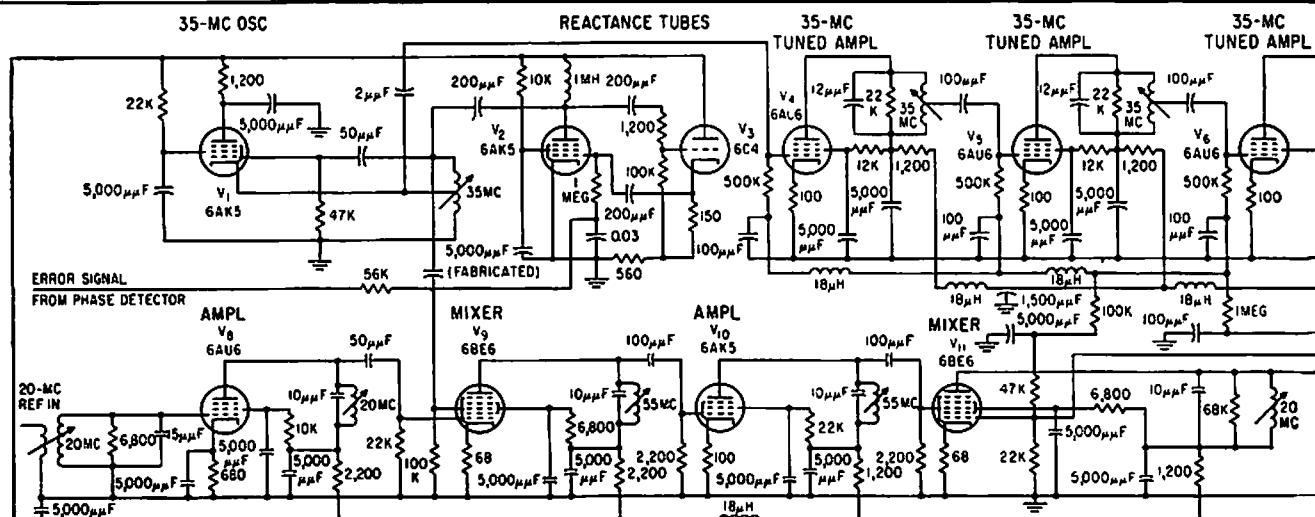


MEASURING PHASE UP TO 400 MC—Three plug-in tuning circuits cover range from 15 to 400 Mc. Phase delay is compared with

continuously variable delay standard with accuracy of 1% or 0.1°.—Y. P. Yu, How to Measure Phase at High Frequencies, *Electronics*, 34:11, p 54–56.



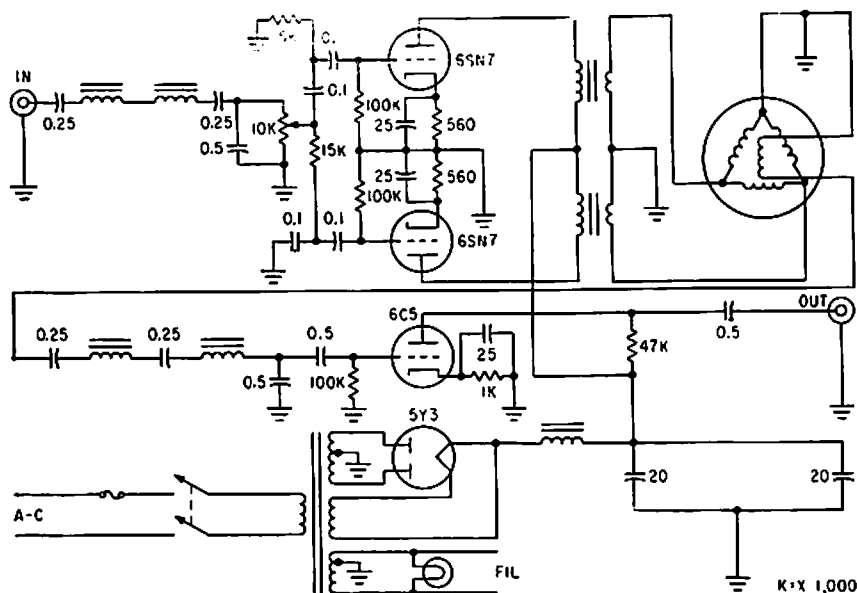
360-DEG SHIFTER—One variable component provides phase difference between outputs that is adjustable from -180 to +180 degrees without substantial change in magnitude, at fixed frequency of 2,500 cps.—W. Bacon, Circuit Shifts Phase 360 Degrees, *Electronics*, 31:23, p 94–97.



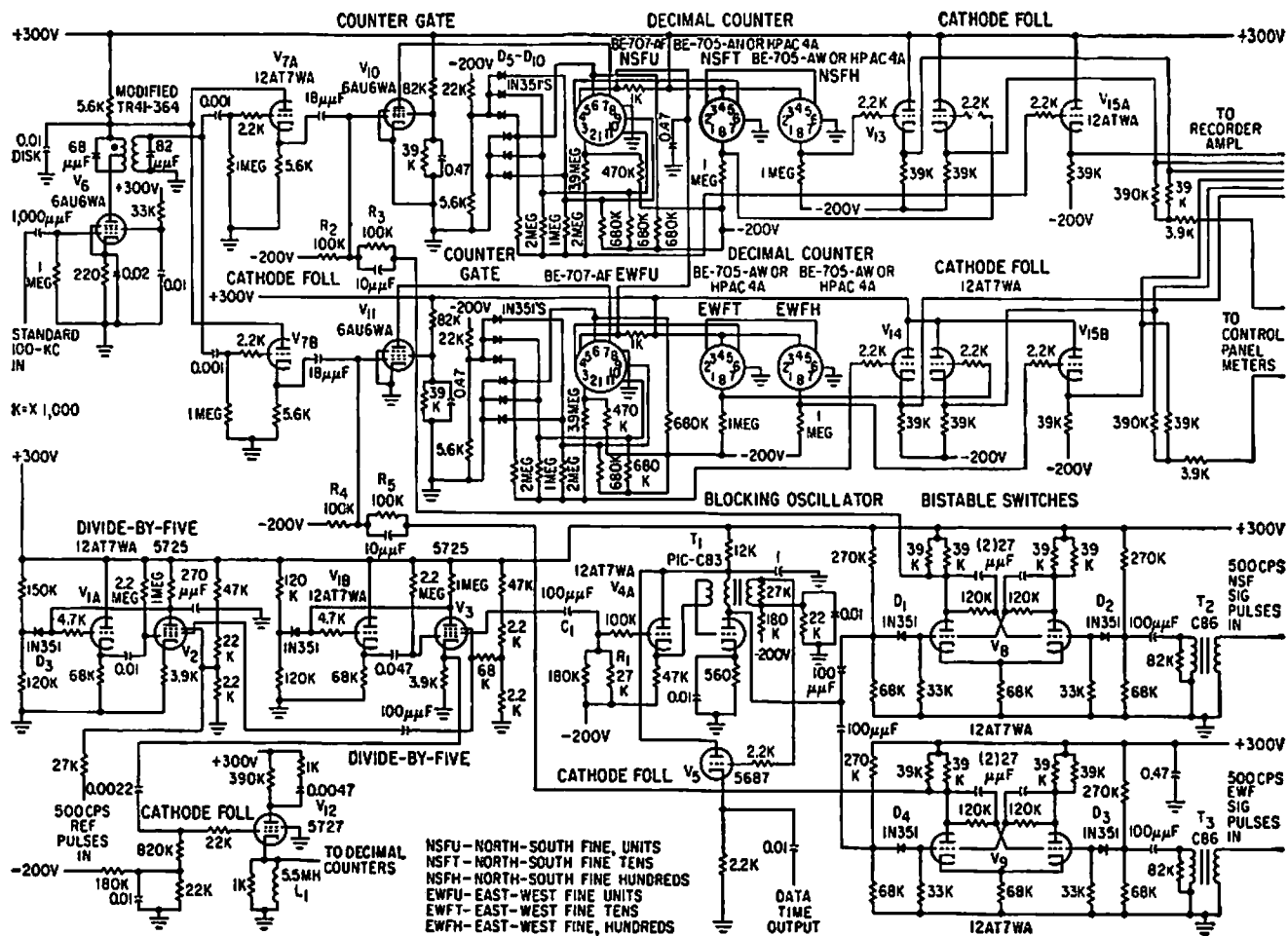
STEERABLE ANTENNA CONTROL—Phase-stabilized 35-Mc uhf amplifier controls directivity of multi-element stationary antenna array.

Phase of amplifier output is compared with input reference signal in phase-sensitive detector. System keeps input-output error under

2°.—E. W. Markow, Servo Phase Control Shapes Antenna Pattern, *Electronics*, 32:1, p 50–52.



SELF-SYNCHRONIZED PHASE SHIFTER—Consists of power supply, inverter, phase-shifting Selsyn, 100-cps filter, and output amplifier, used to vary phase of 100-cps frequency standard output by synchronization with WWV for studies of low-frequency propagation over long distances.—M. M. Newman et al, *Sea-Going Lightning Generator*, *Electronics*, 33:30, p 53-55.

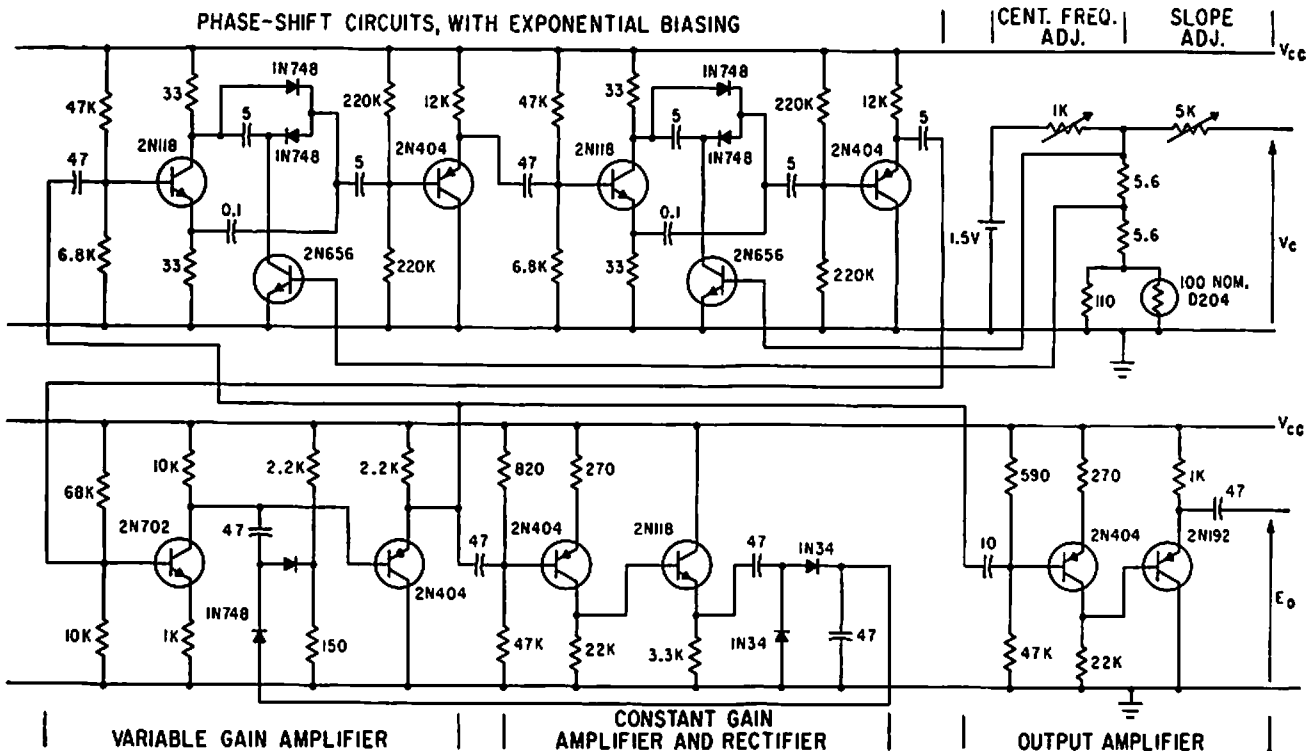
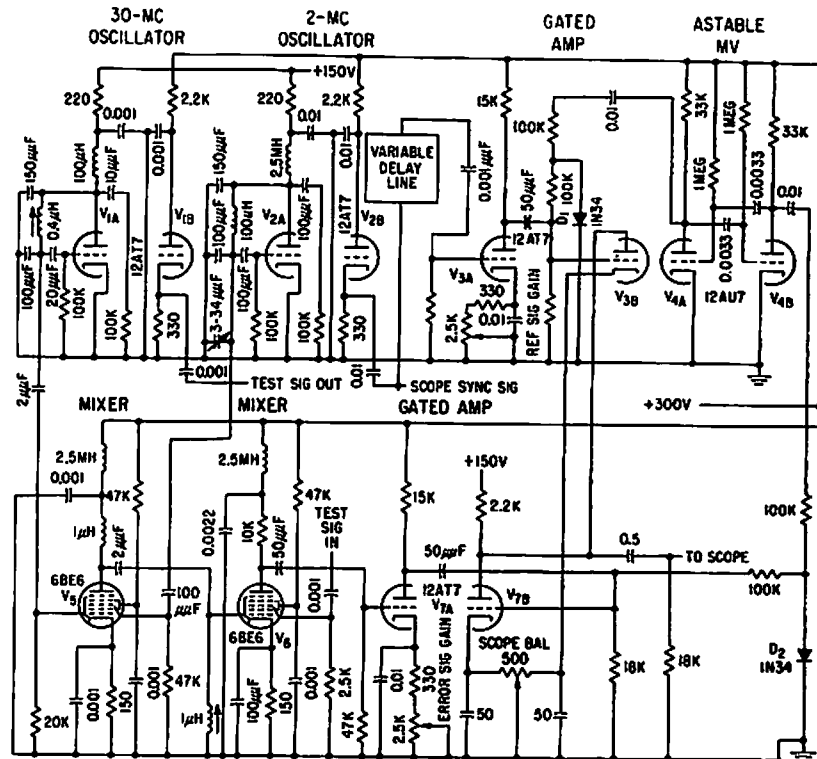


DIGITAL PHASE METER—Measures phase angle between satellite signal and reference pulse. Bistable switch operates as gate that connects 500-cps pulse train to 3-decade deci-

mal counter during time between reference and signal pulses. Two channels measure phase angles of north-south and east-west fine signals.—C. A. Schroeder, C. H. Looney,

Jr., and H. E. Carpenter, Jr., *Tracking Orbits of Man-made Moons*, *Electronics*, 32:1, p 33-37.

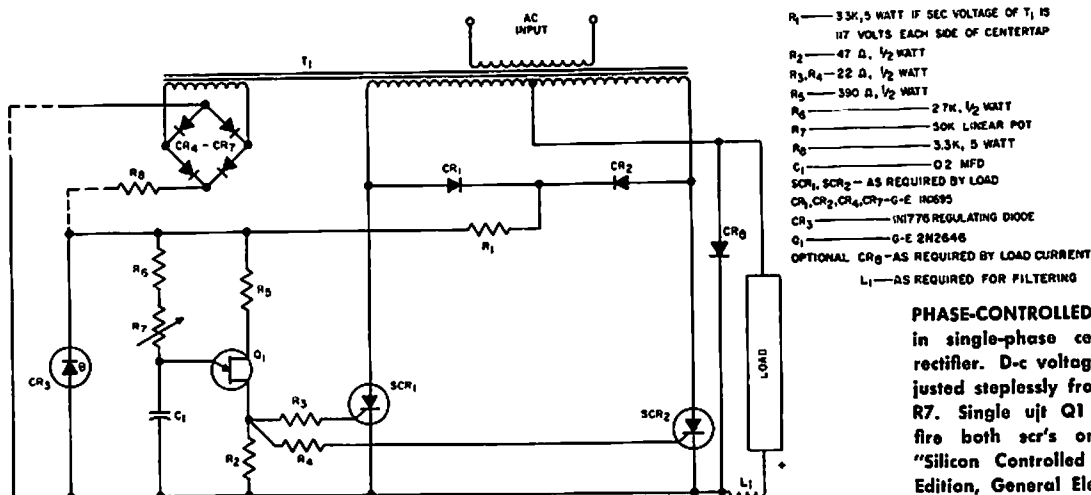
PHASE MEASURING AT 30 MC—Double mixing process linearly transposes phase shifts accumulated at 30 Mc down to 2 Mc, where they are accurately compared against calibrated 2-Mc reference signal. Samples of 30-Mc and 2-Mc signals are mixed in V5, and 32-Mc sum frequency is then mixed with phase-shifted 30-Mc signal from unit under test by V6. Difference frequency, equal to 2 Mc plus phase shift, is amplified in V7A, fed through gate V7B, and associated circuits then superimpose this signal and that of 2-Mc reference oscillator on cto to get display of phase shift.—A. Nirenburg, How to Measure Midfrequency Phase Shift, *Electronics*, 31:35, p 46-47.



VOLTAGE-CONTROLLED PHASE-SHIFT OSCILLATOR—Small-signal a-c resistance of junction diode, related to reciprocal of junction current over two-decade range, is used

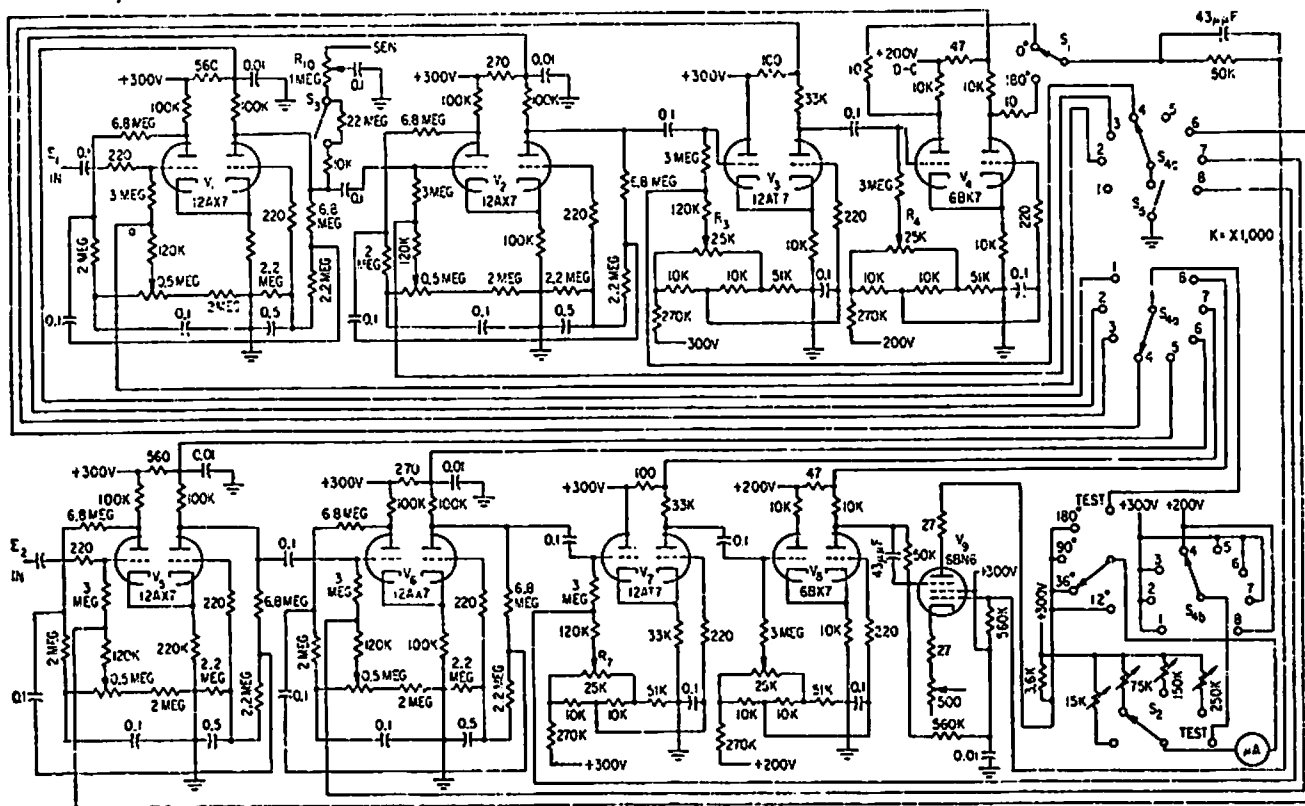
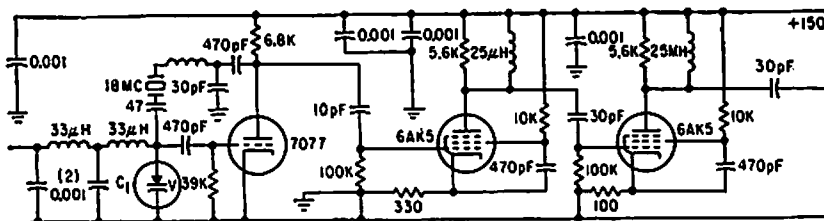
in two-section R-C phase-shift network acting with amplifier and agc to give constant-amplitude voltage-controlled oscillator with frequency range of over two decades.—R. A.

Groiner and S. K. Morgan, Voltage Controlled Wide-Range Oscillator, *Electronics*, 34:51, p 31-35.



PHASE-CONTROLLED D-C SUPPLY—Uses scr's in single-phase center-tap phase-controlled rectifier. D-c voltage across load can be adjusted steplessly from zero to maximum with R_7 . Single ujt Q_1 develops gate signal to fire both scr's on alternate half-cycles.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 140.

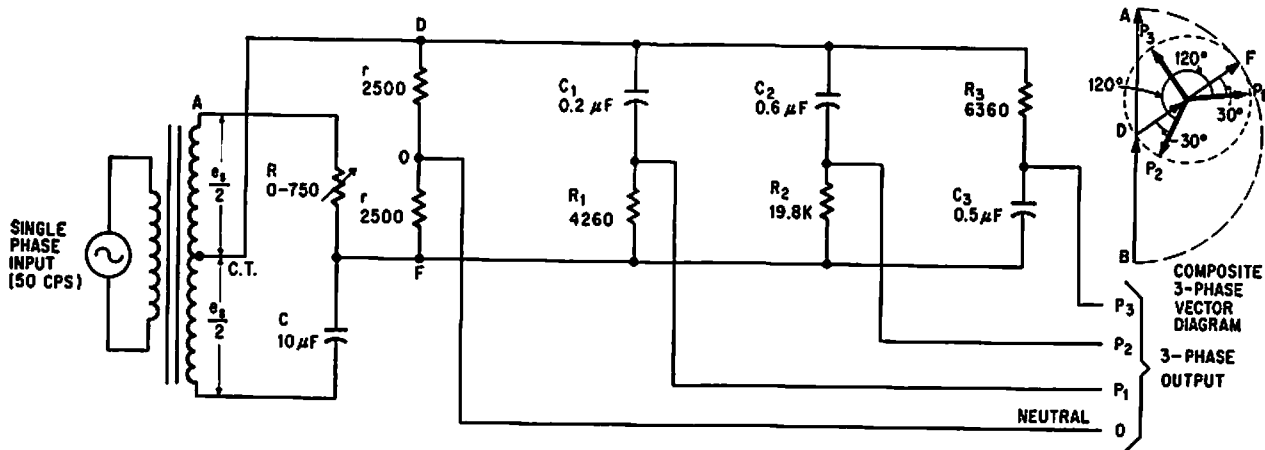
VOLTAGE-VARIABLE 90-MC OSCILLATOR—Ceramic triode in 18-Mc crystal stage has high short-term stability, yet can be pulled about 2 kc by voltage-variable capacitor C_1 . Fifth harmonic is amplified and buffered for use in measuring phase differences between two signals.—R. T. Stevens, Precision Phase-meter for CW or Pulsed UHF, *Electronics*, 33:10, p 54-57.



COINCIDENT-SLICER PHASEMETER—Cathode-coupled limiters convert input signals to square waves and feed them to coincident slicer. Self-adjustment of limiters and use of

coincident slicer to drive direct-reading phase-meter give good accuracy and stability for input-signal fluctuations from 0.3 to 70 v and supply voltages from 94 to 135 v, with ab-

solute accuracy of 1° and relative accuracy of 0.25%.—Y. P. Yu, Coincident Slicer Measures Phase Directly, *Electronics*, 31:37, p 99-101.

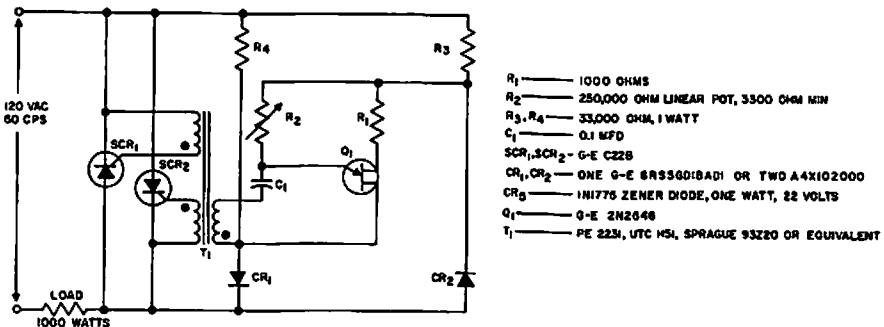


FIRING ANGLE CONTROL—Varying R from 0 to infinity shifts the three phase voltages

from 0° to 180°.—J. J. Vithayathil, Variable-Phase, Polyphase From Single-Phase Supply,

Electronics, 37:27, p 56-57.

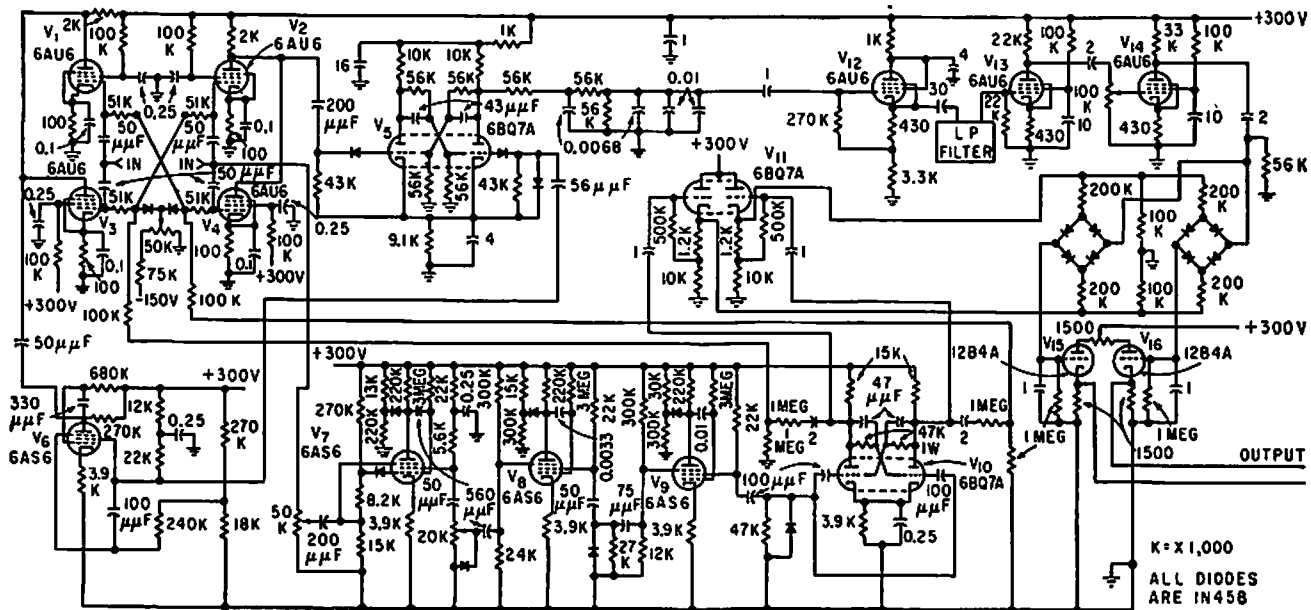
1-KW A-C PHASE CONTROL—Inverse-parallel circuit is economical for manual control of lights, heaters, ovens, or fans.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 138.



ELECTRONIC SWITCH

PHASE DETECTOR

INTEGRATOR AND AMPLIFIER



DELAY CIRCUIT

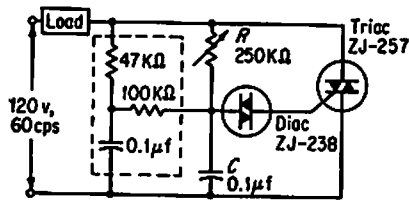
FREQUENCY DIVIDER AND SWITCH GENERATOR

SYNC DETECTOR

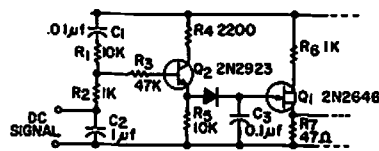
PULSE-CHAIN PHASEMETER—Measures phase difference as small as 0.005 deg between pulses of two nearly coincident pulse chains using electronic switch, mvbr phase detector,

and diode synchronous detector. Output shows both sign and magnitude of phase angle between two corresponding pulses in pulse chains.—F. Vrataric, Jr., *Electronic*

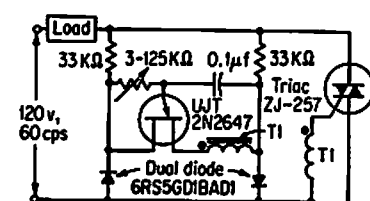
Switching in Phase Measurement, Electronics, 32:23, p 60-61.



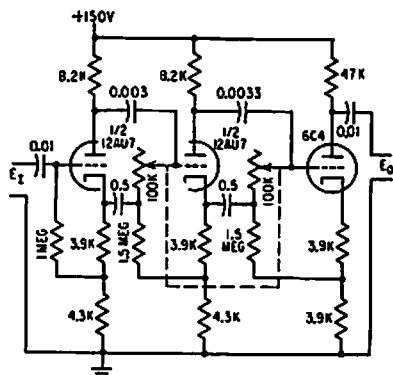
DIAC-TRIAC PHASE CONTROL OF 5-AMP LOAD—Uses two types of semiconductor switches together with R and C to give continuous control of power. Addition of second phase shift network (enclosed in dashed box) extends range of control to cover 5 to 95% of full power.—M. P. Southworth, *Bidirectional Static Switch Simplifies Ac Control*, *Control Engineering*, March 1964, p 75-76.



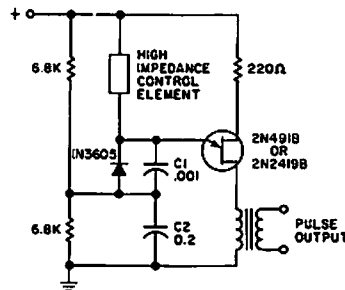
SNAP-ACTION A-C PHASE CONTROL—Provides snap-action switching of load in response to change in d-c signal, a-c signal, or variable resistance element, using small differentiating network R1-R2-C1 that peaks leading edge of pedestal. Triggering can occur only near beginning of each half-cycle, to give snap-on and snap-off action.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 135.



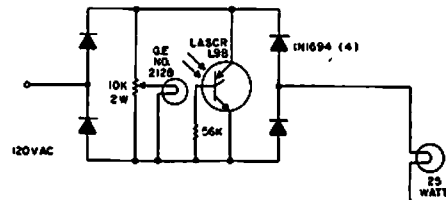
TRIAC-UJT PHASE CONTROL—Provides wide range of stable control without hysteresis at low outputs and without dependence on supply voltage. Triac eliminates need for transient suppression components that would be required with scr control and permits use of simple two-winding pulse transformer.—M. P. Southworth, *Bidirectional Static Switch Simplifies A-C Control*, *Control Engineering*, March 1964, p 75-76.



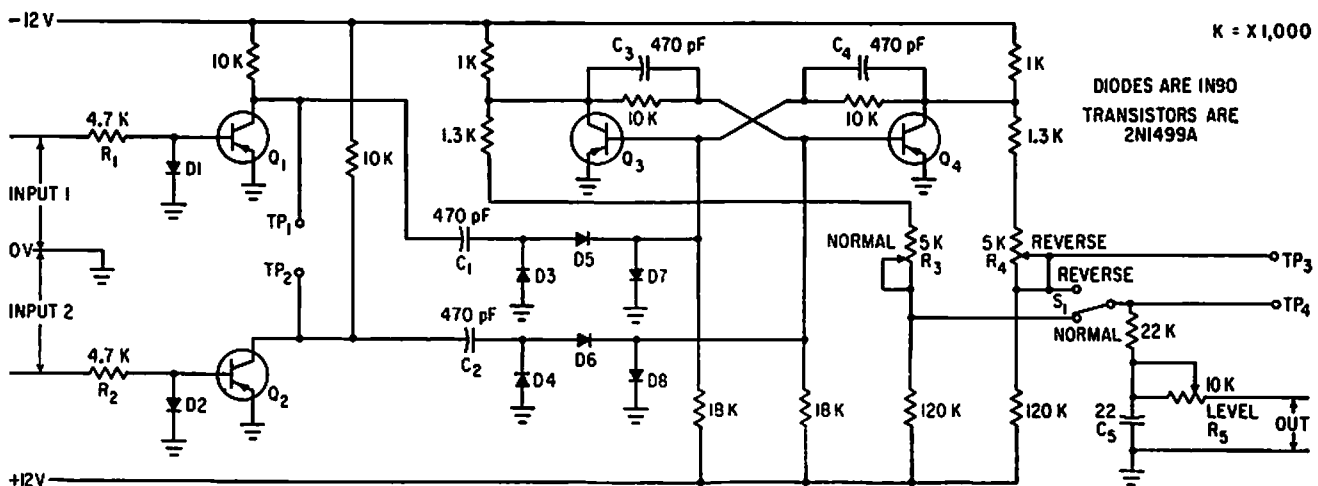
CASCADED TWO-TUBE PHASE SHIFTER—Provides phase shifts well over 180° with highly constant output voltage. Use of tubes in place of transformer gives wide-band operation, from 500 to 2,000 cps.—W. G. Shepard, *Phase Shifter Range Exceeds 180°*, *Electronics*, 31:19, p 96-100.



HIGH-GAIN PHASE CONTROL—Use of two different sizes of charging capacitors in series increases effective gain up to 10,000 times that of conventional ujt/scr phase-control circuit. Eliminates need for two or three stages of transistor amplification.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 332.



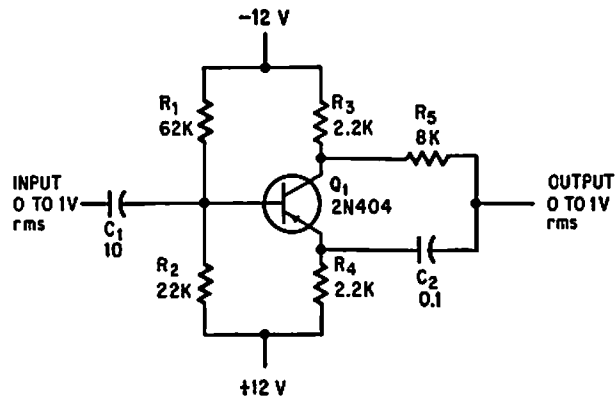
SMALLEST PHASE CONTROL—Miniature lamp No. 2128 with small, low-mass filament can reach firing level of light-activated scr in about three cycles with low applied voltage. As applied voltage is increased, this time is reduced to about 1 millisecc when lamp is directed across LASCR terminals, thus providing phase control. Lamp voltage is removed when LASCR fires, protecting lamp and resetting it for next half-cycle. Useful for dimming 25-w lamp or for controlling temperature of small soldering iron.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 213.



PHASE-DIFFERENCE METER FOR 0.2 TO 20 KC—Measures phase difference between two sinusoidal inputs. Each limiter (Q1 and Q2) drives one side of high-speed flip-flop Q3-Q4 through differentiating and clipping

circuits, giving square wave that turns on when one input signal goes negative and turns off when other input goes negative. D-c value of output voltage is proportional to phase difference, with about 11 v cor-

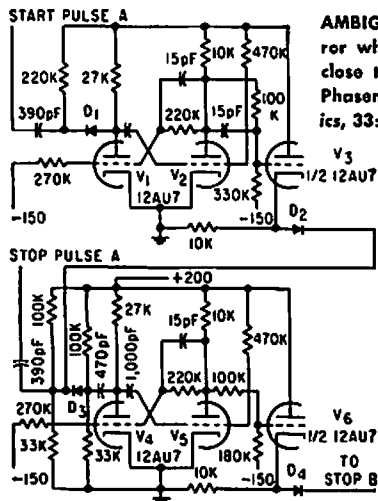
responding to 360°.—J. R. Woodbury, *Measuring Phase with Transistor Flip-Flops*, *Electronics*, 34:38, p 56.



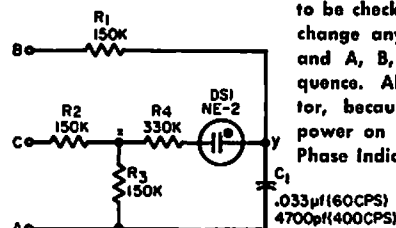
VARIABLE 0-180 DEG PHASE SHIFTER—Single low-cost pnp germanium transistor circuit gives any desired phase shift between

0 and 180 degrees, at constant amplitude, for frequencies up to 3 Mc, by varying values of C2 and R5. Values shown give 90° shift

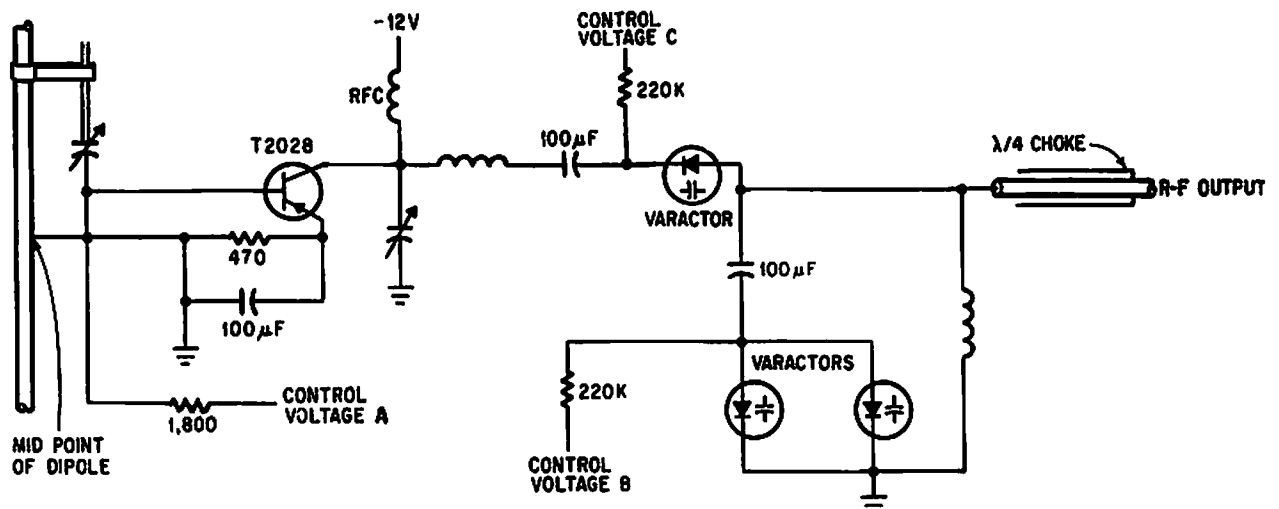
for 200 cps.—J. J. Collins, Single Transistor Provides Low-Cost Phase Shifter, *Electronics*, 37:16, p 92.



AMBIGUITY RESOLVER—Prevents counting error when phase shift between two signals is close to 0° or 360°.—R. T. Stevens, Precision Phasemeter for CW or Pulsed UHF, *Electronics*, 33:10, p 54-57.



PHASE INDICATOR—Used to determine succession of phases of three-phase 120-v a-c source used in synchro work. Terminals A, B, and C are connected to terminals of source to be checked. If neon lamp comes on, interchange any two leads; light then goes out, and A, B, and C then indicate correct sequence. Also serves as phase failure monitor, because neon lamp will come on if power on any one line is lost.—G. Richwell, Phase Indicator, *EEE*, 12:11, p 70.



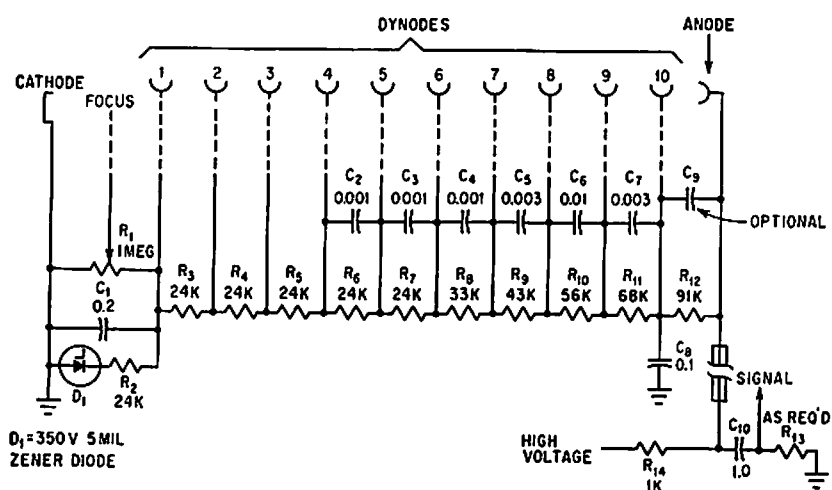
PHASE-SHIFTING ANTENNAFIER—Control voltages A, B, and C together produce up to 180° phase shift with adequate matching, for

beam-steering arrays. Ganged potentiometers with appropriately tapered windings can provide the control voltages and relate beam

position to shaft rotation.—J. F. Rippin, Making the Antenna an Active Partner, *Electronics*, 38:16, p 93-96.

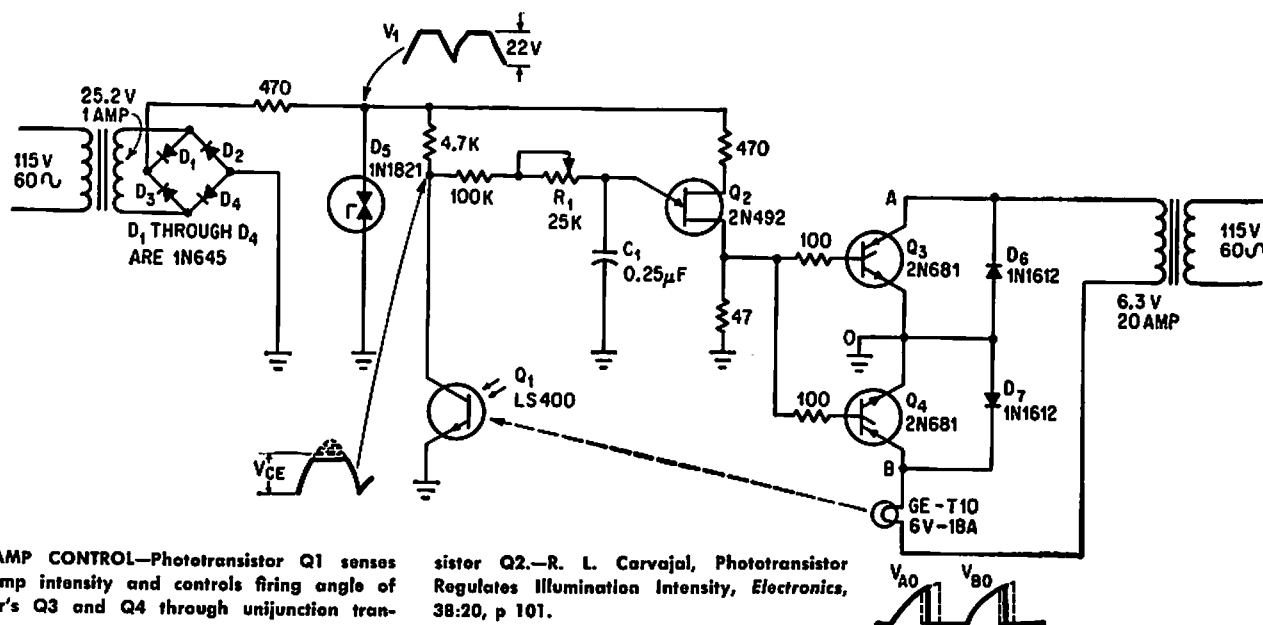
CHAPTER 58

Photoelectric Circuits



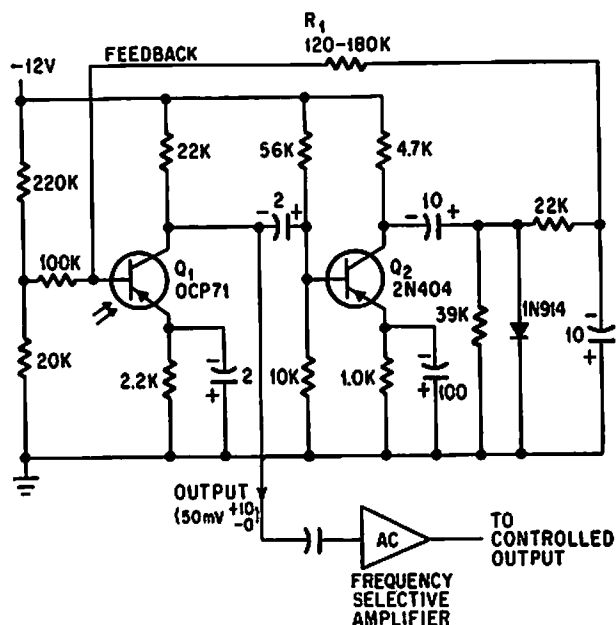
PHOTOMULTIPLIER FOR GAMMA-RAY SPECTROMETER—Flashes of light from scintillator crystal are picked up by EMI9579 photomultiplier to measure underwater gamma radiation, and amplified output of photo-

multiplier is fed to surface equipment through coaxial cable that also serves as 2,000-v high-voltage lead for anode.—G. K. Riel, New Underwater Gamma Spectrometer, *Electronics*, 36:10, p 56-8.



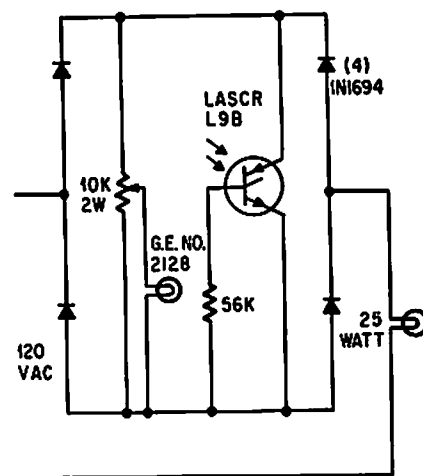
LAMP CONTROL—Phototransistor Q1 senses lamp intensity and controls firing angle of scr's Q3 and Q4 through unijunction tran-

**sistor Q2.—R. L. Carvajal, Phototransistor
Regulates Illumination Intensity, *Electronics*,
38:20, p 101.**

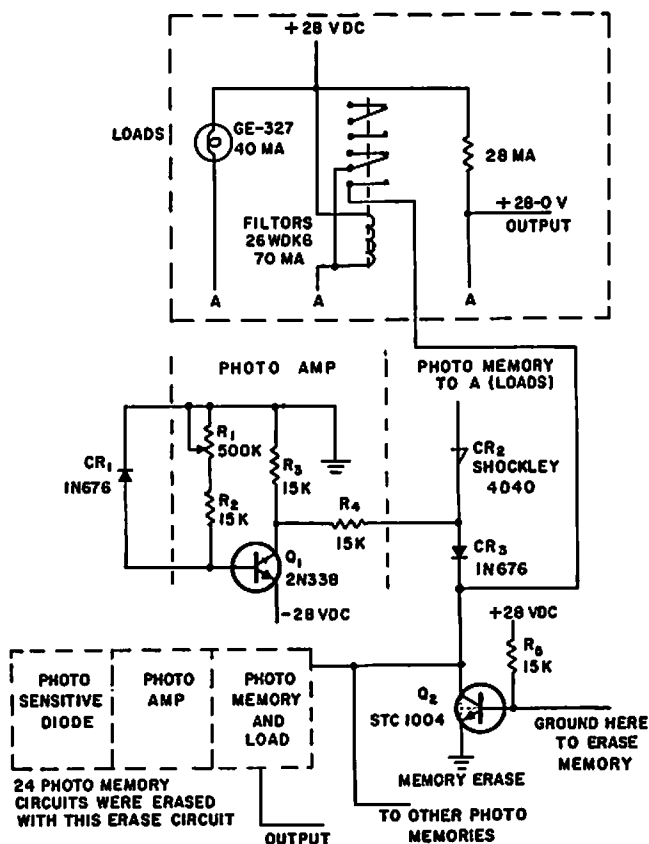


AGC FOR PHOTODETECTOR AMPLIFIER—Holds photodetector output signal constant within 2 db for 4-db variation in ambient light

level.—P. H. Sydenham, Photodetector Gain Control Aids Signal Discrimination, *Electronics*, 38:23, p 111.

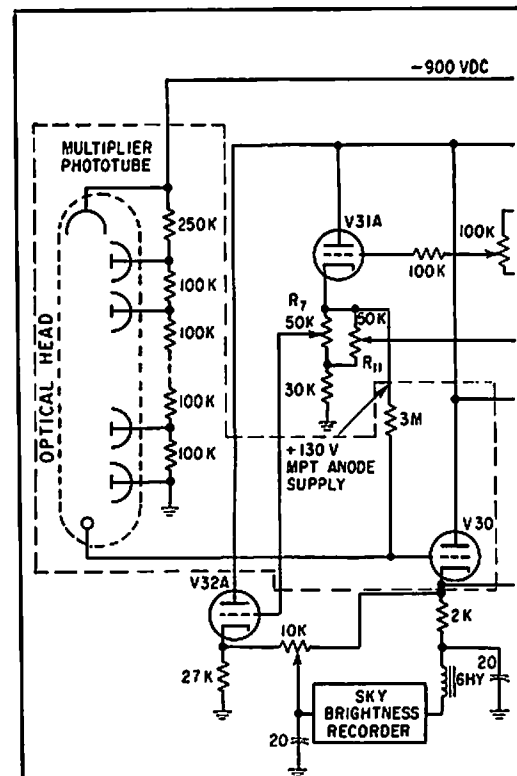


LAMP-TRIGGERED SCR GIVES VARIABLE PHASE CONTROL OF POWER—Miniature 2128 lamp with low-mass filament triggers light-activated scr in 1 millisecc when lamp is across scr and in about 3 cycles at low a-c lamp voltage. Potentiometer thus provides phase control of scr for dimming 25-w lamp or equivalent-wattage load.—E. K. Howell, Light-Activated Switch Expands Uses of Silicon-Controlled Rectifiers, *Electronics*, 37:15, p 53-61.

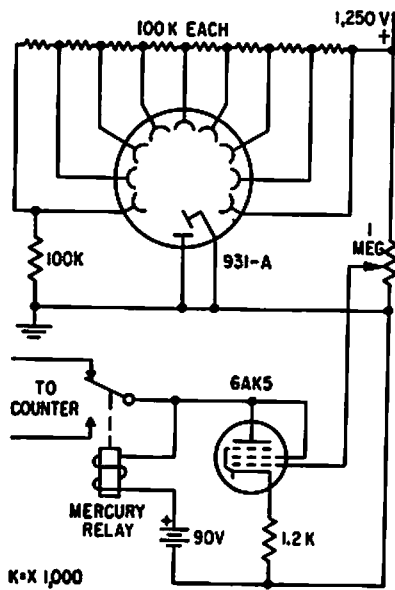


PUNCHED TAPE READER—Uses photo memory to drive loads, to keep signal applied to load until memory is erased. Lamp load can be used for verifying punched paper tape. Relay load controls circuit where it is necessary to handle large currents. Relay used exceeds continuous rating of CR2, so one set

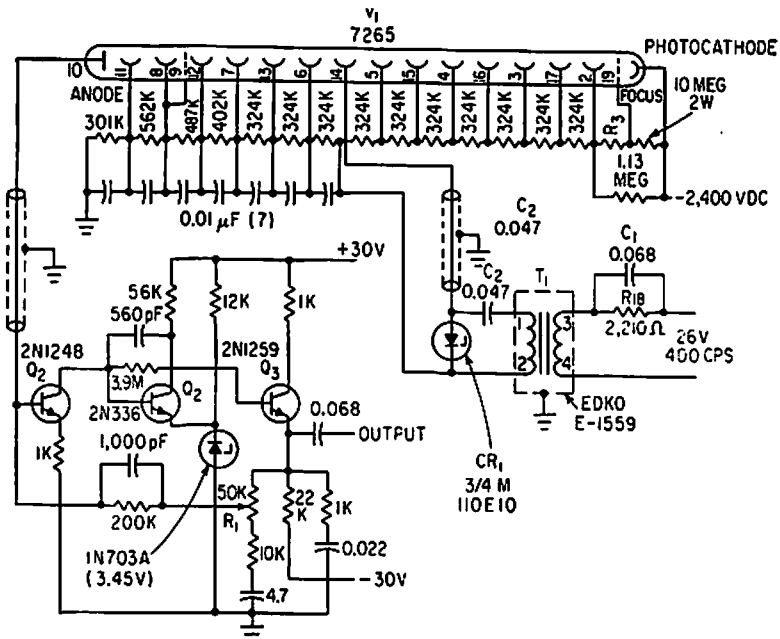
of relay contacts keeps relay latched. Resistive load is used to drive logic circuits. Sensor can be either photodiode or standard 1N676 diode with paint removed from glass case.—Photo Reader for Perforated Tape, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 207.



CORONAMETER—Uses polarized-light technique and closely controlled narrowband circuit to detect, observe, and measure otherwise invisible solar phenomenon. Circuit shows multiplier tube in optical head, with dynode

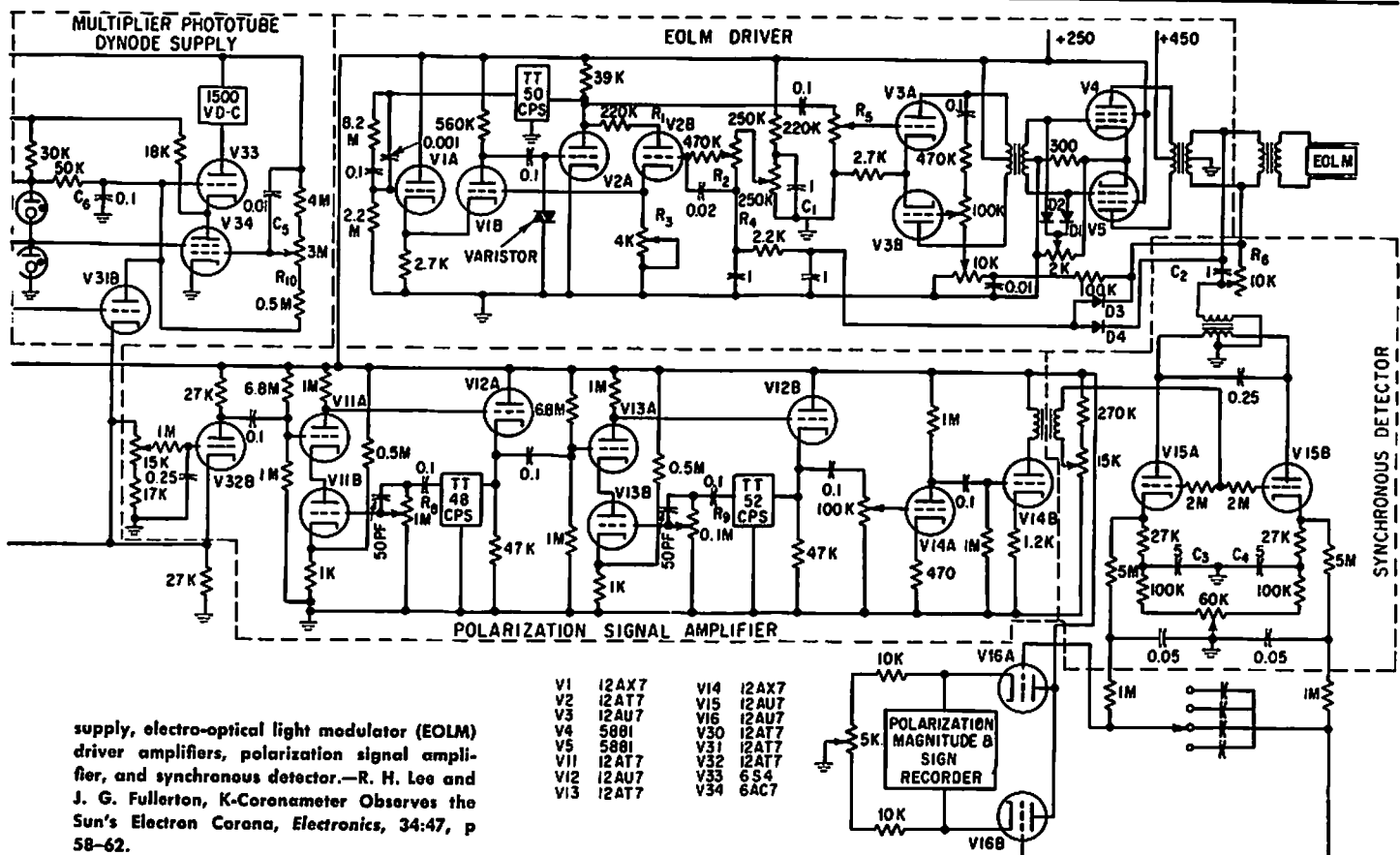


AUTOMATIC CALIPER—Photomultiplier and counter circuit on machine lathe feed recorder to give dimensions of printed circuits and photographic plates in increments of 0.0001 inch.—S. Isaacson, *Electronic Caliper Checks Printed Circuits*, *Electronics*, 32:1, p 44-45.



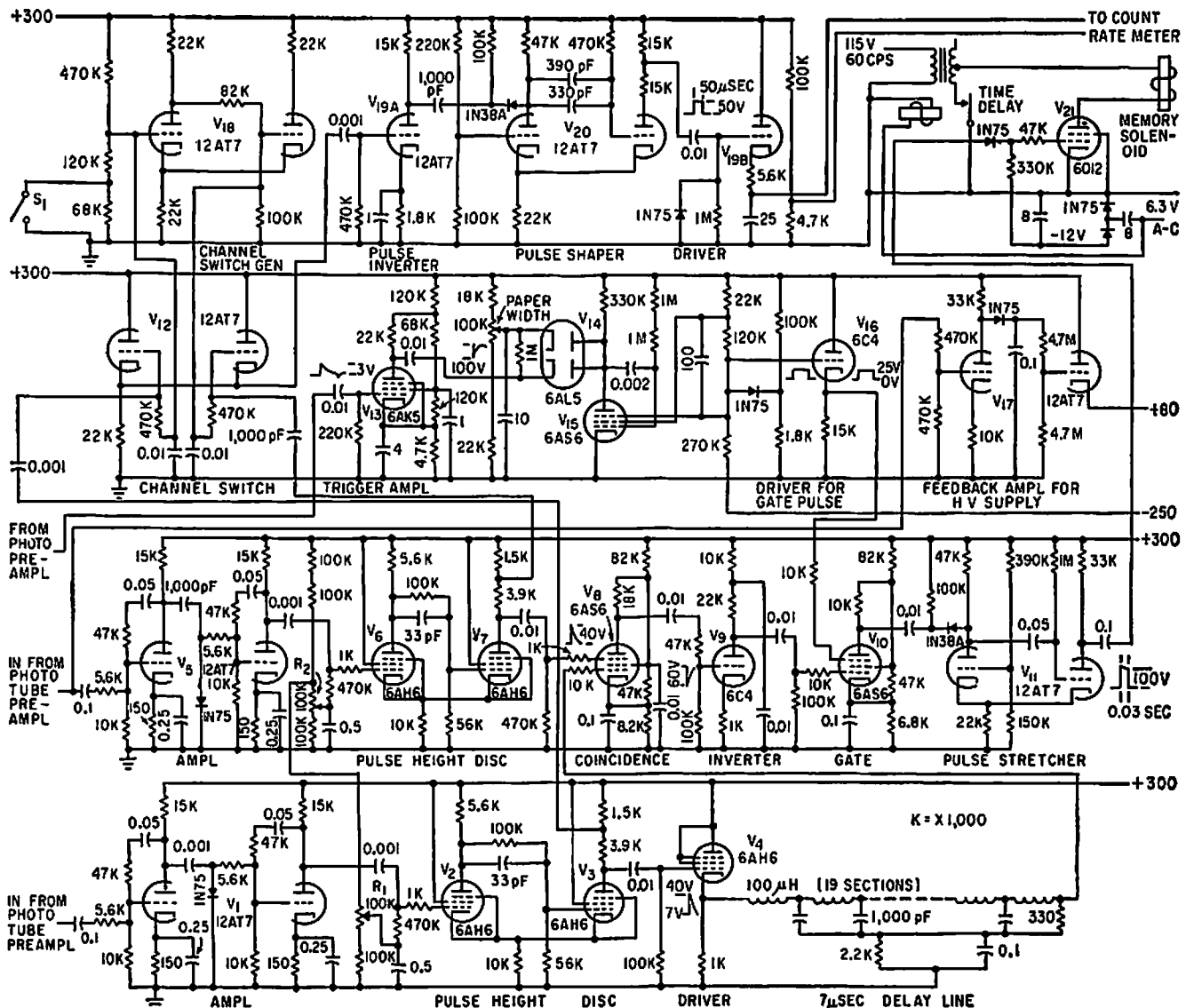
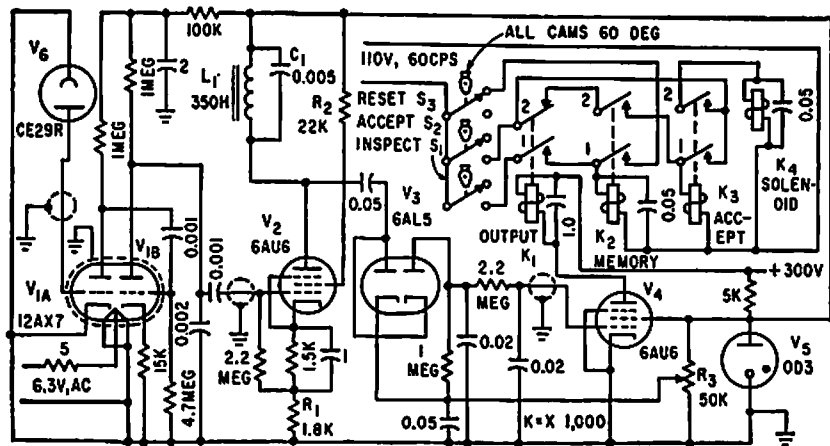
A-C MODULATOR FOR PHOTOMULTIPLIER—Output of star-tracking photomultiplier is converted to a-c by applying 400-cps modulating voltage to dynode 14, to make gain vary between nominal value and 1% of this in

square wave fashion. Amplifier passband is then 130 to 800 cps.—E. R. Schlesinger, *Aiming a 3-Ton Telescope Hanging from Balloon*, *Electronics*, 36:6, p 47-51.



supply, electro-optical light modulator (EOLM) driver amplifiers, polarization signal amplifier, and synchronous detector.—R. H. Lee and J. G. Fullerton, *K-Coronameter Observes the Sun's Electron Corona*, *Electronics*, 34:47, p 58-62.

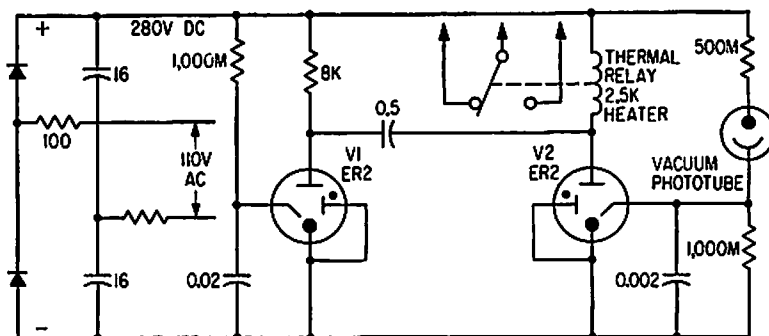
PHOTOELECTRIC GAGING—Checks dimensions of machine parts while they are rotating. Fail-safe circuit assures that only satisfactory pieces are accepted. Sorter is initially calibrated to desired sensitivity with go and no-go gages.—J. C. Frommer, Fail-Safe Photoelectric Inspection for Industry, *Electronics*, 32:31, p 74-75.



PAPER FLAW DETECTOR—Locates defects in paper despite photomultiplier noise amplitudes comparable to flaw-signal amplitudes. Two identical phototubes are used, each with identical preamplifiers, amplifiers, and pulse

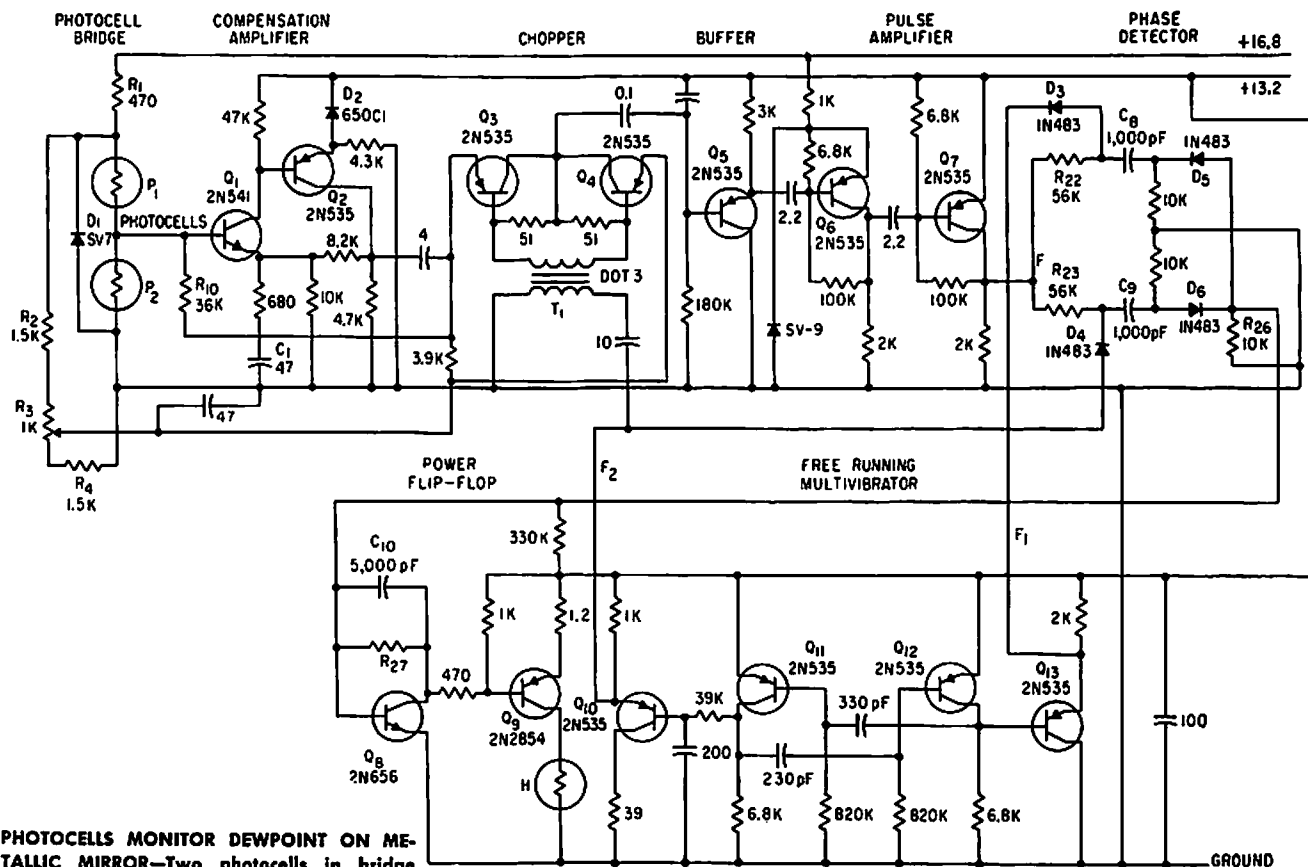
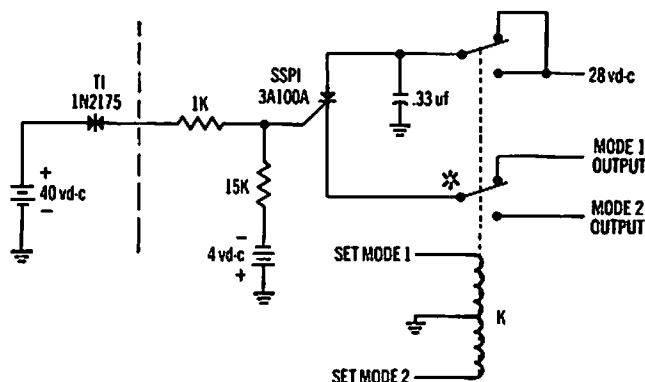
height discriminators. One phototube looks at paper ahead of other. Output of leading phototube is delayed to give same effect as if both looked at same area at same time. Pulses due to real defects then occur at same

time and pass coincidence circuit. Pulses due to noise are random in time and do not pass.—M. P. MacMartin, Sensitive Flaw Detector Solves Noise Problems, *Electronics*, 33:16, p 64-66.



AUTOMATIC LIGHT CONTROL—High-sensitivity vacuum phototube responds to illumination by changing mark-space periods of cold-cathode mvbr so V2 is on most of the time when illumination is excessive. Thermal relay then gets heated sufficiently to switch off lights.—P. Bergwager, *Photoelectric Control Using Cold Cathode Amplifiers*, *Electronics*, 33:27, p 46-47.

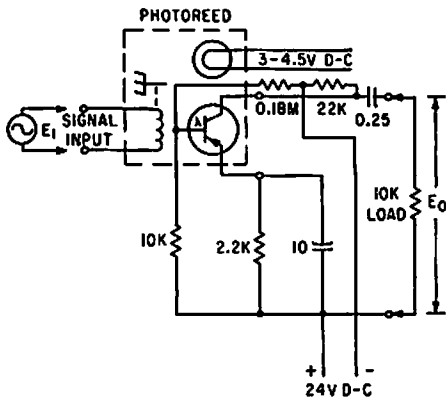
TAPE READER—Simplified circuitry, few components, storage capability, and output power above 20 w are advantages of using silicon controlled switch in place of multistage amplifier for photoelectric paper-tape readers. Thyatron-like characteristics maintain output after photoelectric stimulus disappears, until cut off by control circuit. Asterisk on one pole of relay K indicates that similar pole is required for each bit in two-mode operation.—SCR Switch Eliminates Amplifier for Photoelectric Readers, "Electronic Circuit Design Handbook," MacIster Pub. Corp., N.Y., 1965, p 222.



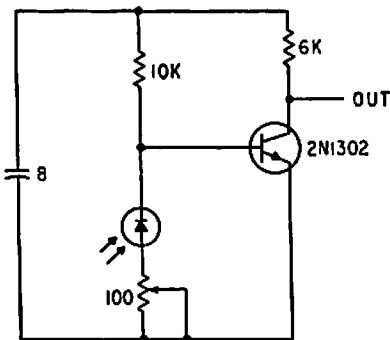
PHOTOCELLS MONITOR DEWPOINT ON METALLIC MIRROR—Two photocells in bridge circuit develop error signal for bang-bang servo that uses power flip-flop to turn heater H of metallic mirror on and off, to maintain

constant-thickness film of dew or frost on mirror. Two-transistor chopper and a-c amplifier eliminate drift problems.—H. R. Farrah

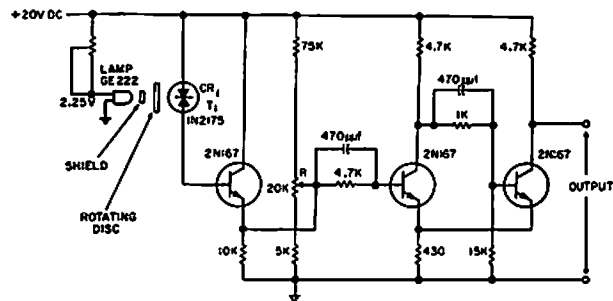
and P. E. Sherr, *New Approach to Weather Data: Every Plane a Station*, *Electronics*, 36:28, p 38-41.



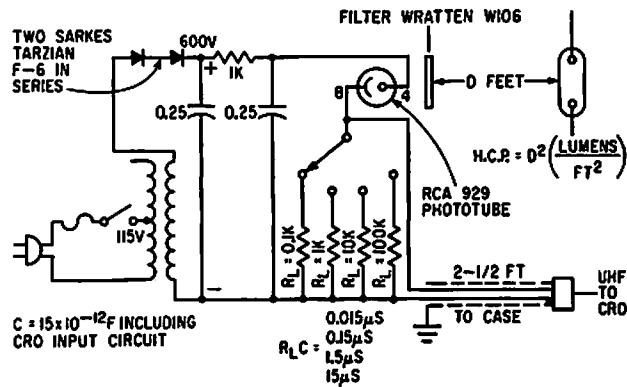
PHOTOELECTRIC BANDPASS FILTER—Output of phototransistor varies linearly with input signal at resonant frequency of photoresonator, to give function of bandpass filter.—Frequency-Sensitive Control Uses Light, *Electronics*, 34:36, p 88-91.



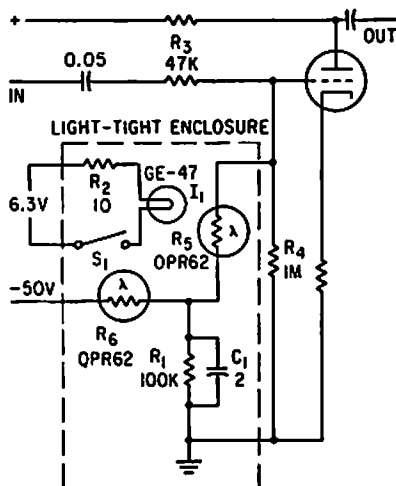
PHOTOSWITCHING CIRCUIT—Circuit provides amplification along with switching for photodiode mounted to pick up changes in light reflected by encoder disk.—F. W. Kear, *How to Select Shaft-Position Encoders*, *Electronics*, 35:35, p 48-51.



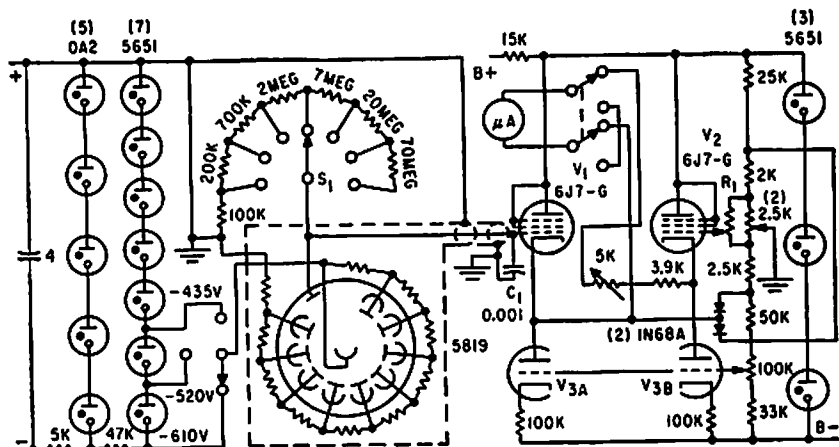
PHOTODIODE PICKOFF—Used in measuring servo system lag. Responds to slot milled near edge of rotating disk. Accuracy is 0.17° in either direction.—J. D. Habegger, Photo Diode Pickoff Gives Accurate Angular Reference, *EEE*, 10:6, p 37.



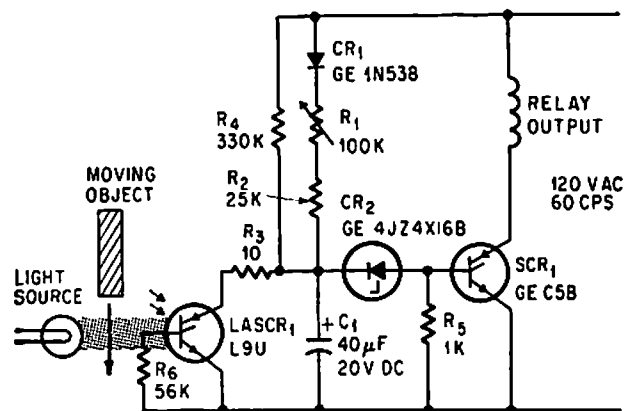
MEASURING TRANSIENT LIGHT—Used with cre for measuring rapidly changing light output of flashlamps.—H. E. Edgerton and R. O. Shaffner, *Measuring Transient Light With Vacuum Phototubes*, *Electronics*, 34:34, p 56-57.



PHOTOCELLS PROVIDE NOISE-FREE AUDIO KEYING—Photoresistors R5 and R6 isolate control function from signal circuit to avoid switching transients. S1 may be replaced by automatic pulsing circuit.—A. Martens, Noise-Free Keying Circuit, *Electronics*, 35:13, p. 53.

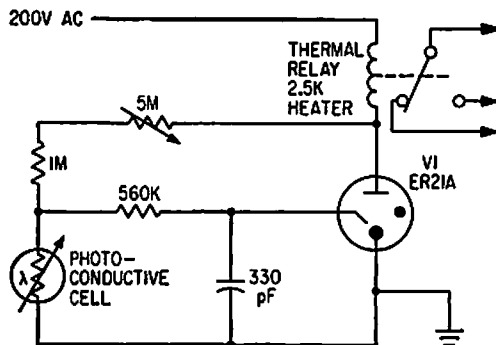


CELL-GROWTH MICROPHOTOMETER—Permits direct measurement of transmittance while stained cells are studied visually at magnification of 1,000 X. Beam-splitting mirror sends 90% of light to multiplier phototube. Maximum current sensitivity is 0.01 microamp full scale.—E. Gordy, *Microphotometer Aids Biologists*, *Electronics*, 32:28, p 62-64.

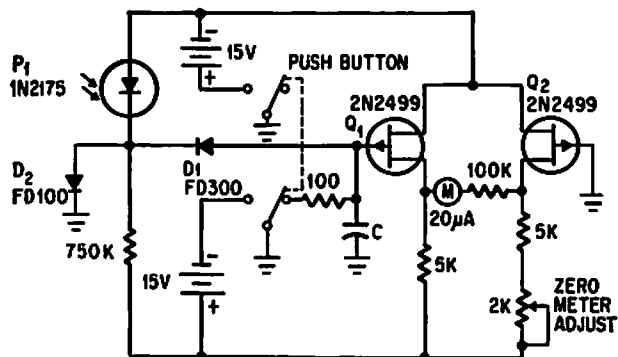


CONVEYOR-LINE JAM DETECTOR—Interruption of light beam to light-activated scr for more than few millisec fires SCR1, opening relay. Momentary interruptions by objects moving normally on conveyor have no effect.

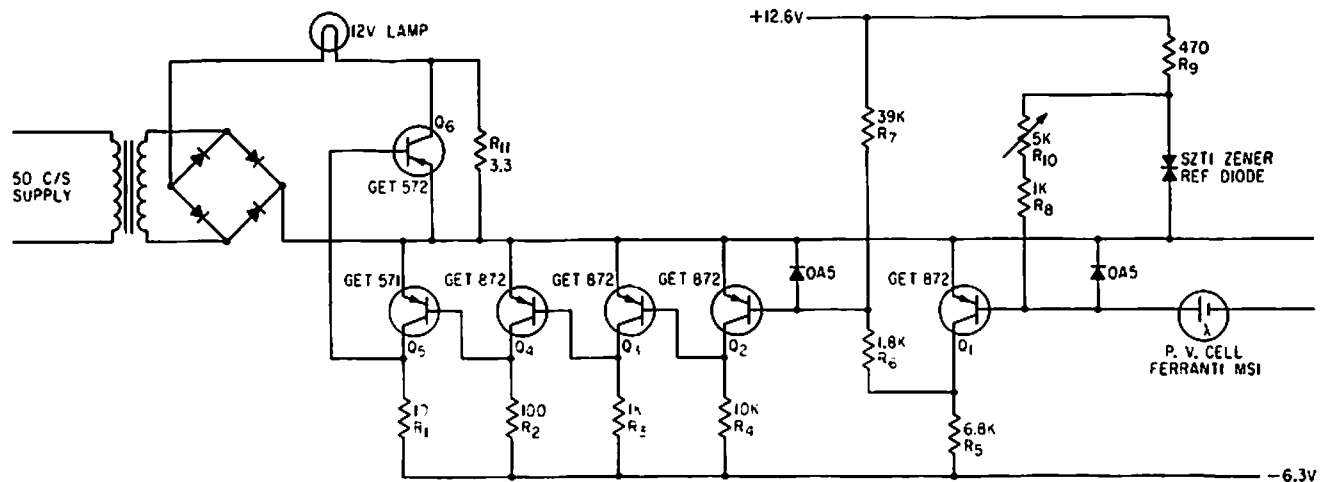
Circuit resets automatically when light is restored.—E. K. Howell, Light-Activated Switch Expands Uses of Silicon-Controlled Rectifiers, *Electronics*, 37:15, p 53-61.



SINGLE COLD-CATHODE AMPLIFIER—Actuates thermal relay directly from photoconductive cell, for turning on lights at sunset.—P. Bergweger, Photoelectric Control Using Cold Cathode Amplifiers, *Electronics*, 33:27, p 46-47.



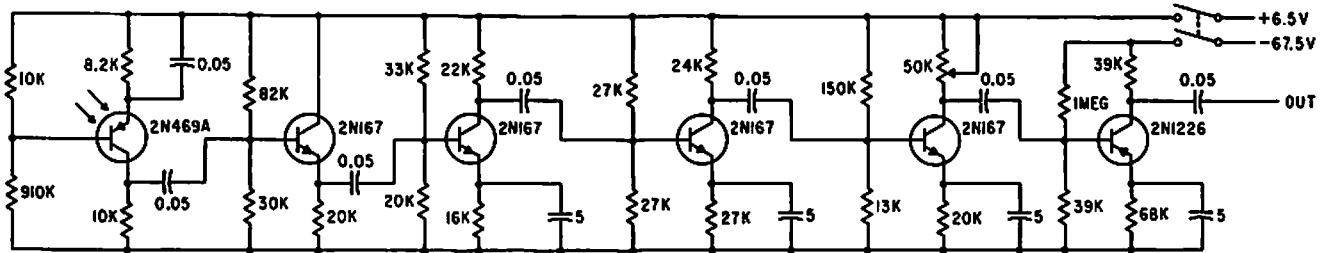
MEASURING FLASHES—Measures and holds intensity of single flash or total value of series of flashes.—C. R. Kerns, FET Circuit Stores Light Measurement, *Electronics*, 38:22, p 66.



LIGHT-SOURCE STABILIZER—Feedback circuit generates precise pulses to control light level of photoelectric light source. Difference be-

tween reference current supplied by zener diode and current from photocell is amplified by transistor chain and applied to Q6 to

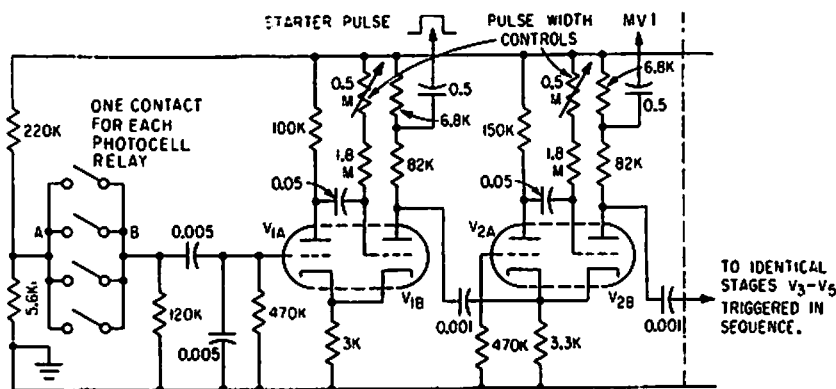
adjust lamp current.—J. R. Dyke, Illumination Stabilizer for Photosensing System, *Electronics*, 34:52, p 44-45.



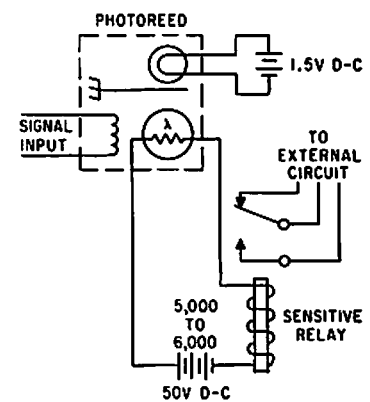
PROJECTILE GLOW DETECTOR—2N469A phototransistor detects brief low-intensity self-luminous shroud of projectile, and feeds high-gain pulse amplifier that elevates volt-

age enough to ionize thyratron that initiates discharge of spark-source capacitor for shadowgraph photography.—O. H. Bock and P. L. Clomens, *Aerodynamic Measurements in a*

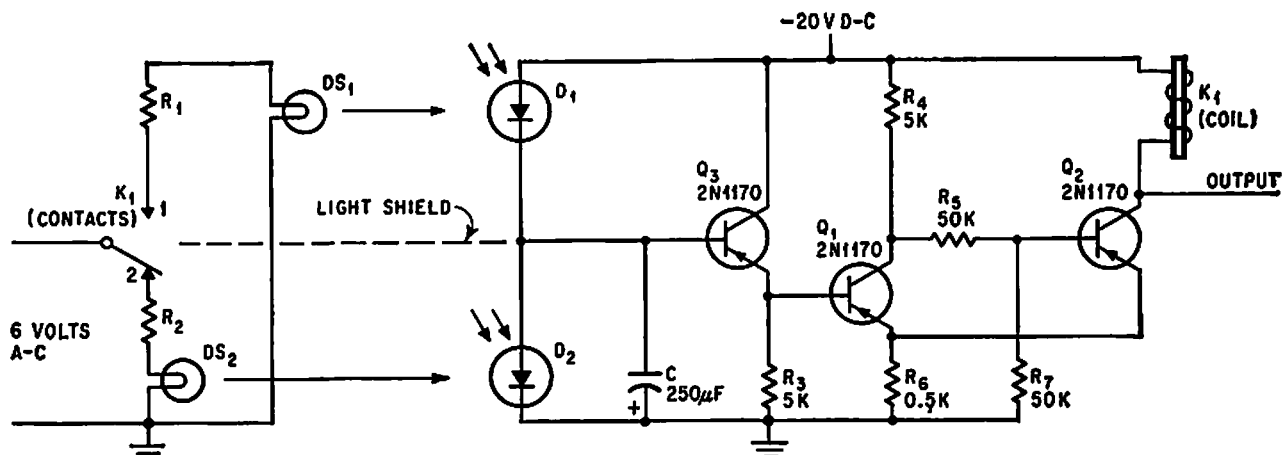
Hypervelocity Gun Range, Electronics, 34:44, p 33-37.



EYE-MOTION MVBR CHAIN—Illuminated photocell triggers mvbr chain. Starter-mvbr is triggered by positive pulse on grid of V1A, but remaining mvbr stages are triggered by negative pulse on cathode. Output pulses are taken from across 6,800-ohm resistor and a-c coupled to gate.—E. L. Thomas, R. Howat, and N. H. Mackworth, *Tv Tracker Records Eye Focus Points, Electronics*, 33:17, p 57-59.



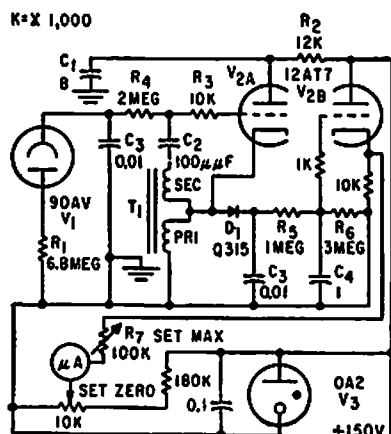
PHOTOREED—Combines resonant reed relay with photosensor to give frequency-sensitive control in which switching of contacts is accomplished by electro-optical techniques. Photosensor is exposed to intermittent light when reed vibrates like shutter between lamp and sensor.—*Frequency-Sensitive Control Uses Light, Electronics*, 34:36, p 88-91.



SQUARE-WAVE GENERATOR—Intensity of light sets pulse and interpulse periods in range from 0.2 to 300 sec, using Schmitt

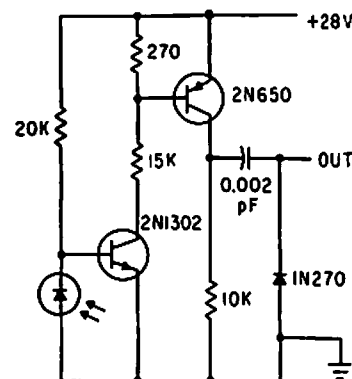
trigger Q1-Q2. Capacitor C is charged and discharged through diodes D1 and D2 consisting of collector-base junctions of 2N1393

phototransistors.—A. K. Horvath, *Photodiodes Control Pulse Intervals, Electronics*, 38:11, p 72.

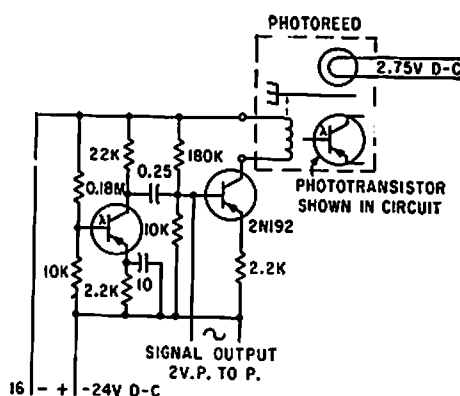


ILLUMINATION TELEMETER—Prf rate of blocking oscillator, controlled by photocell output, can be transmitted over telephone lines to give accurate remote indication of daylight or other light intensity. D1, V2B, and

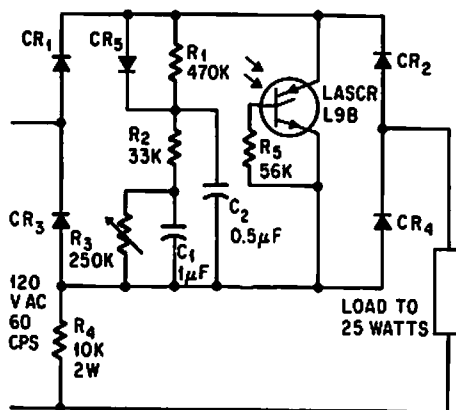
meter provide local indication. Maximum illumination gives highest prf.—E. F. Hasler and G. Spurr, *Ways to Measure Light Intensity at a Distance*, *Electronics*, 32:29, p 48-49.



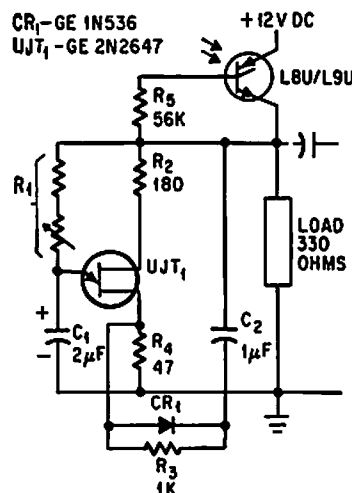
PHOTODIODE AMPLIFIER—Used with transmitted-light encoder disk to produce pulses with correct amplitude and rise time to drive logic circuit.—F. W. Kear, *How to Select Shaft-Position Encoders*, *Electronics*, 35:35, p 48-51.



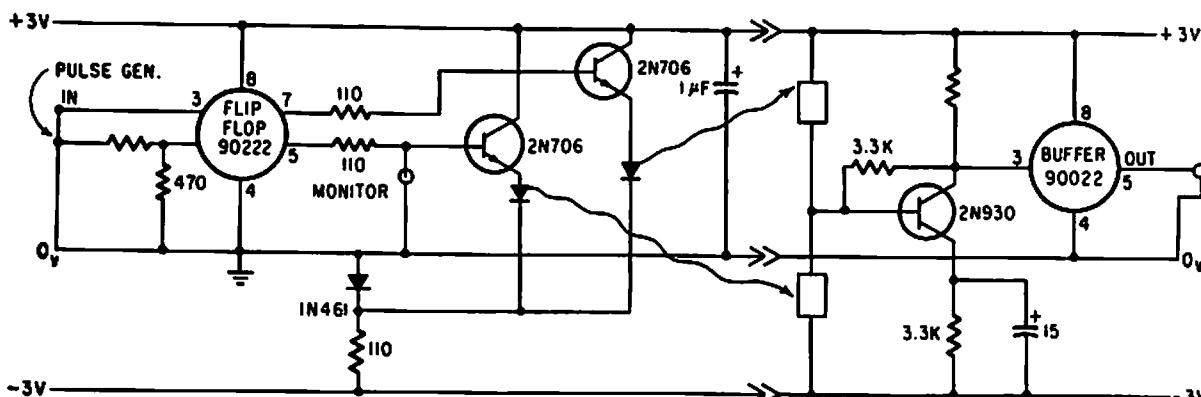
PHOTOREED OSCILLATOR—Gives good sine-wave output at reed frequency, using phototransistor to sense chopped light and transistor to boost output voltage. Can be used for remote control, telemetry, and logic functions.—Frequency-Sensitive Control Uses Light, *Electronics*, 34:36, p 88-91.



FLASH-ACTUATED SCR DELAY—Fractional-cycle light pulse fires glass-window scr at any phase angle, while values of circuit components determine number of cycles of conduction before turn off at zero current.—E. K. Howell, *Light-Activated Switch Expands Uses of Silicon-Controlled Rectifiers*, *Electronics*, 37:15, p 53-61.



LIGHT-ACTIVATED SCR TIME DELAY—Bootstrapped unijunction transistor interrupts load current at desired delay interval (determined by R1 and C1) after short pulse of light hits glass-window scr L8U.—E. K. Howell, *Light-Activated Switch Expands Uses of Silicon-Controlled Rectifiers*, *Electronics*, 37:15, p 53-61.



OPTICAL PUSH-PULL COUPLING—Two gallium arsenide light sources and two high-speed silicon photoconductors provide push-pull op-

tical coupling between integrated-circuit flip-flop and buffer, with two transistors in push-pull amplifier overcoming losses of optical

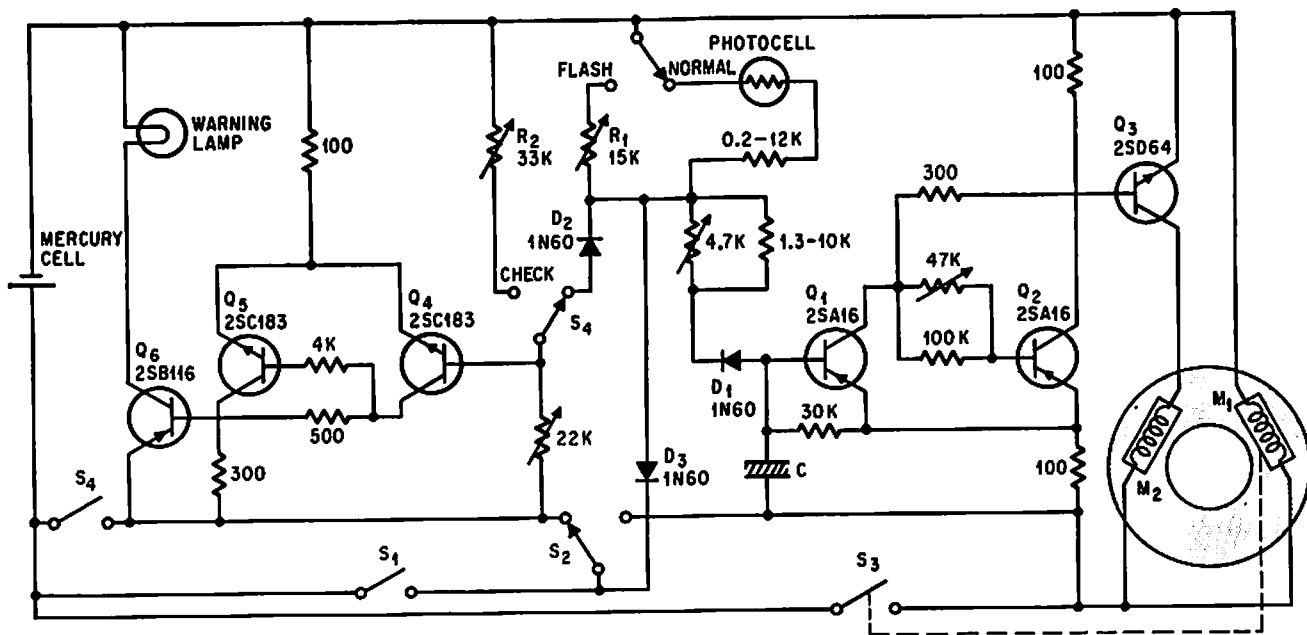
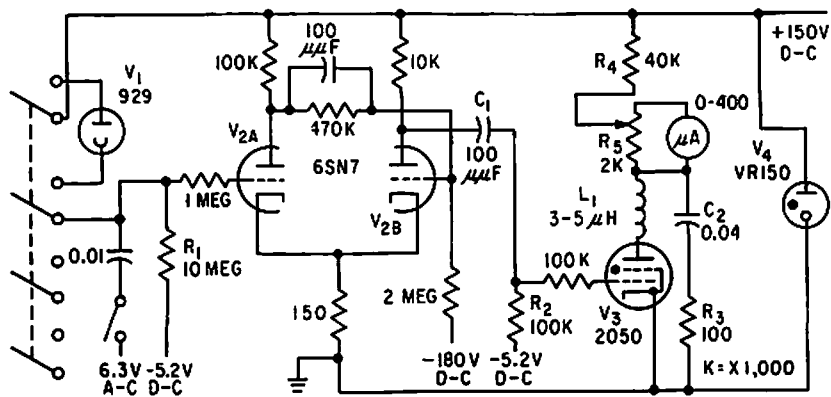
coupling.—T. E. Bray, *Switching With Light*, *Electronics*, 38:22, p 58-65.

CHAPTER 59

Photography Circuits

MOVIE CAMERA FRAME-RATE CHECKER—

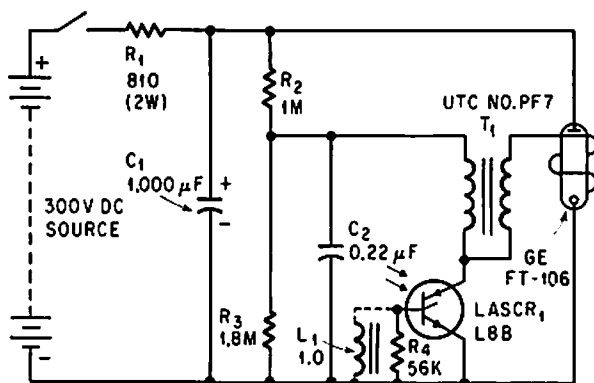
Gives exact frame rate at each instant. Lens is removed for test. Light beam is projected into camera, and reflected back from pressure plate in film gate each time shutter opens. Reflected beam is deflected into phototube that feeds Schmitt trigger. Differentiated output goes to thyatron in circuit of meter that reads frame rates directly from 5 to 64 frames per second. May also be used for checking projectors.—C. Owlett, Frame-Rate Checker for Motion-Picture Cameras, *Electronics*, 31:37, p 88-89.



ELECTRONIC CAMERA SHUTTER—Uses six transistors and photocell to vary both aperture and exposure time automatically according to incident light, from range of 1/30 sec at f/2 to 1/500 sec at f/16. Transistors are split into two groups, each having a

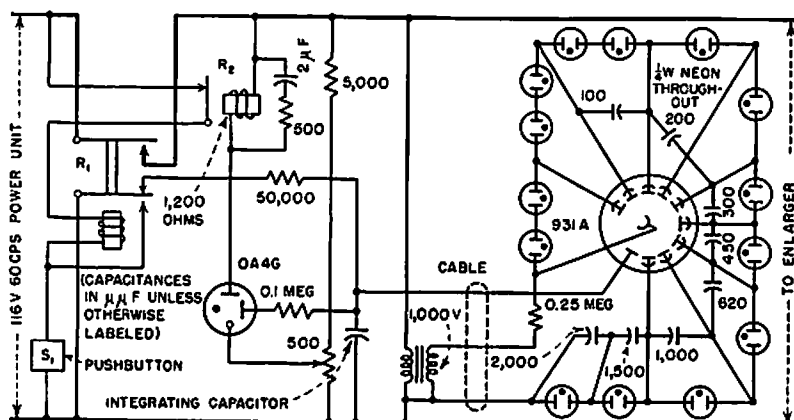
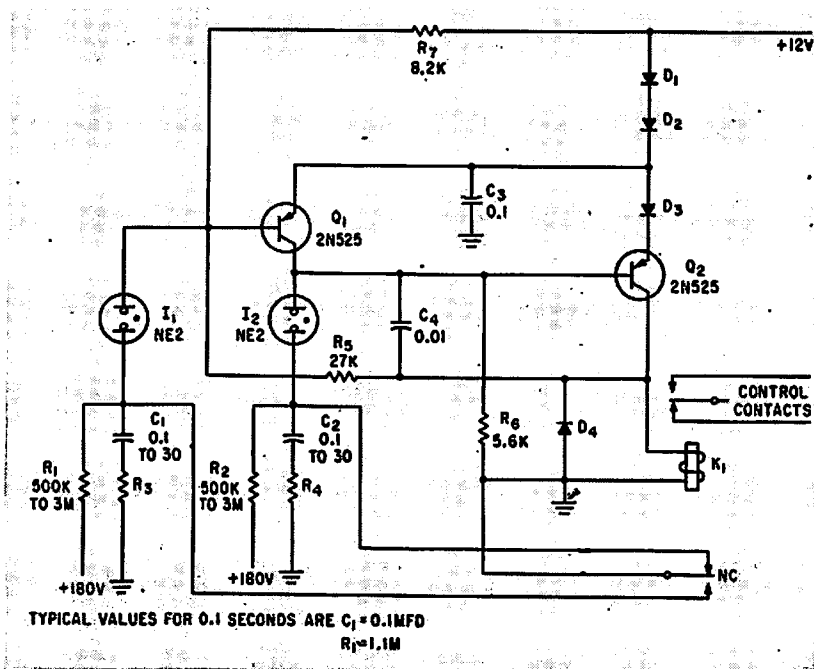
Schmitt trigger and output stage. One group warns photographer when light is insufficient, by turning on warning lamp, and other group drives solenoid that closes shutter at right instant. If light is adequate, depressing shutter button further moves S2 to timing

position, and closes S3 to energize solenoid M1 and open shutter to smallest aperture. Mechanical governor then gradually opens camera's combination shutter-iris until M2 snaps it closed under control of Q3.—Open and Shut Case, *Electronics*, 39:17, p 153-155.

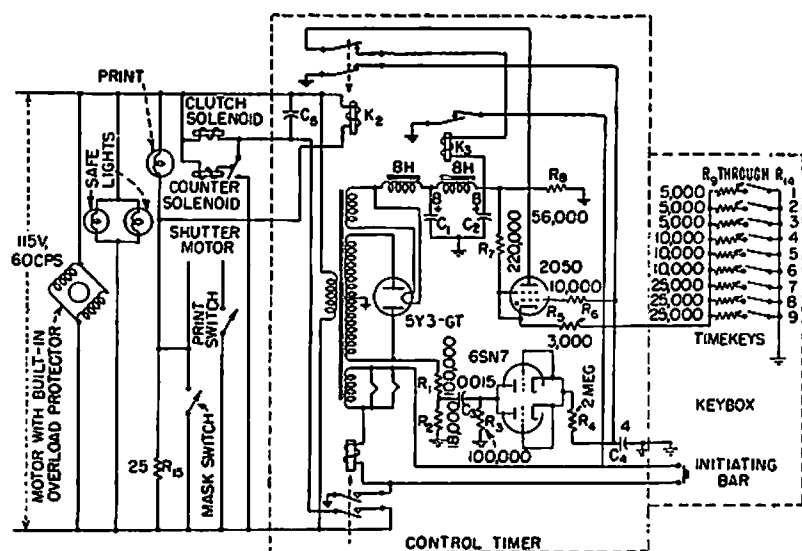
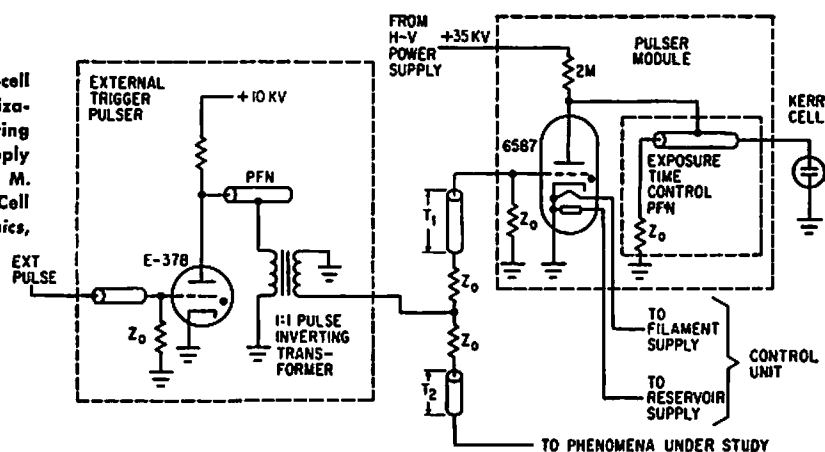


SLAVE FLASH—Addition of light-activated scr to ordinary flashgun gives fast-acting slave unit, with response speed of only few micro-sec to give perfect sync between master and slave. Use of L1 between gate and cathode of LASCR prevents triggering by high-level ambient light because L1 offers low impedance to ambient and high impedance to flash.—E. K. Howell, Light-Activated Switch Expands Uses of Silicon-Controlled Rectifiers, *Electronics*, 37:15, p 53-61.

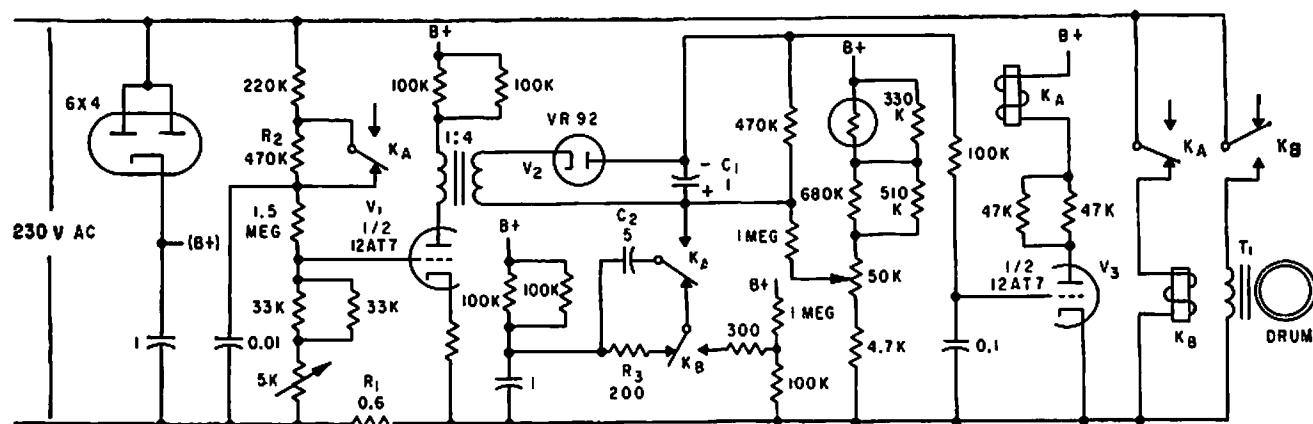
DATA RECORDING CAMERA TIMER—Controls exposure time and interval between exposures over ranges between 0.1 sec and 2 hours, independently of each other, by changing time constants with C1 and C2.—J. G. Fullerton, Bistable Circuit Times Camera Exposures, *Electronics*, 34:45, p 91.



KERR-CELL SHUTTER—High-voltage Kerr-cell pulser and parallel triggering synchronization give 5-nsec exposure, with triggering time jitter less than 1 nsec. Power supply must deliver 350-amp pulse at 35 kv.—S. M. Hauser and H. Quan, *Applying the Kerr Cell to Nanosecond Photography*, *Electronics*, 34:33, p 56-59.



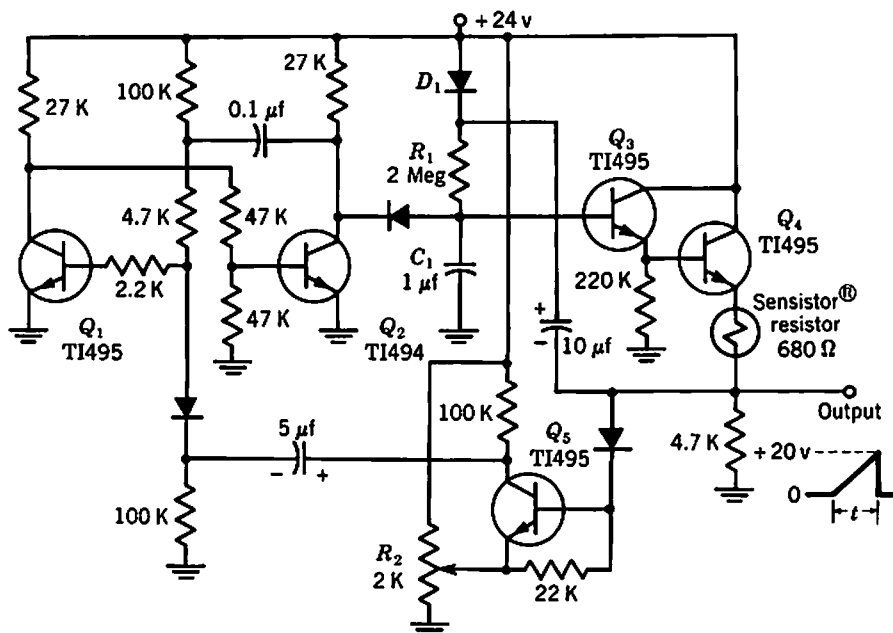
MAGNIFICATION-COMPENSATING DARK-ROOM TIMER—Pushbutton timer provides automatic compensation of exposure time with magnification of negative.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p. 296.



PHOTOGRAPHIC DRYER CONTROL—Copper drum, serving as single-turn shorted secondary of transformer, is heated by several thousand amperes of induced current. As

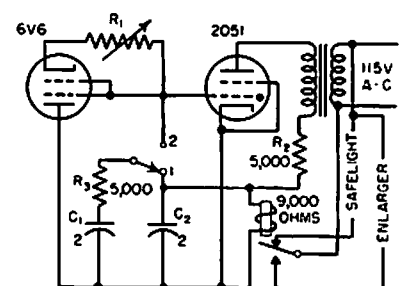
drum heats up, transformer primary current decreases. When desired temperature is reached, KA energizes and T1 is disconnected by KB.—D. A. Senior, Temperature Control for

**Hot Rollers in Industry, Electronics, 32:30,
p 40-42.**

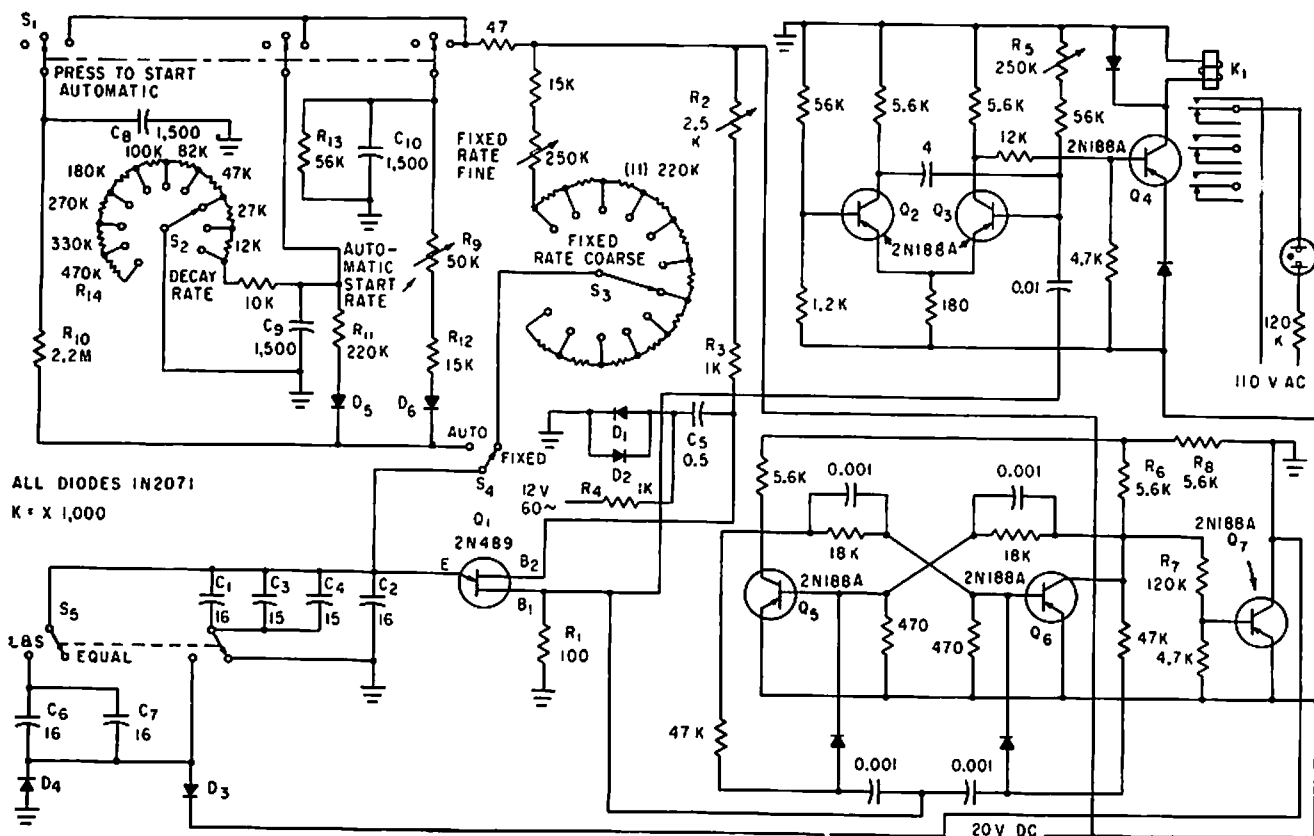


BOOTSTRAP TIMER—Q1 and Q2 form one-shot mvbr, with Q1 normally on. C1 charges toward 24 v through R1 and D1. Voltage on C1 is followed by Darlington circuit Q3-Q4. Feedback from Q4 to C1 gives nearly linear output voltage rise across emitter resistor of Q4, with length of time cycle varied by con-

trolling emitter voltage of Q5. Overall accuracy of circuit, from -50 to $+50^{\circ}\text{C}$, is 3%. Can give long time cycles for photographic and acid-bath control. All diodes are 1N2069. —Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 415.



EXPOSURE TIMER—Uses thyatron to stop relay chatter. Gives long time delays with relatively small capacitance. Ordinary volume control covers complete timing range. Circuit is backwards relay, in which coil is energized except during timing interval. Relay pulls in at 10 ma and drops out at 6 ma.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, N.Y., 1956, p 291.

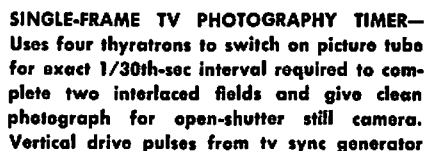
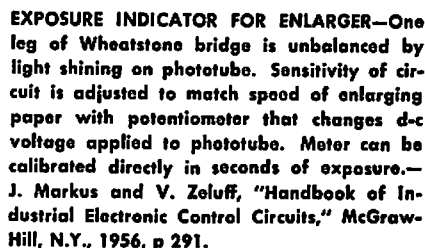


ALL DIODES 1N2071
K = X 1,000

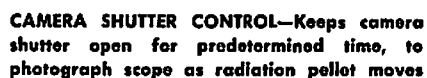
INSTRUMENTATION CAMERA TIMER—Varies camera exposure rates and durations automatically according to desired program. Triggering rate can be constant and adjustable

or variable for selected period between predetermined initial and final rates. Monostable mvbr Q2-Q3 determines length of triggering pulse that operates relay K1.—B. E. Bourne,

Variable-Program Triggering Source, *Electronics*, 33:37, p 76-77.



provide time-reference triggering. Stabilized high-voltage supply minimizes defocusing.—A. A. Tarnowski and K. G. Lisk, Timer Shutters CRT for Single Frame Photos, *Electronics*, 31:15, p 83-85.

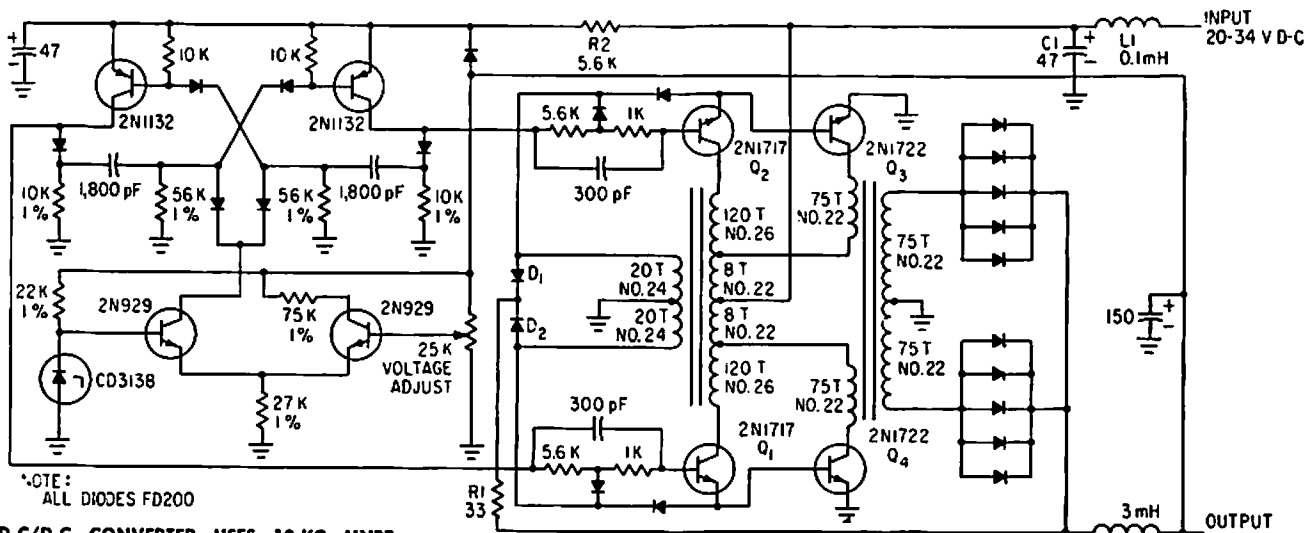
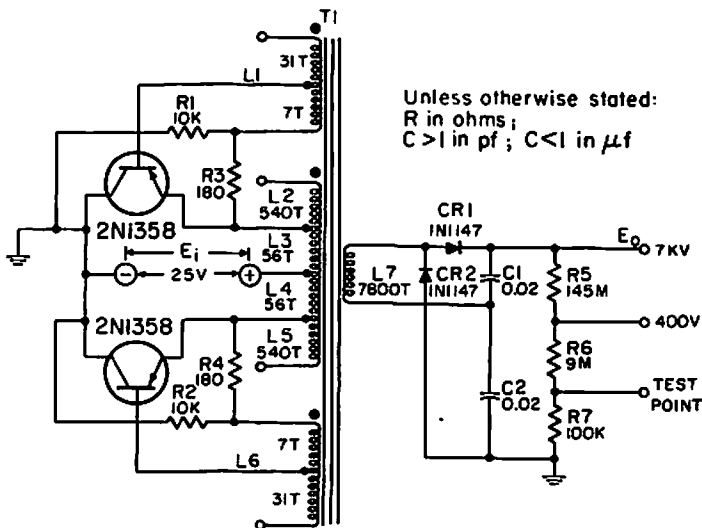


past a succession of radiation detector tubes facing conveyor belt. Pellet interrupts light beam to start sweep. RS and C1 control

reset time of sweep.—R. L. Nuckolls, Slow Sweep Generator Controls Camera Shutter, *Electronics*, 38:16, p 82.

Power Supply Circuits

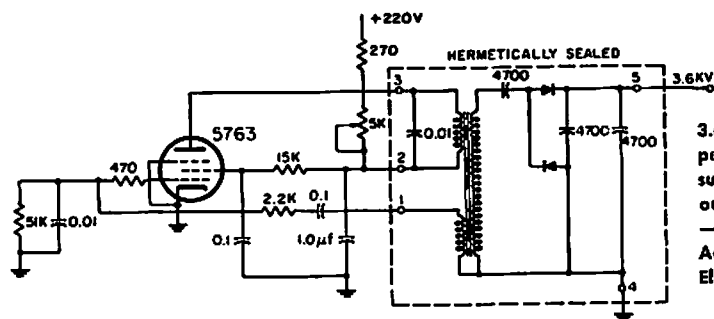
7-KV CRT SUPPLY—Provides high-voltage source for screen grid and final anode of 5 to 12-inch magnetic-deflection cathode-ray tubes in equipment having full or partial transistorization. Full-wave d-c to d-c converter, with transistor load connected between voltage source and emitter, permits attaching collectors to grounded or chassis-connected heat sink.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 6 (originally PC 202), p 6-2.



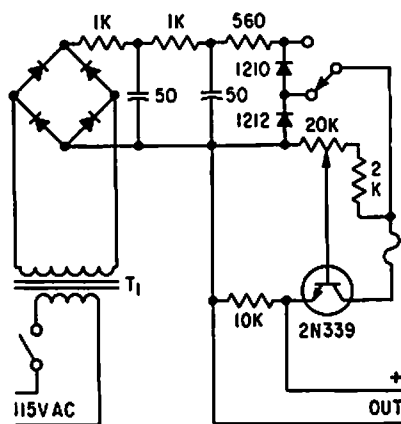
D-C/D-C CONVERTER USES 10-KC MVBR—
Free-running mvbr and square-hysteresis-loop
transformer together fire Q1 and Q2 alter-
nately to give constant frequency independ-

ent of changes in input voltage, while varying pulse width to give voltage regulation. Short-circuits cannot damage power supply.

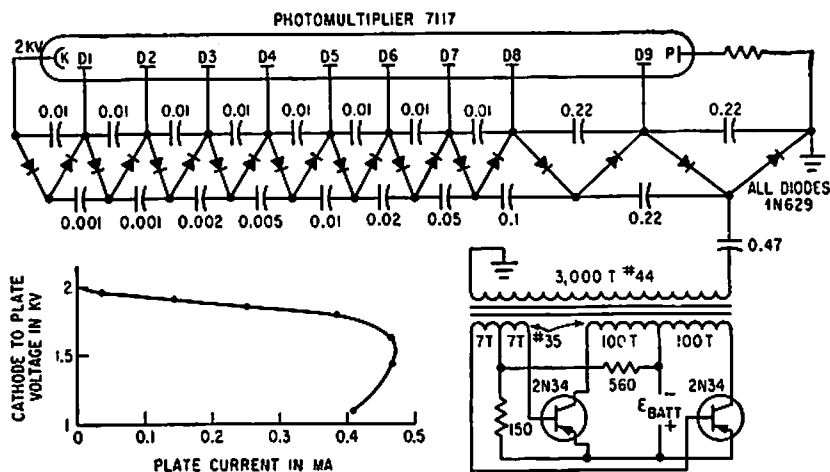
-E. Josephson, Satellite Power Supply has Variable Pulse Width, *Electronics*, 35:8, p 47-49.



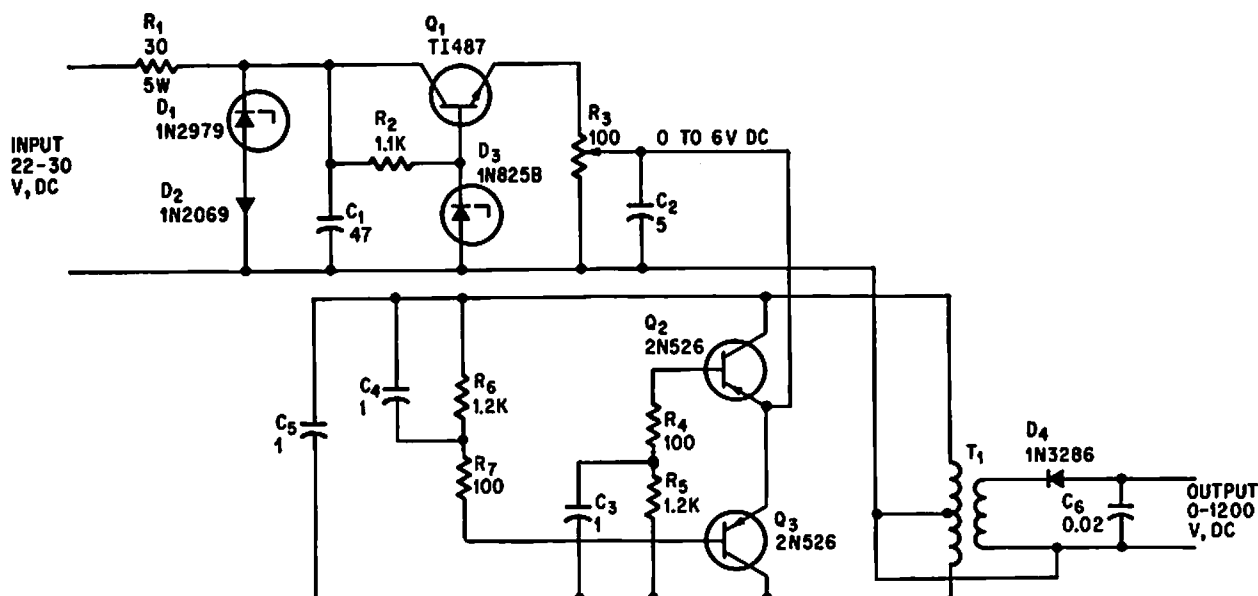
3.6-KV OSCILLATOR-TYPE SUPPLY—Single pentode in audio oscillator circuit provides sufficient power for step-up transformer and output rectifier filter. Used for dark-face crt. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-3.



ANALOG VOLTAGE SOURCE—Consists of bridge-rectifier supply with R-C filtering and zener diode regulation, feeding control potentiometer that is isolated from load by grounded-collector transistor. Used as analog voltage source for computer circuits.—E. R. James, Semiconductors Provide Analog Voltage Source, *Electronics*, 31:33, p 96-100.



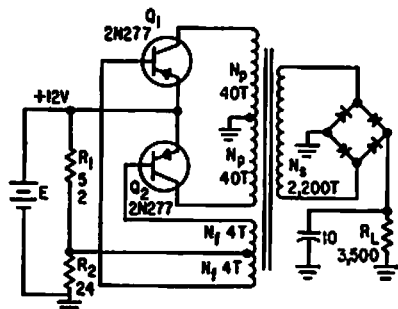
PHOTOMULTIPLIER SUPPLY—String of Cockcroft-Walton voltage doublers multiplies a-c output voltage of blocking oscillator to step up battery voltage to required 2 kv. Regulation is reasonably constant up to 0.4 ma plate current.—R. P. Rufer, Battery Powered Converter Runs Multiplier Phototube, *Electronics*, 33:28, p 51.



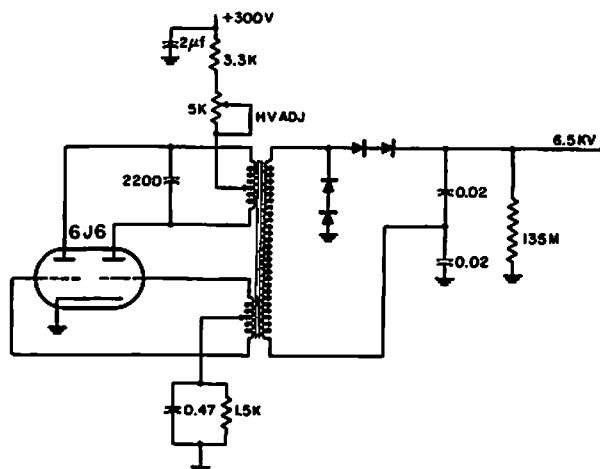
0-1,200 V REGULATED SUPPLY FOR PHOTOMULTIPLIER—Silicon diodes in R-C filter network gives 0.5% regulation over entire d-c

output range, with temperature coefficient only 0.1% of output voltage per deg C, for photomultiplier stage in airborne equipment.

—J. G. Poddia, Network Filters Stabilize d-c Supply Over Wide Range, *Electronics*, 37:18, p 83.

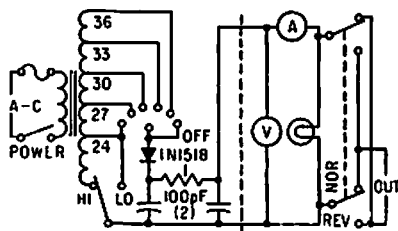


OSCILLATOR-TYPE SUPPLY—Article gives basic design equations for d-c to d-c power supply using power transistors. Efficiency is up to 90%. D-c output voltage is 590 v for 3,500-ohm load.—T. Hamm, Jr., Equations for Designing Transistor Power Supplies, *Electronics*, 32:43, p 122-124.

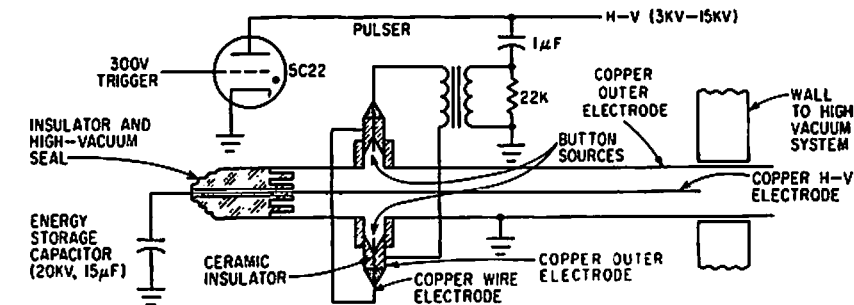


6.5-KV TWIN-TRIODE A-F OSCILLATOR CRT SUPPLY—Oscillator develops square wave because of saturable square-loop core material of transformer. Diodes eliminate extra load placed on oscillator by high-vacuum rectifier

tube filaments.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-5.

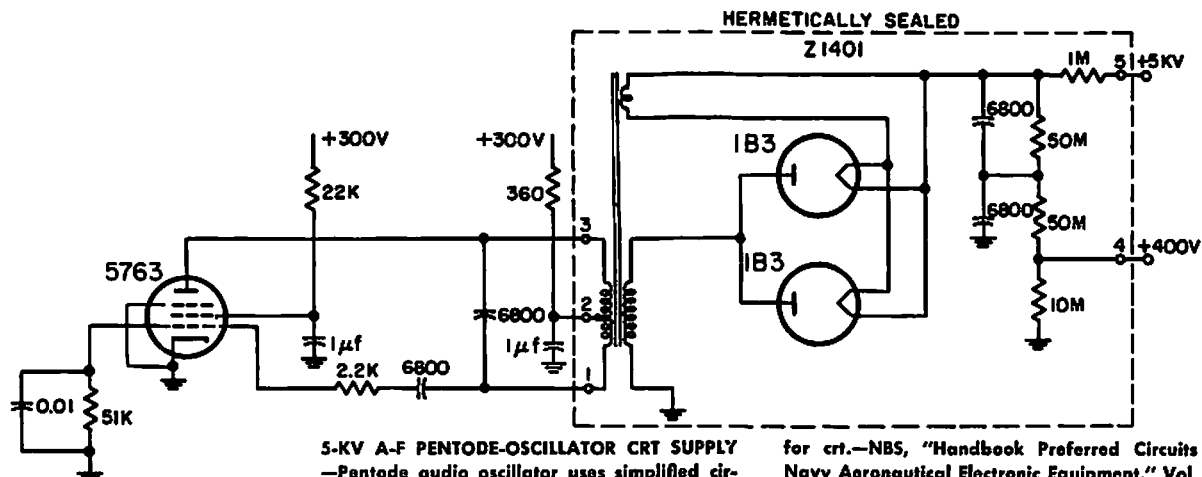


TRANSISTOR-TESTING SUPPLY—Six taps on transformer, plus range switch that transfers negative bus to 24-v tap, provide choice of nine constant outputs from 3 to 36 v d-c.—F. W. Kear, Laboratory Supply for Transistors, *Electronics*, 35:30, p 55-57.



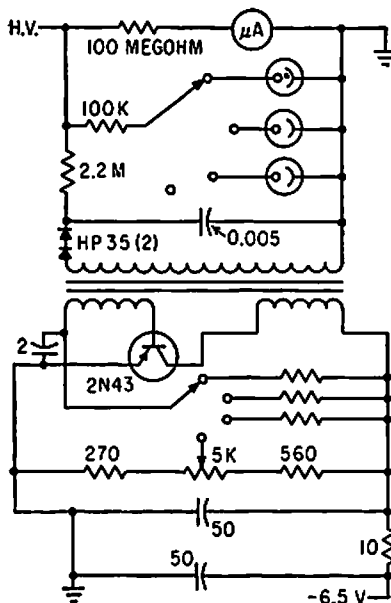
TRIGGERED THYRATRON PULSER—Coaxial rail gun generates high-velocity copper plasma when triggered by series-connected button guns energized through transformer by thyatron pulser. Vaporized copper from buttons

shorts main 15-mfd capacitor, vaporizing inner copper high-voltage electrode.—M. F. Wolff, Plasma Engineering—Part 1: Generating and Heating Plasma, *Electronics*, 34:28, p 47-53.

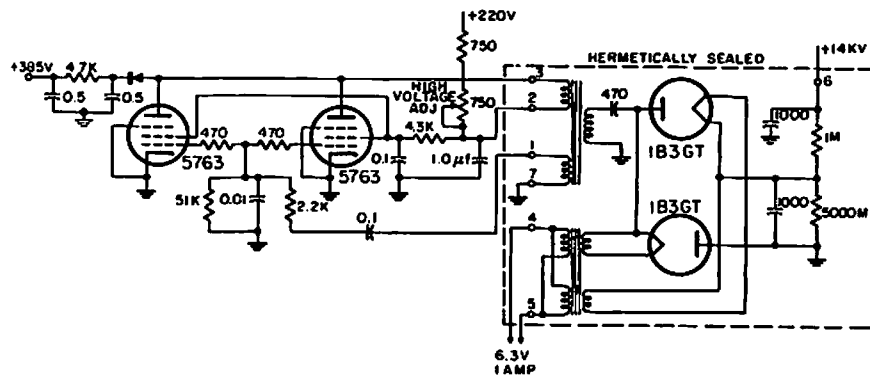


5-KV A-F PENTODE-OSCILLATOR CRT SUPPLY—Pentode audio oscillator uses simplified circuit to furnish only second anode potential

for crt.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-4.

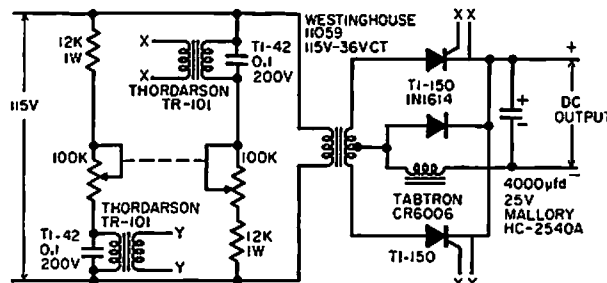


GEIGER-MULLER SUPPLY—Uses blocking oscillator to provide three stabilized levels of high voltage, at 900, 1,000, and 1,100 v, for G-M tube. Corona discharge tubes are switched in to provide regulation.—F. E. Armstrong, *Battery Powered Portable Scaler*, *Electronics*, 33:19, p 74-75.



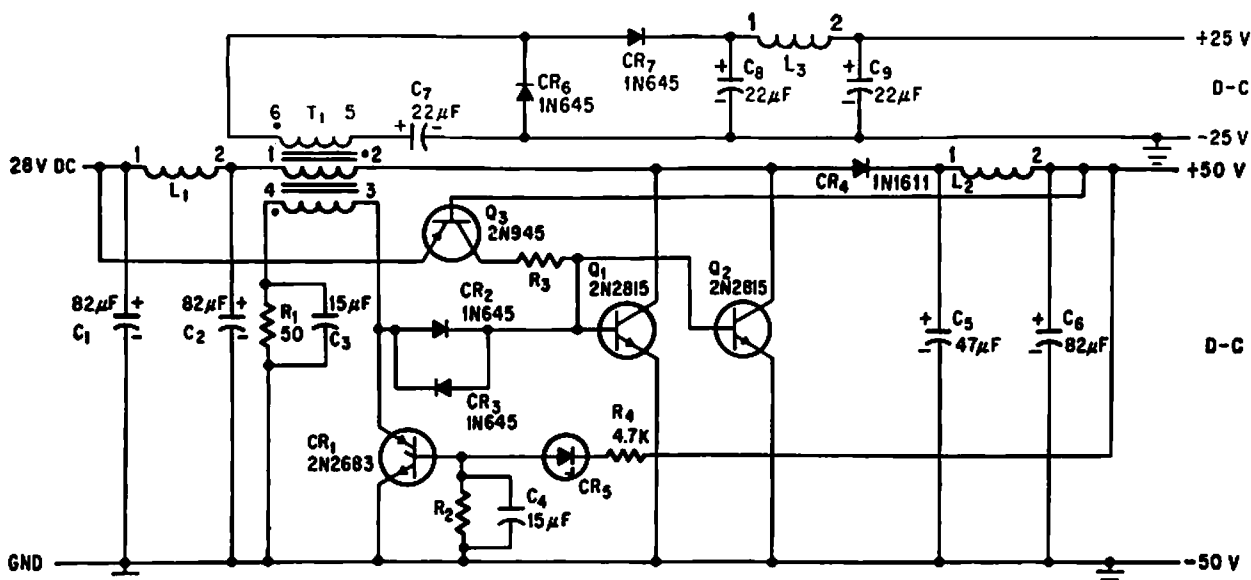
14 KV AND 385 V FOR DARK-FACE CRT—Uses two pentodes in parallel in a-f oscillator to provide sufficient power for final anode potential in oscillator-type supply.—NBS,

"Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, *Electron Tube Circuits*, 1963, p N14-3.



VARIABLE REMOTE POWER SUPPLY—Permits varying output d-c voltage of scr power supply without changing a-c input voltage. Conduction time of scr's during each half-cycle determines average power delivered to load. Conduction time is controlled with pulse gat-

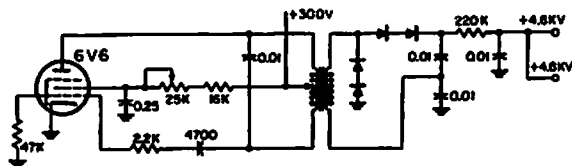
ing circuit that is synchronized with a-c line and is phase-variable. Provides maximum output of 60 amp at 20 v.—B. F. Gilbreath, *Variable High Current Remote Power Supply*, *EEE*, 10:12, p 27-28.



D-C/D-C REGULATED SUPPLY—Efficiency is 93% in converting 28 v d-c to 25 and 50 v d-c for telemetry transmitter. Regulation is achieved by storing energy in magnetic field of coil

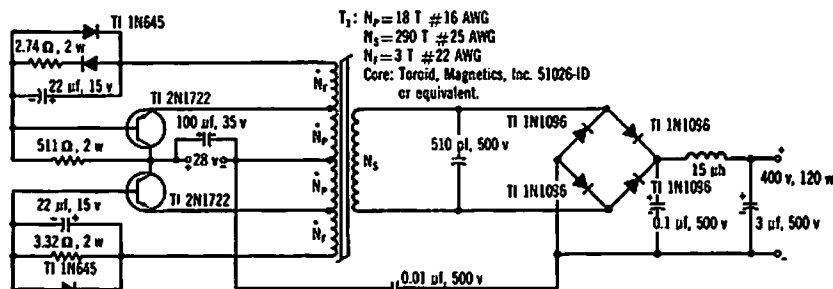
during half of each switching cycle created by transistors Q1 and Q2 after Q3 initiates switching cycle. SCR CR1 and diode CR5 control percentage of time switching transistors

are on.—N. Downs and B. van Sutfin, *Solid-State Transmitter Ready for UHF Telemetry*, *Electronics*, 37:17, p 76-80.



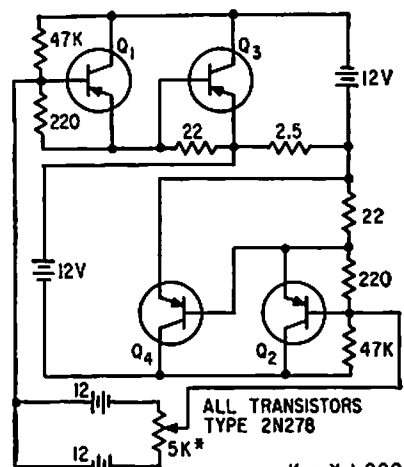
4.8-KV OSCILLATOR-TYPE CRT SUPPLY—One of earliest circuits in which a-f sine-wave oscillator was used as power source. Filter capacitors are significantly smaller than in

conventional line-transformer supplies.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-2.



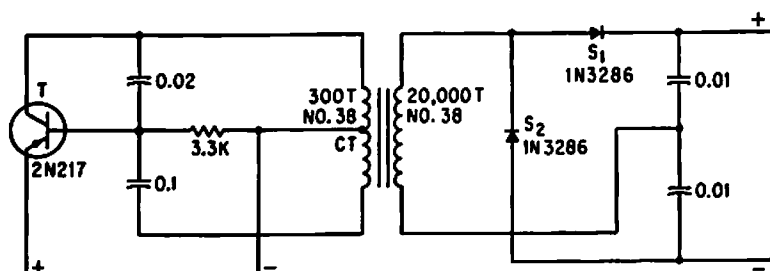
120-WATT D-C/D-C CONVERTER—Circuit boosts 28 v d-c input to 400 v d-c with 85% efficiency, drawing 5 amp and operating at

10 kc.—120-Watt D-C/D-C Converter Operates From -55° to $+125^{\circ}$ C, *Electronics*, 36:2, p 15.

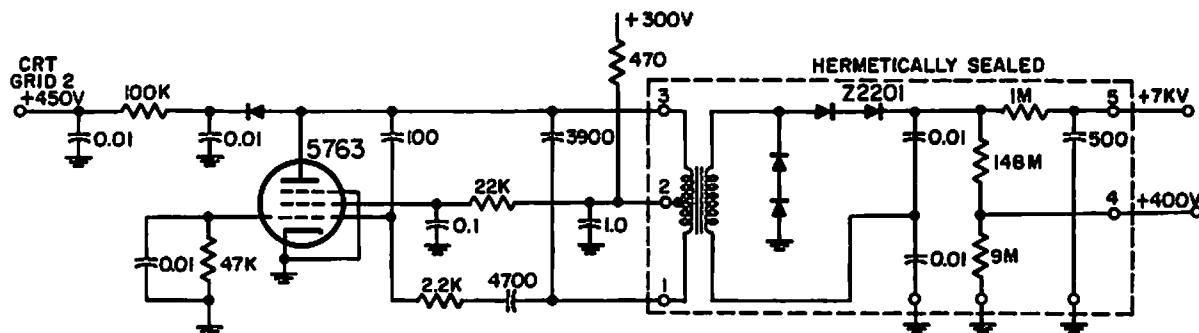


*10-TURN HIGH-RESOLUTION POT

DUAL-POLARITY VARIABLE D-C SUPPLY—Diagonally symmetrical power transistor circuit permits smooth load current variation over range of several amperes at either polarity. Rectifier supply can be used in place of storage batteries. Maximum current drain from two 12-v dry cells in 5K potentiometer control circuit is 7 ma.—R. R. Bockemuhl, Transistor Rectifier Gives D-C of Either Polarity, *Electronics*, 32:25, p 76.



5,000 V D-C FROM 26 V D-C—Uses transistor as sinusoidal oscillator. Voltage-doubling capacitors keep ripple below 0.01%.—R. D. Morrow, Inexpensive Converter Gives 5,000 Volts D-C, *Electronics*, 35:28, p 54.



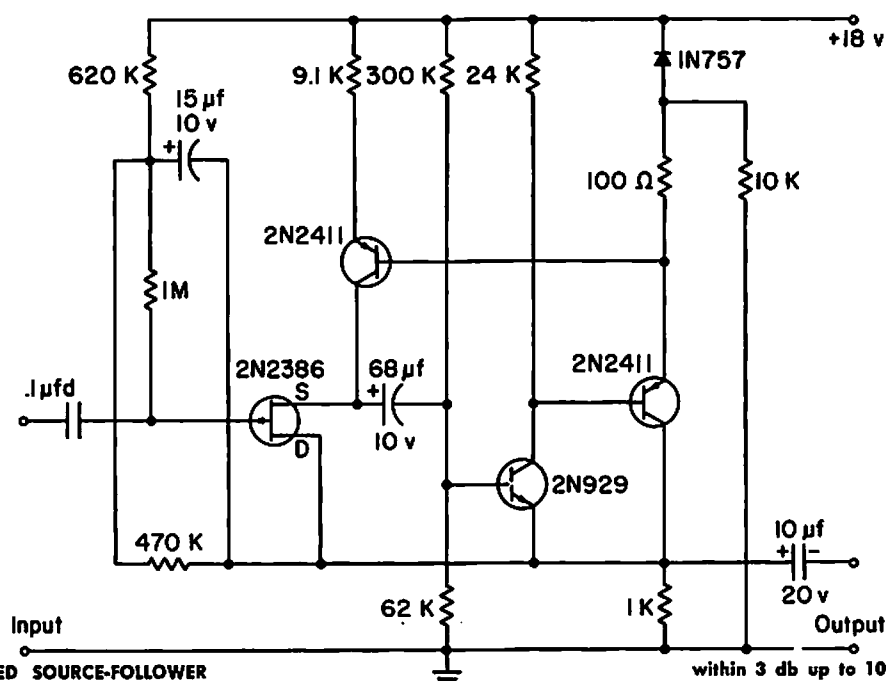
7 KV AND 450 V OSCILLATOR-TYPE CRT SUPPLY—Pentode audio oscillator feeds hermetic-

ally sealed transformer-rectifier-filter unit.—NBS, "Handbook Preferred Circuits Navy

Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-4.

CHAPTER 61

Preamplifier Circuits

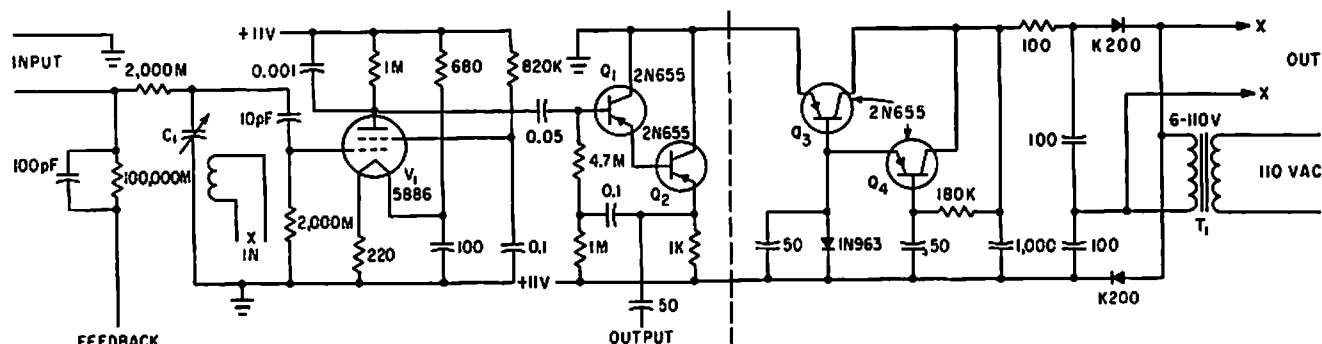


FET-PNP BOOTSTRAPPED SOURCE-FOLLOWER

—Drain and gate divider are bootstrapped in phase with source, to reduce input capacitance of fet to minimum so only real part of input impedance is seen at high frequen-

cies, in unity-gain high-input-impedance wide-band preamplifier. Low-frequency input impedance is 100 meg. Frequency response is

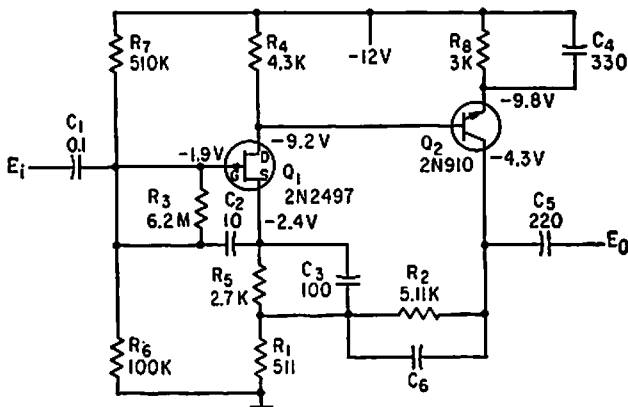
within 3 db up to 10 Mc for 50-ohm generator resistance, to 1 Mc for 100,000 ohms, and to 0.1 Mc for 1 meg.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 138.



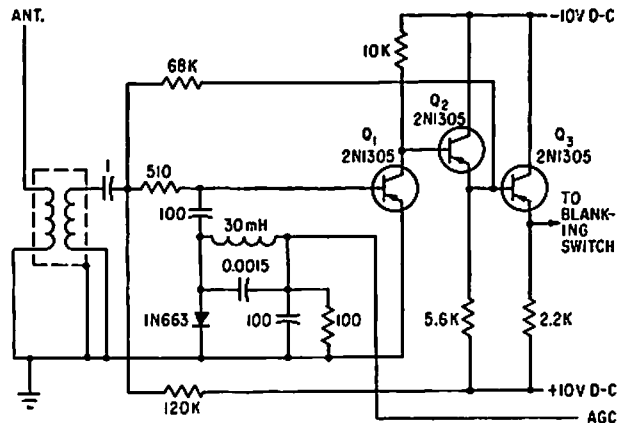
ELECTROMETER PREAMP—Transformer T1 feeds drive coil of vibrating capacitor C1 used in place of conventional chopper for

measuring picoampere currents. Output of electrometer, taken from Q2 of preamp, goes to amplifier that is source of feedback signal.

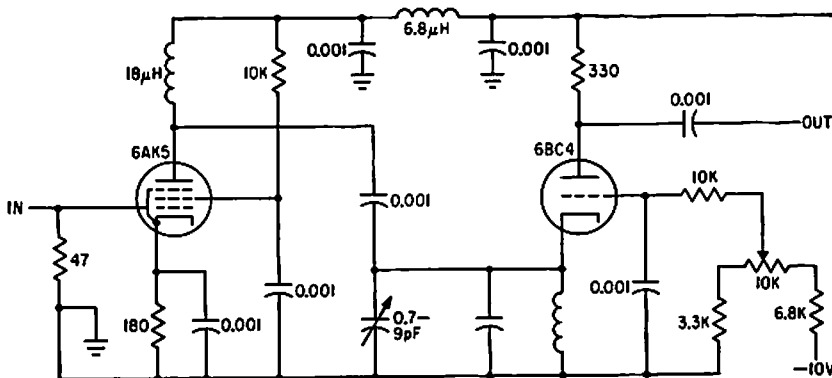
—V. J. Caldecourt, Using a Vibrating Capacitor as an Electrometer Input, *Electronics*, 35:14, p 48-50.



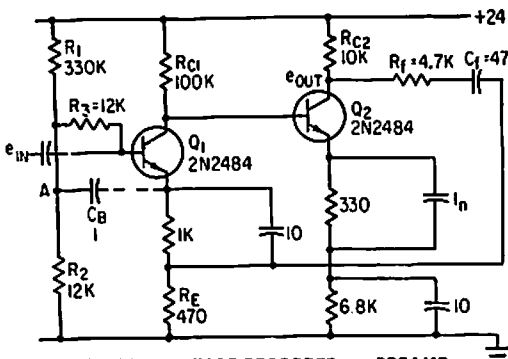
LOW-NOISE FET PREAMPLIFIER—For 600-ohm source, 3-db response is 0.5 cps to 700 kc, with voltage gain of 10.5, using common-emitter direct-coupled amplifier stage after for stage, with 26 db of feedback. With 1-meg source, upper limit is 20 kc.—E. G. Fleener, Low-Noise Preamplifier Uses Field-Effect Transistors, *Electronics*, 36:15, p 67-69.



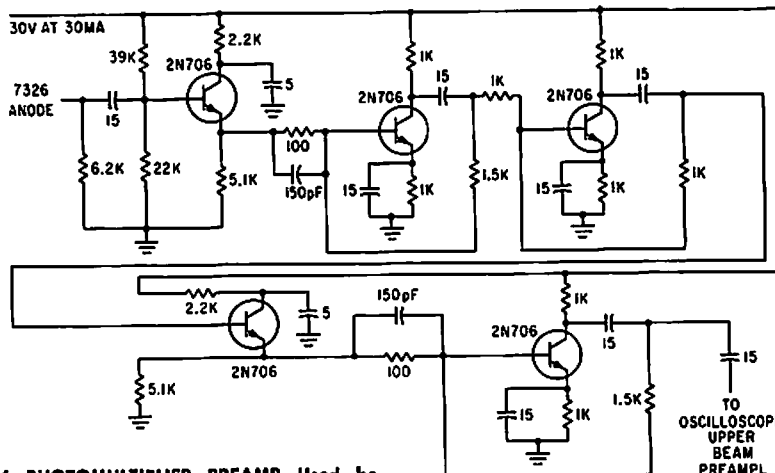
VLF PREAMP WITH AGC—Fast-recovery computer diode acts as pure variable resistance shunting base of Q1 to ground, for full agc control without phase shift in output signal. In frequency range of 13 to 24 kc, phase shift is less than 0.25 microsec over 40-db input signal range.—J. D. Echols, Calibrating Frequency Standards with VLF Transmissions, *Electronics*, 35:17, p 60-63.



ELECTRONICALLY CONTROLLED BANDWIDTH—For search radar, potentiometer adjusts bias on 6BC4 tube of 30-Mc i-f preamp to vary bandwidth over range of 200 kc to 15 Mc.—Variable Bandwidth Preamplifier Electronically Varied Between 15 Mc and 200 Kc, *Electronics*, 35:2, p 102.



LOW-NOISE TAPE-RECORDER PREAMP—Closed-loop gain is 20 db from 100 cps to nearly 8 Mc for source impedance of 1,000 to 6,000 ohms.—J. J. Rado, Designing Input Circuits with Lowest Possible Noise, *Electronics*, 36:31, p 46-49.



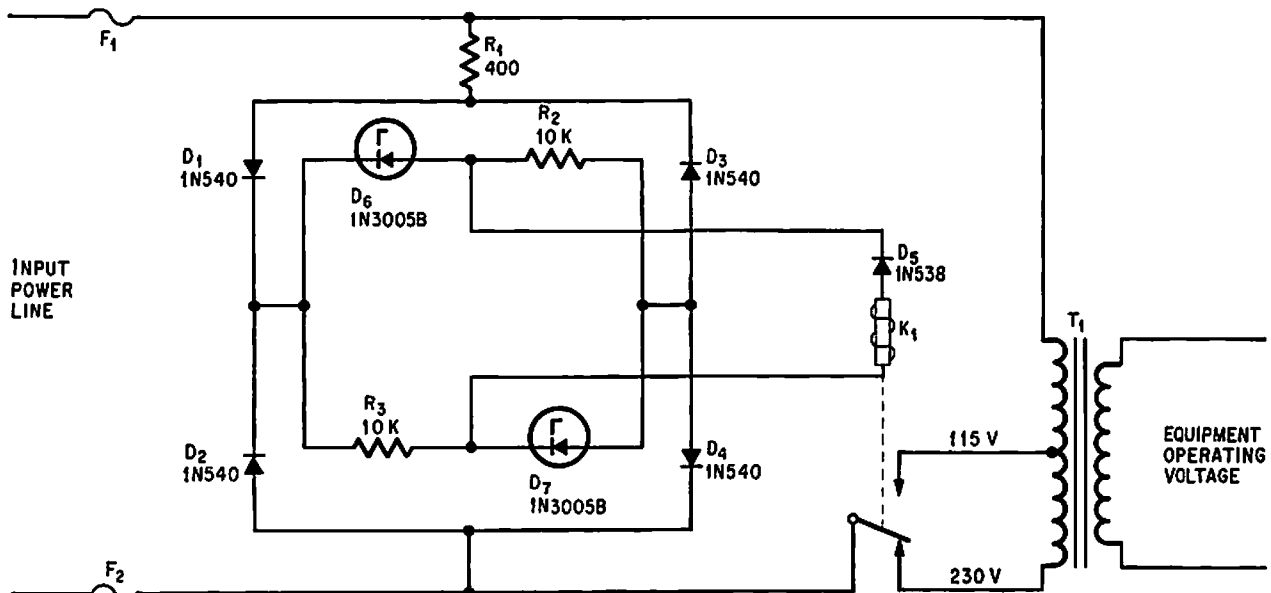
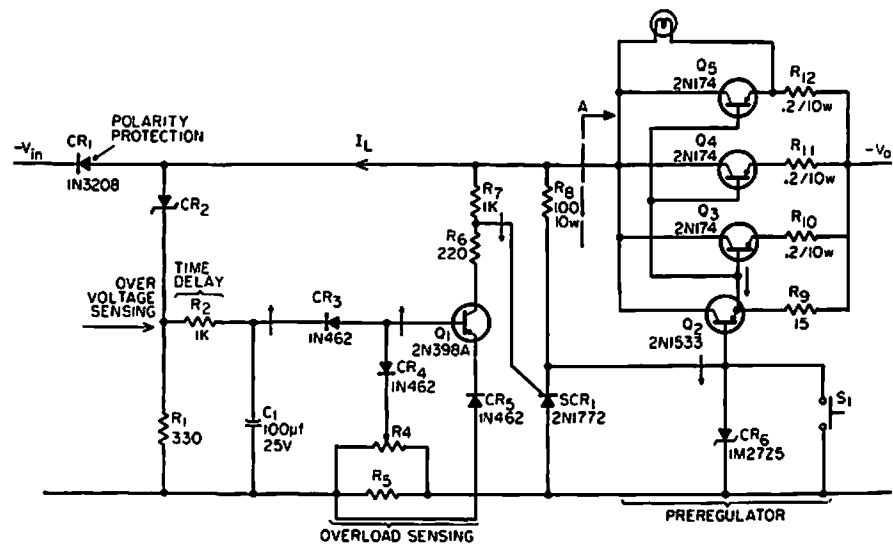
7326 PHOTOMULTIPLIER PREAMP—Used between type 7326 photomultiplier and dual-beam oscilloscope in receiver of optical ranging system. Voltage gain is 100 for output dynamic range of 1.5 v, but noise figure is

only about 14 db.—M. L. Stitch, E. J. Woodbury and J. H. Morse, Optical Ranging System Uses Laser Transmitter, *Electronics*, 34:16, p 51-53.

CHAPTER 62

Protection Circuits

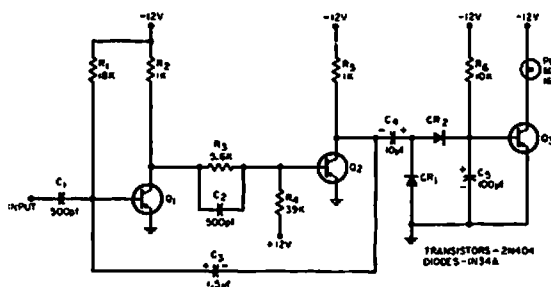
SUPPLY OVERLOAD AND REVERSE-POLARITY PROTECTION—Uses signal from Q1 to trigger SCR1, which turns off series-pass transistor when overload reaches 15 amp. Will also provide limiting of output voltage at 25 v, input overvoltage protection at 32 v, and input reverse-polarity protection by CR1.—J. J. Rado, Versatile SCR Protection for Power Supplies, *EEE*, 13:8, p 56–62.



POWER TRANSFORMER SWITCHING RELAY SENSES LINE VOLTAGE—For 230-v line voltage, zener diodes back-bias diode D5, preventing energization of relay. For 115 v, diode conducts and relay closes, connecting line to 115-v tap of transformer.—L. K. Moyer,

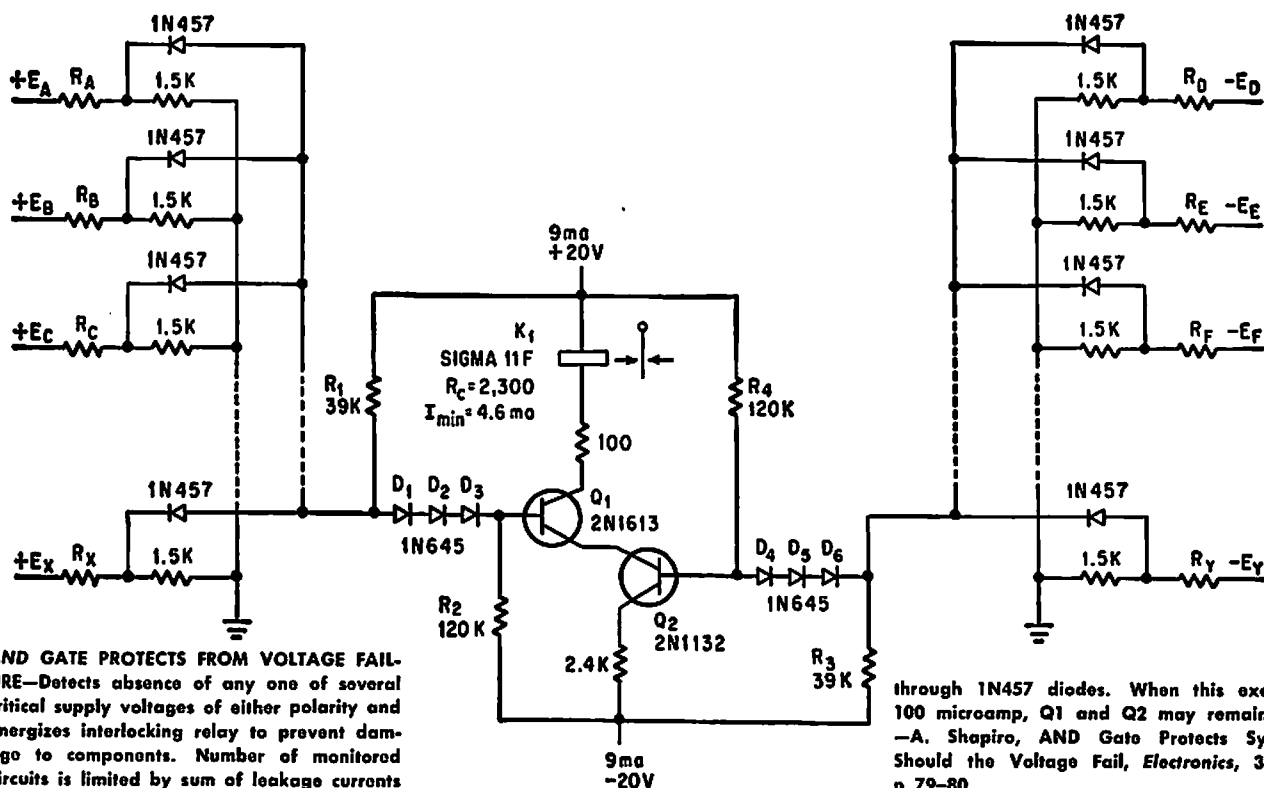
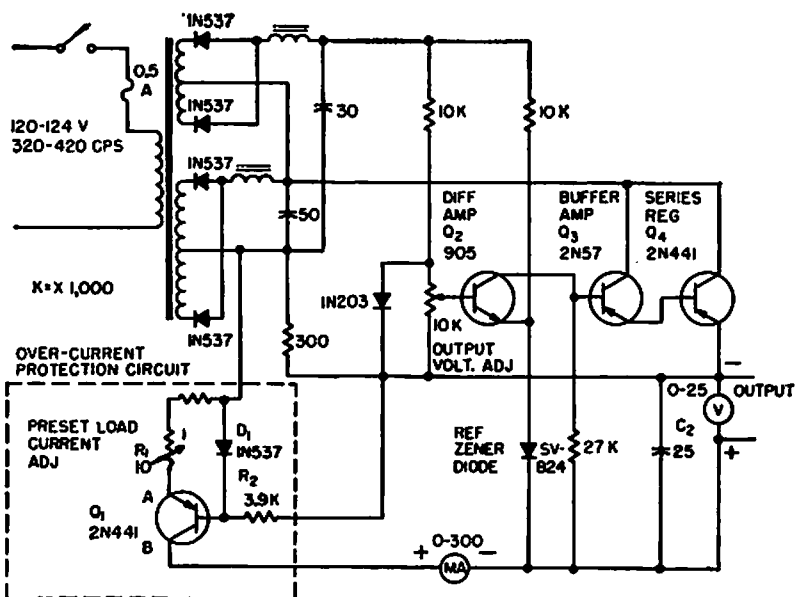
venting energization of relay. For 115 v, diode conducts and relay closes, connecting line to 115-v tap of transformer.—L. K. Moyer,

Circuit Always Applies Correct Operating Voltage, *Electronics*, 37:25, p 77.



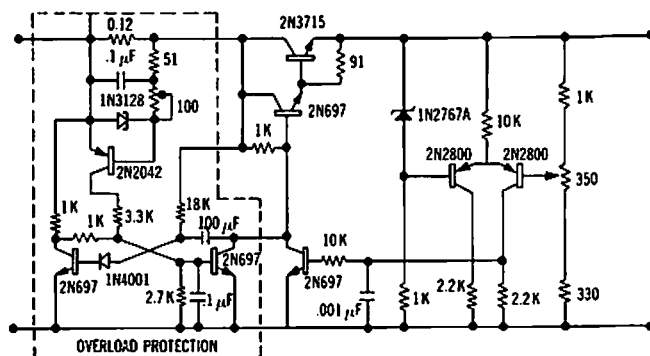
MISSING-PULSE DETECTOR—Turns on lamp if one of input pulses in continuous pulse input is missing. Pulses are very narrow (4 microsec wide) and 50 microsec apart for low duty cycle; Q1 and Q2 form pulse stretcher that increases width to about 40 microsec. In absence of stretched pulse, Q3 loses its bias and is turned on, making lamp light.—C. Gerston, Missing-Pulse Detector for Narrow Pulses, *EEE*, 12:8, p 72-74.

OVERLOAD PROTECTION—Transistor Q4 in conventional series regulated power supply is protected against charging current of load capacitance C2 by sharp current-limiting-characteristic protection circuit that operates statically, without need for resetting, in preset range of from 50 to 250 ma, and provides instantaneous response when regulator transistor is overloaded. Line regulation is 0.001% and load regulation is 0.002%.—H. D. Ervin, Transistor Power Supply has Overload Protection, *Electronics*, 31:25, p 74-75.

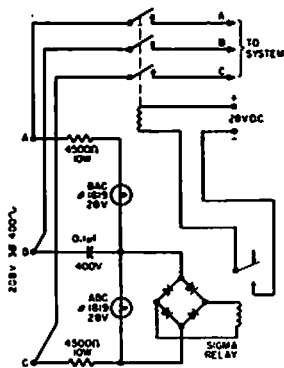


AND GATE PROTECTS FROM VOLTAGE FAILURE—Detects absence of any one of several critical supply voltages of either polarity and energizes interlocking relay to prevent damage to components. Number of monitored circuits is limited by sum of leakage currents

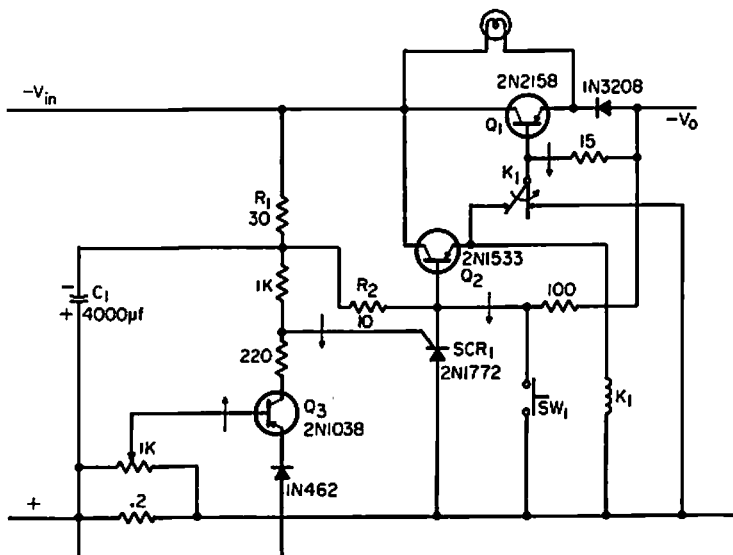
through 1N457 diodes. When this exceeds 100 microamp, Q1 and Q2 may remain on.—A. Shapiro, AND Gate Protects System Should the Voltage Fail, *Electronics*, 39:14, p 79-80.



SERIES REGULATOR WITH OVERLOAD PROTECTION—Tunnel diode and transistor serve as overload sensing circuit used to trigger monostable mvbr, to protect series-pass transistors against overload. Circuit resets continuously after overload until trouble is cleared. Protection is adequate for resistive loads only.—J. Takesuye and H. Weber, "Silicon Power Transistors Provide New Solutions to Voltage Control Problems," Motorola Application Note AN-163, Aug. 1965.

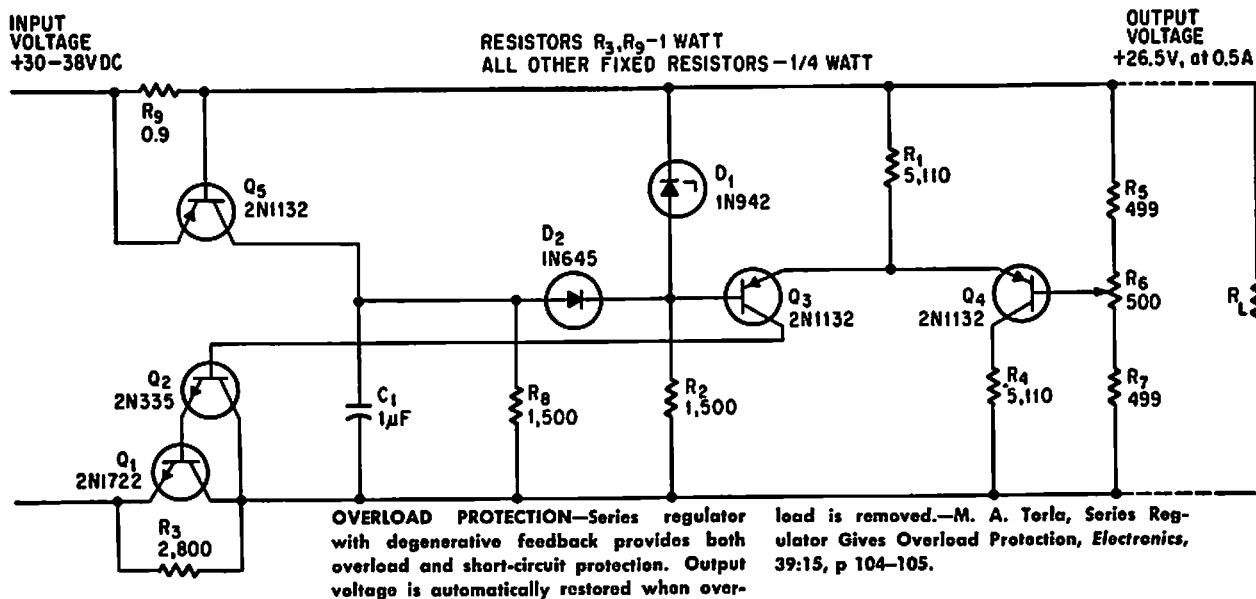


REVERSE-PHASE PROTECTION—Used to protect navigation system against damage if phase rotation is reversed by careless or accidental power transfers. With correct rotation, lamp ABC lights and relay closes control circuit to allow operation. With reverse phase rotation, lamp BAC lights and relay does not close.—J. J. Pirch, Simple Reverse-Phase Protection, *EEE*, 11:12, p 26.

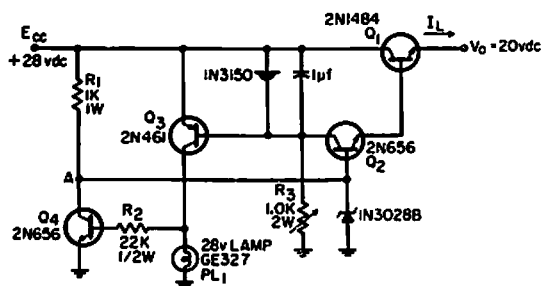


OVERLOAD PROTECTION WITH RIPPLE CLIPPING—Power transistor interrupts load when current exceeds safe limit, and also serves as

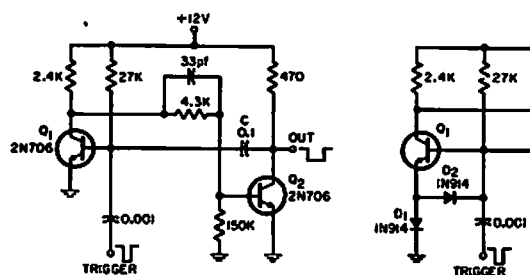
part of ripple clipper.—J. J. Rado, Versatile SCR Protection for Power Supplies, *EEE*, 13:8, p 56-62.



OVERLOAD PROTECTION—Series regulator with degenerative feedback provides both overload and short-circuit protection. Output voltage is automatically restored when over-

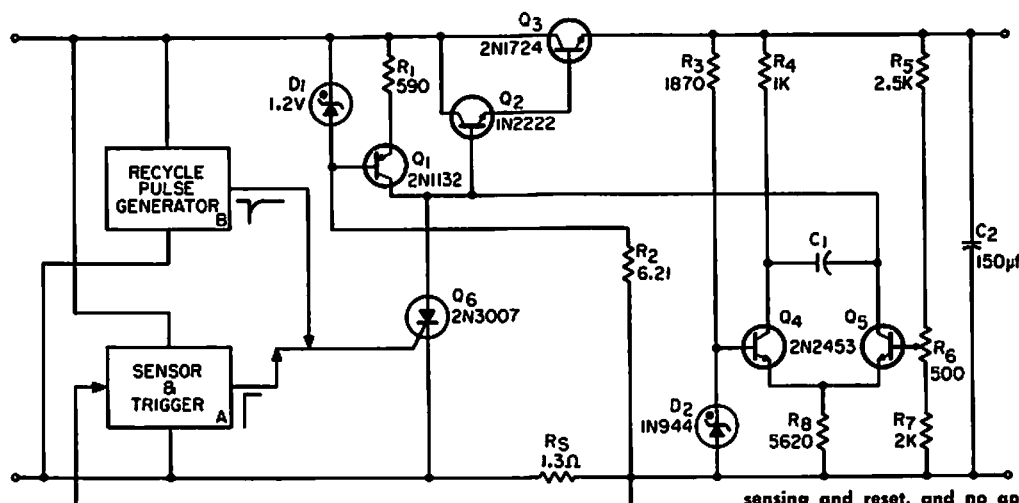
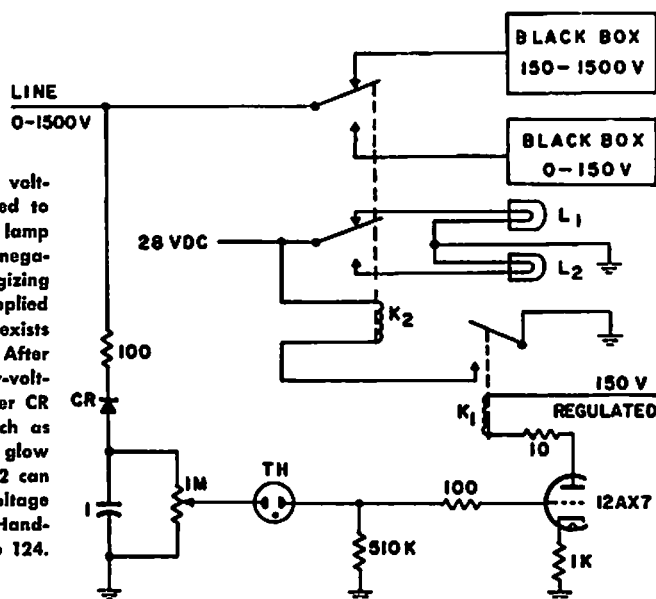


OVERLOAD PROTECTION WITH TD—Tunnel diode-transistor level detector reduces load current of series voltage regulator to zero when preset limit is exceeded. Protective circuitry consists of TD1, PL1, R2, R3, Q3, and Q4.—G. E. Bloom, Overload Protection with a Tunnel Diode, *EEE*, 12:10, p 60 and 75.



TRANSISTOR PROTECTION IN MONO—In typical transistor monostable mvbr (A), transient base-to-emitter voltage caused by discharging C often surpasses transistor rating. Two diodes alleviate trouble (B). Circuit produces width of 200 microsec.—J. S. Milkuckis, Base-Emitter Protection in Monostable Multivibrators, *EEE*, 12:9, p 63.

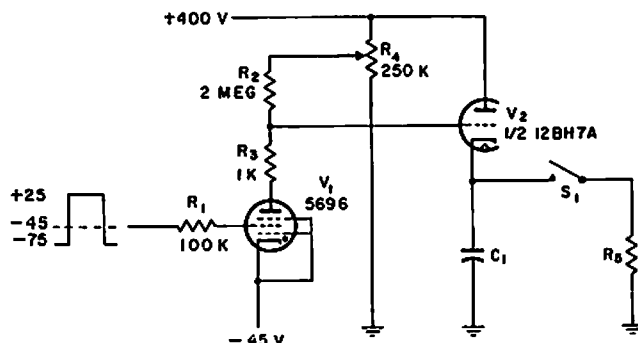
OVERVOLTAGE INDICATOR—Routes line voltage automatically to black box designed to handle it. Potentiometer is set so neon lamp TH fires at 152 v. This applies high negative voltage to grid of tube, deenergizing relays K1 and K2 so line voltage is applied to upper black box. This condition also exists when system is first connected to line. After tube is warmed up, it switches to low-voltage box if line is below 152 v. Rectifier CR is any 2,000-v low-current rectifier, such as Sarkes Tarzian 126-100-H-Q. NE68 glow lamp TH must be in lightproof box; NE2 can be used only if suitably aged.—Overvoltage Indicator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 124.



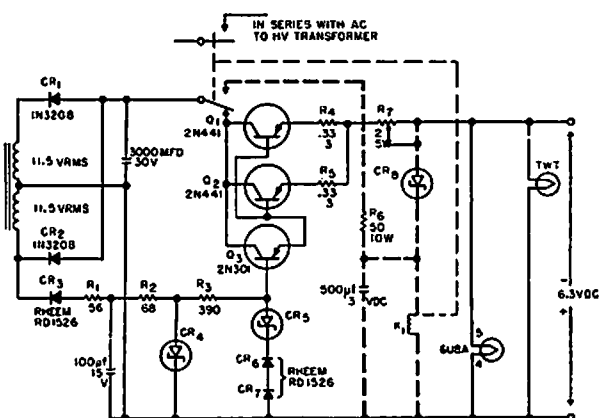
BASIC GTO FOR POWER SUPPLY OVERLOADS—Gate-turnoff scr (GTO) provides superior overload protection for d-c power

supplies. Chief advantages are elimination of turn-on current surges, switching off within 30 microsec of overload, automatic load

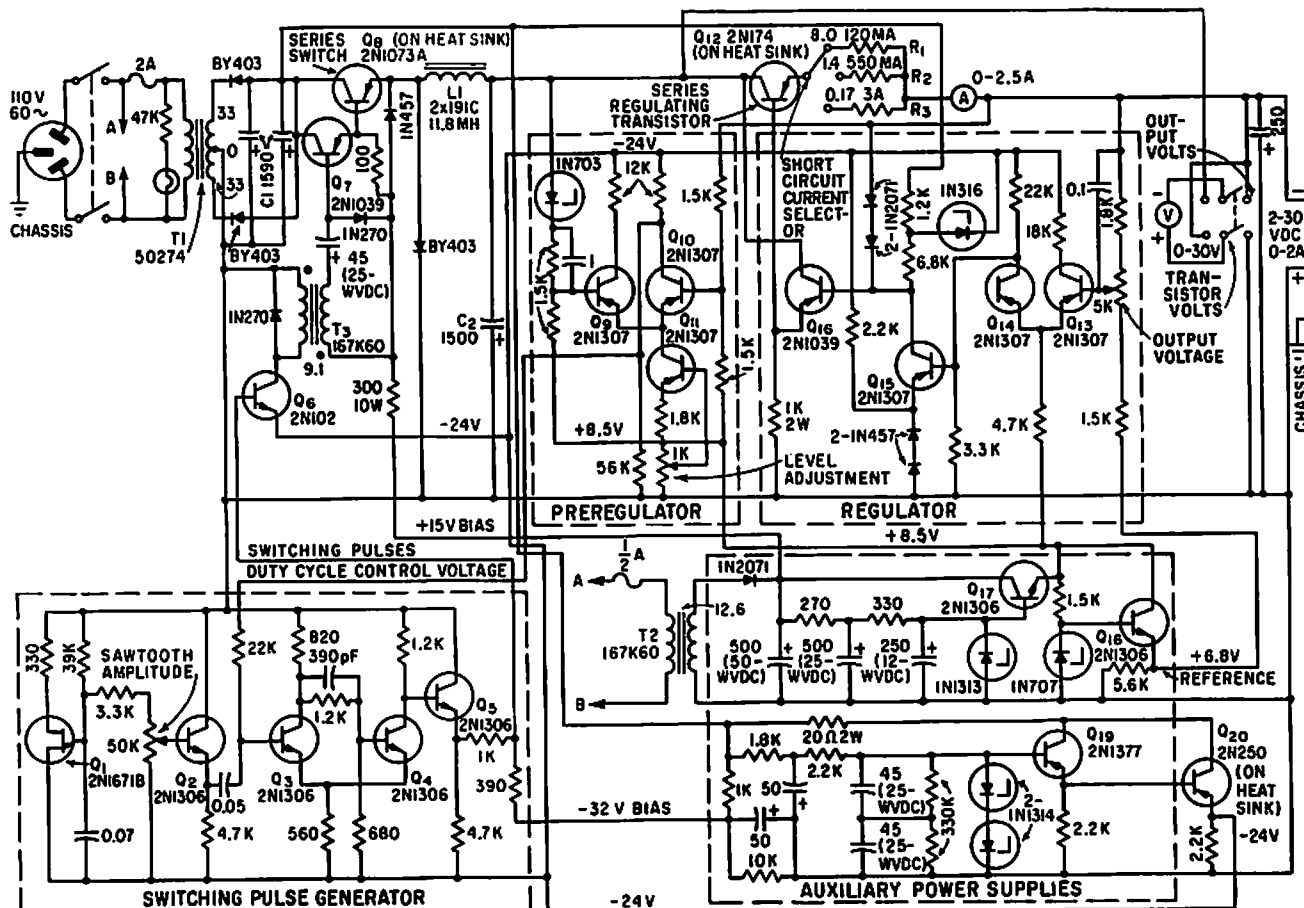
sensing, and reset, and no appreciable effect on power supply efficiency.—W. C. Mesley, GTO Protection Circuitry for DC Supplies, *EEE*, 12:11, p 57-59.



THYATRON PROTECTION IN HIGH-VOLTAGE SWITCH—C1 is charged to some value up to 400 v, then discharged into load R5 by gas tube represented by S1. V2 acts as cathode follower to charge C1 as long as V1 does not conduct. When operation of S1 is desired, V2 is turned off by turning on V1 long enough for S1 to operate and recover, using square-wave input pulse. Circuit action prevents destruction of thyatron switch and load without sacrificing pulse rate. Tubes need separate filament transformers.—Voltage Controlled High Voltage Switch, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 233.



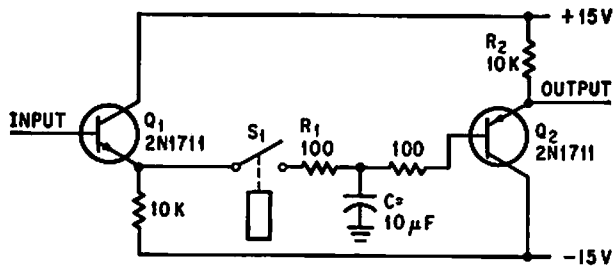
FAIL-SAFE TWT FILAMENT REGULATOR—Designed to supply well-regulated voltage of 6.3 v d-c at 2 amp to filament of travelling-wave tube, while providing temperature compensation and fail-safe capability. Protective circuit shown in heavy dotted lines operates if one of transistors shorts or if filament voltage rises for any other reason.—G. Stanley, Fail Safe DC Filament Regulator, *EEE*, 10-6, 32-33.



MINIMUM-DISSIPATION SERIES REGULATOR—Regulation of short-circuit-proof variable voltage-regulated supply is 0.1% for 2 to 30 v

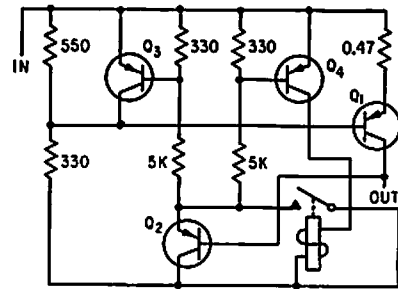
output and up to 2 amp. Dissipation in series regulating transistor is minimized by controlling on time of series switching transistor Q8.

—J. S. Riordan, Power Supply Uses Switching Preregulation, *Electronics*, 35:10, p 62-64.

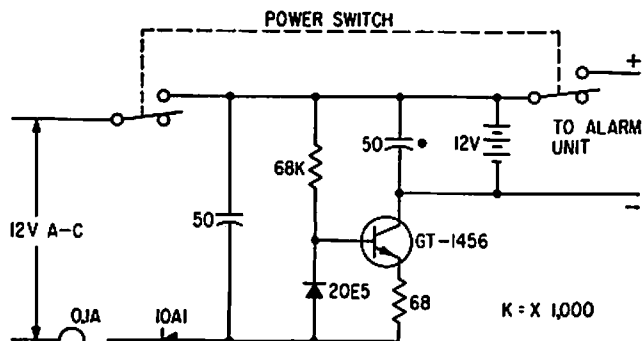


PULSED-TRANSDUCER METER PROTECTION—Circuit stores transducer output while transducer is momentarily disconnected during pulse period by relay drive circuit that operates coincidentally with transducer pulse drive. Meter voltage is stored by C during

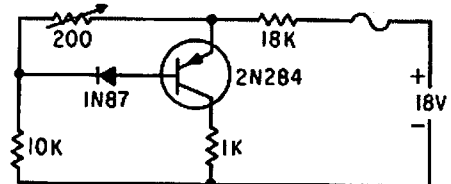
pulse period. No storage occurs when S1 is closed; meter then indicates voltage proportional to transducer current, as gain of circuit is 1.—C. Pittman and B. Birnbaum, Circuit Protects Meter from Periodic Current Spikes, *Electronics*, 39:12, p 108.



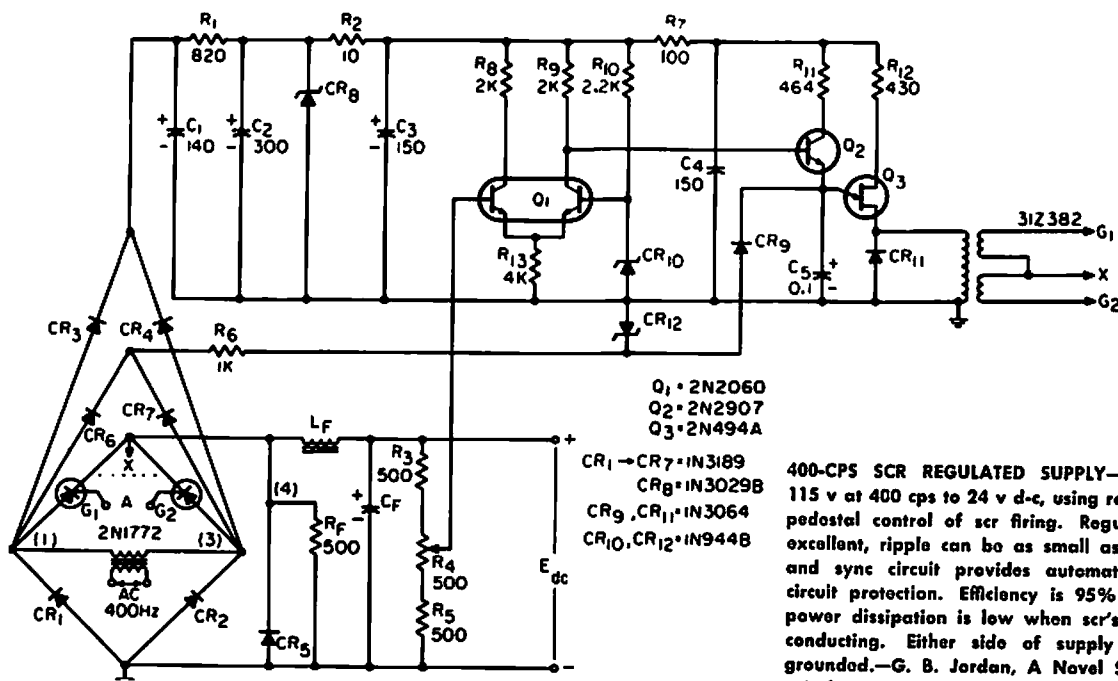
OVERLOAD PROTECTION—Switches power off rapidly to prevent current overloads from damaging transistors in breadboard circuits under test. Voltage drop across 0.47-ohm resistor and Q1 biases Q2 to saturation, causing Q3 and Q4 to open power relay.—F. W. Kear, Laboratory Supply for Transistors, *Electronics*, 35:30, p 55-57.



BATTERY STANDBY—Protects against false alarms caused by power failure of intruder detector.—S. M. Bagno, Sensitive Capacitance Intruder Alarm, *Electronics*, 33:38, p 65-67.



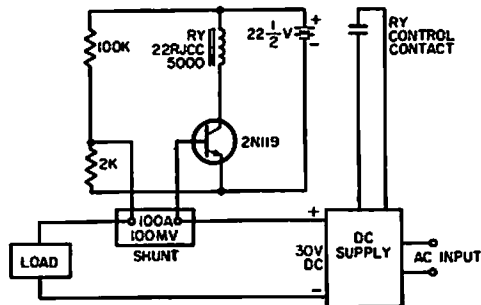
LOW-DISSIPATION TRANSISTOR OVERVOLTAGE FUSE—Series collector resistor lowers dissipation of fuse circuit during normal line voltages, and serves also as voltage regulator for transistors being protected (represented by 1K load).—K. Redmond, Low-Cost Transistor Overload Safety Circuit, *Electronics*, 33:42, p 102.



Q1 • 2N2060
Q2 • 2N2907
Q3 • 2N494A

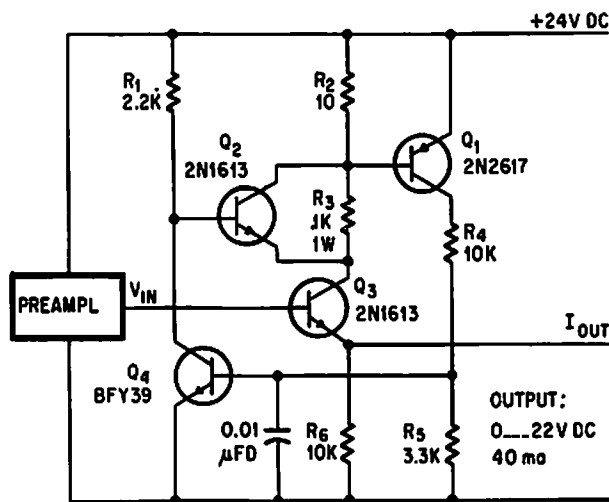
CR1—CR7 • IN3189
CR8 • IN3029B
CR9, CR11 • IN3064
CR10, CR12 • IN944B

400-CPS SCR REGULATED SUPPLY—Converts 115 v at 400 cps to 24 v d-c, using ramp-and-pedestal control of scr firing. Regulation is excellent, ripple can be as small as desired, and sync circuit provides automatic short-circuit protection. Efficiency is 95% because power dissipation is low when scr's are not conducting. Either side of supply can be grounded.—G. B. Jordan, A Novel Sync Circuit for Scr Control, *EEE*, 14:7, p 58-61.

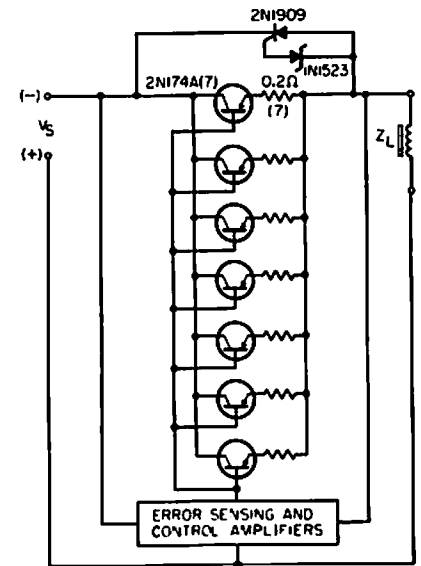


SHORT-CIRCUIT DETECTOR—Shunt used in d-c power circuit for metering also serves here to drive base of transistor that senses overloads. Relay in transistor circuit disconnects

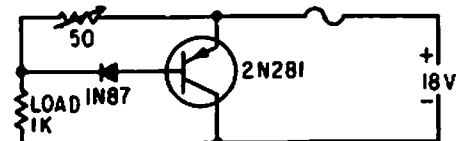
d-c power when drop across 100-mv shunt approaches 400 mv (4 times normal load current).—J. J. Pirch, Single-Transistor Short-Circuit Detector, *EEE*, 12:6, p 64.



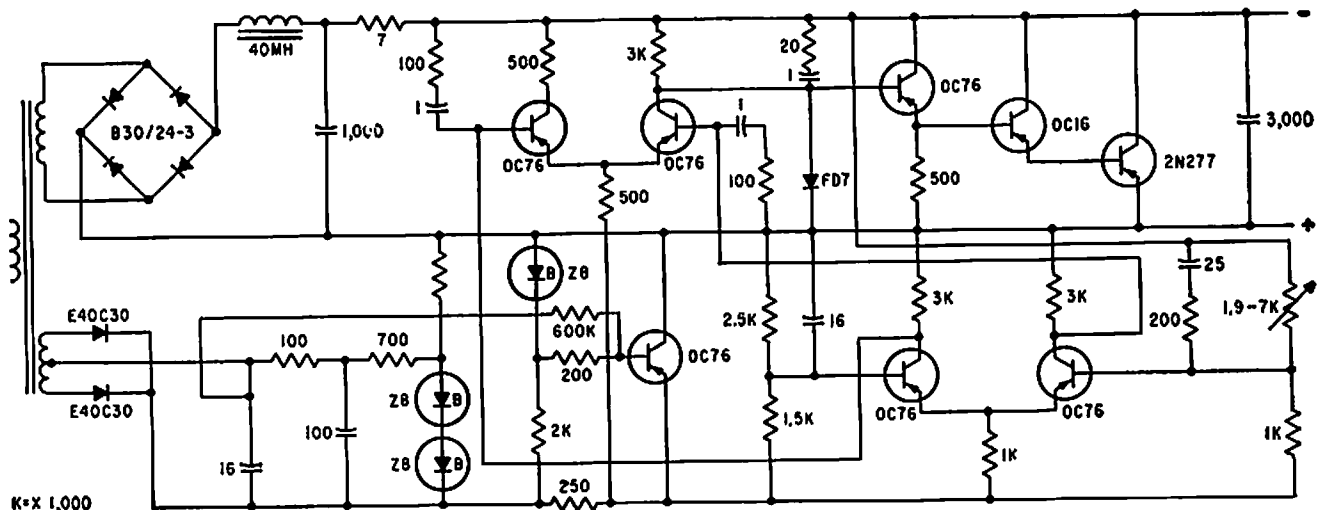
ELECTRONIC FUSE—Switches high series resistance R3 into circuit only when overload or short-circuit occurs. R3 is shunted out of load R2 by Q2.—L. Payerl, Overload Protection for D-C Amplifier, *Electronics*, 39:7, p 91.



ZENER-GATED SCR PROTECTS POWER TRANSISTORS—Scr serves as controllable short-circuit across power transistors. Reaction time is about 2 microsec.—C. A. Blanchard, Zener-Gated SCR Protection for Power Transistors, *EEE*, 14:5, p 117-118.



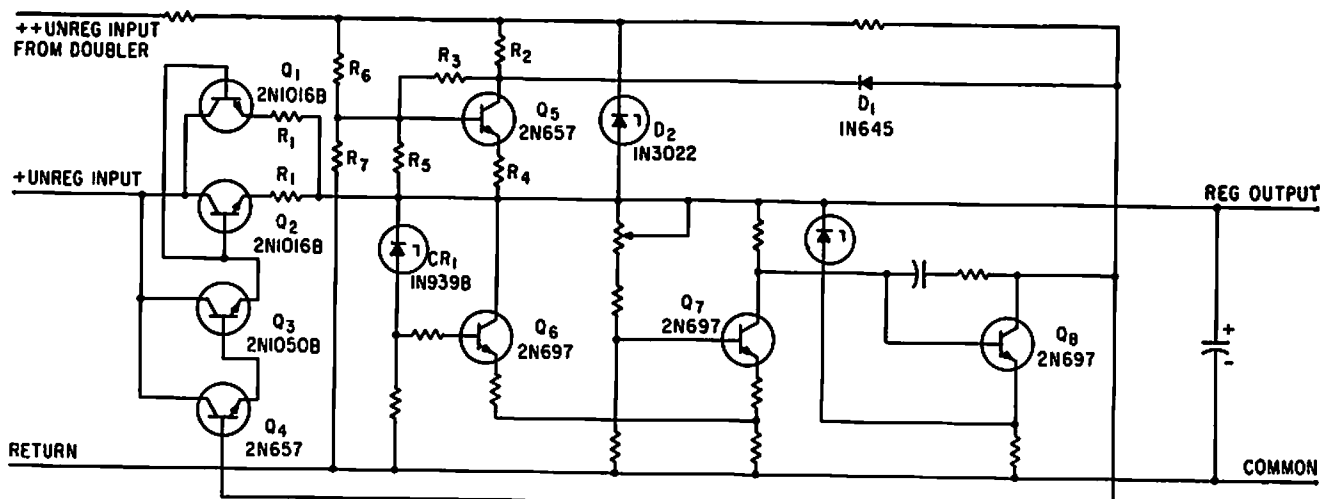
TRANSISTOR OVERVOLTAGE FUSE—Protective circuit uses one resistor, one diode, and one transistor. Transistor across supply line is cut off by 1N87 diode until overload occurs. When transistor conducts, fuse is open by current that would ordinarily destroy transistors being protected (represented here by 1K load).—K. Redmond, Low-Cost Transistor Overload Safety Circuit, *Electronics*, 33:42, p 102.



SHORT-CIRCUIT PROOF SHUNT-TYPE SUPPLY—Output is variable from 1 to 17 v, maximum ripple is 1 mv peak-to-peak, and maxi-

mum current is 2.5 amp at 1 v or 0.8 amp at 17 v. After two hours of warmup, output drift is negligible (fraction of mv).—E. Bal-

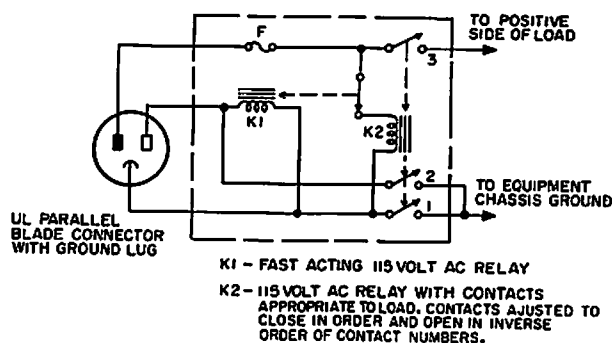
inger and W. Czaja, Designing Highly Stable Transistor Power Supplies, *Electronics*, 32:39, p 70-73.



OVERLOAD PROTECTION FOR REGULATED POWER SUPPLY—When rated load current is exceeded in series-regulated power supply,

D1 conducts and collector voltage of Q1 acts as clamp to prevent further increase in load current. At short-circuit, load current is only

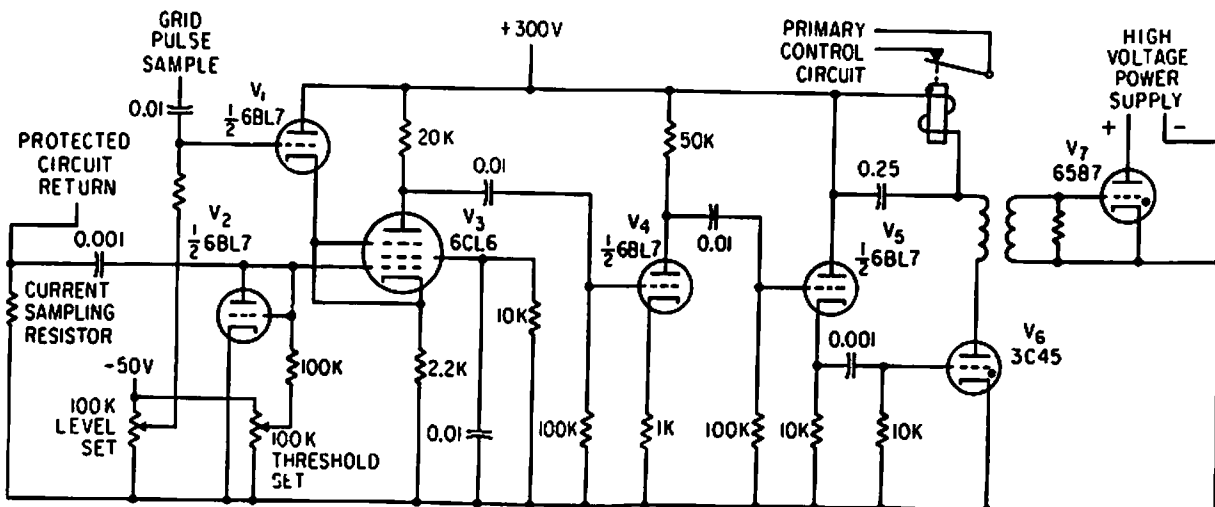
fraction of full value.—K. L. Burfeindt, Overload Protection Without High Power Dissipation, *Electronics*, 36:13, p 36-37.



LIFESAVER—Used with transformerless line-operated equipment to minimize possibility of chassis being hot. Relays are so arranged that they automatically search for

proper relationship of voltages between hot, neutral and ground terminals before power is applied to equipment. Only limitation is failure to protect against rare fault in which

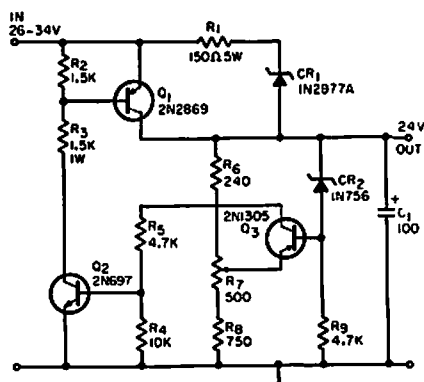
both neutral and conduit ground are at line potential with respect to earth ground.—R. E. Pafenberg, Lifesaver Circuit, *EEE*, 10:7, p 26-27.



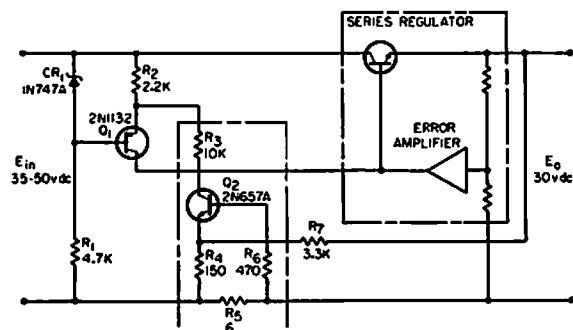
ARC-PROTECTION CIRCUIT—Circuit ignores desired peak pulse currents by sensing their coincidence with drive pulse, but fires trigger

thyatron V6 and V7 in absence of drive pulse, to make thyatrons discharge power supply before breakdown and flashover of

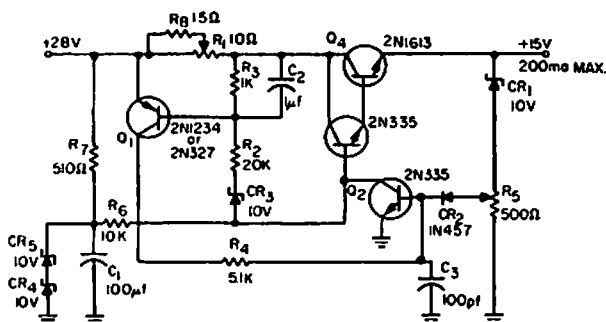
high-voltage electron device under test.—D. D. Mawhinney, Latest Thing in Arc-Protection Circuits, *Electronics*, 36:8, p 54-55.



SHORT-PROOF REGULATOR—Provides constant 24 v at up to 500 ma and turns itself off when load is shorted. Restarts automatically when short is removed. Regulation is within 1% from no load to 500 ma and with input voltages from 26 to 34 v.—D. E. Wilson, *Inexpensive Short-Proof Voltage Regulator*, *EEE*, 12:6, p 64.

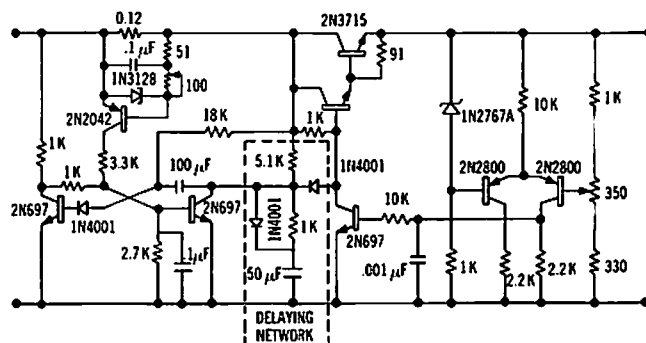


REDUCED-POWER OVERLOAD PROTECTION—Circuit reduces power dissipation in series regulator transistors under overload or short-circuit conditions by making output current decrease as load resistance decreases. Values shown are for 30 v d-c supply delivering 0.25 amp, with current limiting starting at 0.31 amp.—R. A. Lewis, *Reduced-Power Overload Protection*, *EEE*, 12:11, p 67.

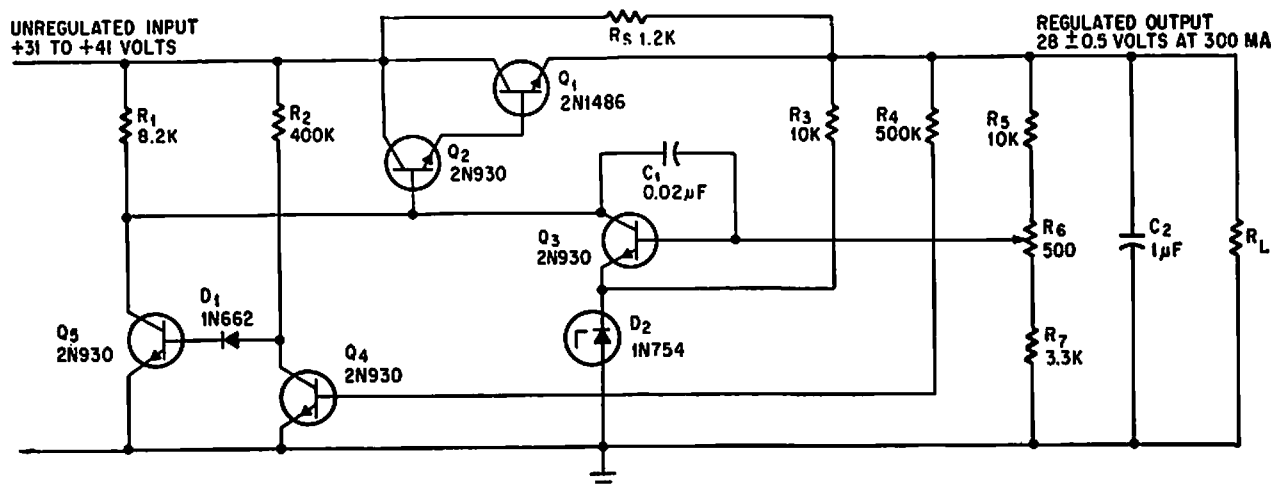


ADJUSTABLE OVERLOAD TRIP—Protection circuit, added to conventional regulator, consists of R1, R2, R3, R4, C2, CR2, CR3, and Q1. When load current reaches preset trip level, drop across R1 turns on Q1, which in turn saturates Q2 and cuts off regulator transistor

Q3-Q4 to protect these transistors and reduce output voltage to zero. Turnoff is regenerative, hence fast. To reset, supply voltage is switched off and then back on.—W. A. O'Berry, *Adjustable Overload Protection*, *EEE*, 12:2, p 29.



SERIES REGULATOR WITH CAPACITIVE OVERLOAD PROTECTION—R-C delaying network in dotted box applies drive slowly to series-pass transistor to prevent overload protective circuit from turning off regulator when surge current charges capacitive load. Network does not reduce response time.—J. Takesuye and H. Weber, "Silicon Power Transistors Provide New Solutions to Voltage Control Problems," *Motorola Application Note AN-163*, Aug. 1964.



SHORT-CIRCUIT PROTECTION—Voltage-sensing short-circuit switch Q4-Q5 turns off

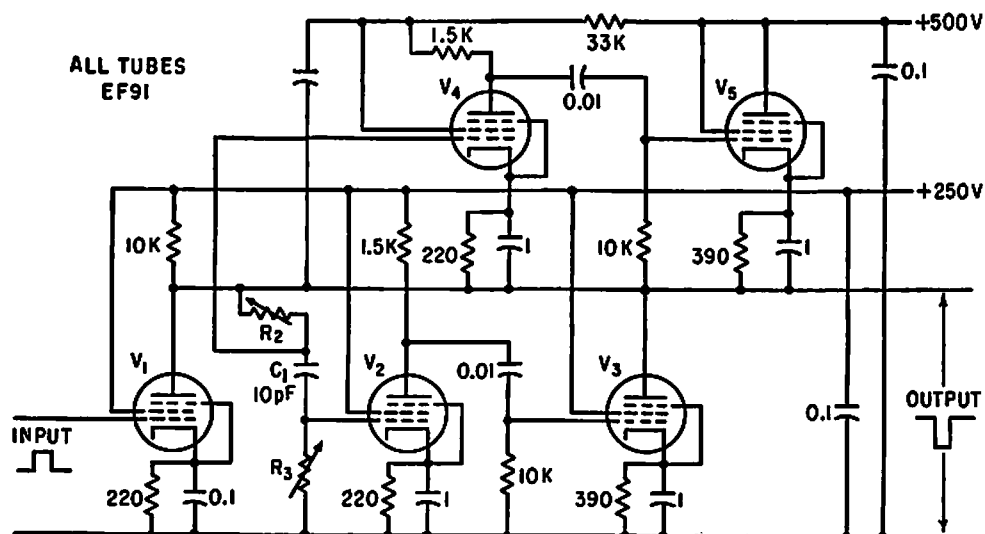
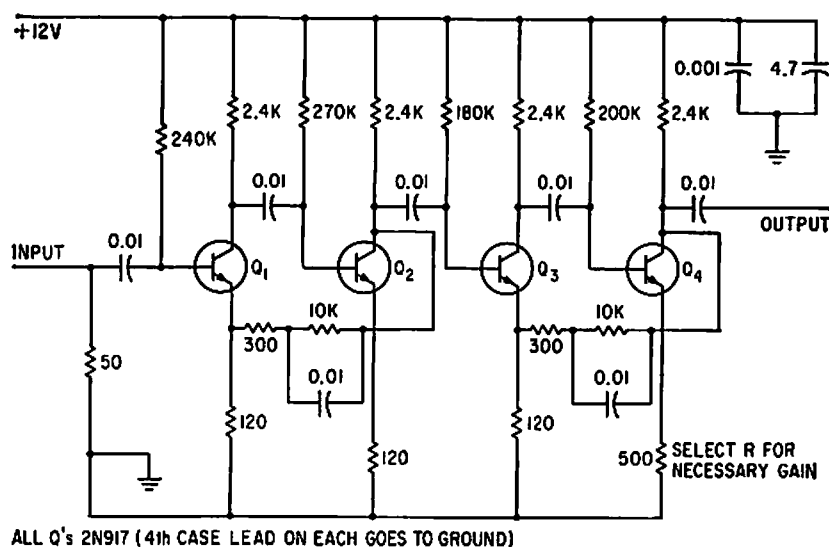
series-regulating transistor Q1 when load R1 is short-circuited.—G. A. Chunn and G. D.

Norton, *Short-Circuit Protection Consumes Little Power*, *Electronics*, 38:22, p 68.

CHAPTER 63

Pulse Amplifier Circuits

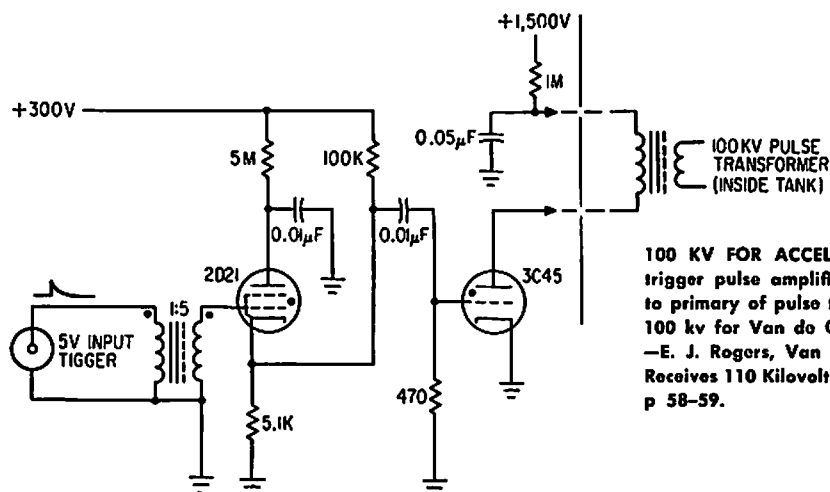
WIDEBAND DIGITAL PULSE AMPLIFIER—Common-emitter a-c coupled cascaded amplifiers, with negative feedback at every second stage, give voltage gain of 12, bandwidth of 100 Mc, rise time of 3 nsec, pulse pair resolution of 5 nsec, and s/n ratio of 100 to 1 for inputs from 0 to 200 mv.—A. A. Fleischer and E. Johnson, New Digital Conversion Method Provides Nanosecond Resolution, *Electronics*, 36:18, p 55-57.



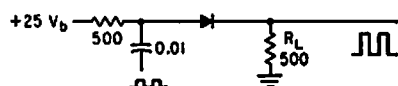
TWO AMPLIFIERS FOR BIPOLAR PULSES—Design procedure is based on fact that wide bandwidth is required only for leading and trailing edges of pulses. Auxiliary am-

plifier supplies current to charge load and stray capacitances, reducing standby current and improving gain. Auxiliary amplifier V4-V5 provides charging current for shunt

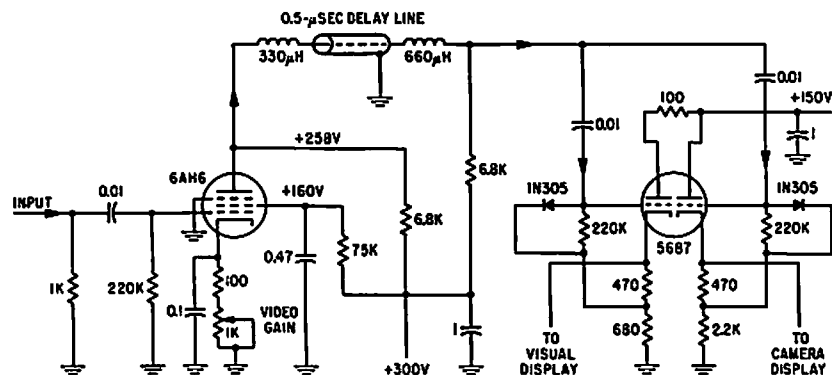
capacitance during positive-going edge of output pulse.—J. F. Golding, Novel Approach to Pulse Amplifier Design, *Electronics*, 33:19, p 64-66.



100 KV FOR ACCELERATOR—Two thyatrons trigger pulse amplifier to send 1,500-v pulse to primary of pulse transformer for stepup to 100 kv for Van de Graaff accelerator source.—E. J. Rogers, Van de Graaff Proton Source Receives 110 Kilovolt Boosts, *Electronics*, 35:13, p 58-59.

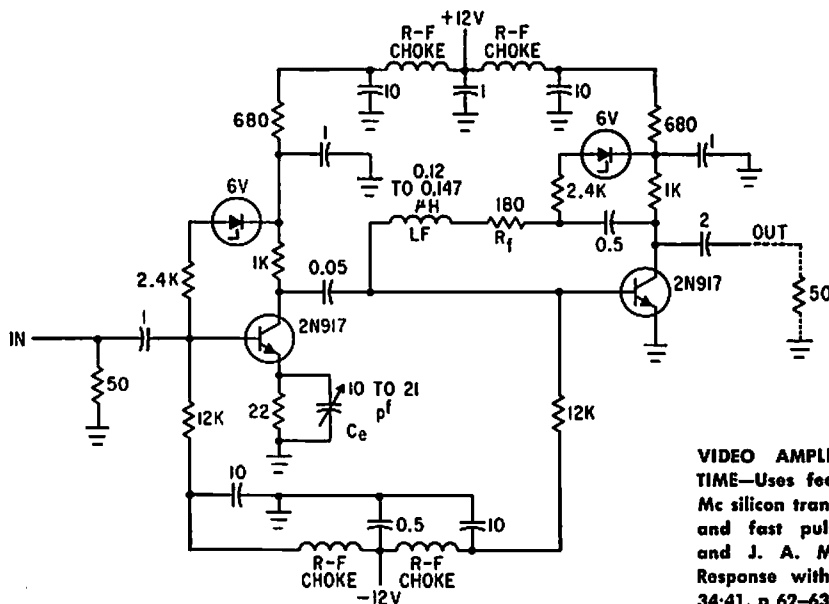


NEGATIVE-RESISTANCE DIODE—Input pulses as low as 0.01 ma are sufficient to hold negative-resistance diode in high-current region. When pulse is shut off, diode current decays to low-current state. Amplifier tends to square up input pulses.—A. P. Schmid, Jr., Negative-Resistance Diode Handles High Power, *Electronics*, 34:34, p 44-46.

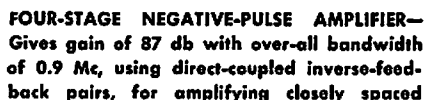


LINEAR PULSE AMPLIFIER—Simple linear amplifier drives two cathode followers through delay line. One output goes to one crt grid for intensity modulation. Other output goes

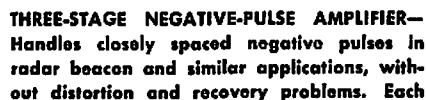
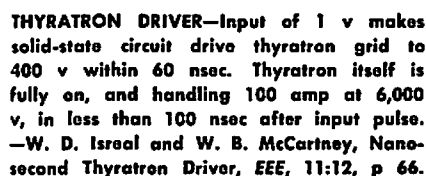
to horizontal plates of another crt for bar presentation.—M. T. Nadir, Microsecond Sampler Handles 126 Channels, *Electronics*, 32:4, p 36-39.



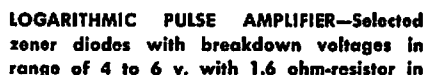
VIDEO AMPLIFIER WITH TWO-NSEC RISE TIME—Uses feedback techniques with 1,000-Mc silicon transistors to give wide bandwidth and fast pulse response.—P. J. Beneteau and J. A. MacIntosh, Getting Fast Pulse Response with Video Amplifiers, *Electronics*, 34:41, p 62-63.



pulse code groups coming from crystal detector of radar video receiver.—R. E. Koncen, Wide-Range Multiple-Pulse Amplifier, *Electronics*, 33:38, p 78-81.



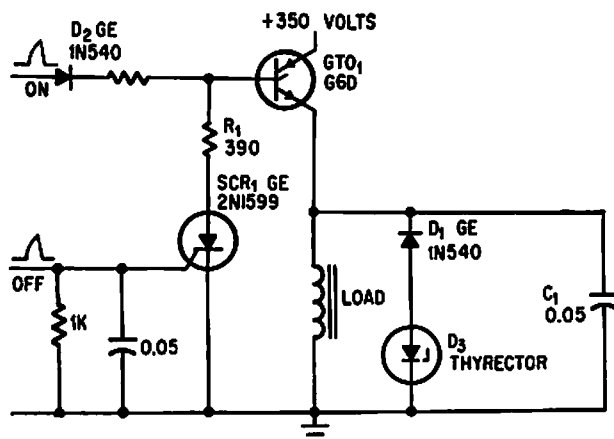
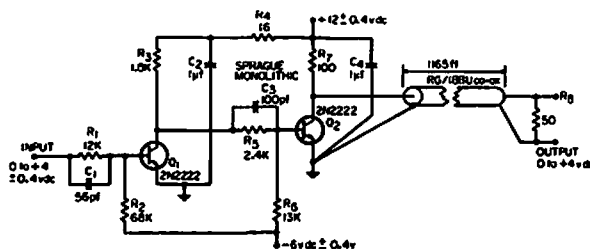
amplifier stage is inverse-feedback pair of triodes with 360° total phase shift.—R. E. Koncen, Wide-Range Multiple-Pulse Amplifier, *Electronics*, 33:38, p 78-81.



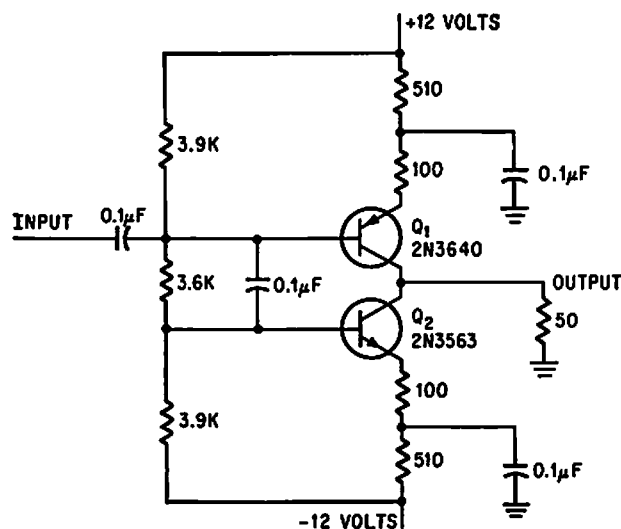
series with D1 for straightening curve, give close approximation to logarithmic amplification of pulses over three decades of cur-

rent, from 0.1 to 100 ma.—D. Ophir and U. Galil, Zener Diode creates Logarithmic Pulse Amplifier. *Electronics*, 34:28, p 68-70.

COAXIAL CABLE DRIVER—Can drive digital information through long lengths of coaxial cable. Will send pulses with 30-nsec rise and fall time through 1,155 feet of 50-ohm RG/188U or through 650 feet of 93-ohm RG/62U.—B. Strunk, Coaxial Cable Driver Circuit, *EEE*, 13:5, p 43-44.



HAMMER-DRIVER FOR COMPUTER LINE PRINTER—Gate turnoff scr overdrives load solenoid momentarily by connecting it across 350-v bus and disconnecting before overheating occurs. Load current rise and fall times are less than one millisecond.—D. R. Grafham, New the Gate Turnoff Switch Speeds Up D-C Switching, *Electronics*, 37:12, p 64-71.

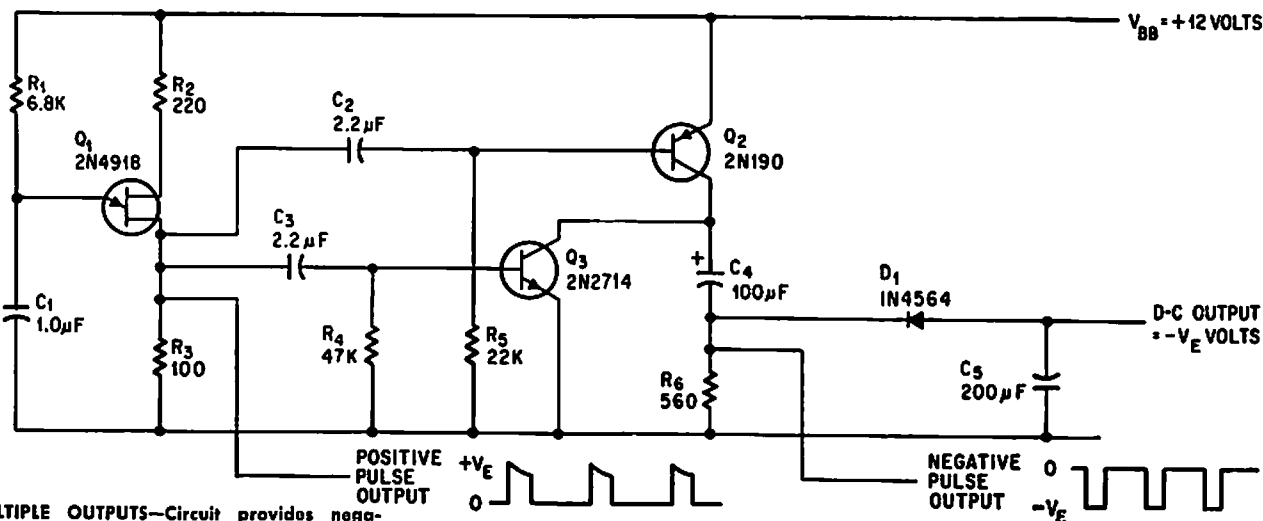
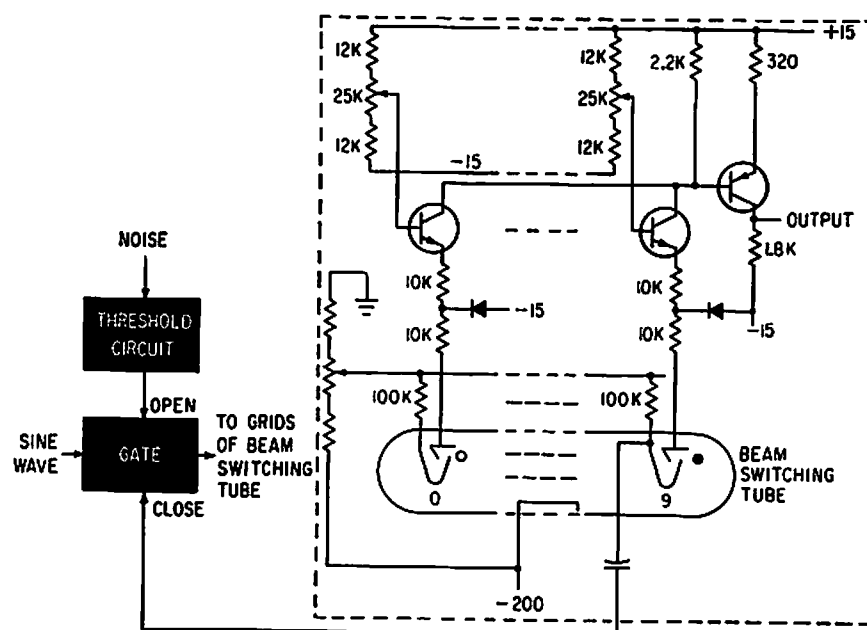


CURRENT DRIVER—Provides fast rise time and equal-amplitude positive and negative output pulses (equal-polarity drive) for 50-ohm load.—E. J. Kennedy, Fast-Pulse Amplifier Drives 50-Ohm Load, *Electronics*, 39:2, p 76.

CHAPTER 64

Pulse Generator Circuits

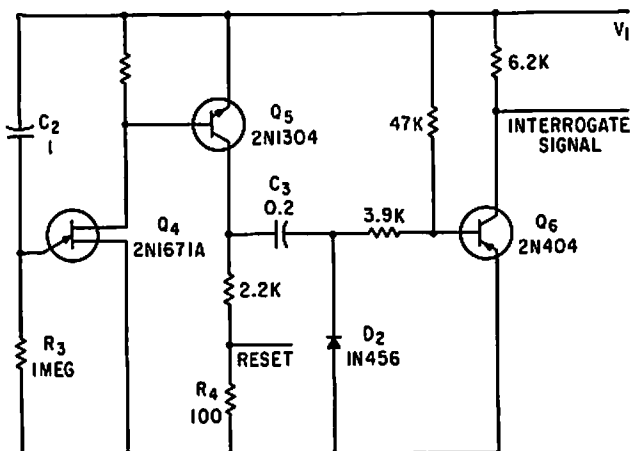
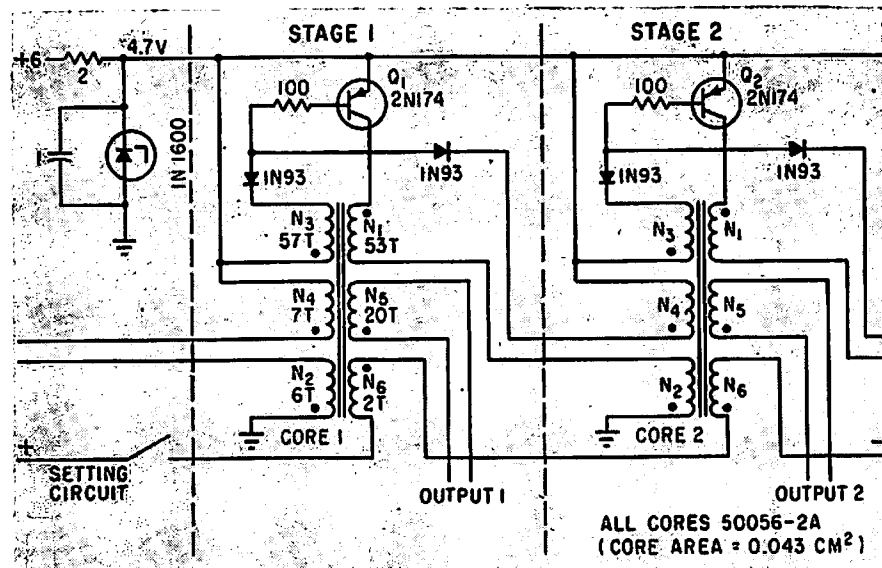
RANDOM-TIME PULSES—When gate is opened by noise, sine wave steps electron beam of Burroughs tube through its ten sections. Transistor connected to each target produces voltage pulse whose magnitude depends on potentiometer setting, giving sequence of different voltages in output. When beam reaches position 9, pulse is fed back to close the gate.—C. V. Jakowatz and G. M. White, Self-Adaptive Filter Finds Unknown Signal in Noise, *Electronics*, 34:7, p 117-119.



MULTIPLE OUTPUTS—Circuit provides negative d-c output voltage along with positive and negative output pulses, using only single d-c source. Unijunction-transistor oscillator Q1 provides positive pulses, while Q2 and Q3 together invert those and drive rectifier D1 that gives -5 v at 1 ma to drive low-

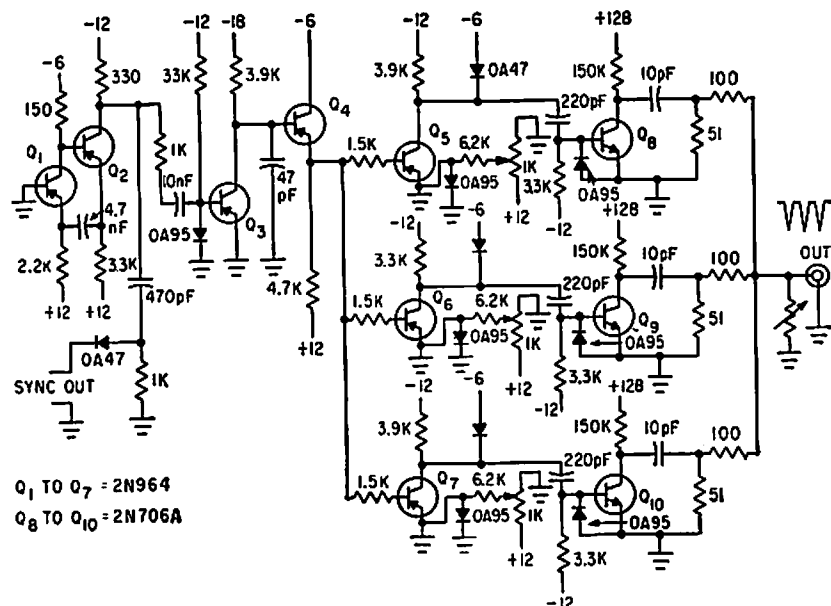
power amplifier that may be used in same integrated circuit.—M. H. Hussain, Circuit Inverts D-C Voltage, *Electronics*, 38:19, p 100.

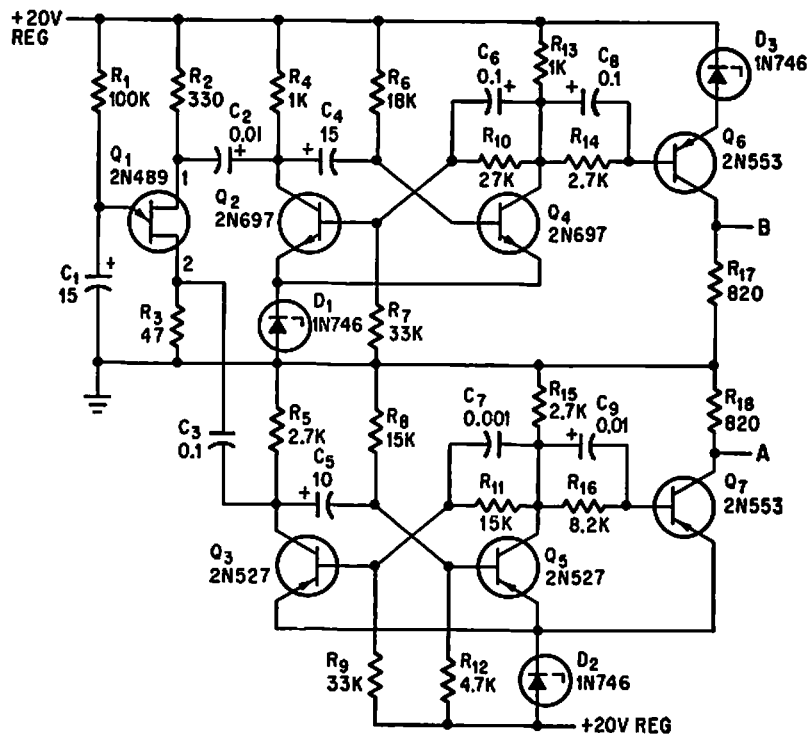
RING-TYPE OSCILLATOR—After core-setting current is removed, pulse output of Q1 is followed by output of Q2 after delay of 100 microsec to 3 sec, depending primarily on input voltage and core size. No separate drive oscillator is required when used as ring counter.—J. M. Marzolf, *Magnetic-Core Ring Counter Needs No Drive*, *Electronics*, 35:12, p 52-53.



TWO-OUTPUT SQUARE-WAVE PULSE GENERATOR—Pairs of control pulses are provided in sequence by silicon unijunction transistor in relaxation oscillator. Interval between pulses is determined by R3-C2. When C2 charges enough to trigger Q4, pulse fed to base of Q5 makes it conduct heavily; C3 charges and reset pulse is then developed across R4. Next, Q5 switches off, thereby feeding negative pulse to base of Q6 to switch Q6 off and make its collector voltage rise rapidly to form negative second pulse of pair.—C. D. Todd, *Tunnel Diode Detects Currents Down to 100 Femtoamperes*, *Electronics*, 36:14, p 33-37.

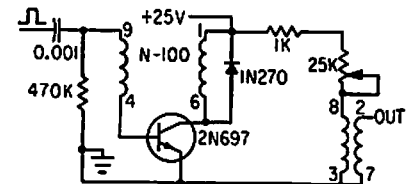
THREE-PULSE GROUPS TEST 10-NSEC DECADE COUNTERS—Free-running mvbr Q1-Q2 triggers sawtooth generator Q3, which in turn acts through emitter-follower Q4 to drive delay-adjusting amplifiers Q5, Q6 and Q7, each driving silicon transistor working in avalanche mode. Common output is group of three 10-v pulses having rise times below 1 nsec.—R. Charbonnier, *Avalanche Transistors Test 10-Nsec Logic*, *Electronics*, 36:28, p 46.



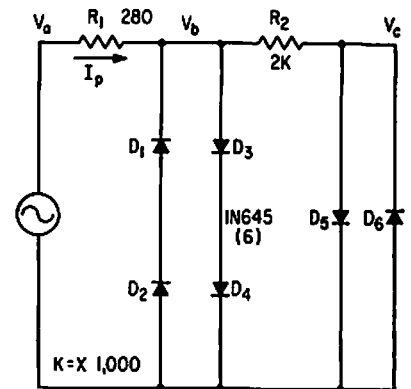


RECTANGULAR PULSES GENERATED IN PAIRS—Output A gives 50-millisecond positive pulses and output B gives 120-millisecond positive pulses, both square-wave and both at 0.5 cps, with rise and fall times under 2 micro-

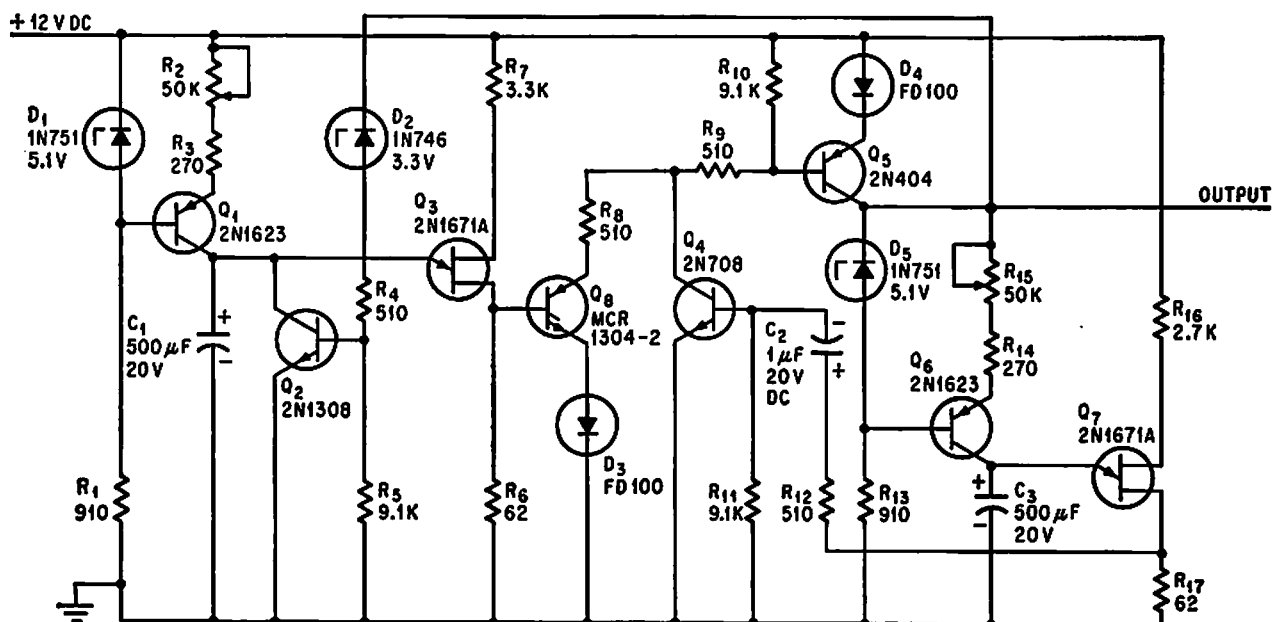
sec for 12-v pulses. Circuit uses one uni-junction transistor, two npn transistors, and four pnp transistors.—R. W. Maine, *Generating Two Rectangular Waves*, *Electronics*, 37:18, p 82-83.



VARIABLE-WIDTH PULSE GENERATOR—Rheostat in series with pulse transformer primary winding controls bias current to adjust output pulse width over range of 0.06 to 5 microsec. Rise time is less than 40 nsec.—*Blocking Oscillator Has Variable Width Output*, *Electronics*, 36:11, p 156.



SINE-WAVE CLIPPER—When driven by sine waves, circuit gives high-quality square waves over wide frequency range. Output voltage is essentially constant at 1.5 v peak-to-peak if input voltage is high enough to saturate silicon diodes.—W. E. Nometh, *Two-State Sine-Wave Clipper*, *Electronics*, 34:11, p 64.

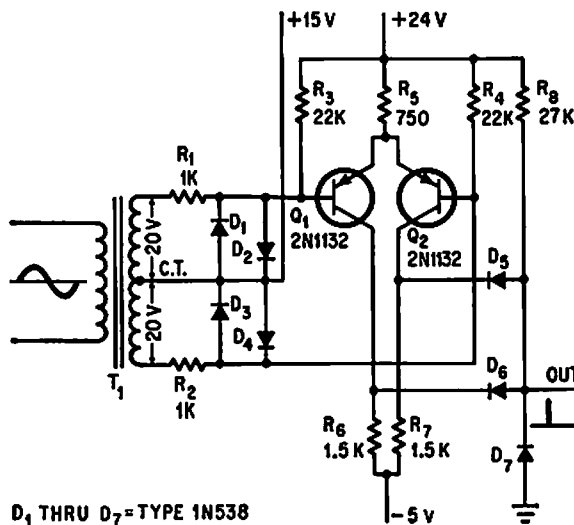
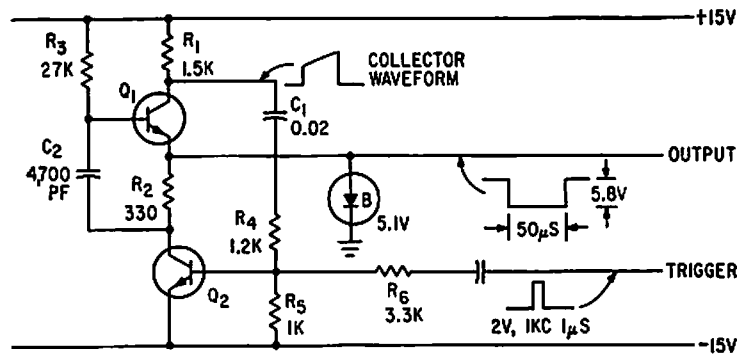


ADJUSTABLE DUTY CYCLE—R2 varies off time from 0.25 to 40 sec, while R15 provides variation of over 100 to 1 in ratio of on

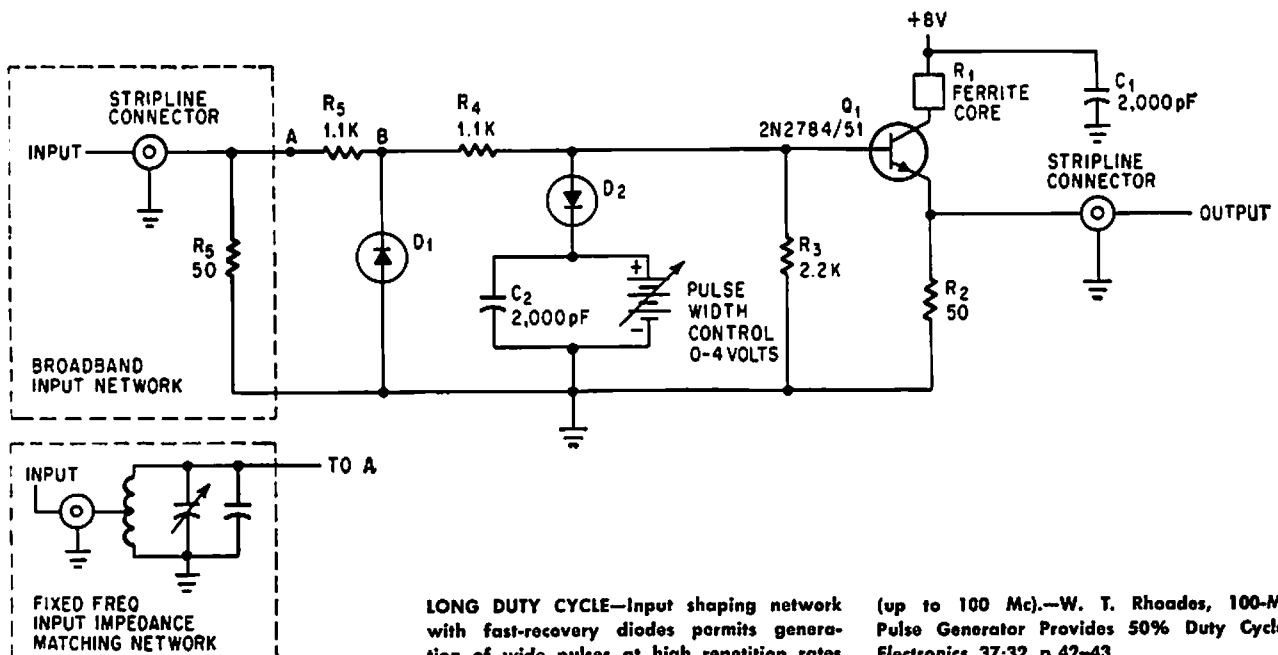
time to off time. Pulse width and inter-pulse time can thus be adjusted independently.—A. A. Dargis, *On and Off Time*

Adjusted Independently, *Electronics*, 37:31, p 50-51.

MONOSTABLE WITH ZENER CLAMP—Produces pulses of known length and amplitude when triggered by external pulse.—C. M. Stewart, Monostable Pulse Generator Employs Zener-Diode Clamp, *Electronics*, 34:19, p 76-77.

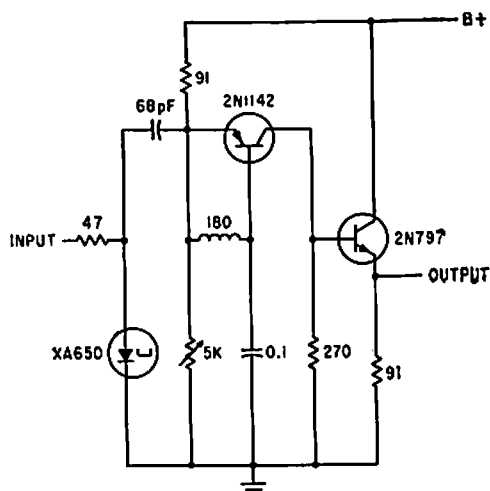


ZERO-CROSSING PULSES—Sharp pulse is produced at each zero crossing, for phase control of scr power supply. When instantaneous line voltage is zero, differential amplifier Q1-Q2 is balanced and collectors swing to +2.5 v, causing gate to produce 2-v, 20-microsec output pulse.—S. Prigozy, Zero-Crossing Detector Provides Fast Sync Pulses, *Electronics*, 38:8 p 91.



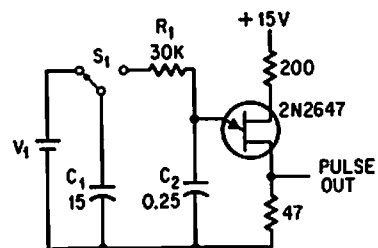
LONG DUTY CYCLE—Input shaping network with fast-recovery diodes permits generation of wide pulses at high repetition rates

(up to 100 Mc).—W. T. Rhoades, 100-Mc Pulse Generator Provides 50% Duty Cycle, *Electronics*, 37:32, p 42-43.

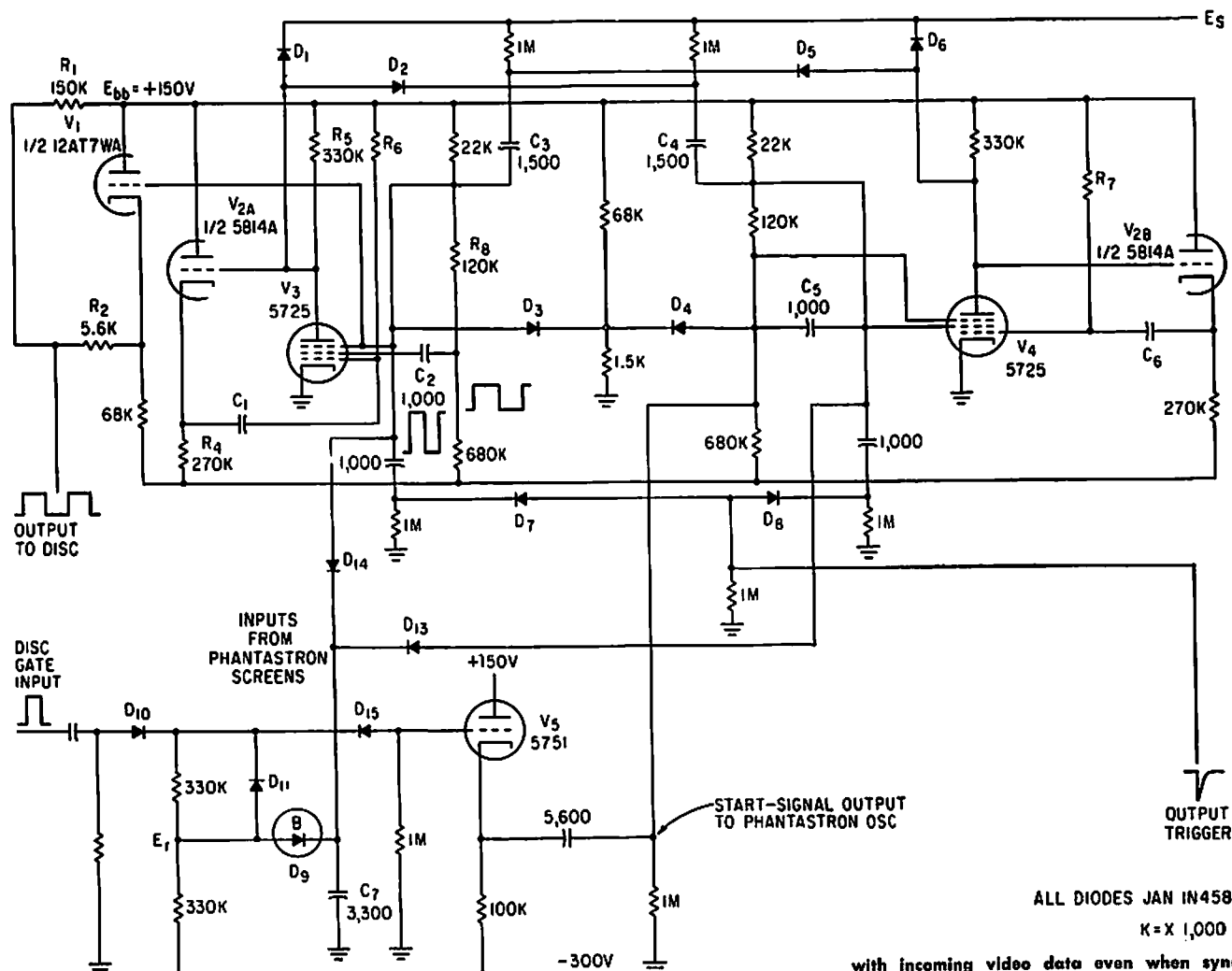


TUNNEL-DIODE PULSER—Switching voltage change of tunnel diode is differentiated and amplified by grounded-base amplifier. With 8-v supply, 5-v output pulses are

obtained into 91 ohms at clock rates up to 140 Mc.—M. V. Harrison and R. S. Foote, Tunnel Diodes Increase Digital-Circuit Switching Speeds, *Electronics*, 34:32, p 154-156.



UNIUNCTION TRANSISTOR GENERATES DESIRED NUMBER OF PULSES—Number of pulses generated each time switch S1 is operated increases linearly from 0 to 140 as battery voltage is increased from 11 to 35 v. Charge transferred from C1 to C2 fires transistor, discharging C2, with cycle repeating until C2 voltage drops below firing point.—R. Ferrie, Unijunction Circuit Generates Specific Number of Pulses, *Electronics*, 37:15, p 78.



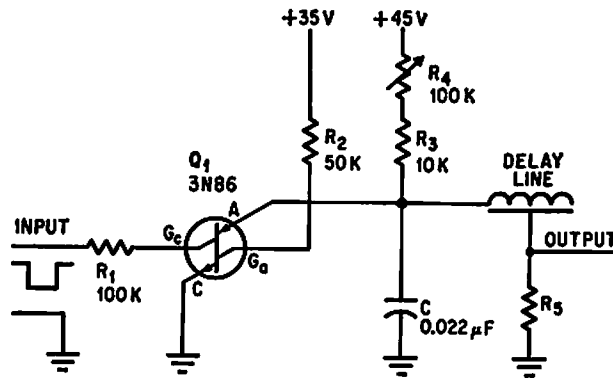
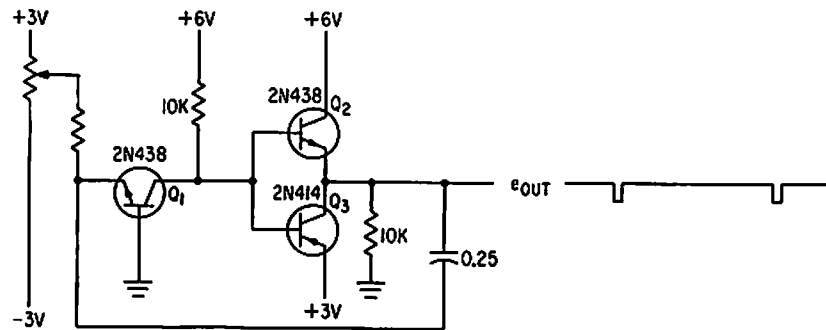
TWIN-PHANTASTRON—Frequency of free-running twin-phantastron oscillator V3-V4 varies with controlled voltage ES (upper right). V5

gates starting pulses to phantastron whenever it fails to oscillate. Used to generate train of pulses that continues in synchronism

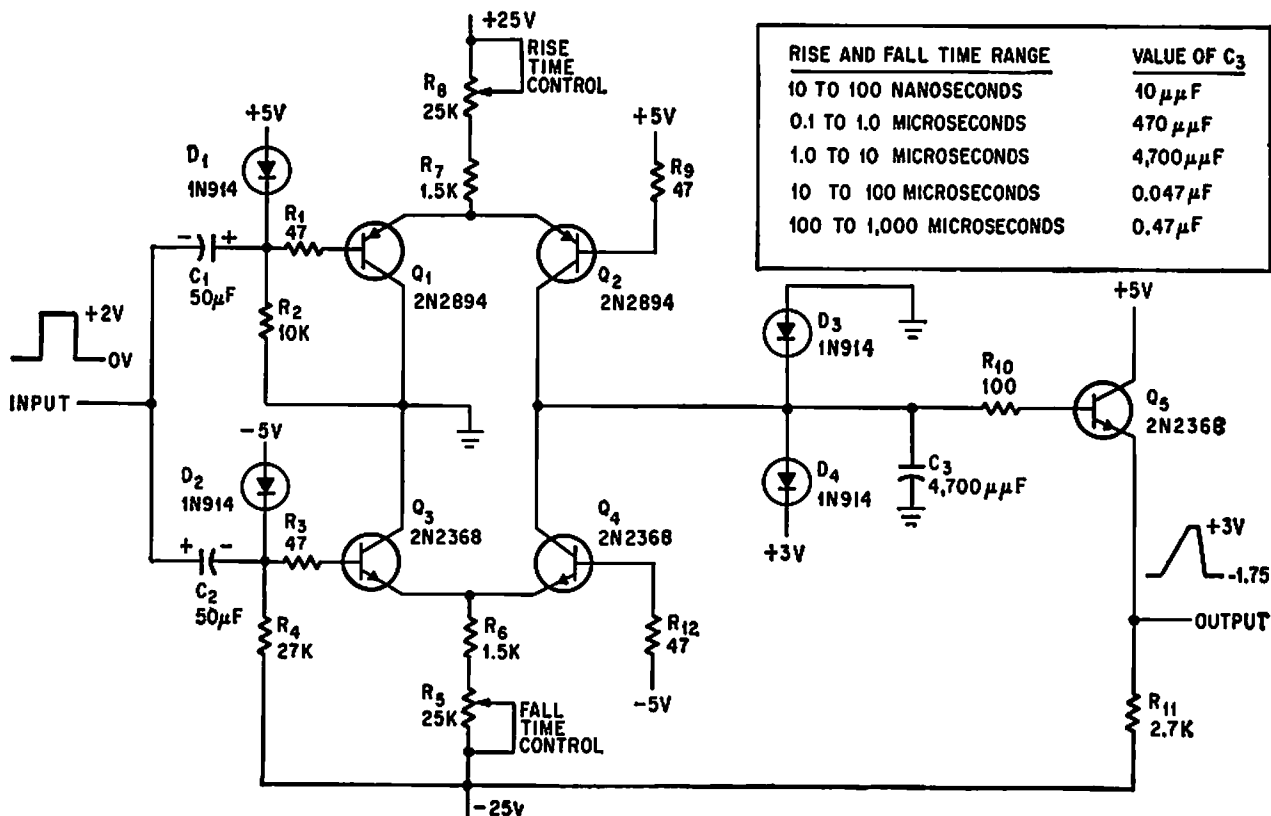
with incoming video data even when sync pulse is missing or below noise level.—W. C. Whitworth, Plate Voltage Control of Phantastron Frequency, *Electronics*, 34:6, p 73-74.

ALL DIODES JAN IN458
K = X 1,000

PHANTASTRON—Reversal of current and voltage functions of basic three-transistor phantatron sweep generator results in pulse output that is derivative of sawtooth sweep.—N. C. Hekimian, Phantatron Circuits Using Transistors, *Electronics*, 34:8, p 46-47.



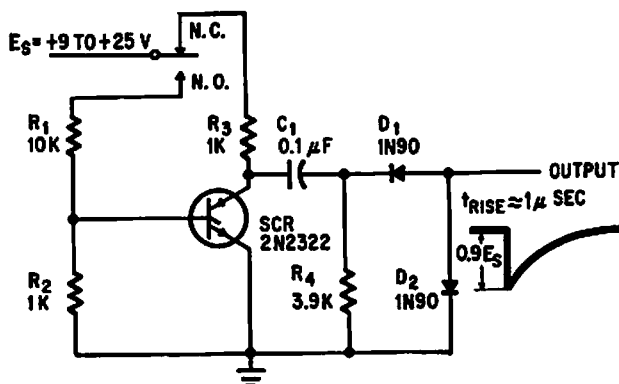
SINGLE SCS—R4 varies relaxation frequency of pnpn silicon controlled switch from 1 to 500 pps, independently of pulse duration and amplitude. For operation in gated mode, cathode gate pulse at input should be -1.5 v at 50 microamp.—H. H. Wieder, Silicon Controlled Switch Can Generate Pulses, *Electronics*, 38:2, p 79.



ADJUSTABLE RISE AND FALL TIMES—Constant-current source Q1-Q2 charges C_3 , while

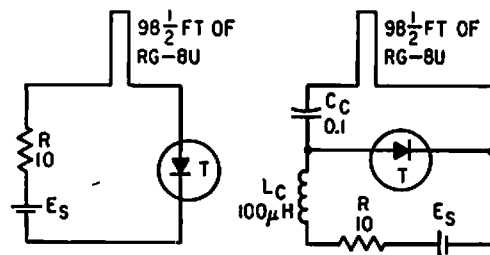
constant-current sink Q3-Q4 discharges C_3 . —D. N. Lee, Rise Time Adjustment Inde-

pendent Of Fall Time, *Electronics*, 38:2, p 76-78.

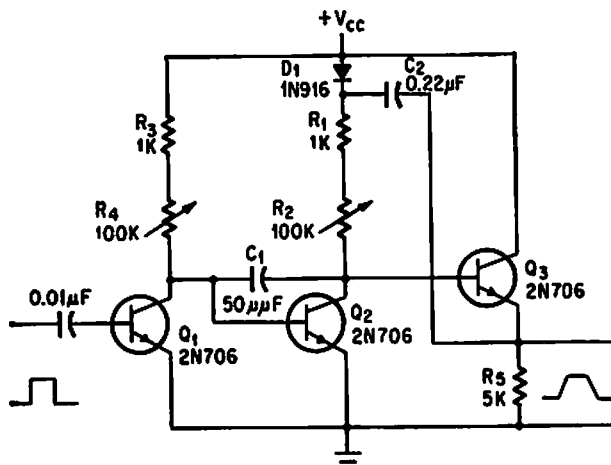


SINGLE PULSE—Push button fires scr to produce single pulse with rise time of 1 micro-

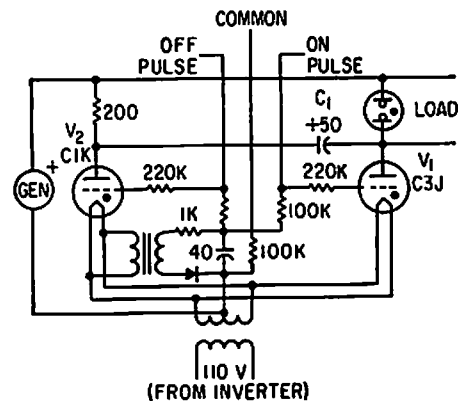
sec.—R. W. Bailey, Push Button SCR Equals Fast Pulse, *Electronics*, 37:30, p 41-42.



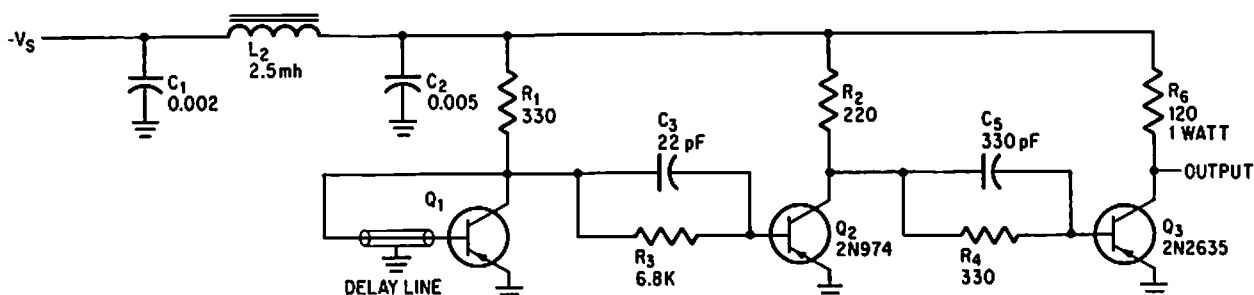
SQUARE-WAVE TUNNEL DIODE—Short-circuited coaxial cable may be connected either in series or in parallel with tunnel diode of basic relaxation oscillator, to get square-wave output with excellent frequency stability over entire bias range.—Wen-Hsiung Ko, Designing Tunnel Diode Oscillators, *Electronics*, 34:6, p 68-72.



RISE AND FALL CONTROL—C1 controls range of rise and fall times, from 10 nsec for 10pf, to 10 millsec for 0.1 mfd. Used for testing pulse networks.—D. G. Larson, Pulse Generator Controls Rise, Fall Time Independently, *Electronics*, 38:19, p 98-99.



FISH SHOCKER—Two thyratrons serve as d-c interrupter that alternately connects and disconnects d-c generator from load consisting of fresh-water path between aluminum boat and aluminum grid 6 ft away. Fish swim toward positive electrode, receive shock, and are temporarily stunned. Timing circuits determines number and duration of pulses.—H. P. Dalo, Electronic Fishing with Underwater Pulses, *Electronics*, 32:4, p 31-33.

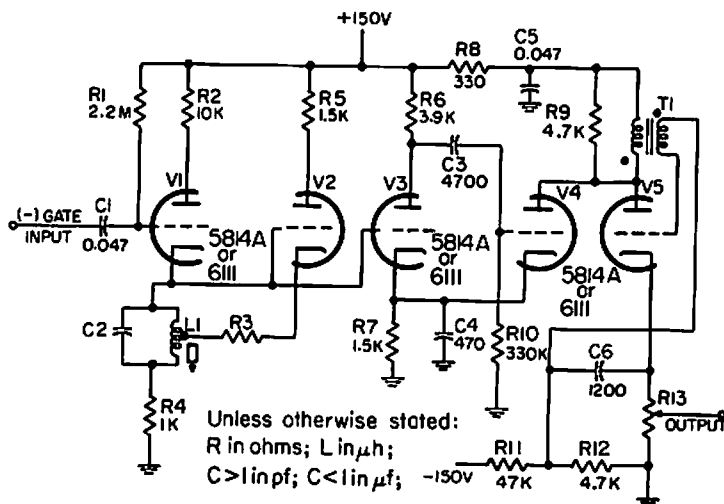
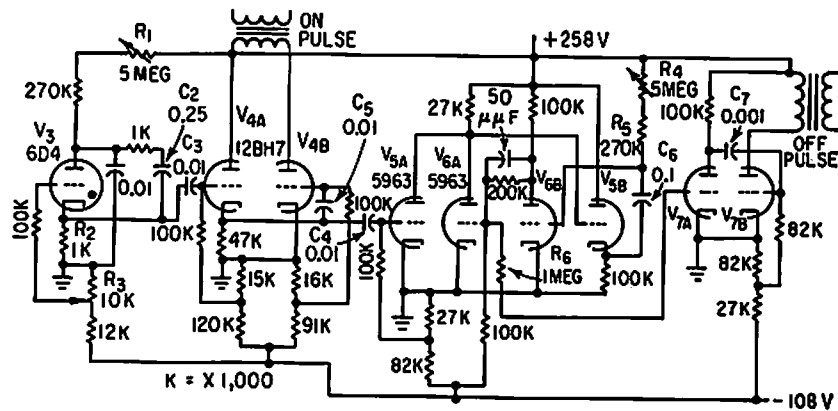


CONSTANT-WIDTH HIGH-CURRENT PULSES—Circuit generates negative pulses from -2 to -12 v with rise and fall times less than 30

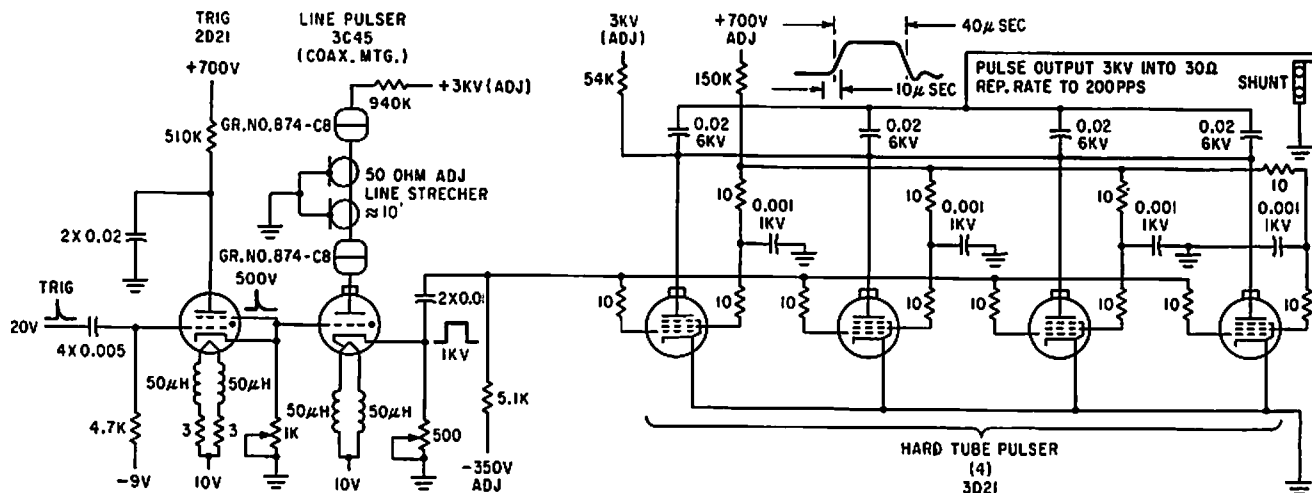
nsec. Amplitude and spacing depend on supply voltage. For positive output pulses, use npn transistors and positive supply.—

C. P. Hohberger, Fast Pulse Generator Tests Digital Circuit Delay, *Electronics*, 39:4, p 88-89.

CONTROL FOR FISH SHOCKER—Produces timing pulses that can be varied in range of 2 to 30 cps, with durations up to 250 millise for square waves. R1 changes frequency of oscillator V3, while C6, R4, and R5 in delay mvbr V5-V6 determine width of pulse.—H. P. Dale, *Electronic Fishing with Underwater Pulses*, *Electronics*, 32:4, p 31-33.



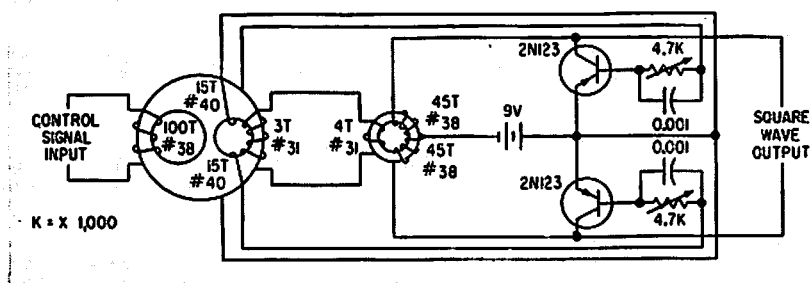
PREFERRED DISTANCE-MARK GENERATOR—Produces train of accurately spaced pulses. Number of pulses in train is determined by duration of input gate, and distance mark spacing is controlled by values of L1, C2, and R3. R13 is 250 ohms maximum. Output is 0 to 50 v positive, for distance mark spacings of 0.5 to 25 miles in search radar. V2 is switched Hartley oscillator, whose output is shaped by mvbr shaper V3-V4, for triggering blocking oscillator V5 to produce narrow marker pulses.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 55, p 55-2.



30-NSEC, 5,000-V, 30-AMP PULSES—Used for testing magnetic materials at narrow pulse widths. Four hard tubes in parallel drive

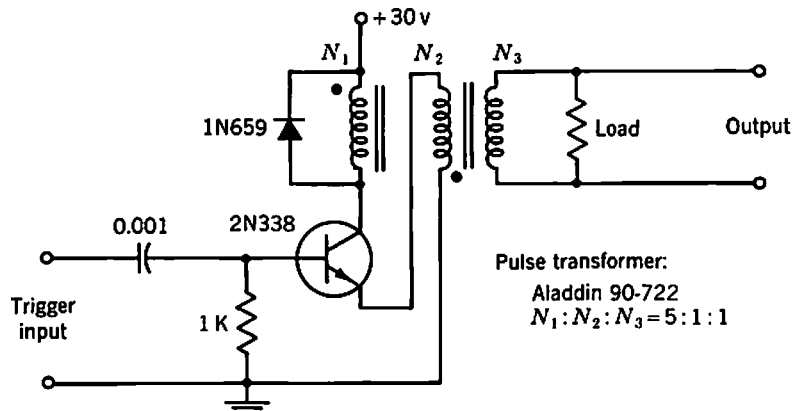
test cores with 0.1 megawatt peak power and give some degree of regulation during pulsing.—G. A. Reesor, *How Magnetic Ma-*

terials Behave at Nanosecond Pulse Widths, *Electronics*, 34:36, p 72-75.

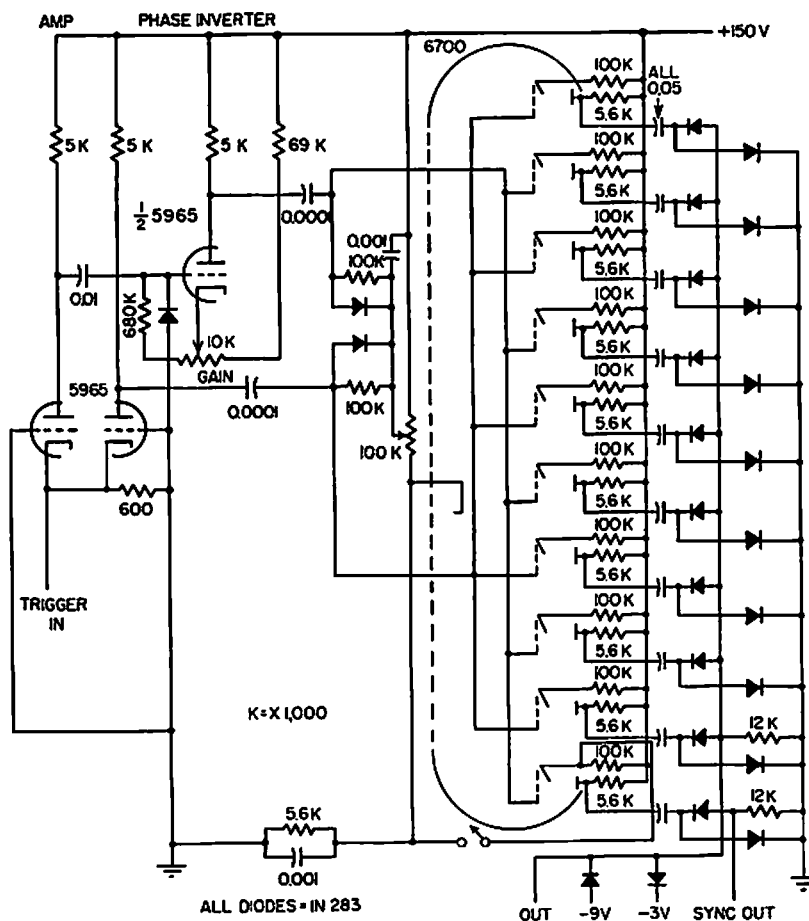


TRANSFLUXOR OSCILLATOR—Holds frequency setting for many hours after removal of control signal. Operates between 100 kc and 1 Mc. Gives square-wave output.—R. J. Sherin, Transfluxor Oscillator Gives Drift-Free Output, *Electronics*, 33:10, p 48-49.

COMMON-BASE BLOCKING OSCILLATOR—Nonsaturating-type triggered blocking oscillator is used when fast response is desired. Often used as pulse generator when waveform requirements are not critical.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 430.

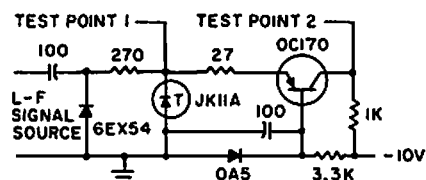


Pulse transformer:
Aladdin 90-722
 $N_1:N_2:N_3=5:1:1$

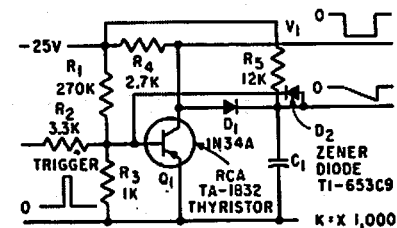


NINE-BIT WORD GENERATOR—Beam-switching tube provides arbitrary nine-bit words at pulse rates from few cps to 1 Mc, for testing digital systems. Output can be

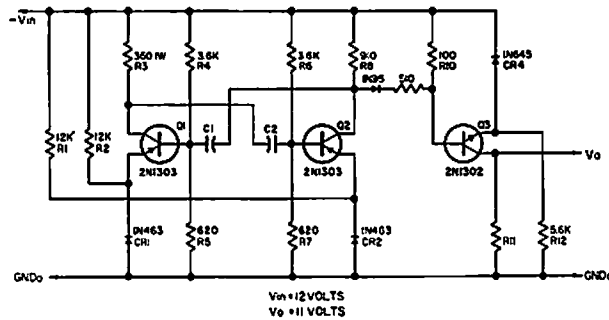
changed from spike to square wave by changing plug-in capacitors.—R. R. Hartel, Word Generator for Digital Testing, *Electronics*, 31:9, p 71.



TUNNEL-DIODE PULSE GENERATOR—Negative-resistance characteristics of tunnel diode gives fast-rise-time rectangular pulses, independently of signal frequency. Used here with common-base transistor amplifier.—G. B. Smith, Tunnel Diode Generates Rectangular Pulses, *Electronics*, 33:48, p 124-125.

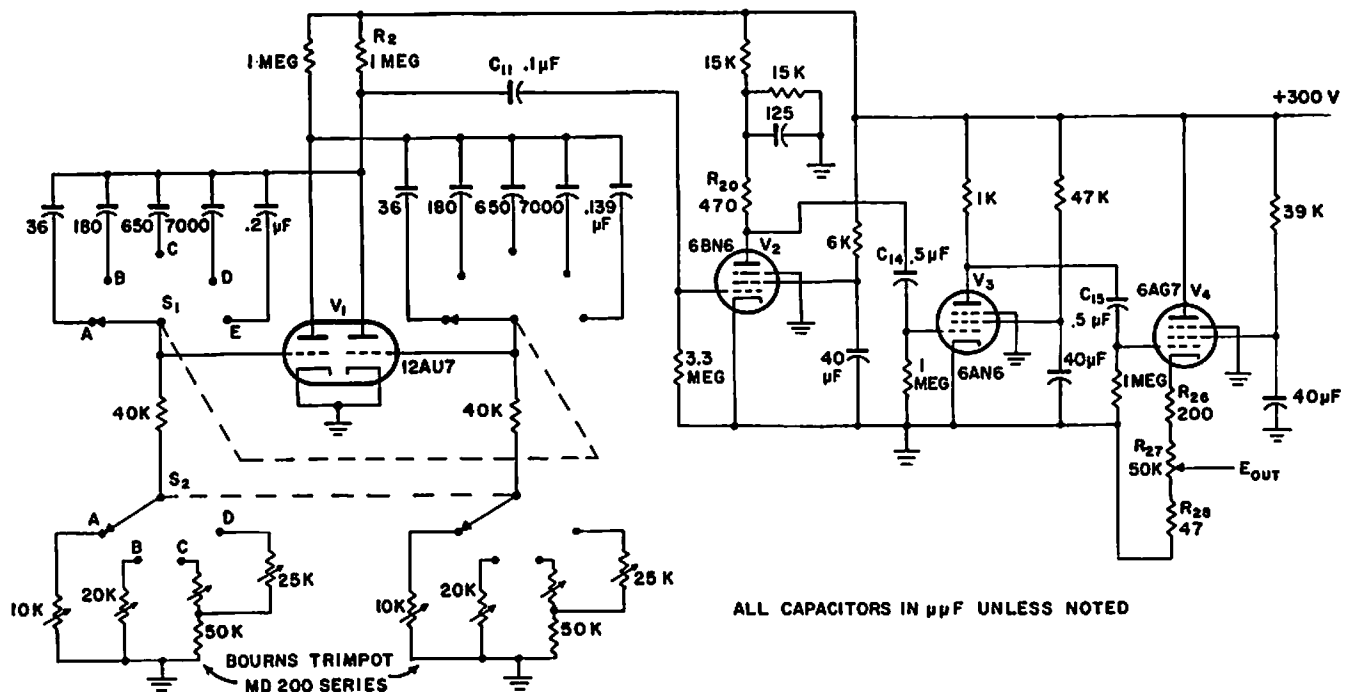
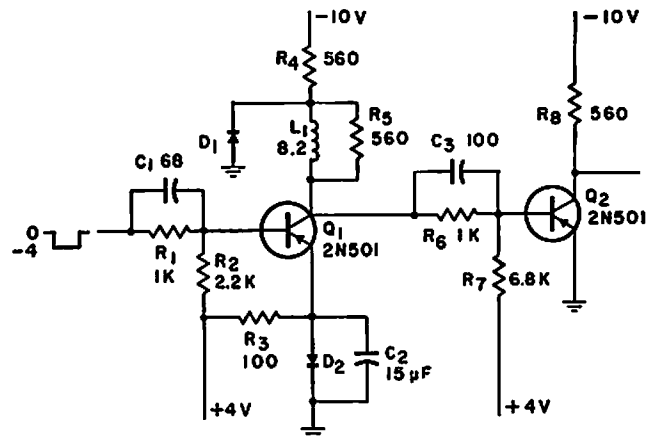


SIMPLE SQUARE-WAVE GENERATOR—Circuit performance is made independent of active elements by using transistor only as switch. For reliable operation, circuit requires extra 10% of output-pulse-width dead time between triggers.—C. A. Van Uff and R. W. Ahrons, How to Generate Accurate Sawtooth and Pulse Waves, *Electronics*, 32:50, p 64-66.



VARIABLE SQUARE WAVES—Output is adjustable from 0.5 cps to 60 kc at currents up to 150 ma, without appreciable corner rounding of waveform, with variable pulse width and variable interval between pulses, for driving flash lamps, relays, and computer gates.—J. D. Reed, Square Wave Generator with Variable On and Off Times, *EEE*, 10:10, p 27-28.

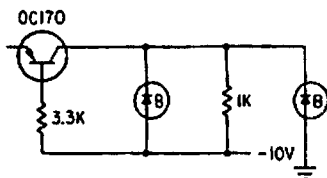
RINGING-TYPE PULSE GENERATOR—Used in some large high-speed computers to transmit pulses from central unit over long distances as d-c levels, then convert back to pulse forms. To convert level back to pulse, transistor switch is turned off by positive-going wavefront energizing ringing circuit. Input triggering of ringing stage is accomplished when definite threshold level is exceeded.—Pulse Generator for High-Speed Computers, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 75.



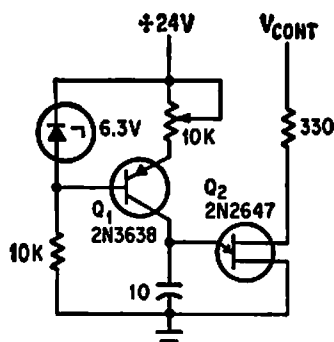
GATED-BEAM SQUARE-WAVE GENERATOR—Amplifies without attenuation up to tenth harmonic of square-wave fundamental, from which output of 50 to 500,000 pps can be adjusted over desirable range without waveform distortion. Uses twin-triode 12AU7 as symmetrical mvbr, 6BN6 as gated-beam

tube, 6AN6 as wideband amplifier, and 6AG7 as cathode-follower output. Operating frequencies are changed by S1 and S2. R27 adjusts output signal level from 0.8 to 8 v peak to peak. Rise time for 500-kc signal is better than 0.07 microsec. Provides 5 distinct repetition rates (50, 1,000, 10,000,

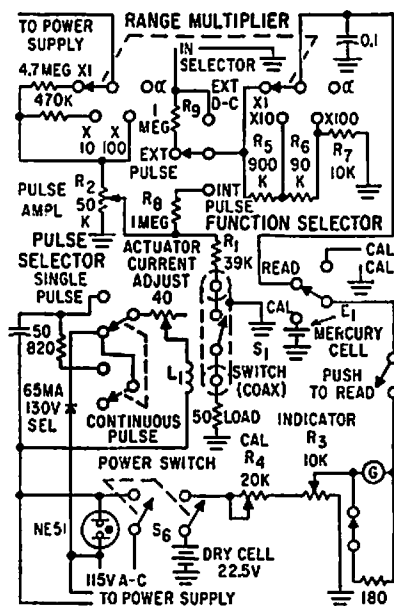
100,000, and 500,000 pps) for checking amplifiers up to at least tenth harmonic of fundamental repetition ratio.—Gated-Beam Tube Square-Wave Generator, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 175.



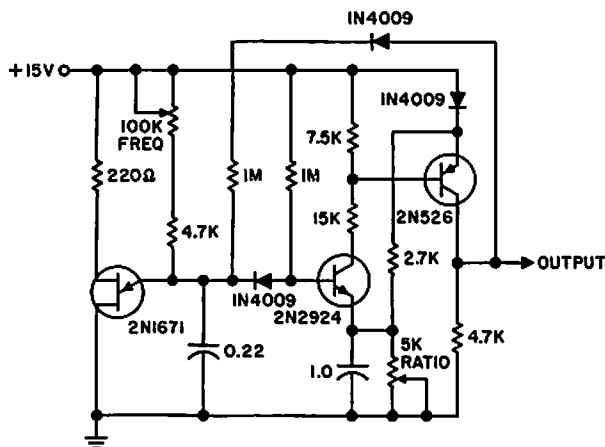
PULSE-SQUARING ZENERS—Addition of zener diodes to transistor amplifier fed by tunnel-diode pulse generator improves output waveform.—G. B. Smith, Tunnel Diode Generates Rectangular Pulses, *Electronics*, 33:48, p 124-125.



VOLTAGE-CONTROLLED PULSE SPACING—Unijunction transistor circuit generates train of pulses with constant pulse width but with spacing linearly adjustable over 20-to-1 range by voltage V , which varies trigger point of ujt Q2.—A. M. Ridenour and F. Turco, Unijunction Controls Spacing Between Pulses, *Electronics*, 39:14, p 82-83.

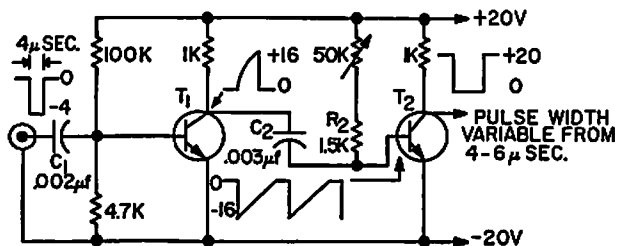


CALIBRATED MILLIMICROSECOND PULSER—Uses coaxial discharge line to produce precise short pulses on keyed single-shot basis or at constant repetition rate. Works into 50-ohm load.—E. J. Martin, Jr., Calibrated Source of Millimicrosecond Pulses, *Electronics*, 32:16, p 56-57.



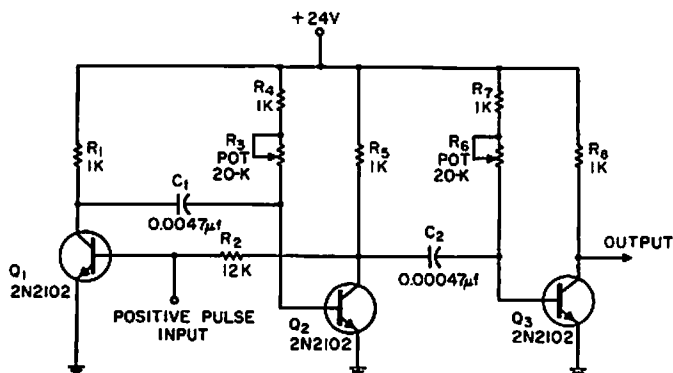
VARIABLE FREQUENCY AND VARIABLE DUTY CYCLE—Frequency and duty ratio can be varied independently to generate desired rectangular wave. Ujt is used in conventional sawtooth generator. Two transistors provide

positive-going output when ujt emitter voltage exceeds npn emitter voltage. Frequency range is 60 to 1,000 cps.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 344.



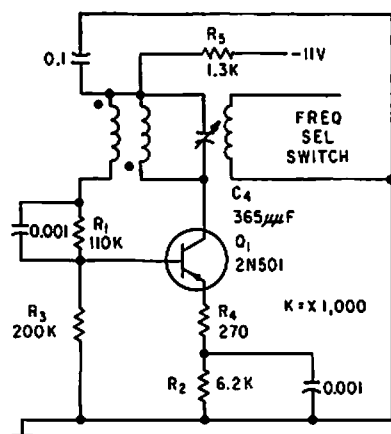
VARIABLE-PULSE-WIDTH GENERATOR—Converts fixed negative pulse-width input to variable and stable pulse width. Will accept positive inputs if T1 and T2 are changed from 2N1308 to 2N1309 and collector voltages reversed. Will operate at repetition

rates from 30 cps to 2 Mc, with pulse widths from 600 microsec to 100 nsec. With values shown, maximum duty cycle is 92% with 63-microsec input rep rate.—H. D. Flagle, High-Duty-Cycle Pulse-Width Generator, *EEE*, 11:8, p 27-28.

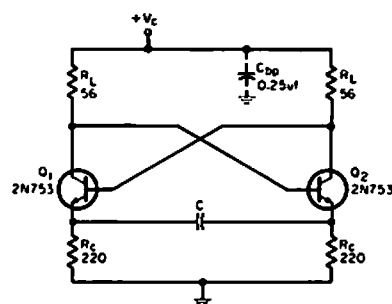


DELAYED OUTPUT PULSE—Only three transistors are required to generate output rectangular pulse that is delayed a predetermined time after arrival of input pulse. Delay time is determined by C1, R3, and R4,

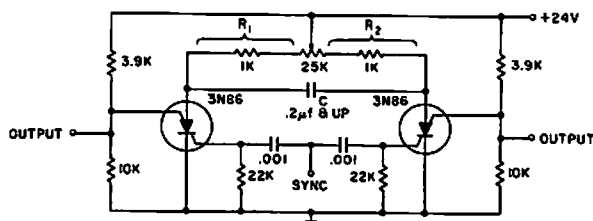
and is about 10 microsec for values shown. Output pulse width is also about 10 microsec.—T. R. Ferrara, Delayed Pulse Generator, *EEE*, 13:10, p 71.



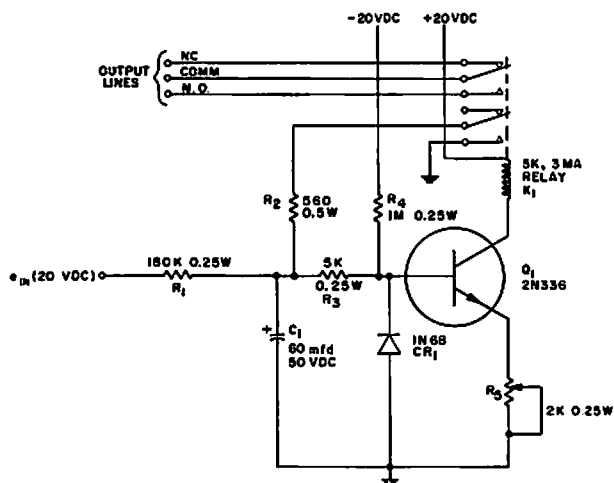
BASIC HARTLEY—Sine-wave oscillator, tunable over 3:1 frequency range by C4, uses switching transistor. Used in pulse generator for testing high-speed digital computers.—L. Neumann, *Transistorized Generator for Pulse Circuit Design*, *Electronics*, 32:14, p 47-49.



MULTI-WAVEFORM OSCILLATOR—By varying collector load, emitter resistors, and C, oscillator can produce triangular wave, square wave up to 30 Mc, microwatt audio signal, or serve as voltage-controlled oscillator. Values shown, with 6-v supply and 0.01 mfd for C, give 0.8 v peak-to-peak square wave at about 1 Mc.—P. Lefferts, *Multi-Oscillator Gives Simple Waveforms, 30-Mc Output*, *EEE*, 12:10, p 60.

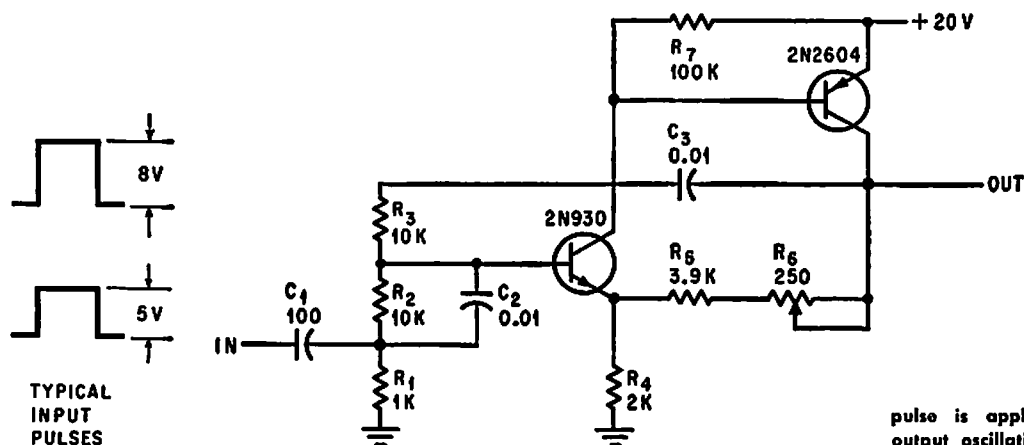


SCS SQUARE-WAVE GENERATOR—R1-C determines half the period, and R2-C the remainder. R1 should equal R2 for square-wave output. Potentiometer varies pulse width without affecting frequency. Outputs are equal and oppositely phased.—*Transistor Manual*, Seventh Edition, General Electric Co., 1964, p 434.



STEPPING SWITCH PULSER—Used to advance stepping switch automatically at predetermined rate, in automatic test equipment providing go-no-go indications. C1 controls output pulse width to give 80-msec on time,

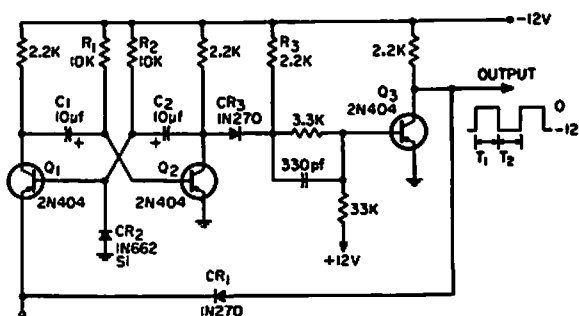
for reliable actuation of switch that normally requires 20 millise. Range of off time, controlled by R5, is 20 millise to 7 sec.—C. Wilson, *Step Switch Pulser*, *EEE*, 10:11, p 26-27.



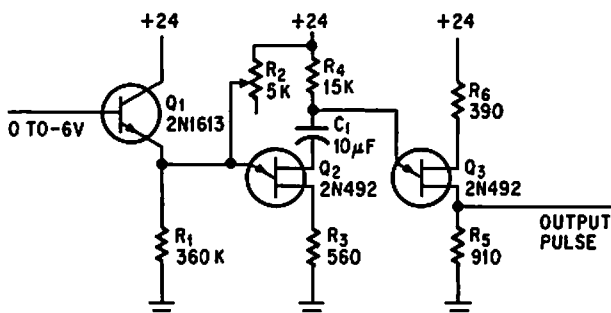
PULSED OSCILLATOR—Circuit is pulsed on only when required, as in tone generators where output is needed only occasionally

and power must be conserved. Uses Wien-bridge oscillator having range of 100 cps to 100 kc, which operates only when gating

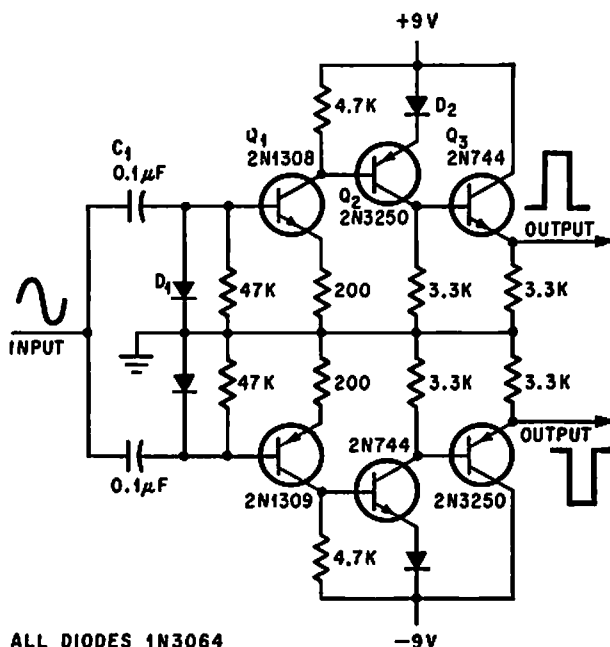
pulse is applied to input. Amplitude of output oscillations varies with amplitude of gating pulse.—R. C. Lavigne and L. L. Kleinberg, *Pulsed Oscillator Conserves Power*, *Electronics*, 39:17, p 98-99.



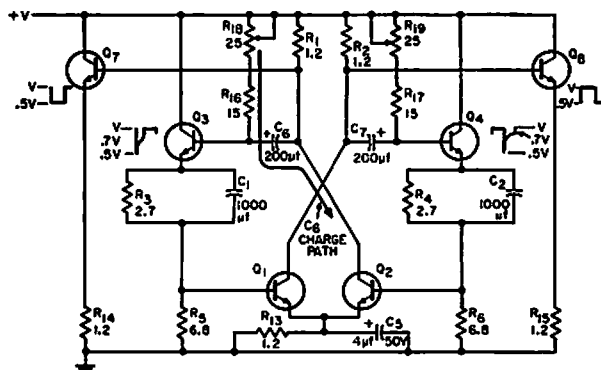
GATED ASTABLE PULSE TRAIN GENERATOR—Uses standard logic circuits with self-completing action to produce pulse train having any desired integral number of pulses. First pulse starts when S1 is closed, and last pulse is completed when S1 is open. Self-completing action is produced by or function of S1 and CR1.—M. Neidich, Self-Completing Gated Astable, *EEF*, 13:1, p 66 and 75.



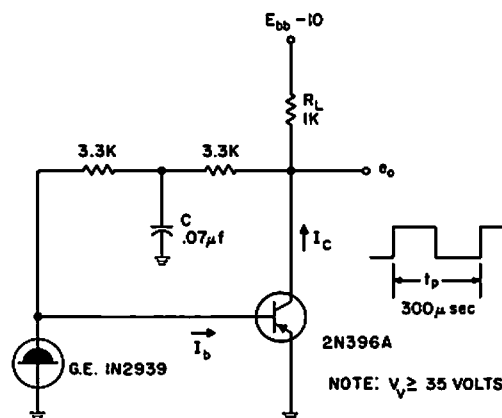
LEVEL DETECTOR—Provides constant-width pulses at fixed repetition rate whenever input signal exceeds predetermined level. Maximum current drawn from signal source is only 35 microamp.—J. G. Poddie, Two Unijunctions Form Low-Cost Level Detector, *Electronics*, 39:8, p 94.



ALL DIODES 1N3064

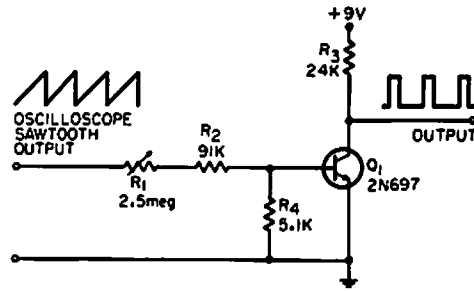


VARIABLE PULSER—Operates with any power supply voltage from 1.5 to 20 v, and generates symmetrical or nonsymmetrical low-impedance pulses from 0.5 ppm to above 200,000 pps. Used for controlling repetitive operation of certain analog computers, and as source for checkout of digital circuits.—J. V. Gaudiosi, Variable Pulse Generator, *EEF*, 11:2, p 27-28.

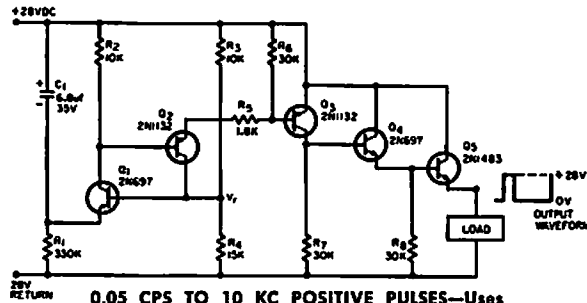


ASTABLE HYBRID TD-TRANSISTOR SQUARE-WAVE GENERATOR—Fast switching capability of tunnel diode contributes to clean output waveform of mvbr configuration.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 367.

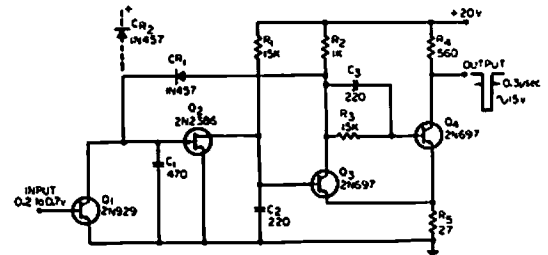
LOW-COST SQUARE WAVES—Conversion circuit coupled to sine-wave audio oscillator gives square-wave generator at half usual cost. Will shape sine waves up to 3 Mc before trailing edges of square-wave output begin to deteriorate. Can be triggered by input signals from 0.2 to 10 v. Positive input turns on D1 and Q1, driving amplitude-limiting pair Q2-D2 into conduction to square up waveform. Lower half of circuit generates negative square pulse in negative manner.—R. S. Sellock, Converting Audio Oscillators to Square-Wave Generators, *Electronics*, 39:16, p 123.



VARIABLE WIDTH AND VARIABLE PRR—Gives wide range of control over pulse width and pulse repetition rate, while maintaining synchronization with oscilloscope. Can be constructed with banana plugs for sawtooth output jacks of scope.—R. G. Rakes, Simple Variable Width, PRR Pulse Generator, *EEE*, 13:11, p 45-46.



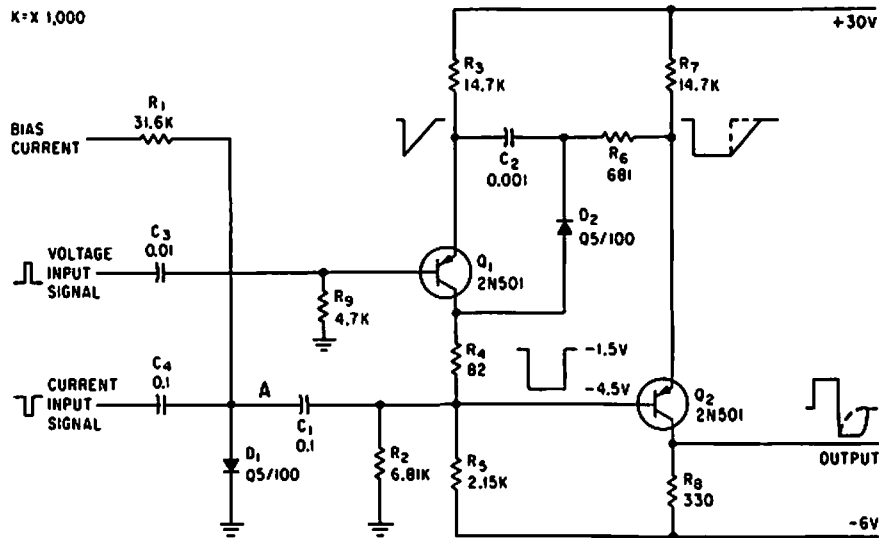
0.05 CPS TO 10 KC POSITIVE PULSES—Uses two-transistor equivalent circuit for double-base diode, to give better reliability and more uniform performance in recycling timers, indicator readouts, and switching regulators. With parameter values shown, frequency is about 1 cps and pulse width is 30 millisec.—G. B. Mahoney, Low-Frequency Pulse Generator, *EEE*, 12:6, p 63-64.



VOLTAGE-CONTROLLED PRR—Input change of only 0.5 v will change pulse repetition rate of generator by factor of more than 1 to 10,000,000. Output pulse is about 0.3 microsec wide, with rise and fall times of 20 nsec. Addition of back-biased diode CR2 extends low-frequency limit below 0.05 pps.—G. Richwell, Wide-Range Voltage-Controlled Pulse Generator, *EEE*, 13:10, p 72-77.

CHAPTER 65

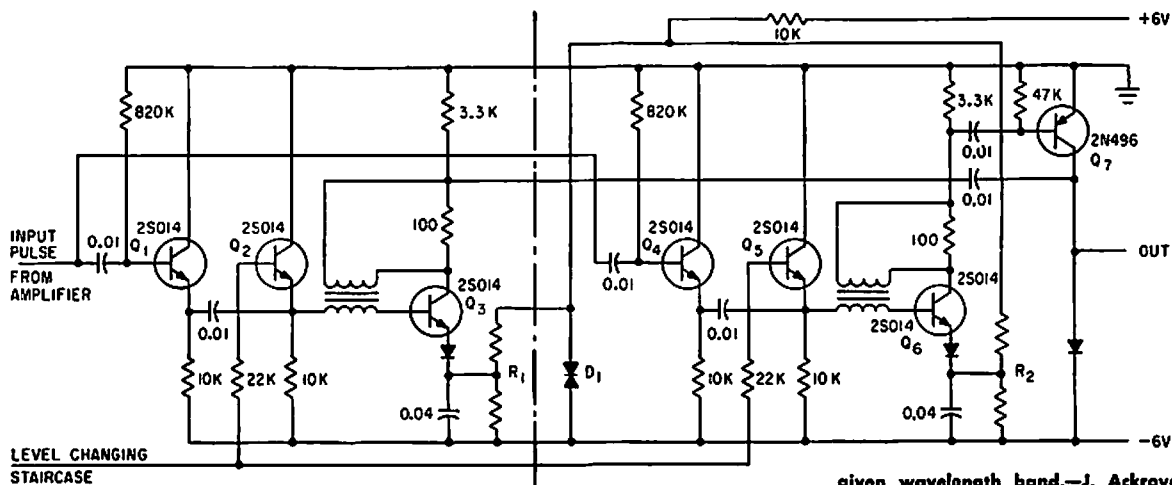
Pulse Height Analyzer Circuits



AMPLITUDE DISCRIMINATOR—Used in nuclear physics to determine whether voltage or current input pulse is above or below predetermined level. Triggering threshold is

set by adjusting bias of diode D1. Accuracy is within 1 microamp at 50-microamp triggering level, or within 0.4 mv at 10-mv triggering level.—F. S. Goulding and L. B. Robin-

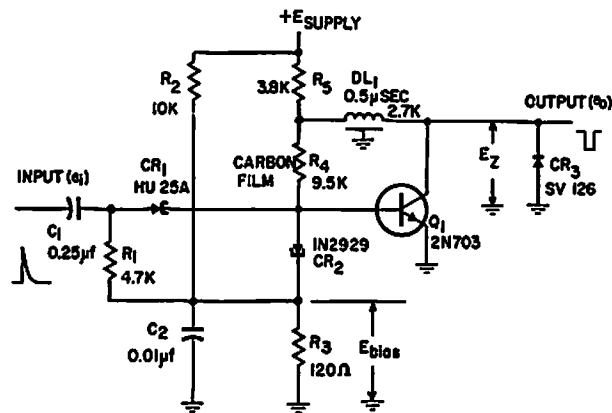
son, Achieving Discriminator Levels with a Biased Input Diode, *Electronics*, 33:21, p 89-91.



PULSE-HEIGHT DISCRIMINATOR—Selected pulses pass through timing gate Q7 (open

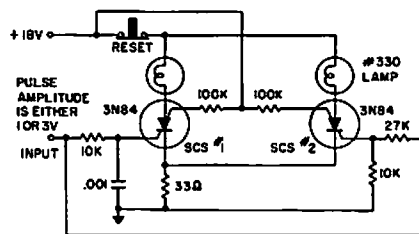
for fixed time) to counter that registers average intensity of solar radiation falling in

given wavelength band.—J. Ackroyd, Orbiting Spectrometer Plots Solar X-Rays, *Electronics*, 34:43, p 55-57.

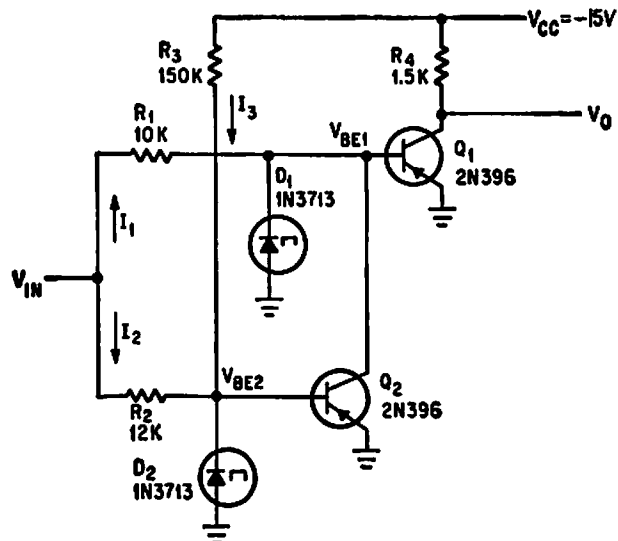


PULSE HEIGHT DISCRIMINATOR—Delivers trigger pulse when input signal reaches predetermined threshold. Used in nuclear counting experiments, in satellite and rocket ap-

plications where sensitivity and stability are essential.—R. H. Wagner, *Stable, Sensitive Pulse Height Discriminator*, *EEE*, 10:7, p 28-29.

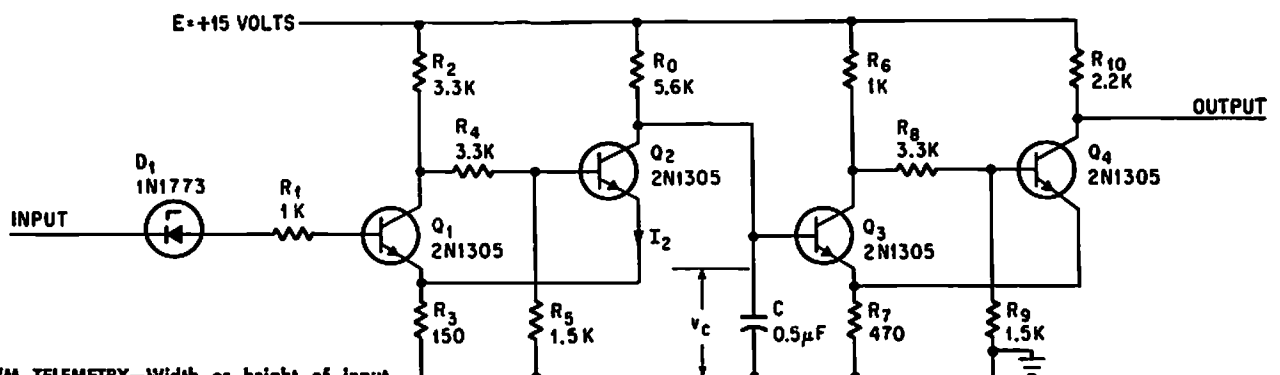


PULSE AMPLITUDE DISCRIMINATOR—Input of 1 v triggers SCS1 but not SCS 2. Input of 3 v is delayed in reaching SCS1 by R-C integrating network and therefore triggers SCS2, which raises common-emitter voltage to prevent SCS1 from triggering.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 433.



VOLTAGE DISCRIMINATOR—Output changes sharply when input signal rises above preset threshold such as -10 v, with circuit returning to initial state when input reaches still higher threshold such as -11 v. Width of resulting

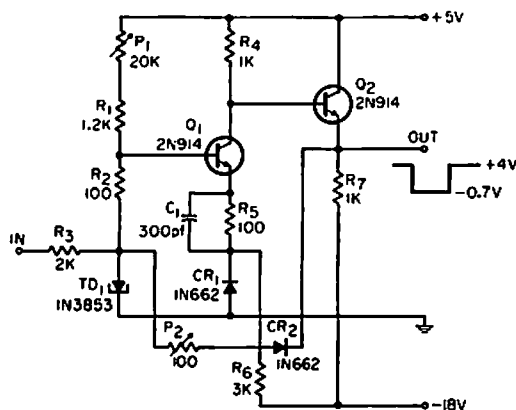
output pulse can be changed by varying R1, R2, or R3.—C. D. Todd, *Sharp Discrimination of Voltage Differences*, *Electronics*, 38:19, p 97-98.



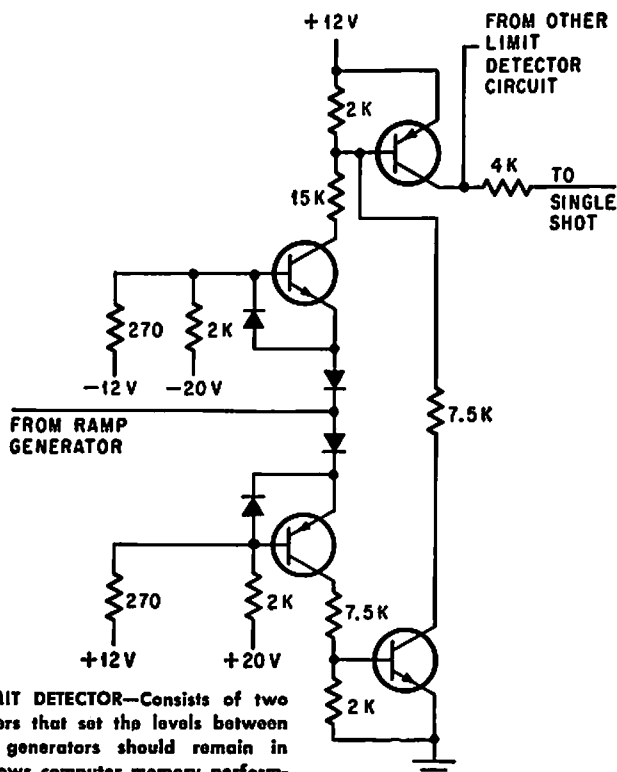
PWM TELEMETRY—Width or height of input pulse determines whether trigger Q1-Q2 will be on long enough for C to charge to voltage

that will make trigger Q3-Q4 produce output pulse.—R. G. Ferrie, *One Discriminator*

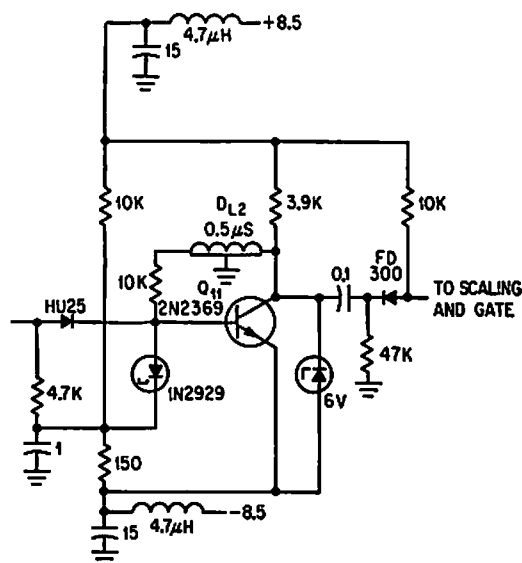
Senses Pulse Width and Height, *Electronics*, 38:8, p 90-91.



TUNNEL-DIODE DISCRIMINATOR—Td1, biased close to its peak current, is connected to d-c amplifier to overcome inherent limitations of tunnel diode. When input voltage makes current through R3 exceed diode peak current, diode switches to its high-voltage state of 0.5 v. Q1 amplifies this change by factor of 10, and Q2 initiates start of square-wave output. When input drops, circuit and diode revert to original state. Rise and fall times are about 10 nsec.—G. Marosi, Versatile Tunnel-Diode Discriminator, *EEE*, 14:5, p 120.



diodes are 1N921.—J. E. Gersbach, The Great Schmoa Plot: Testing Memories Automatically, *Electronics*, 39:15, p. 127-134.

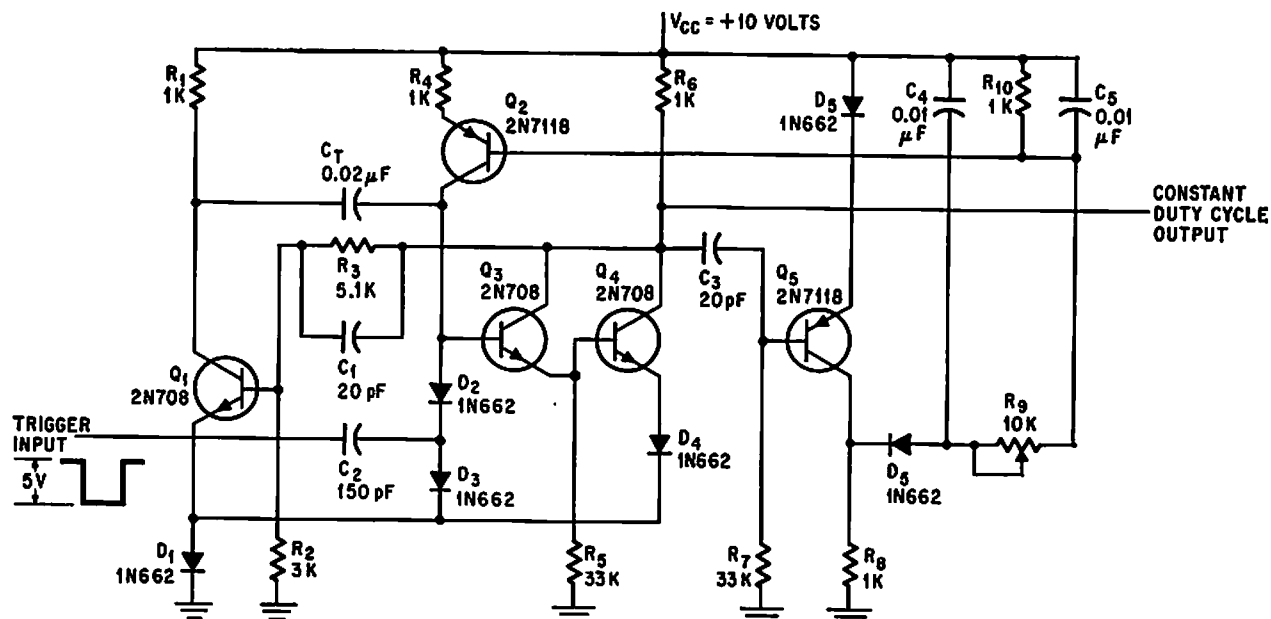
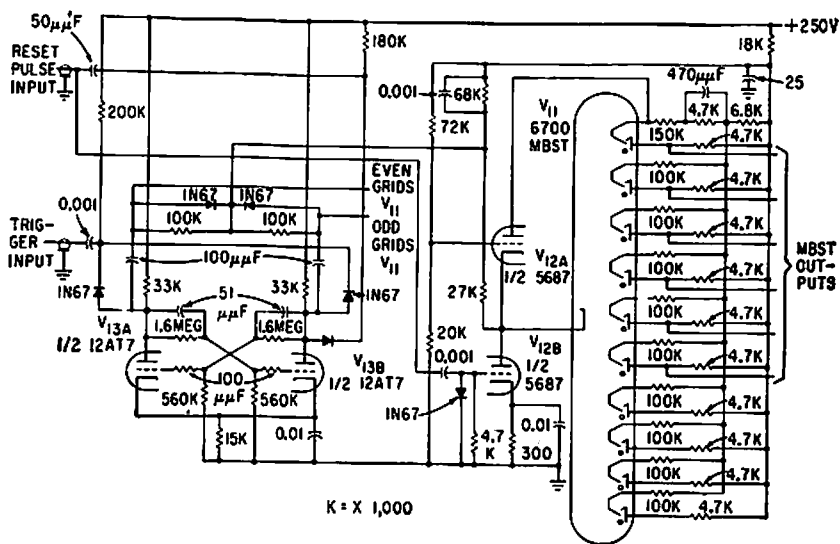


PULSE HEIGHT SENSOR FEEDS SCALER—Tunnel-diode 1N2929 senses pulse height through isolating unijunction diode HU25. Voltage input signals above preset threshold give negative output pulse for scaler.—R. Cuikey and T. Callhan, Orbiting Observatory to Measure Stars' Dim Light, *Electronics*, 37:9, p 28-31.

CHAPTER 66

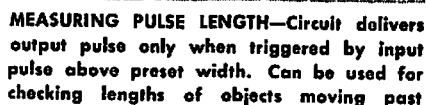
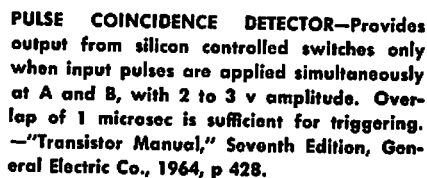
Pulse Processing Circuits

MAGNETRON BEAM SWITCHING—First trigger pulse switches beam to first position, and each succeeding trigger advances beam one position, until reset pulse zeroes V11 for repetition of switching sequence. Succeeding switching-circuit output pulses are thus time-coincident with succeeding amplitude portions of input signal, for pulse amplitude measurement.—J. F. Lyons, Jr., Analyzing Multipath Delay in Communications Studies, *Electronics*, 32:36, p 52-55.

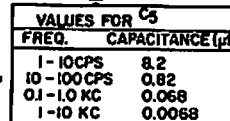
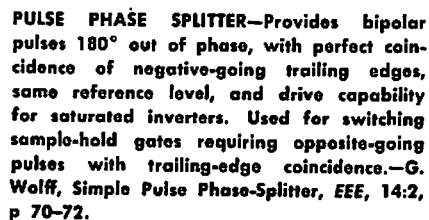
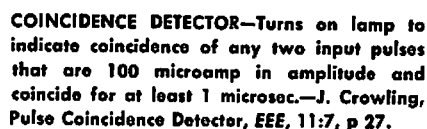


CONSTANT DUTY CYCLE—Width of output pulse varies with frequency to keep duty cycle constant at preadjusted value from 25% to 75%, over input trigger range of 100 to 5,000 pps. Q1, Q3, and Q4 form one mvbr, and Q5 is second mvbr. Q2 is voltage-

to-current converter.—G. P. Klein, Duty Cycle is Constant at any Trigger Frequency, Electronics, 38:15, p 62-63.



photocell.—K. R. Whittington and G. Robson, Versatile Discriminator Measures Pulse Length, *Electronics*, 35:31, p 48.

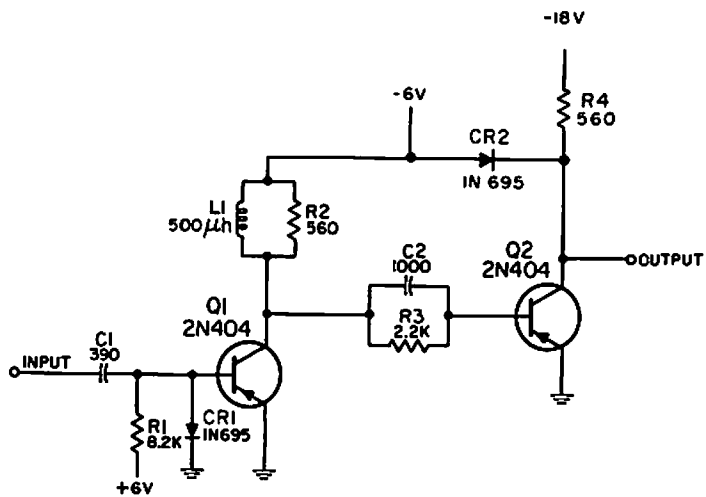


LINEAR PULSE WIDTH CONTROL—Blanking pulse generator provides blanking signal starting with input pulse and remaining on for some nominal portion of pulse, regardless of dropouts due to noise in triggering

pulse.—Blanking Pulse Generator with Linear Pulse Width Control, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 78.

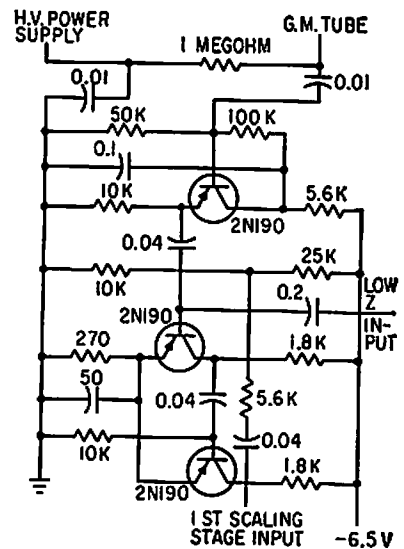
CHAPTER 67

Pulse Shaping Circuits

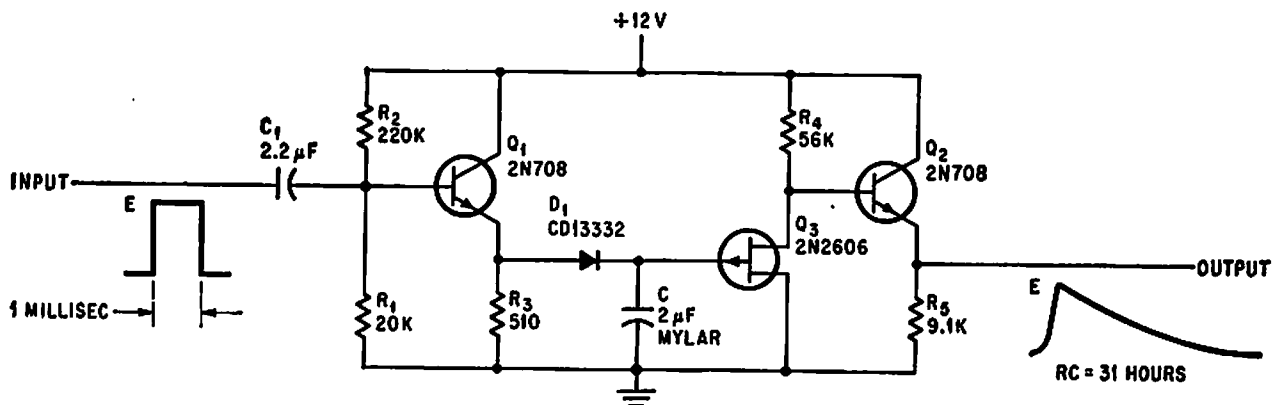


PREFERRED PULSE SHAPER—Generally used with monostable mvbr, to form 1-microsec pulse at end of delay period. Also used to reshape pulse that has suffered deterioration by passage through long chain of gates, or to produce 1-microsec pulse whose leading

edge coincides with trailing edge of a positive pulse, for delaying output by width of input pulse.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 11 (originally PC 214), p 11-2.



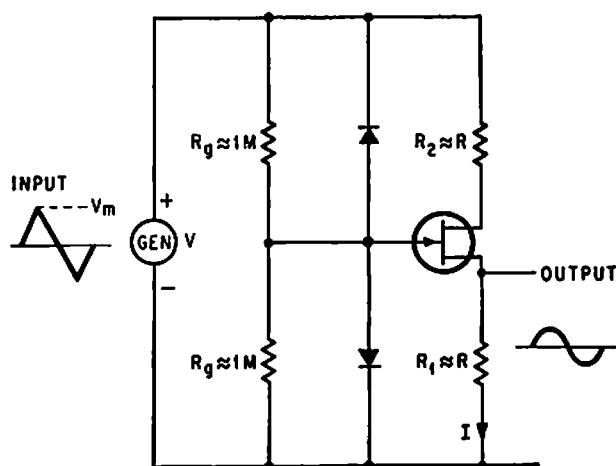
G-M DRIVE FOR SCALE-OF-64 COUNTER—Shapes pulses from Geiger-Muller tube and uses one-shot mvbr to drive first scaling stage.—F. E. Armstrong, *Battery Powered Portable Scaler, Electronics*, 33:19, p 74-75.



1 MILLISEC TO 30 HOURS—Input and output emitter-followers isolate fet. Extremely

low leakage current through D1 and fet Q3 give circuit 30-hour time constant.—M. E.

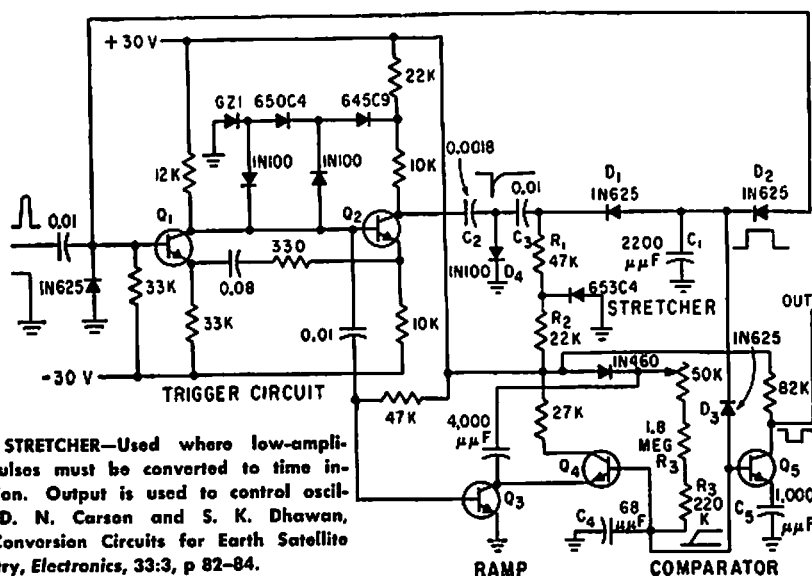
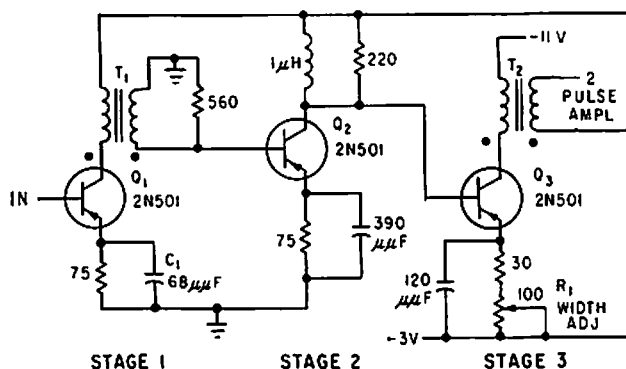
McGeo, FET Circuit Stretches 1-MSEC Pulse to 30 Hours, *Electronics*, 38:7, p 87-88.



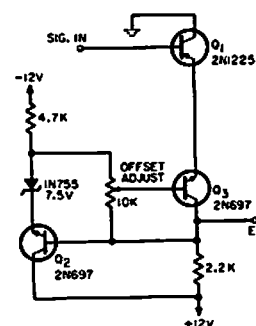
FET CONVERTS TRIANGULAR TO SINE—Eliminates need for signal rectification by using symmetrical properties of Fairchild FSB40 fet with respect to source and drain. For

p-channel units, reverse the diodes.—R. D. Middlebrook and J. Richar, *Nonreactive Filter Converts Triangular Waves to Sines*, *Electronics*, 38:5, p 96-101.

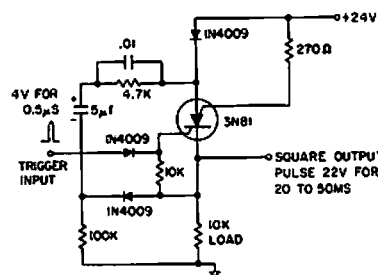
PULSE FORMER AND SHAPER—Converts sine-wave input to 40 millimicrosec output. Used in versatile pulse generator for testing high-speed computer circuits. Input frequency range is 3 to 20 Mc.—L. Numann, *Transistorized Generator for Pulse Circuit Design*, *Electronics*, 32:14, p 47-49.



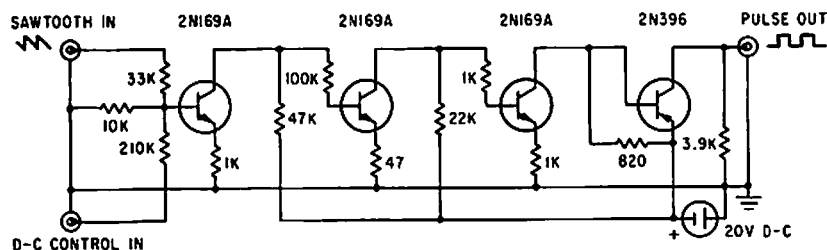
PULSE STRETCHER—Used where low-amplitude pulses must be converted to time information. Output is used to control oscillator.—D. N. Carson and S. K. Dhawan, *Data Conversion Circuits for Earth Satellite Telemetry*, *Electronics*, 33:3, p 82-84.



ADJUSTABLE D-C LEVEL SHIFTER—Shifts d-c level of signal accurately and continuously without affecting gain, from +4 v to +7 v d-c center-voltage output. Input a-c signal varies 2 v about +4 v d-c. Other offset voltages can also be obtained.—H. Anway, *Continuously Adjustable DC Level Shifter*, *EEE*, 12:10, p 59.

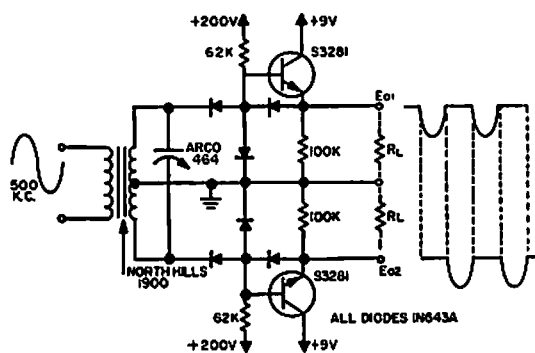


SCS PULSE STRETCHER—Stretch interval is determined by 5-mfd capacitor and 4.7K resistor.—“Transistor Manual,” Seventh Edition, General Electric Co., 1964, p 435.

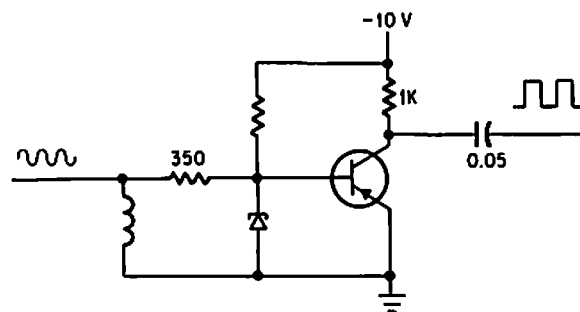


SAWTOOTH CLIPPER—High-gain amplifier converts sawtooth input to rectangular output pulse whose width is proportional to portion of sawtooth amplitude that is above

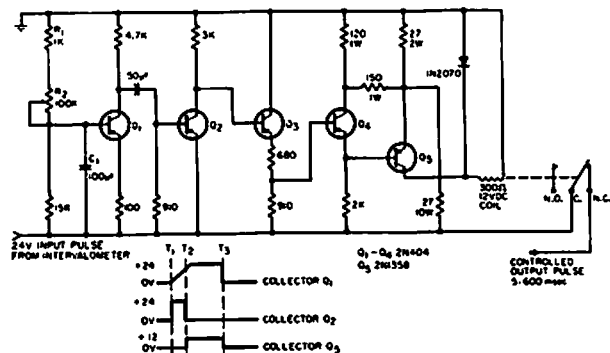
threshold level.—B. E. Mathews and F. R. Sias, Jr., Testing Space Craft with Induction Heaters, *Electronics*, 35:34, p 38-41.



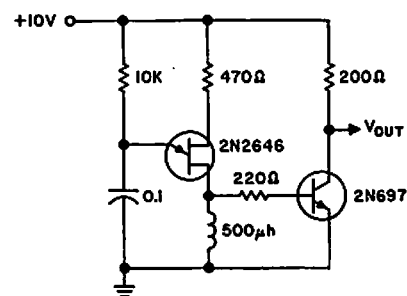
500 KC TO 1 MC D-C RESTORER—Modified clamp circuit is used with 500-kc sine-wave input to provide complete restoration of reference potential for 1-Mc half-wave output.—H. Kundrat, Jr., High Frequency DC Restoration with Gain, *EEE*, 11:10, p 26-27.



SINE TO SQUARE WAVES—Japanese Esaki or tunnel diode acts like Schmitt trigger in converting sine-wave input signal to square-wave pulse train.—T. Kojima and M. Watanabe, When You're Second, You Try Harder, *Electronics*, 28:25, p 81-89.



PULSE-LENGTH CONTROLLER—Reduces duration of intervalometer pulse from 400 millisec to 100 millisec without affecting intervalometer operation for other purposes. Used to control airborne strip-chart camera.—J. S. Peddo, Low-Cost Pulse-Length Controller, *EEE*, 12:7, p 26.

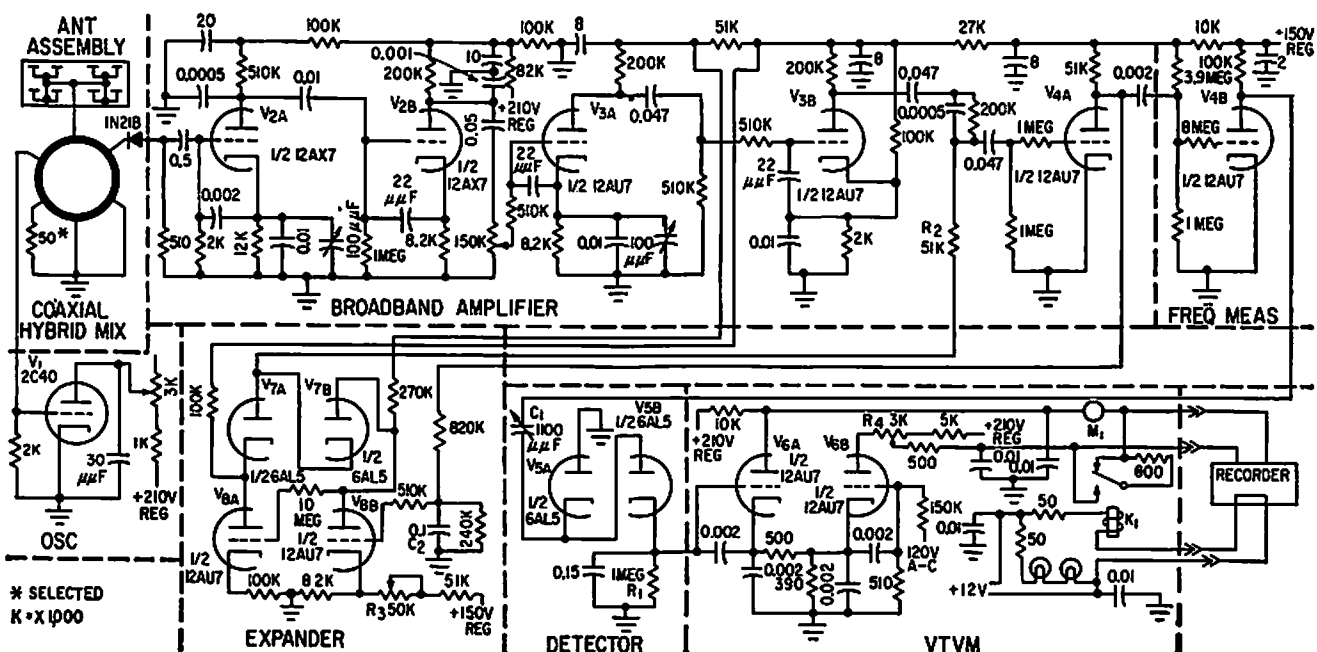
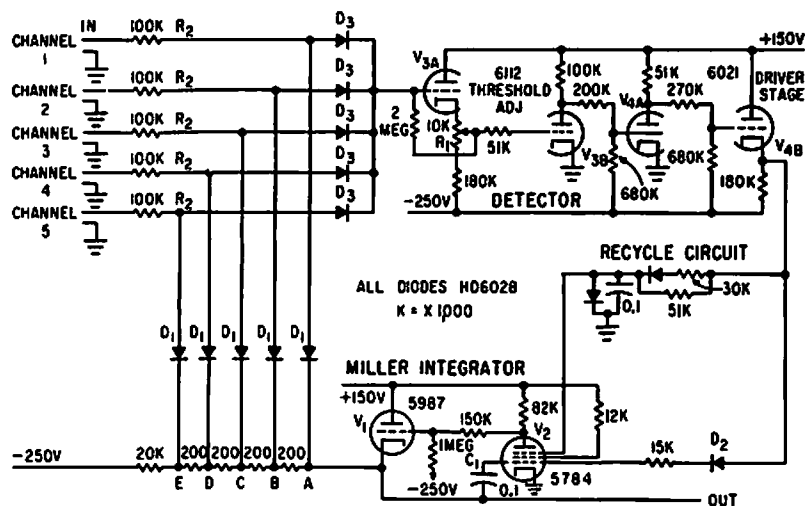


UJT PULSE SHAPER—Use of inductance in ujt relaxation oscillator gives significant improvement in output pulse shape over that of conventional resistance-coupled circuit. Pulse width for various transistors is between 11 and 12 microsec, and rise and fall times are typically 0.3 microsec. With 47-ohm resistor in place of inductance, fall time would be 3 microsec.—“Transistor Manual,” Seventh Edition, General Electric Co., 1964, p 316.

CHAPTER 68

Radar Circuits

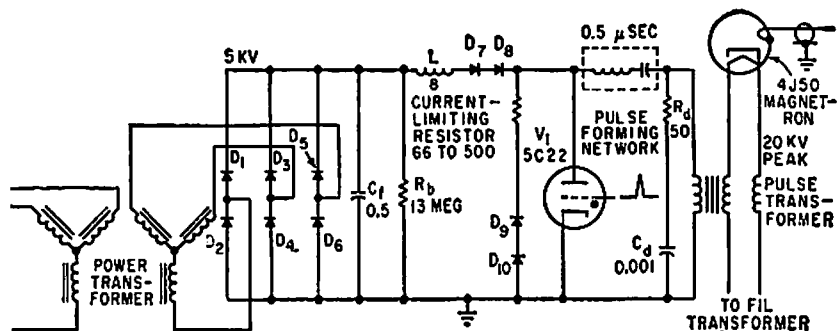
MULTICHANNEL MONITOR—Automatically detects single signal coming from large number of separate sources and identifies source, as required in doppler radar sets that must search bank of sharp filters placed side by side, to detect target, while antenna scans field of search. Positive signal reaching detector is amplified to drive Miller integrator V1-V2. As V1 goes negative, it disconnects one channel at a time (by driving its disconnecting diode D1 below 0 v) until live channel is reached. Detector output is then cut off, and C1 stores level at which disconnect occurred.—R. Kronlage, *Monitoring Multiple Inputs Simultaneously*, *Electronics*, 32:35, p 50-51.



RADAR SPEED METER—Translates doppler or difference frequency between transmitted and received frequencies into mph and displays

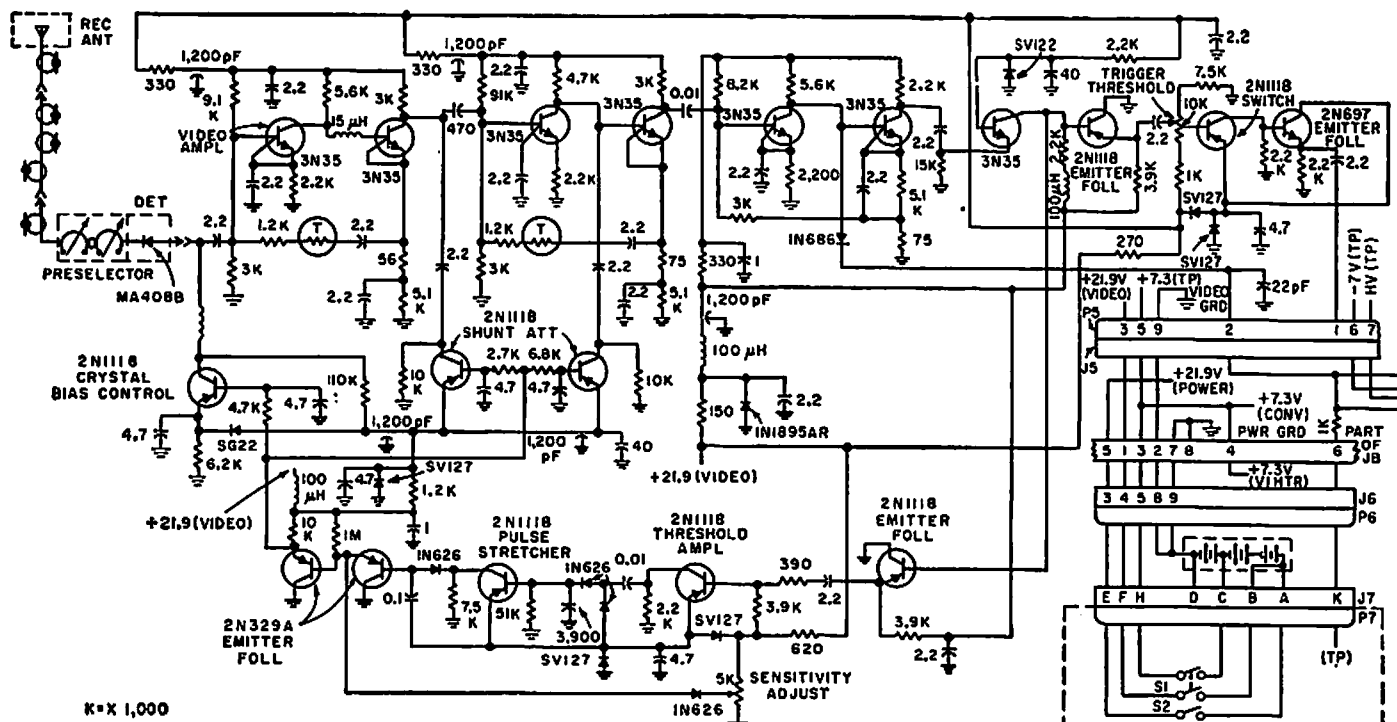
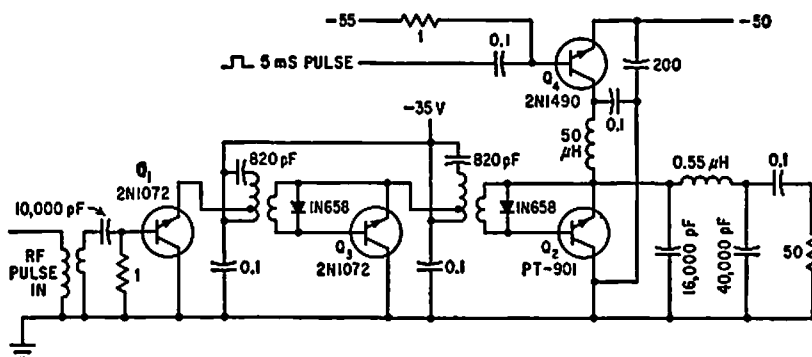
on meter or records on strip chart. Operates at 2,455 Mc and is accurate within 2 mph up to 100 mph.—J. Barker, *Radar Meter Helps*

Enforce Traffic Laws, *Electronics*, 32:10, p 48-49.



250-KW MODULATOR—Ten silicon diodes replace five vacuum tubes in artificial line-type modulator for airborne radar operating at peak power of 250 kw.—M. G. Gray, *Using Silicon Diodes in Radar Modulators*, *Electronics*, 32:24, p 70-72.

RADAR POWER AMPLIFIER—Handles pulses in range of 100 to 500 microsec at 2.2 Mc. Class B final stage Q2 delivers 105 w to pi loading network serving as 51-ohm load.—S. Horowitz and L. Humphrey, *Satellite Sounder and Telemeter Chart Ionosphere Electron Density*, *Electronics*, 34:25, p 50-53.

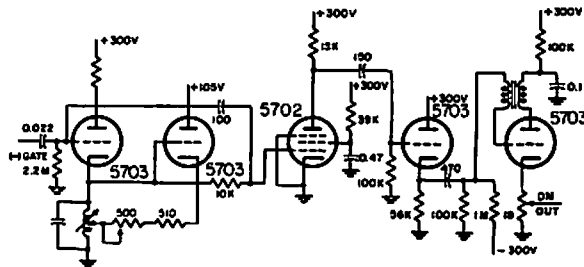
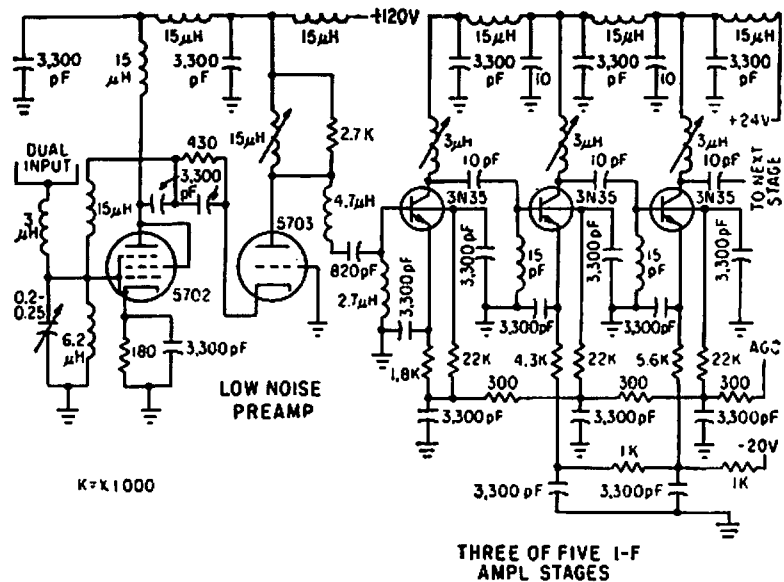


RADAR TRANSPONDER—Used in missiles for tracking during test firing on range. Semiconductor modulator minimizes power drain and thereby reduces heating in transponder, while providing r-f output pulse having ex-

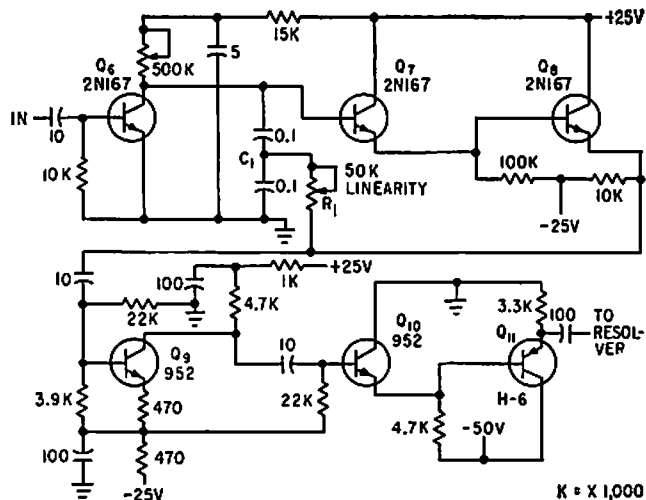
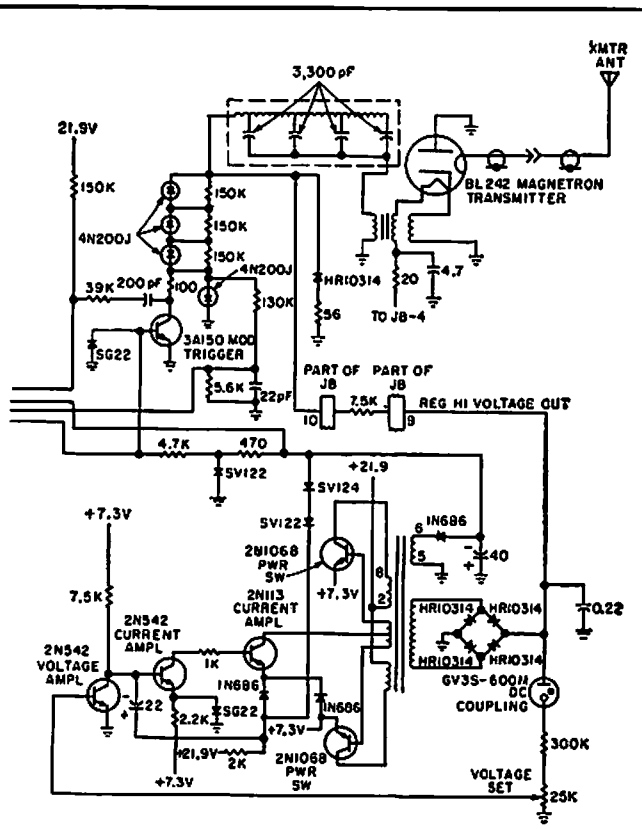
tremely fast rise and fall times. Delay stability gives range accuracy of 1 yard.—L. Diven, *Solid-State Modulator Feeds Subminiature Transponder*, *Electronics*, 33:27, p 48-51.

P7 MUST BE WIRED TO AN EXTERNAL SWITCH OR RELAY AS SHOWN

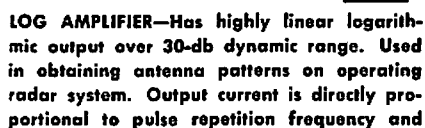
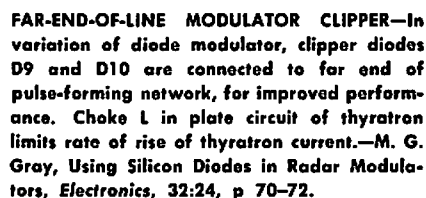
HYBRID 30-MC I-F—Bandwidth is 6 Mc, noise figure is below 2.5 db, and gain is enough to give 1 v peak-to-peak noise output into 1,000-ohm load when using two transistorized video stages following the five transistorized i-f gain stages.—J. Scott, D. Randise, and R. P. Lukacovic, *Portable Radar Traces Battlefield Deployment, Electronics*, 33:12, p 67-70.



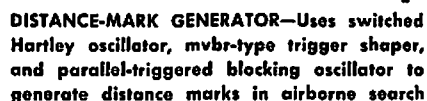
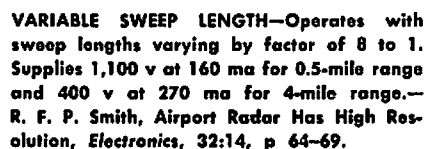
DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, pentode amplifier-shaper, and series-triggered blocking oscillator to generate 20-mile distance marks in airborne search radar.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N8-2.



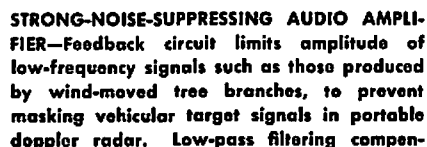
SWEEP GENERATOR—Accepts pulse from monostable mvbr and generates signal for sweep resolver of ppi radar. Voltage rises at constant rate during mono off time, and is held at zero during on time. Cascaded emitter-follower Q7-Q8 provides impedance match to output. Q9-Q10-Q11 provide required power for sweep resolver while preventing thermal runaway at normal temperatures.—C. E. Voazio, *Transistorized Radar Sweeps Circuits Using Low Power, Electronics*, 32:26, p 46-47.



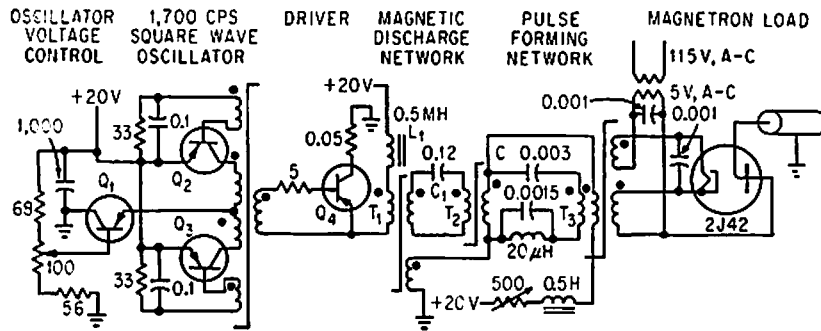
log of peak r-f pulse power.—Logarithmic Amplifier for Radar Signals, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 107.



radar. RLC unit is switched to change mark spacing.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N8-1.

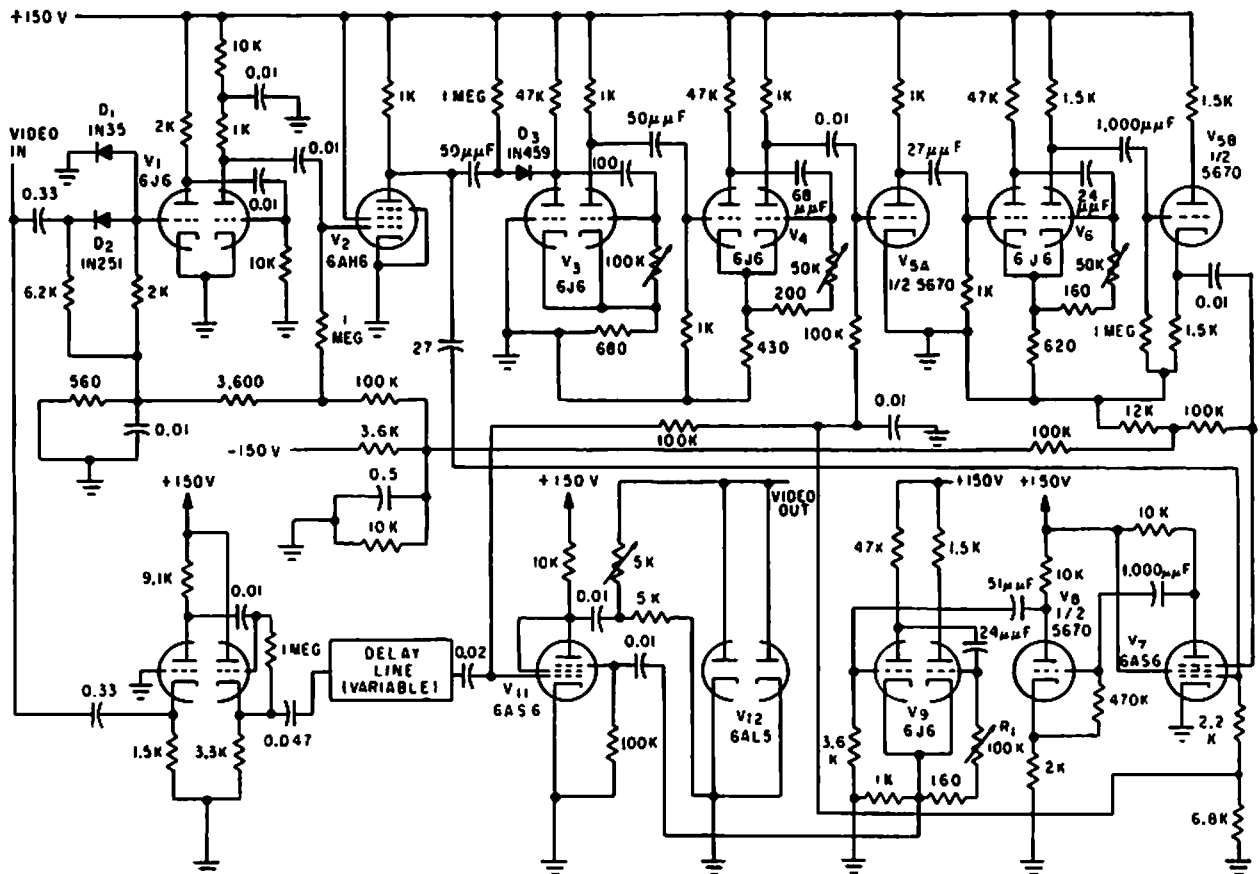
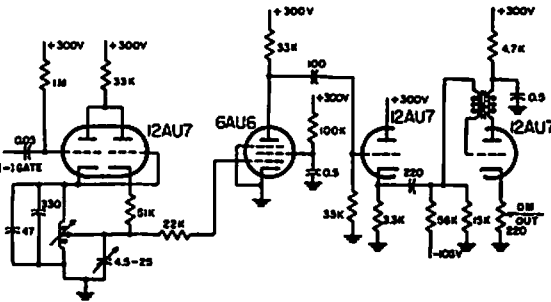


sates for poor bass response of human ear, permitting detection of slow-moving targets such as walking man.—J. Scott, D. Randise, and R. P. Lukacovic, *Portable Radar Traces Battlefield Deployment, Electronics*, 33:12, p 67-70.



HIGH-POWER PULSE GENERATOR—Power transistors and saturable transformers serve in place of hydrogen thyatrons for generating pulses with 1-megawatt peak power for sonar and radar. Low-voltage capacitor is first charged to voltage that is regulated on pulse-to-pulse basis rather than from regulated supply. Capacitor is then discharged through saturable step-up transformer L1 to charge high-voltage capacitor, which in turn is discharged through magnetron load.—R. T. Maguire, SCR's to Pulse Radar, *Electronics*, 37:3, p 14-15.

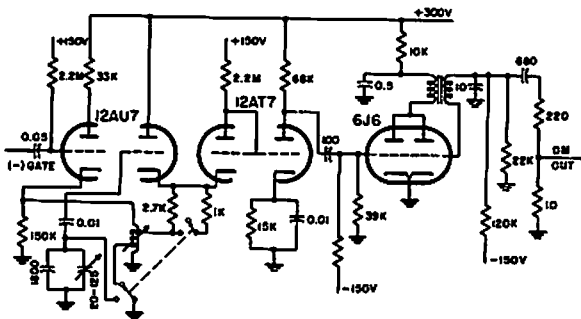
DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, pentode amplifier-shaper, and series-triggered blocking oscillator to generate 1-mile distance marks in airborne search radar. Frequency dividers are used for 10- and 20-mile marks.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N8-2.



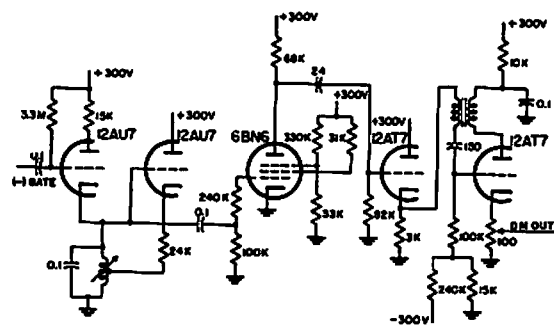
PULSE-WIDTH DISCRIMINATOR—Cuts scanning loss from interfering radars in half, by blanking video signal only if it falls within notch

developed by gating circuits. Gives marked improvement in acquisition capability.—K. H. Chase and J. L. Pierzga, *Reducing Mutual*

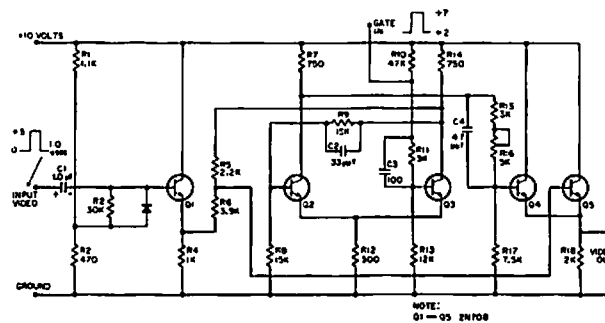
Radar Interference, *Electronics*, 32:28, p 39-43.



DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, amplifier-shaper, and parallel-triggered blocking oscillator to generate distance marks for 10 and 40 miles in airborne search radar.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N8-3.

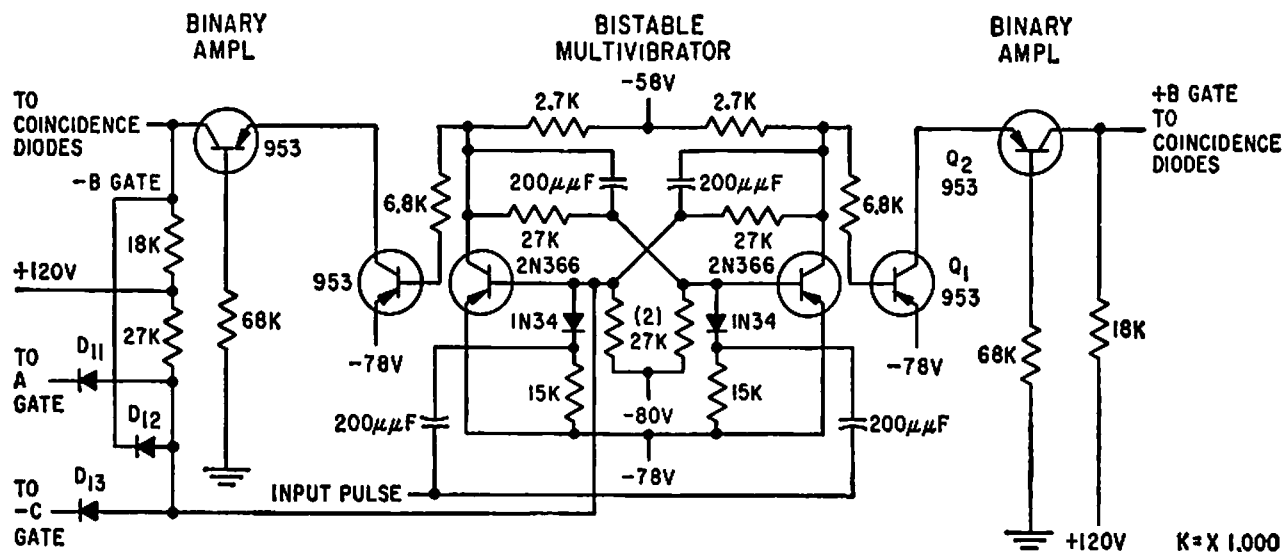


DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, gated-beam amplifier-shaper, and series-triggered blocking oscillator to generate distance marks for 2, 5, and 25 miles in airborne search radar.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N8-3.



VIDEO SWITCH—Used to either pass or blank out video signals going to ppi visual display. Blanking gate input pulse is applied to switch

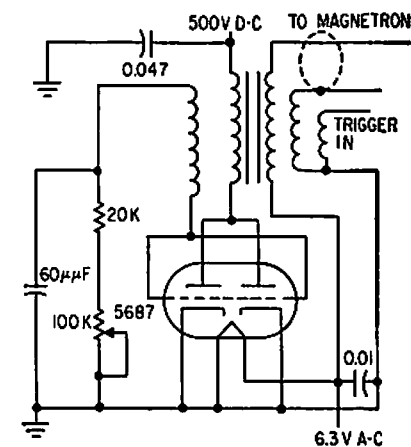
if video fails to identify itself as signal from associated radar set.—L. Turf, Video Switch for Radar, *EEE*, 11:2, p 24-25.



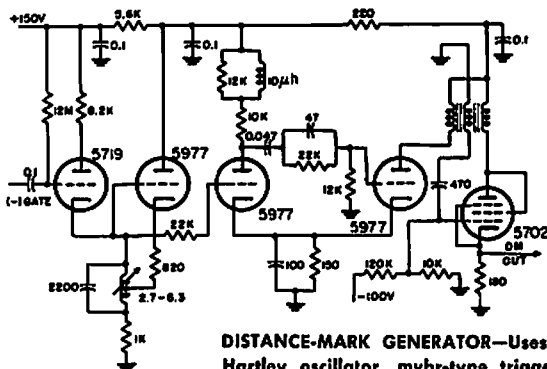
ELECTRONIC SWITCH FOR RADAR INDICATOR—Used to produce aircraft identification

markers on ppi. Coincidence of binary voltages supplies gating signals for switch.—J. B.

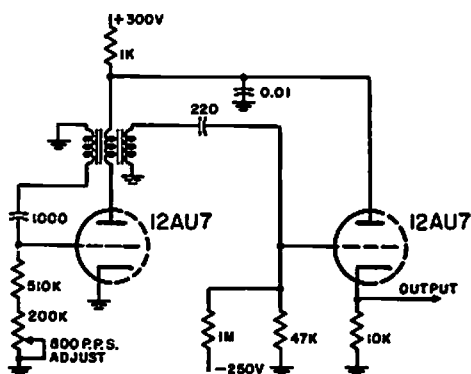
Beach, Coincidence Diodes Gate Electronic Switch, *Electronics*, 32:8, p 66-68.



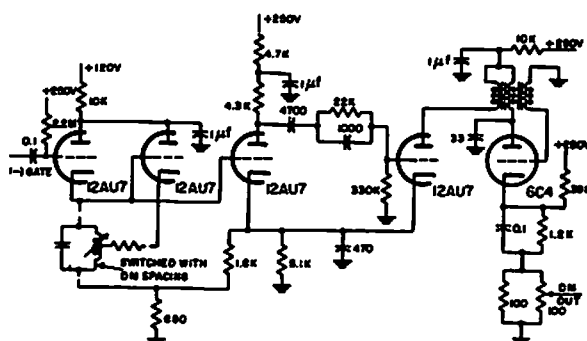
BLOCKING-OSCILLATOR MODULATOR—High permeability of ferrite-core transformer allows use of few coil turns, keeping capacitance at minimum so narrow pulses are produced.—C. D. Hardin and J. Salerno, *Miniature X-Band Radar Has High Resolution, Electronics*, 32:5, p 48-51.



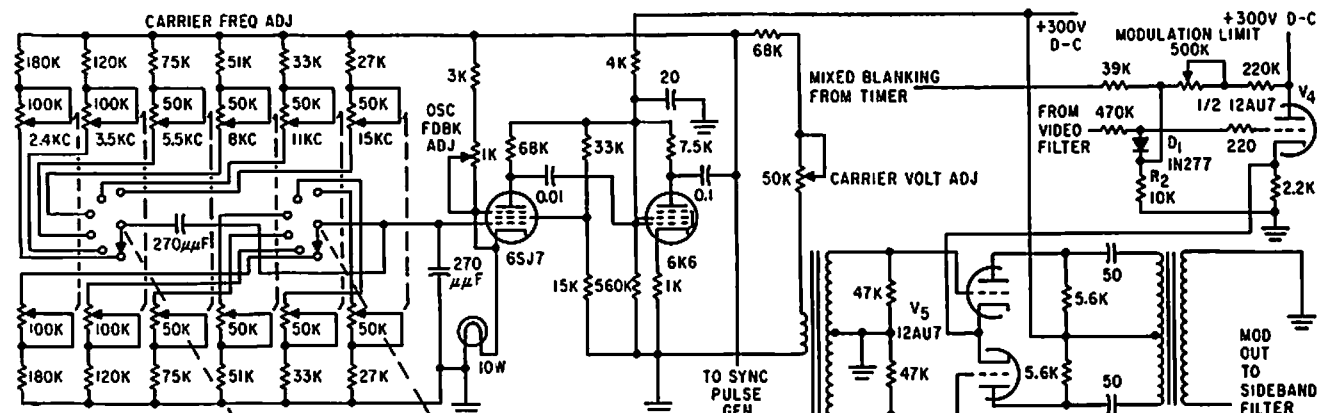
DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, mvbr-type trigger shaper, and parallel-triggered blocking oscillator to generate 1.67-mile distance marks in airborne search radar. Blocking-oscillator frequency dividers are used to get 5- and 10-mile marks.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p. N8-2.



PRF GENERATOR—Blocking oscillator operates in range of 200 to 2,000 pps, as radar repetition-rate generator having frequency stability of about 5%.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N5-2.



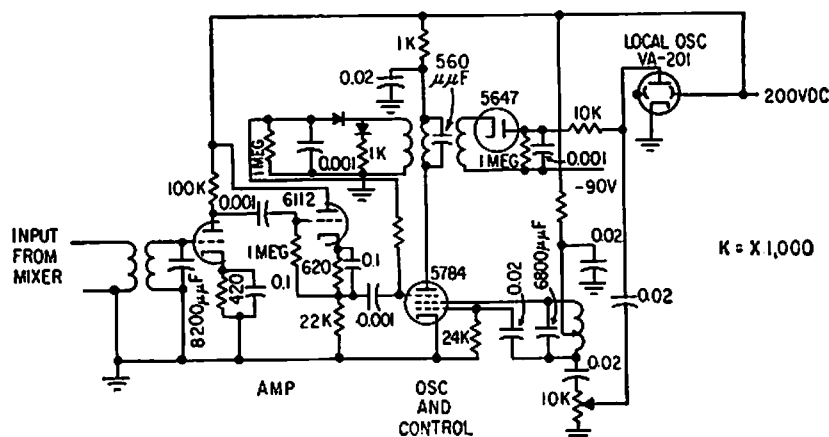
DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, monostable mvbr-type trigger shaper, and parallel-triggered blocking oscillator to generate distance marks for 2, 5, and 25 miles in airborne search radar. RLC unit is switched to change mark spacing. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p NB-1.



NARROW-BAND BALANCED MODULATOR—
Yields two sidebands and carrier while balancing out original video signal. Gang switch permits use of six different carrier

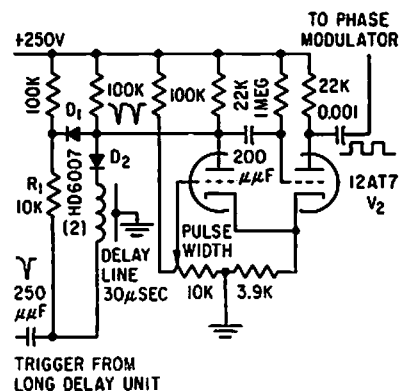
frequencies if sufficient telephone-line bandwidth is available. Sideband filters remove upper sideband and part of carrier to provide vestigial sideband operation.—H. W. Gates

and A. G. Gatfield, Scan Converter Aids Phono-Line Radar Relay, *Electronics*, 32:16, p 48-51.

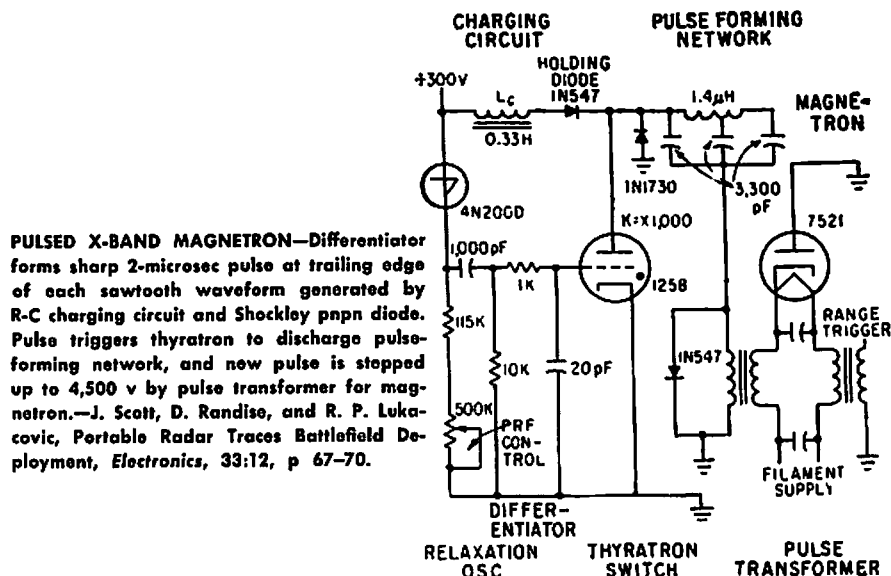


KLYSTRON SERVO—Simple three-tube modulating servo is only control required for local-oscillator klystron in wide-band receiver

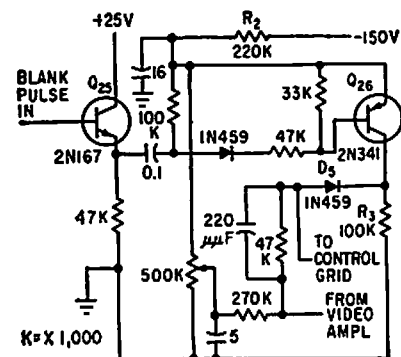
of short-pulse radar system.—C. D. Hardin and J. Salerno, *Miniature X-Band Radar Has High Resolution*, *Electronics*, 32:5, p 48-51.



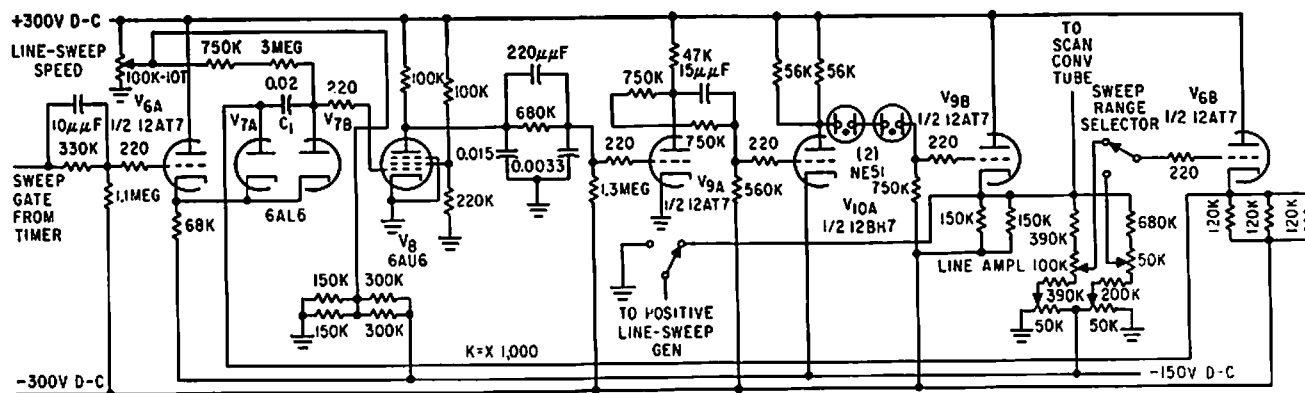
DOUBLE-PULSE GENERATOR—Used in responder-interrogator range computer to produce pair of 15-microsec-wide pulses spaced 30 microsec. Monostable mvbr receives two triggers, one through 30-microsec delay line, and produces 15-microsec pulse for each trigger received.—H. Vantine, Jr., and E. C. Johnson, *Modified Transceivers Compute Distance*, *Electronics*, 31:37, p 94-98.



PULSED X-BAND MAGNETRON—Differentiator forms sharp 2-microsec pulse at trailing edge of each sawtooth waveform generated by R-C charging circuit and Shockley pnp diode. Pulse triggers thyatron to discharge pulse-forming network, and new pulse is stepped up to 4,500 v by pulse transformer for magnetron.—J. Scott, D. Randise, and R. P. Lukacovic, *Portable Radar Traces Battlefield Deployment*, *Electronics*, 33:12, p 67-70.



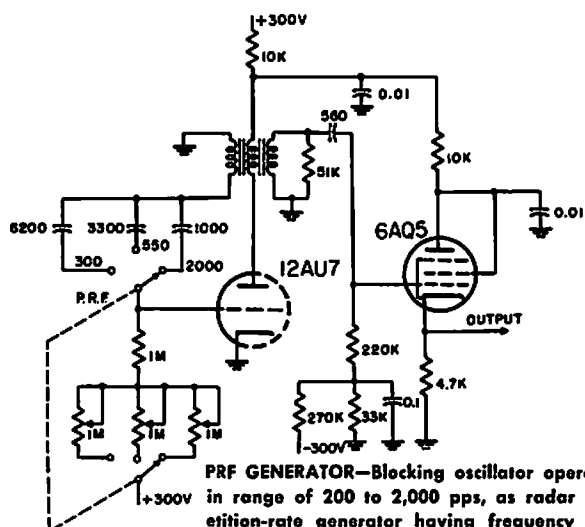
FLYBACK BLANKING—Amplifies blanking pulse from monostable mvbr sweep circuit to level required for blanking crt screen. Q26, normally nonconducting, is driven to saturation by blanking pulse, thereby applying high negative voltage to crt control grid.—C. E. Veazie, *Transistorized Radar Sweep Circuits Using Low Power*, *Electronics*, 32:26, p 46-47.



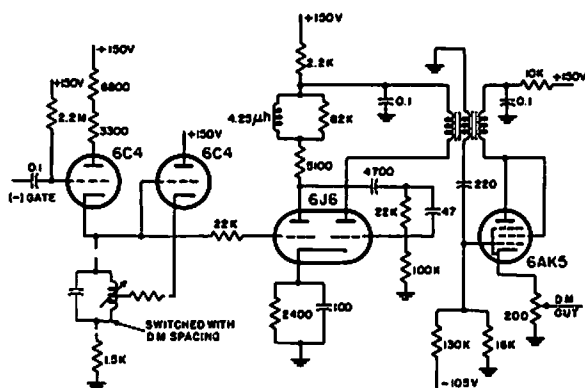
LINE SWEEP GENERATOR FOR ENCODER—Circuit is basically negative-feedback linearized R-C sawtooth generator in which charging voltage is held constant while negative end

of sweep-forming capacitor is driven negative. Amplifier V8-V9A-V10A is direct-coupled throughout. Loop-stabilizing networks pass high-frequency components.—H. W.

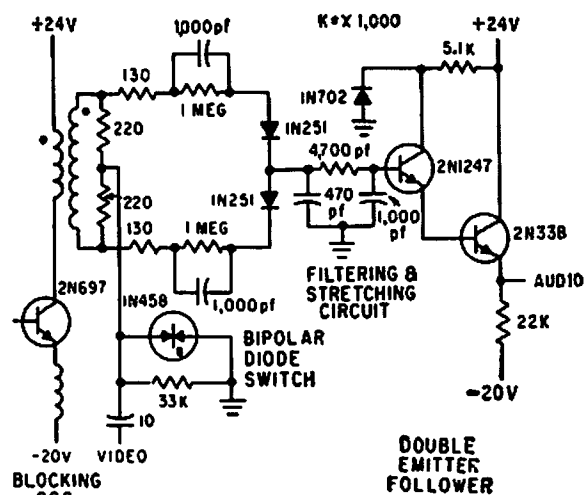
Gates and A. G. Gatfield, *Scan Converter Aids Phone-Line Radar Relay*, *Electronics*, 32:16, p 48-51.



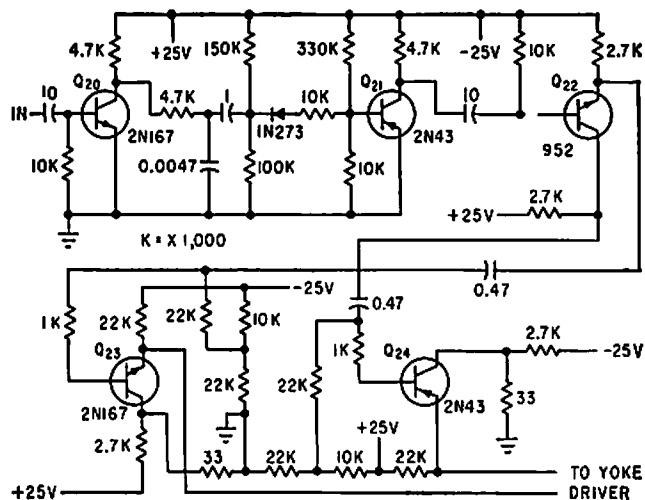
PRF GENERATOR—Blocking oscillator operates in range of 200 to 2,000 pps, as radar repetition-rate generator having frequency stability of about 5%. Has positive grid return, although this may decrease frequency stability if heater voltage drops below rated value. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N5-2.



DISTANCE-MARK GENERATOR—Uses switched Hartley oscillator, mvbr-type trigger shaper, and parallel-triggered blocking oscillator to generate distance marks for 2, 5, and 25 miles in airborne search radar. RLC unit is switched to change mark spacing. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N8-2.



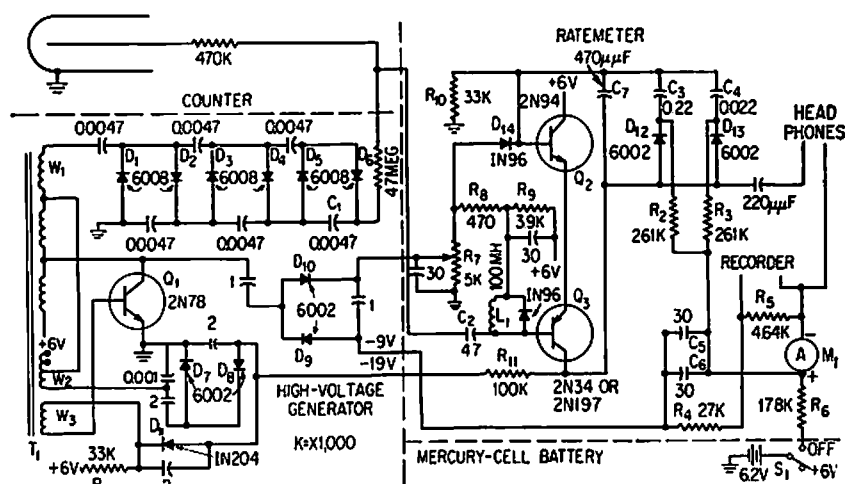
BOXCAR DETECTOR—Diodes conduct during range gate interval of 0.2 microsec in portable doppler radar, to connect video signal to filter circuit. —J. Scott, D. Randise, and R. P. Lukacovic, Portable Radar Traces Battlefield Deployment, *Electronics*, 33:12, p 67-70.



GATED CLAMP FOR PPI SWEEP—Uses monostable mvbr signal to generate reference level for yoke driver. —C. E. Veazie, Transistorized Radar Sweep Circuits Using Low Power, *Electronics*, 32:26, p 46-47.

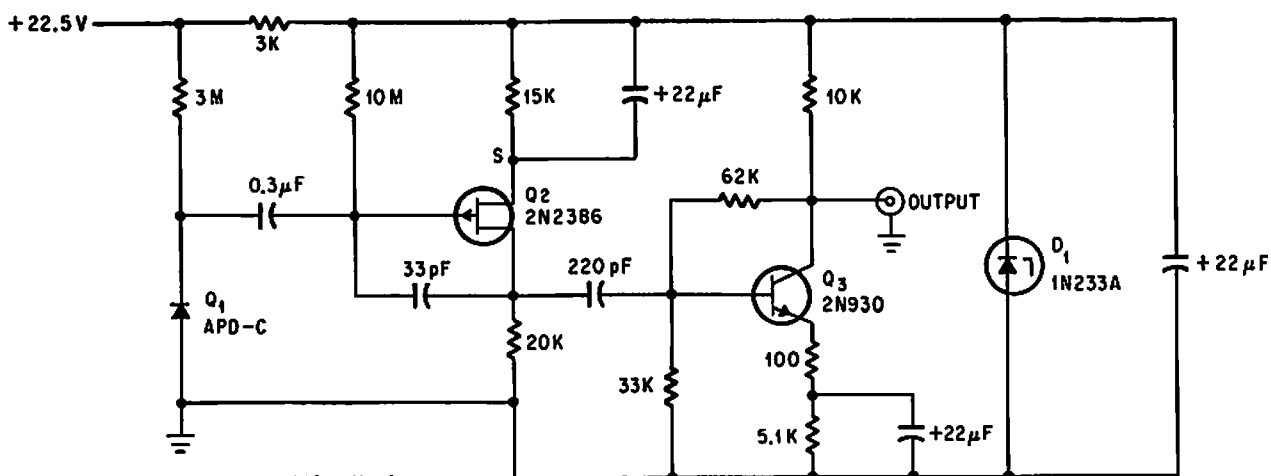
CHAPTER 69

Radiation Circuits



TRANSISTORIZED GEIGER COUNTER—Rate-meter circuit converts output of halogen-type counter directly into meter indication corresponding to radiation intensity. Counter triggers two-transistor switch to place low-im-

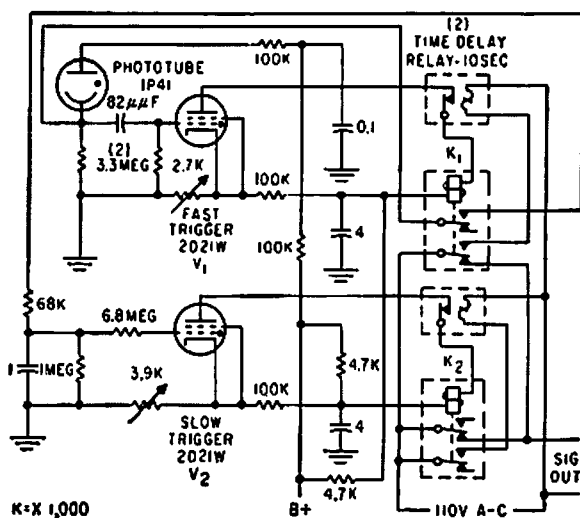
pedance load across conventional dual-output diode pump. Two halves of pump current are summed in metering circuit.—F. S. Goulding, Transistorized Geiger Counter Fits in Probe, *Electronics*, 32:3, p 64-66.



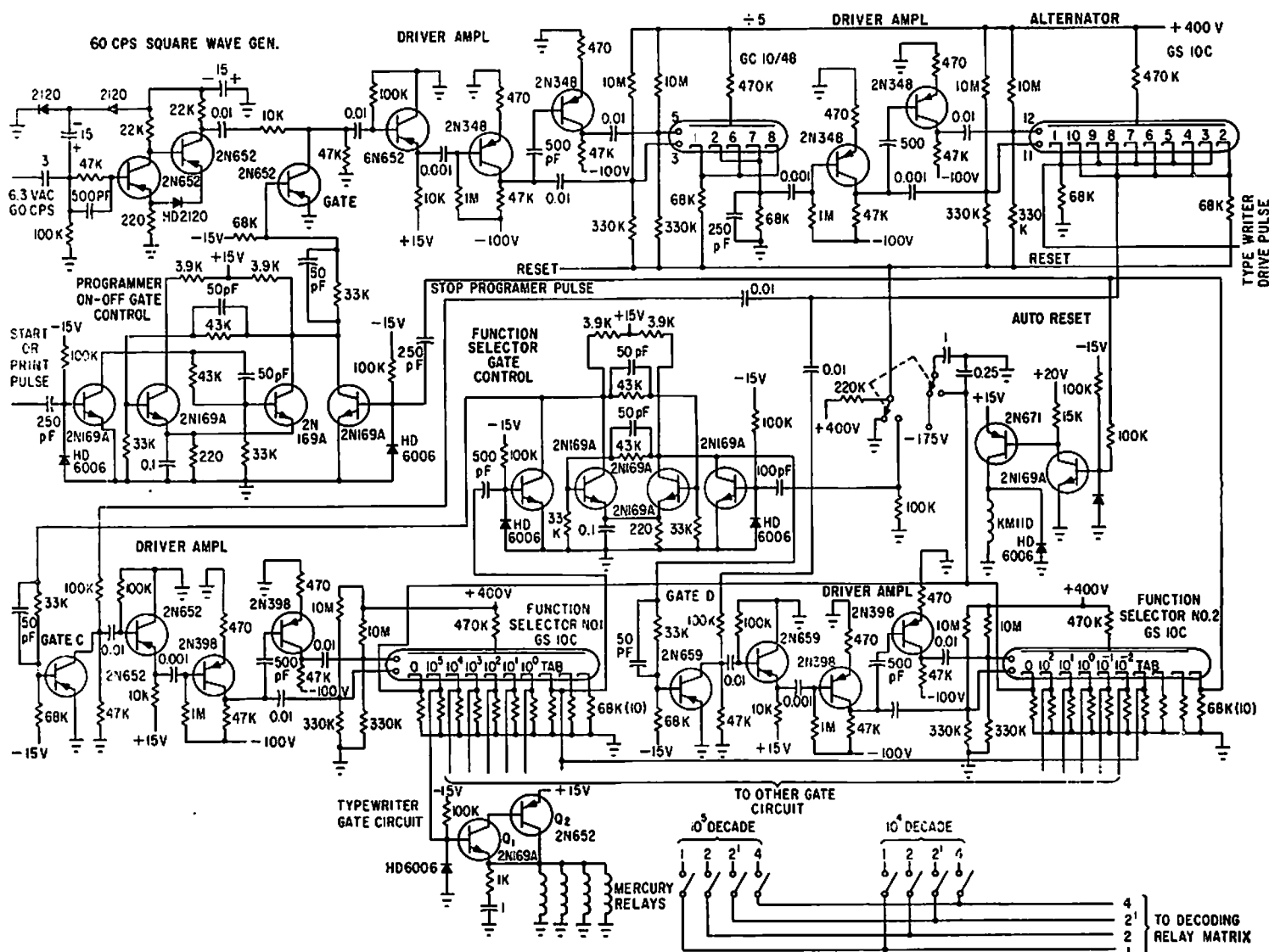
JUNCTION-DIODE ALPHA DETECTOR—Used for counting alpha particles at high altitudes in low-point hygrometer. Signal-to-noise

ratio is poor (about 4 to 1).—C. R. Seashore and C. D. O'Brien, FET Detects Alpha Particles

Better and More Precisely, *Electronics*, 38:3, p 64-66.



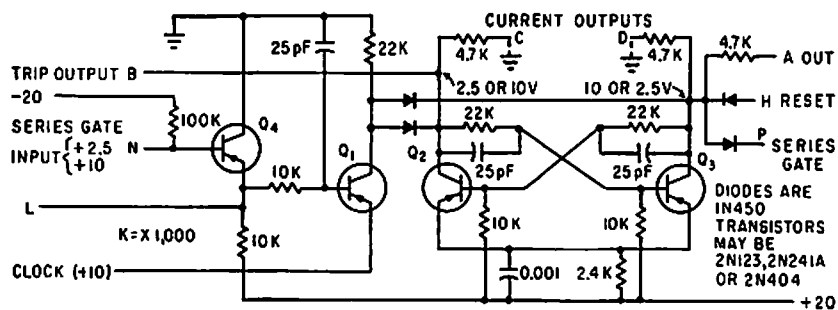
THERMAL NUCLEAR RADIATION DETECTOR—Triggers only on light flash from nuclear explosion, consisting of initial fast-rising pulse lasting a few millises, followed by pulse lasting over 1 sec. Discriminates against short flashes from lightning and shell bursts, and long slowly rising pulses caused by headlights and sunlight reflections.—J. C. Champeny, T. E. Petriken, and S. Siciliano, *Nuclear Bomb Alarm Systems, Electronics*, 32:19, p 53-55.



NEUTRON DIFFRACTOMETER—Neutron beam from reactor strikes sample, producing diffraction pattern. Multielement glow tubes control sequence of operation in which length of data accumulation time at each angle of dif-

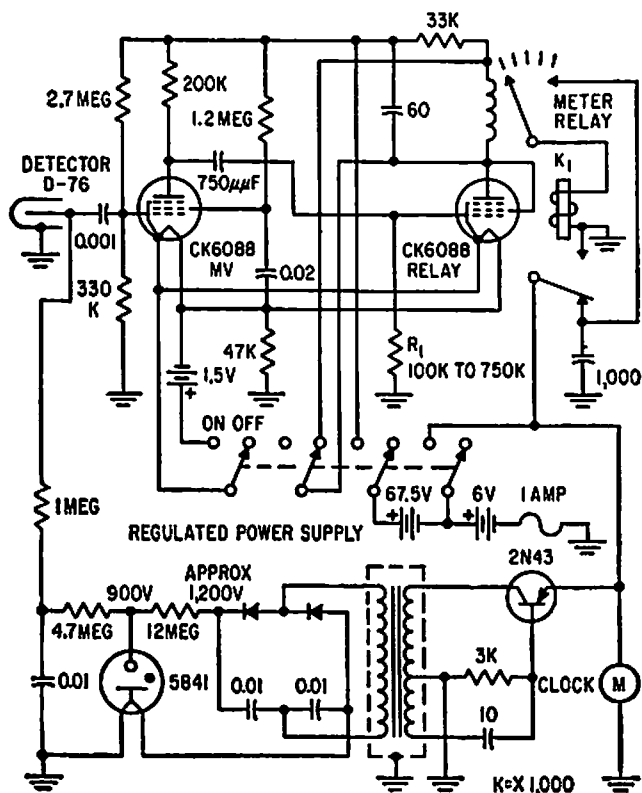
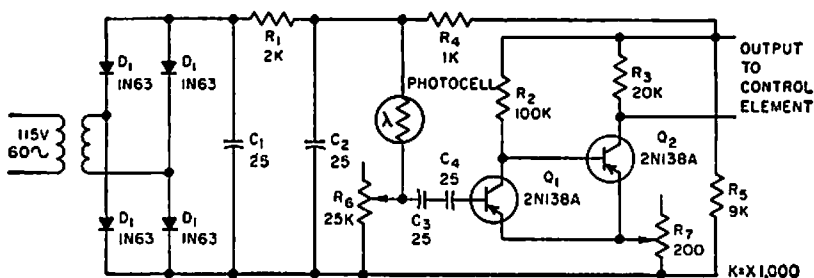
fraction is determined by counting neutrons in incident beam. This eliminates counting errors due to reactor level fluctuations. Circuit drives key solenoids of electric type-

writer to give printout of results.—E. W. Johanson, *Glow-Tube Programmer Controls Neutron Spectrometer Experiments, Electronics*, 34:19, p 65-67.

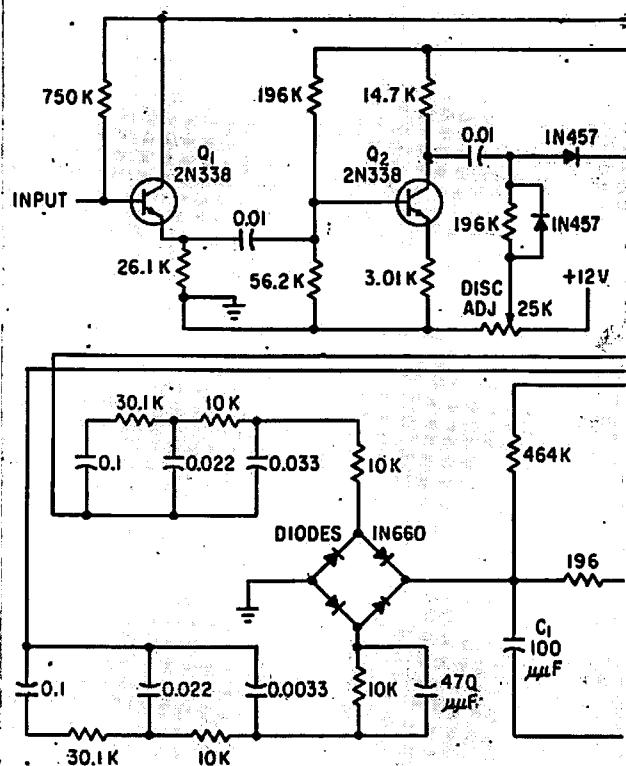


MULTI-OUTPUT BINARY—Basic binary circuit of 256-channel neutron analyzer is controlled by diode gates in coincidence with clock pulses derived from 200-kc crystal oscillator. Used in countdown, address overflow, memory cycle, sync, and gate stages.—E. J. Wade, *Digital Instrumentation for Nuclear Research Tests*, *Electronics*, 33:43, p 68-71.

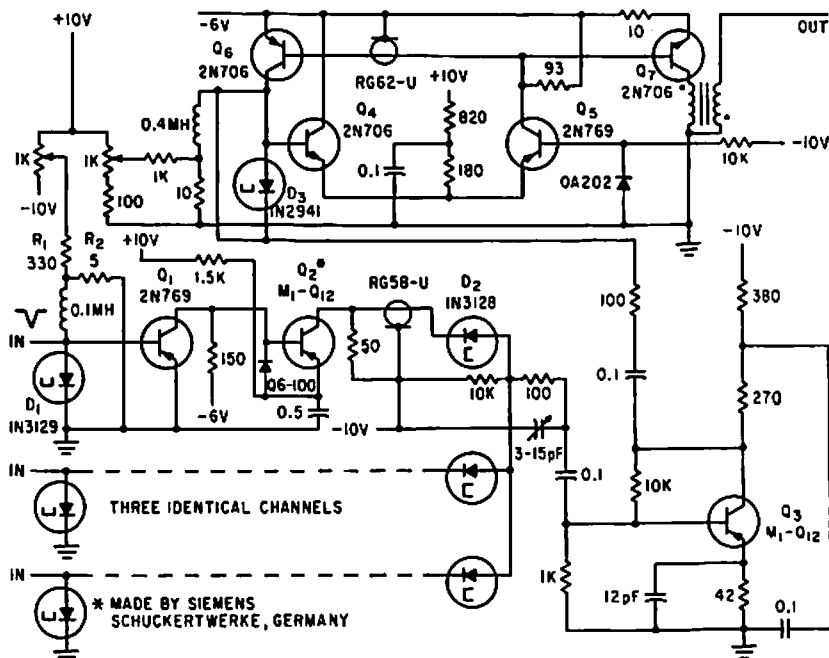
PHOTO RELAY USES SR-90 SOURCE—Interruption of high-energy beam from strontium-90 radioactive source changes resistance of cadmium sulphide photocell. Transistor amplifier converts variation into signal that actuates relay or other control element. Source-detector separation must be less than 4 inches. Maximum counting rate is five pieces per second.—P. Weisman and S. L. Ruby, *Solid-State Photocell Sees Through Haze*, *Electronics*, 31:25, p 62-63.



FALLOUT TIME-OF-ARRIVAL INDICATOR—Auto clock operates until fallout at level above 2 milliroentgens per hour arrives. Geiger-counter detection circuit then blows power-supply fuse, stopping clock with hands pointing to time of arrival.—R. W. Farmer and O. Reiner, Jr., *Determining Arrival Time of Radioactive Fallout*, *Electronics*, 31:31, p 69-71.

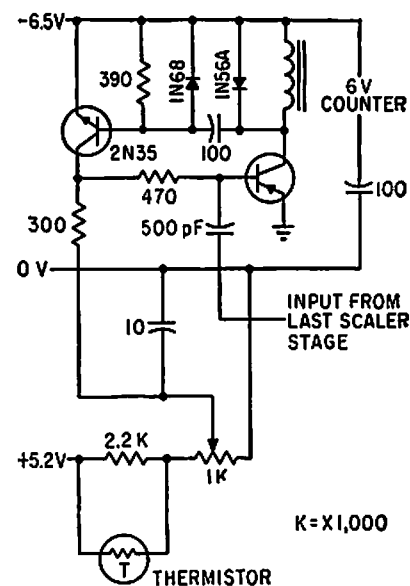


RADIATION ALARM—Input is from multiplier phototube having anthracene scintillation crystal on its window. Signals are amplified

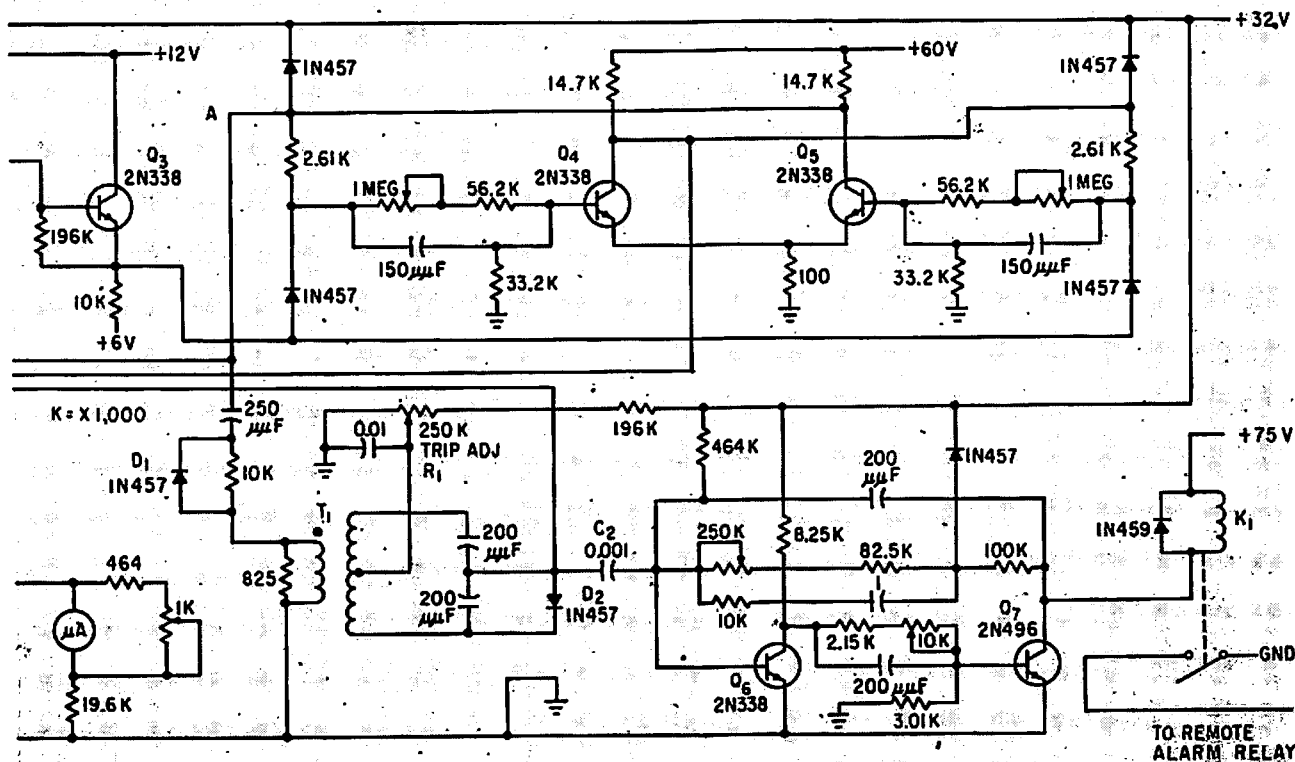


TUNNEL-DIODE COINCIDENCE CIRCUIT—Determines coincidence of pulses from scintillation counter within nanosecond limits, for high-energy physics experiments. Circuit has limited timing jitter, good temperature stabil-

ity, and is insensitive to transistor parameters.—C. Infante and F. Pandaresco, Tunnel Diodes Stabilize Coincidence Circuits, *Electronics*, 34:46, p 133-135.



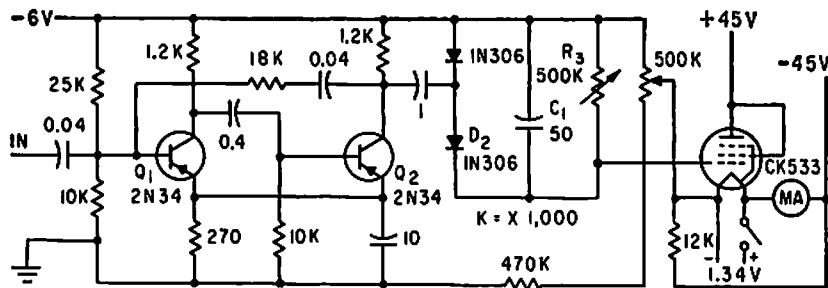
MECHANICAL COUNTER DRIVE—Takes output from scale-of-64 circuit and converts to 40-millisecond square-wave pulse by means of complementary mvbr, to drive coil of mechanical register once for every 64 pulses from G-M tube.—F. E. Armstrong, Battery Powered Portable Scaler, *Electronics*, 33:19, p 74-75.



by Q1, Q2, and Q3, and fed to counter flip-flop Q4-Q5. Flip-flop output goes to logarithmic count circuit whose output level is indi-

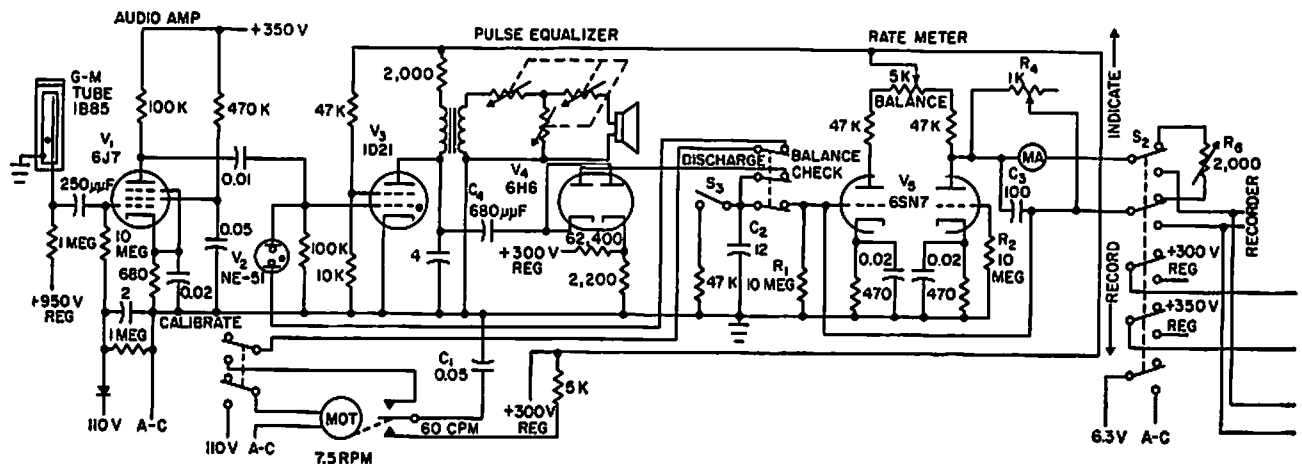
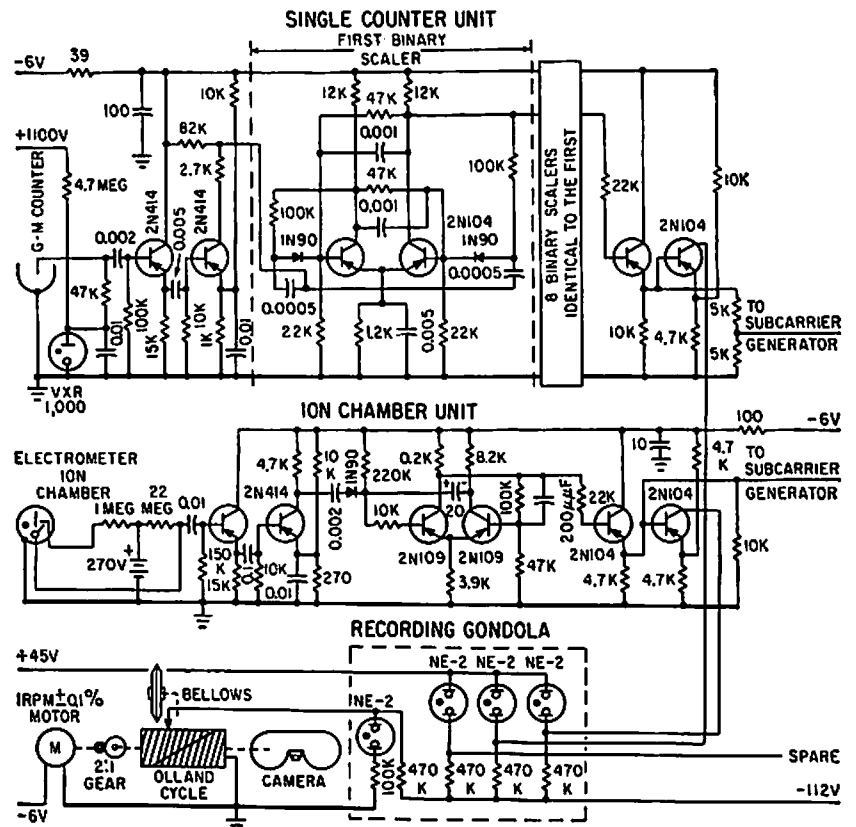
cated by microammeter. When output exceeds predetermined level, alarm circuit closes relay that actuates audible and visual

alarms.—H. E. DeBolt, How Radiation Monitor Guards Nuclear Navy, *Electronics*, 33:4, p 43-45.



G-M COUNTING-RATE METER—Uses two transistors in integrating circuit and pentode recorder drive. Output of counting-rate mvbr Q1-Q2 is 4.5-v, 260-microsec square pulse that charges integrating capacitor C1 through D2.—F. E. Armstrong and E. A. Pavelka, Monitoring Radiosotope Tracers in Industry, *Electronics*, 32:26, p 42-43.

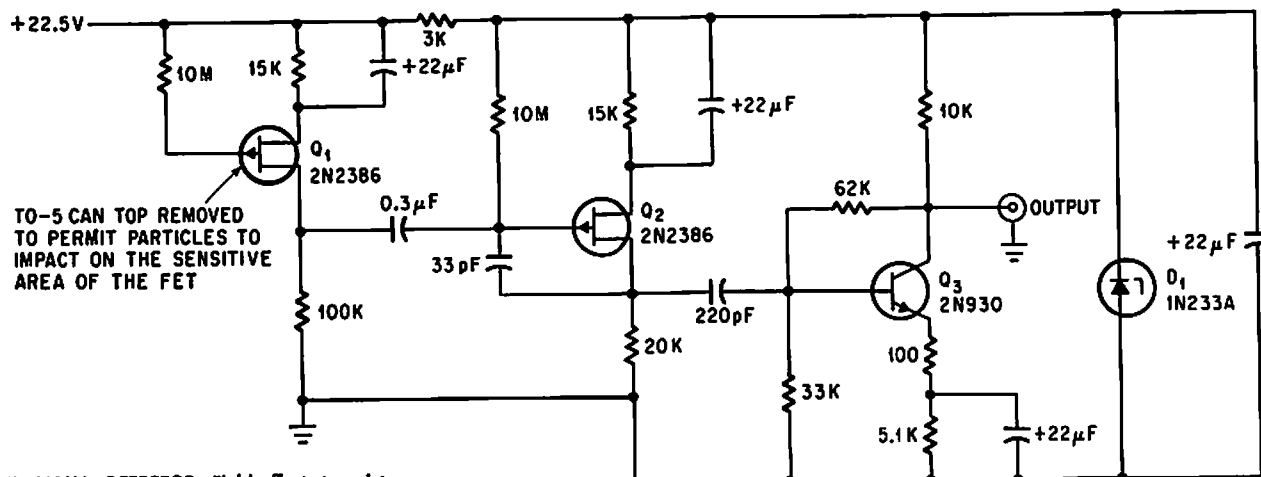
G-M COUNTER AND IONIZATION GAGE—Used to correlate cosmic radiation intensity with other ionospheric and geomagnetic phenomenon. G-M counter provides negative pulse that is differentiated, shaped, and amplified in circuits similar to that of ion chamber. Counter rate is scaled down by 9-stage binary scaler before square-wave output is fed to telemetering unit.—L. E. Peterson, R. L. Howard, and J. R. Winckler, Balloon Gas Monitors Cosmic Radiation, *Electronics*, 31:45, p 76-79.



GEIGER COUNTER—Simple basic monitor provides continuous audio and visual indications of radioactive materials in industrial areas.

If recording is required, four leads at right are connected to 10-cps keep-alive mvbr and triode output stage for driving recorder. Will

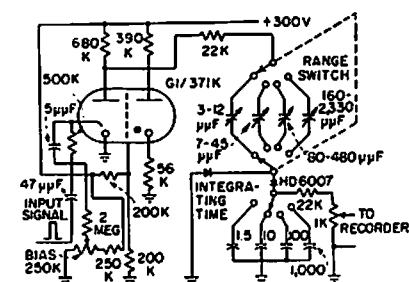
handle count rates up to 10,000 per minute. Strobotron V3 in pulse equalizer provides visual indications.—R. L. Ives, Geiger Radiation



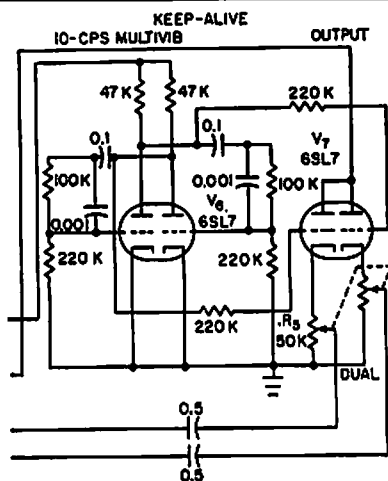
FET ALPHA DETECTOR—Field-effect transistor with cover removed serves as low-noise alpha-particle detector in high-altitude dew-

point hygrometer. Signal-to-noise ratio is 67 to 1.—C. R. Seashore and C. D. O'Brien,

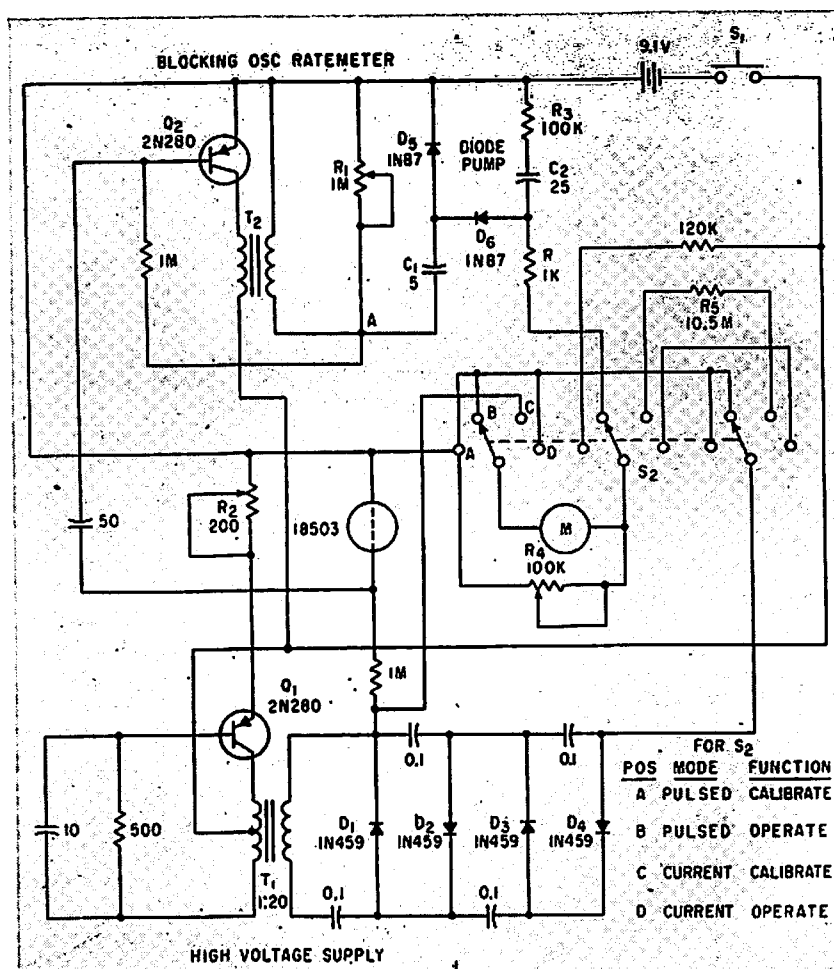
FET Detects Alpha Particles Better And More Precisely, Electronics, 38:3, p 64-66.



COLD-CATHODE COUNT RATE CIRCUIT—Four-element cold-cathode tube operates directly from output pulse of 6292 photomultiplier receiving light output of ZnS screen of alpha particle detector. Maximum counting rate is 100 counts per second.—M. H. Goosey, *Designing Cold-Cathode Tube Circuits*, *Electronics*, 31:3, p 101-108.

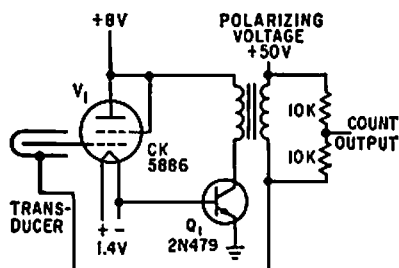


Monitor Indicates Continuously, Electronics, 31:43, p 93-95.

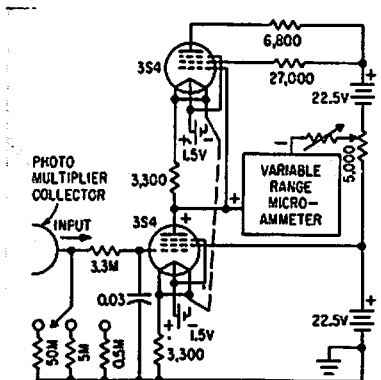


SURVEY METER HAS PULSED AND CURRENT MODES—High-voltage source for G-M counter uses 10-kc blocking oscillator and Cockcroft-Walton multiplier, to give 550 v stabilized by zener region of D1. Range for pulsed operation is 0.5 to 50 milliroentgen per hour.

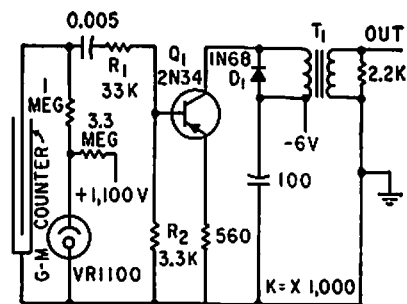
For current mode, same 18503 G-M tube is used, and current in range of 50 milliroentgen to 5 roentgen per hour is logarithmic function of radiation intensity.—R. W. Lehnert and J. M. McKenzie, Radiation Survey Meter, *Electronics*, 35:8, p 50.



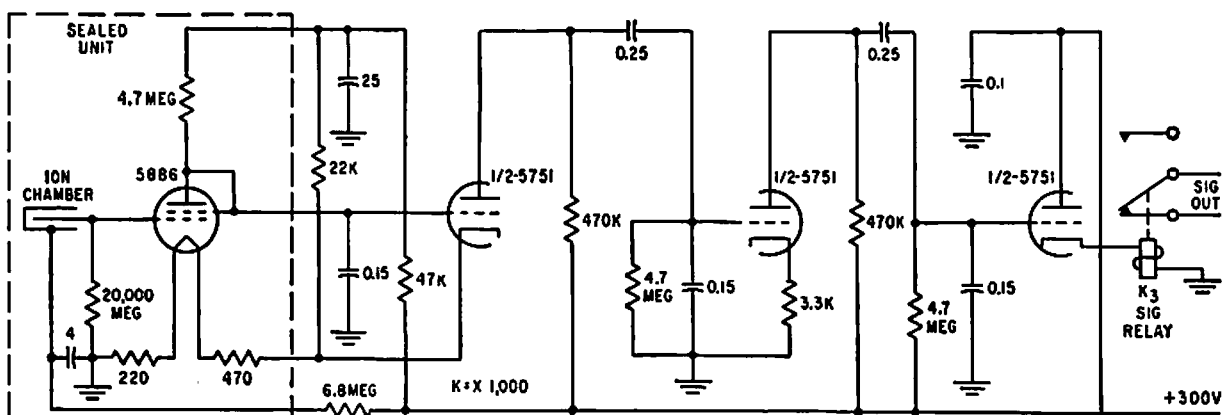
RADIOLOGICAL VACUUM GAGE—Permits measuring extremely low pressures in laboratory equipment and in high-altitude research. Provides digital output that can be used for storage for telemetry. Transformer is audio type with large step-up ratio. Polarizing voltage supplies less than 1 microamp. Transducer is small cylindrical tube lined with radioactive foil.—G. F. Vanderschmidt, *Using Isotopes to Measure Low Pressures*, *Electronics*, 32:25, p 60-61.



ELECTROMETER—Amplifies output of photo multiplier that responds to degree of fluorescence, which in turn is proportional to radiation received by glass dosimetry needle implanted in body of person undergoing radiation treatment.—S. J. Malsky et al, *Measuring Radiation Within Human Body*, *Electronics*, 33:12, p 74-75.

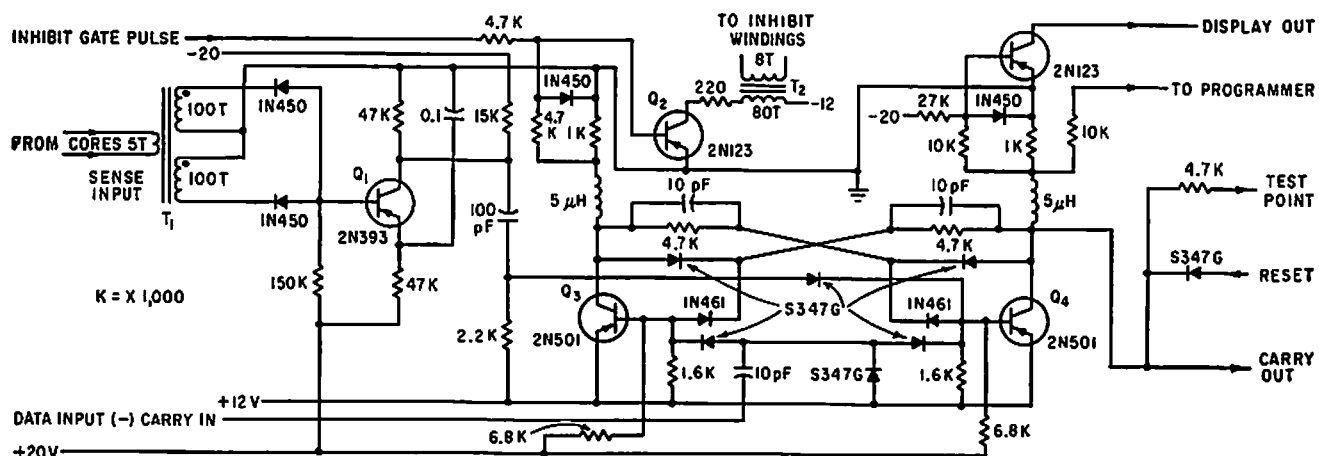


G-M COUNTER FOR TRACERS—Monitors radioactivity level of flowing liquids or gases for long periods of time. Concentration of 0.1 microcurie per liter of liquid gives counting rate of 200 cpm above 300-cpm background count when using iodine-131. Output pulse is 0.75 v in amplitude and 20 microsec wide.—F. E. Armstrong and E. A. Pavelka, *Monitoring Radioisotope Tracers in Industry*, *Electronics*, 32:26, p 42-43.



GAMMA-RAY DETECTOR—Triggers only on gamma-ray pulse produced by nuclear explosion. Uses a-c coupled ion chamber to de-

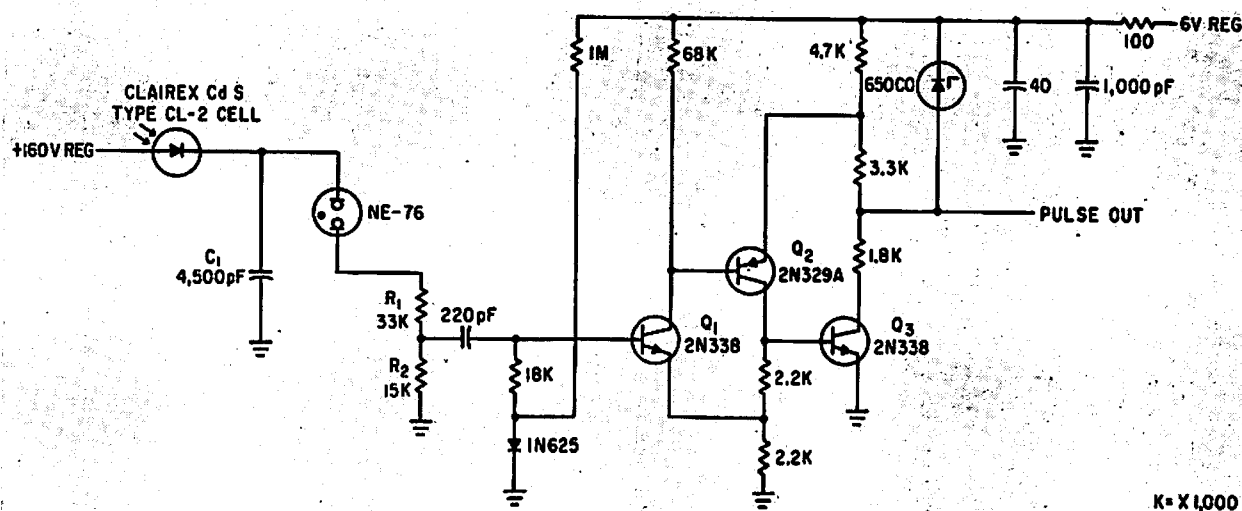
tect pulses of gamma radiation.—J. C. Champeny, T. E. Petriken, and S. Siciliano, *Nuclear Bomb Alarm Systems*, *Electronics*, 32:19, p 53-55.



ARITHMETIC BINARY—Uses 2N501 series-triggered transistors, catching diodes, and peaking coils operating at data input rate of

about 15 Mc, in neutron time-of-flight and pulsed-neutron measurements.—E. J. Wade, *Digital Instrumentation for Nuclear Research*

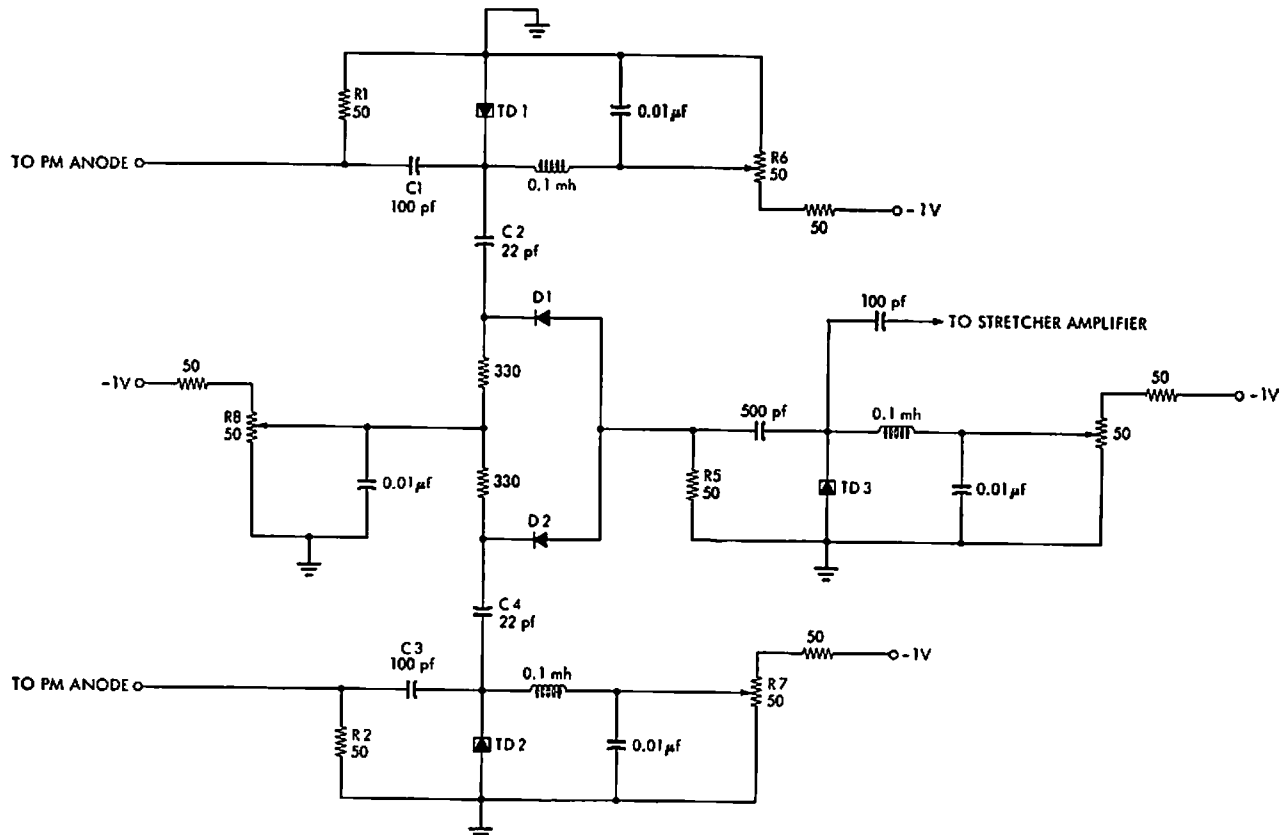
Tests, *Electronics*, 33:43, p 68-71.



LOW-ENERGY PARTICLE DETECTOR—Change in conductivity of single-crystal photocell under irradiation is converted to pulse-code modulation by neon glow-tube relaxation os-

cillator whose firing rate is determined by charging of C1 through photocell. Saturating bootstrap amplifier Q2 inverts and shapes pulses to drive accumulation register.—J. W.

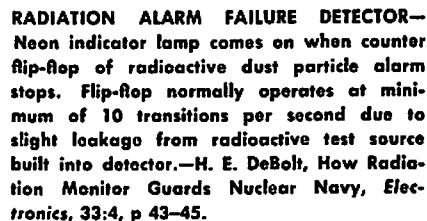
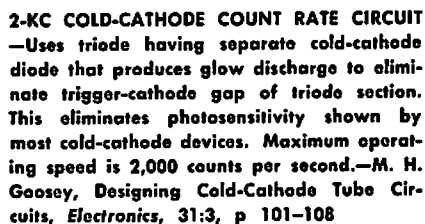
Freeman, Energy Detector for Satellites, *Electronics*, 35:4, p 42-43.



TUNNEL-DIODE COINCIDENCE—Used in liquid scintillation counter for carbon-14 and other radioactive solutions. Delivers output pulse

to stretcher amplifier only for coinciding pulses from two photomultiplier inputs.—G. J. Sprokel, A Liquid Scintillation Counter

Using Anticoincidence Shielding, *IBM Journal of Research and Development*, 7:2, p 135-145.

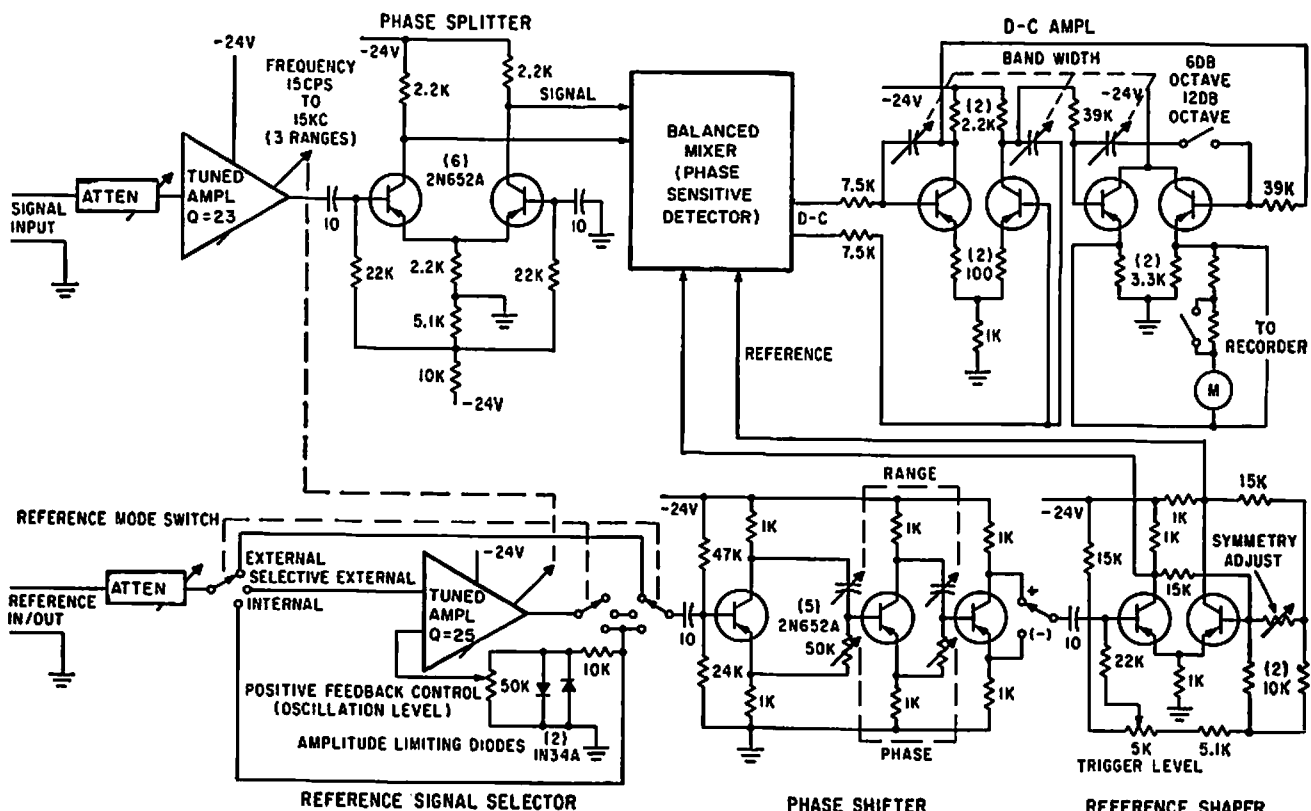
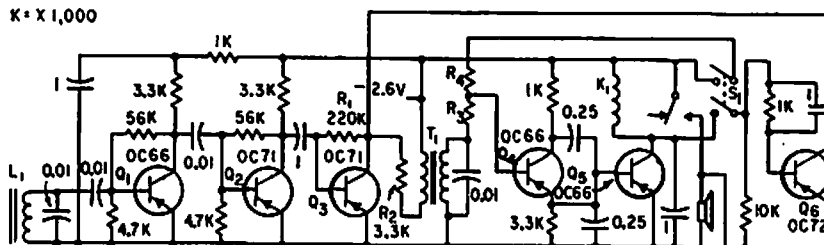


CHAPTER 70

Receiver Circuits

RESONANT-REED PAGING RECEIVER—Ferrite antenna L1 is tuned to one of up to 45 different carrier frequencies in range from 15 to 30 kc, keyed at various repetition rates. Resonant relay K1 in collector circuit of detector Q5 vibrates when excited at its natural keying rate, thereby interrupting loudspeaker current at audio rate to create paging tone. —J. G. DeGraaf, *Selective Paging System Uses Coded Transmission*, *Electronics*, 33:9, p 68-70.

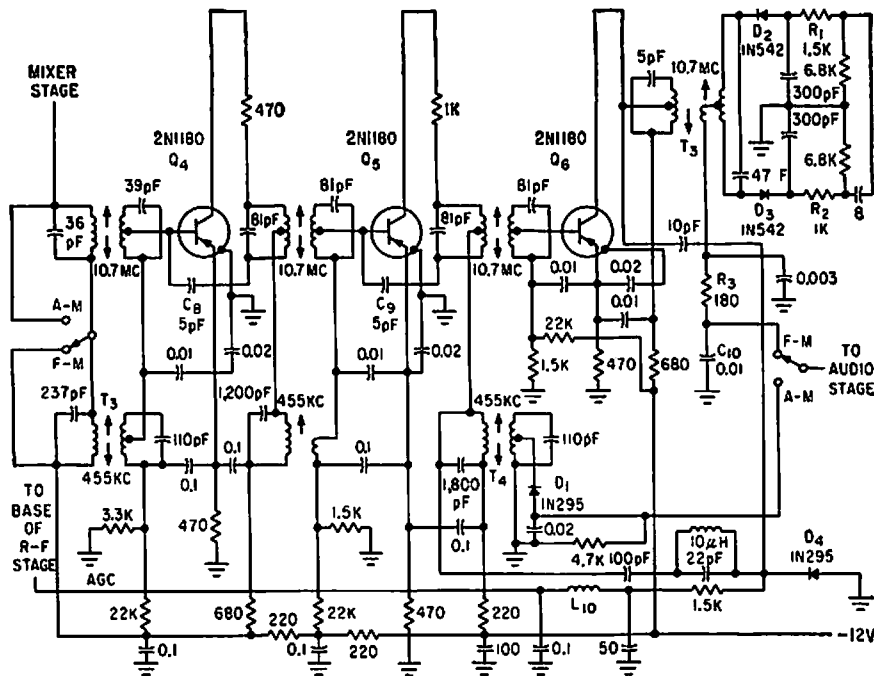
K = X 1,000



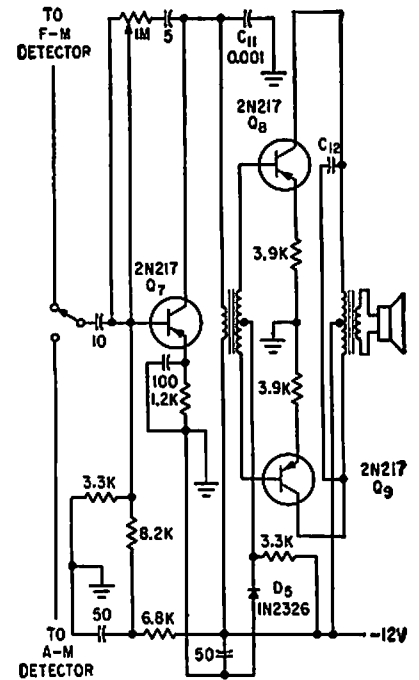
MEASURING SIGNALS IN NOISE—Lock-in amplifier beats desired weak signal (40 db below input noise level) with reference signal of same frequency, to give d-c output that

can be measured or recorded, as required in radio astronomy. Bandwidth is variable down to 0.12 cps for tuning range of from 15 to 15,000 cps. Also used for checking oscilla-

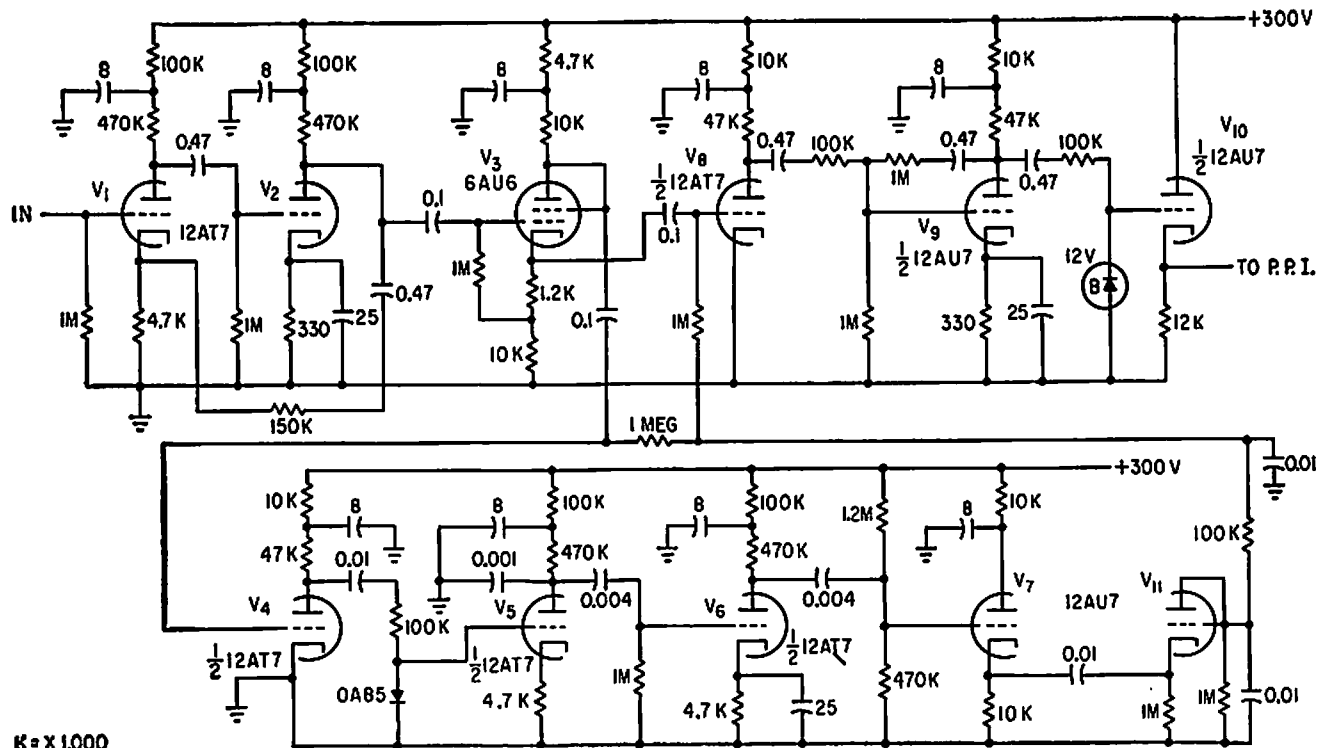
tor frequency against WWV to one part in 10^{10} .—R. D. Moore, *Lock-In Amplifier for Signals Buried in Noise*, *Electronics*, 35:23, p 40-43.



THREE-TRANSISTOR A-M/F-M I-F STRIP—Used in a-m/f-m portable radio. Only two stages operate on a-m.—R. A. Santilli and H. Thanos, *Portable Radio Uses Drift-Field Transistors*, *Electronics*, 33:28, p 48-50.



AUDIO FOR A-M/F-M PORTABLE—Overall power gain is 70 db, with audio output of 1 w at less than 10% distortion.—R. A. Santilli and H. Thanos, *Portable Radio Uses Drift-Field Transistors*, *Electronics*, 33:28, p 48-50.

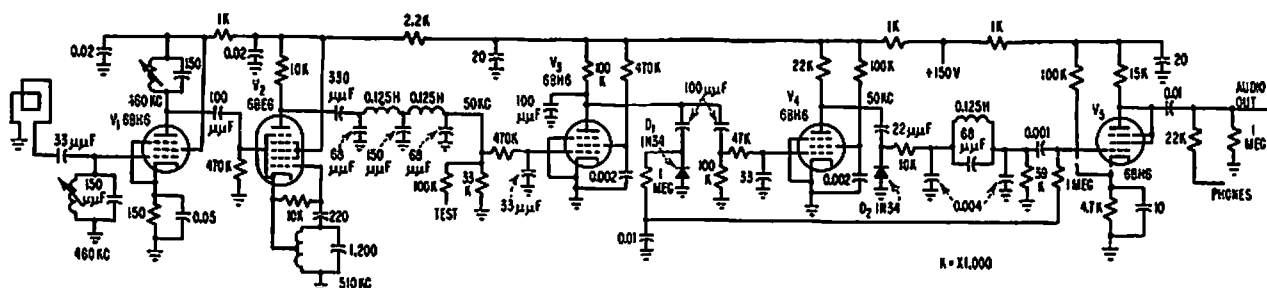


K=X1,000

TONE SIGNAL REJECTOR—Used to separate desired backscatter pulses from unwanted tone signals of interfering stations on same

frequency. Negative-going portion of tone signal is rectified and used as slicer level for passing desired pulse, which rides on

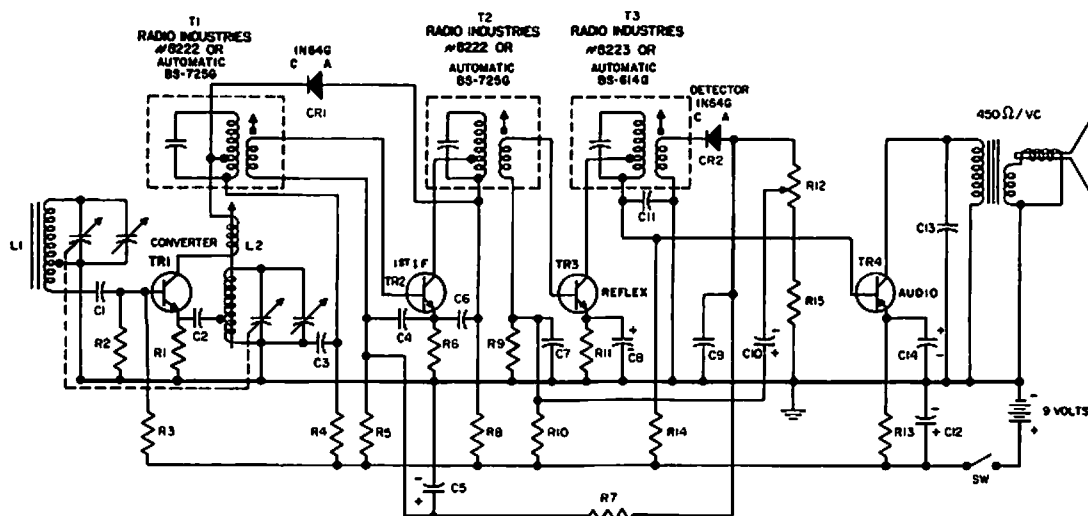
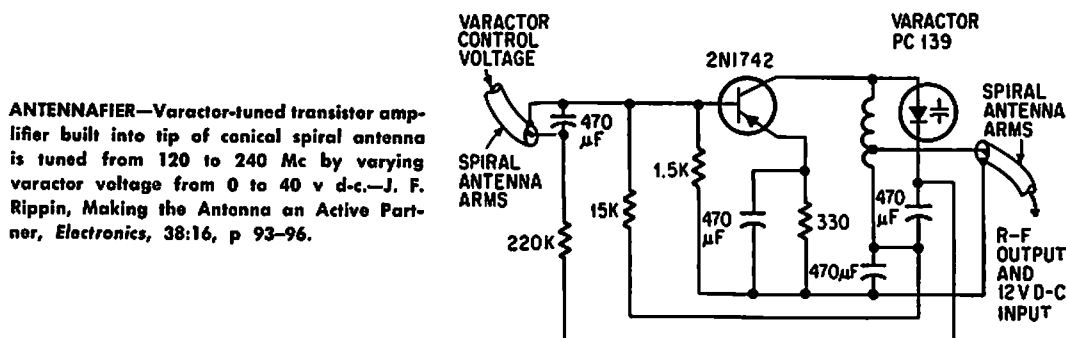
top of interfering tone.—K. Perry, *Reducing Interference in Ionospheric Sounding*, 33:22, *Electronics*, p 118-120.



460-KC F-M RECEIVER FOR WIRELESS MICROPHONE—Loop 5 meters square picks up induction field of four-transistor transmitter and feeds r-f amplifier VI. Operating fre-

quency is converted to 50 kc by V2 and amplified and limited by V3 and V4. Audio signal is recovered after passing through low-pass filter. Peak audio output is about

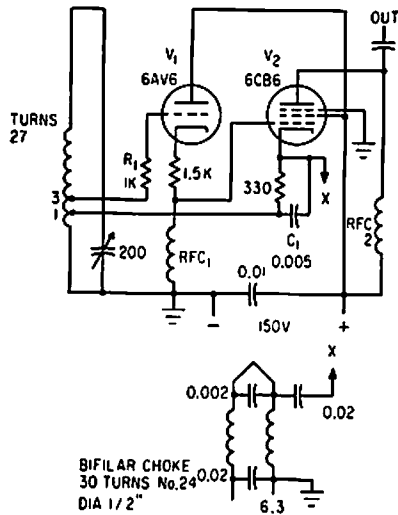
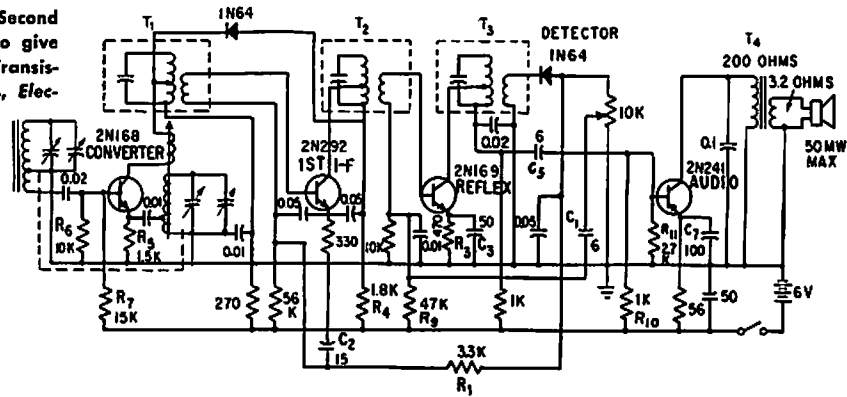
0.5 v, enough to feed p-a or speech pre-amplifier.—G. F. Montgomery, Wireless Microphone Uses F-M Modulation, Electronics, 31:1, p 54-55.



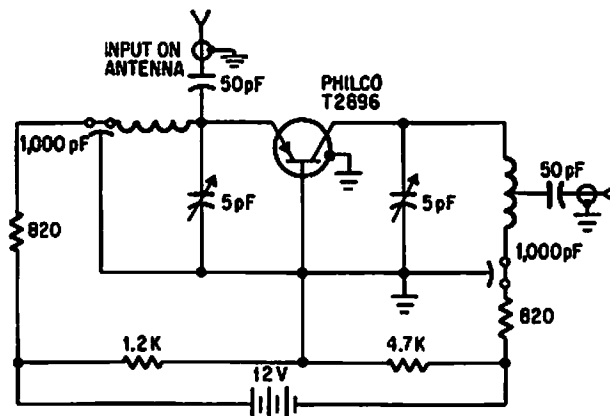
- | | | | | | |
|----------------|--|------------------------|--|-------------------|-----------------------|
| R1, R14, _____ | 1500 OHM | C1, C11, _____ | .02 μ fd | L1, _____ | 435 μ h $\pm$ 10% |
| R2, _____ | 6800 OHM | C2, C3, C7, _____ | .01 μ fd | L2, _____ | 250 μ h $\pm$ 10% |
| R3, _____ | 27,000 OHM | C4, C6, C9, C13, _____ | .05 μ fd | CR1, CR2, _____ | IN54G OR EQUIV. |
| R4, R11, _____ | 470 OHM | C5, _____ | 12 μ fd, 12V | Δ C1 _____ | 190.6 |
| R5, _____ | 82,000 OHM | C8, _____ | 30 μ fd, 3V | Δ C2 _____ | 89.5 |
| R6, _____ | 330 OHM | C10, _____ | 6 μ fd, 12V | } R/C MODEL 242 | |
| R7, R8, _____ | 3300 OHM | C12, _____ | 50 μ fd, 12V | | |
| R9, _____ | 10,000 OHM | C14, _____ | 100 μ fd, 12V | | |
| R10, _____ | 91,000 OHM | TR1, _____ | GE 2N106A, 2N106A
OR 2N1087 CONVERTOR | | |
| R12, _____ | VOLUME CONTROL
10,000 OHM AUDIO TAPER | TR2, _____ | 2N292 1ST I.F. | | |
| R13, _____ | 100 OHM | TR3, _____ | GE 2N169 OR 2N1121 REFLEX | | |
| R15, _____ | 120 OHM | TR4, _____ | 2N1415 AUDIO | | |

FOUR-TRANSISTOR REFLEX—Nominal sensitivity is 200 microvolts per meter at 5 mw reference power output. Maximum power output is 75 mw, and total battery drain is 17 ma.—“Transistor Manual,” Seventh Edition, General Electric Co., 1964, p 291.

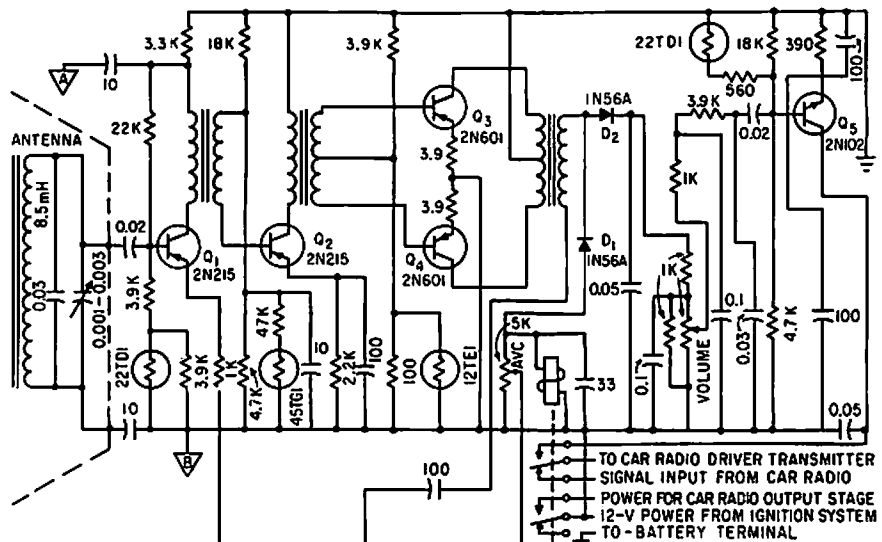
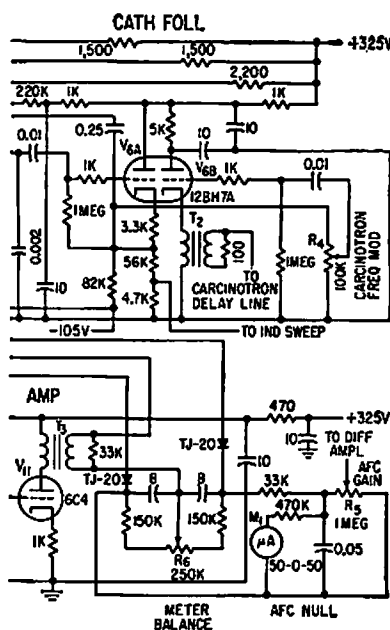
FOUR-TRANSISTOR REFLEX PORTABLE—Second i-f stage doubles as audio amplifier to give five-stage performance.—E. Gottlieb, Transistor Reflex Circuit Trims Receiver Costs, *Electronics*, 31:1, p 66-68.



LOAD-ISOLATING 3-3.5 MC OSCILLATOR—Refined version of Lampkin variable r-f oscillator for dual-conversion receiver gives uniform output over band and sufficient stability for single-sideband reception after 30-sec warmup.—E. Robberson, R-F Oscillator has Improved Stability, *Electronics*, 36:32, p 62-63.

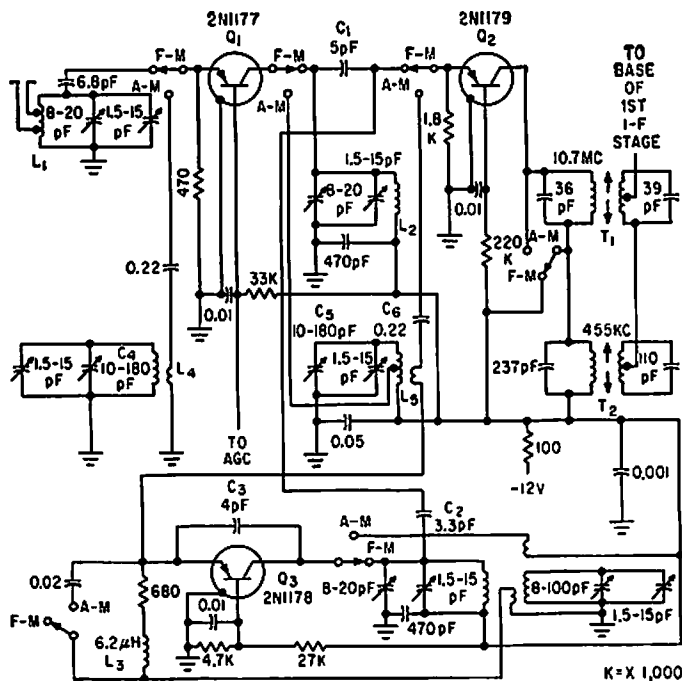


SLOT ANTENNA—T-bar-fed 420-Mc slot antenna for space vehicles has gain of 10 db, 100-Mc bandwidth, and 7.8 db noise figure.—J. F. Ripplin, Making the Antenna an Active Partner, *Electronics*, 38:16, p 93-96.



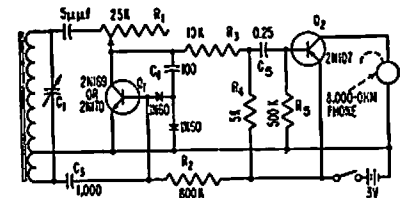
9-KC INDUCTION RECEIVER—Thermistor network in base circuits of transistors provide thermal compensation between -30 and $+140^{\circ}\text{F}$, for picking up messages broadcast

from roadside telephone-line loops.—E. A. Hanyasz, J. E. Stevens, and A. Modovsky, Communication System for Highway Traffic Control, *Electronics*, 33:42, p 81-83.

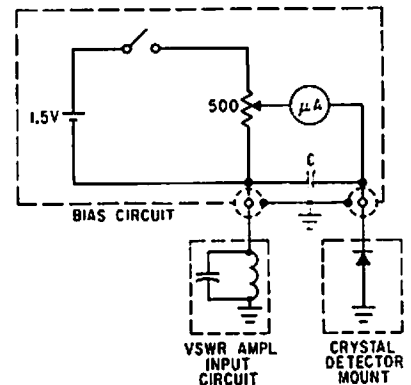


TUNER FOR A-M/F-M PORTABLE—R-f amplifier Q1, mixer Q2, and local oscillator Q3 are all switched to perform same functions on f-m as on a-m. Grounded-base oscillator Q3 requires careful design to compensate for transconductance phase shift at highest fre-

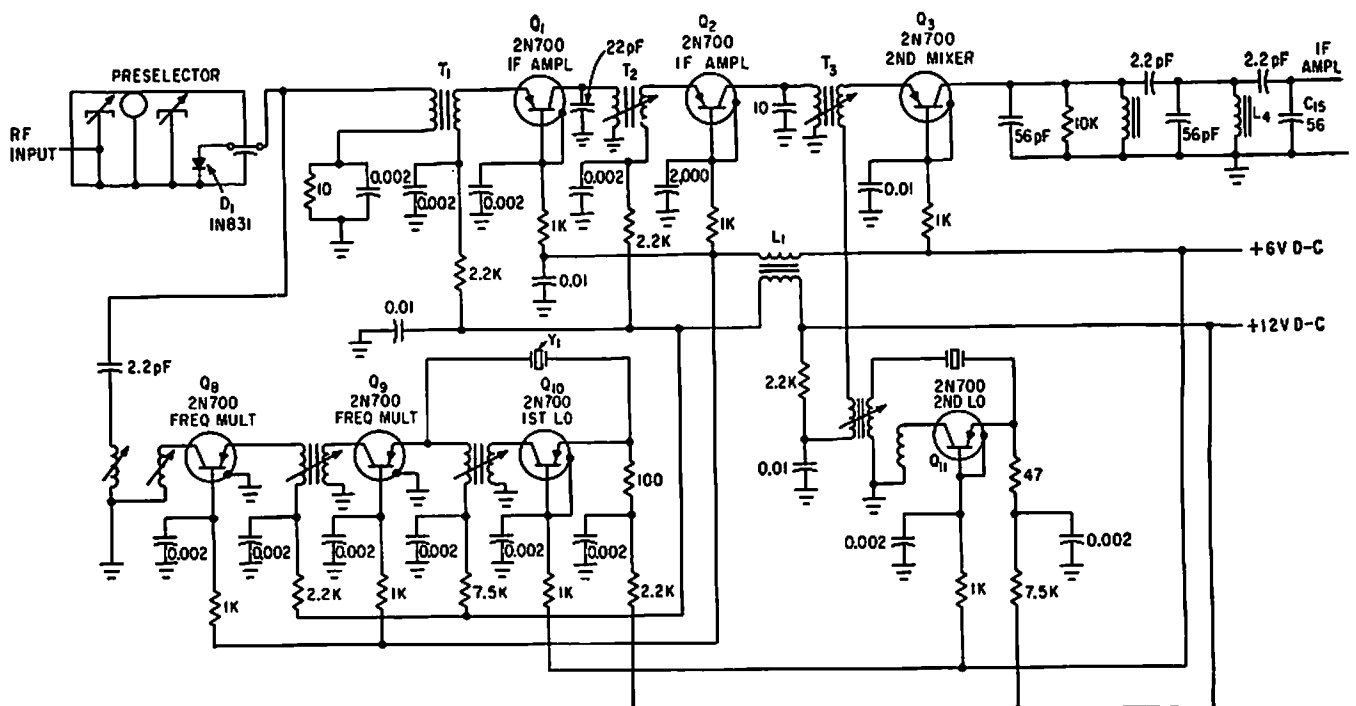
quency of oscillation (118.7 Mc). Overall gain of tuner is 25.5 db at 88 Mc and 22.5 db at 108 Mc.—R. A. Santilli and H. Thanos, *Portable Radio Uses Drift-Field Transistors*, *Electronics*, 33:28, p 48-50.



TWO-TRANSISTOR REFLEX RADIO—Q1 is used regeneratively as r-f amplifier and reflexively as first a-f amplifier, while Q2 serves as power amplifier.—S. A. Sullivan, *Transistor Radio Uses Few Parts*, *Electronics*, 31:1, p 90-92.



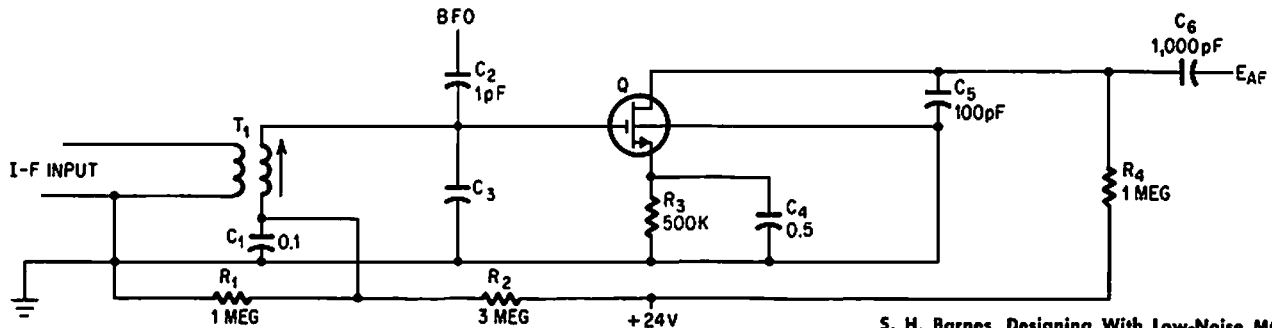
MILLIMETER-WAVE DETECTOR—Biasing with 1N53 crystal detector increases gain 20 db at 73 Mc.—K. Ishii and A. L. Brault, *Crystal Biasing Improves Millimeter-Wave Detector*, *Electronics*, 34:24, p 65.



DOUBLE-CONVERSION F-M SUPERHETERODYNE—Common-base connections in local oscillators Q10 and Q11 give stability with

minimum components in 20-channel Mercury spacecraft command receiver. Each frequency multiplier doubles frequency of first local

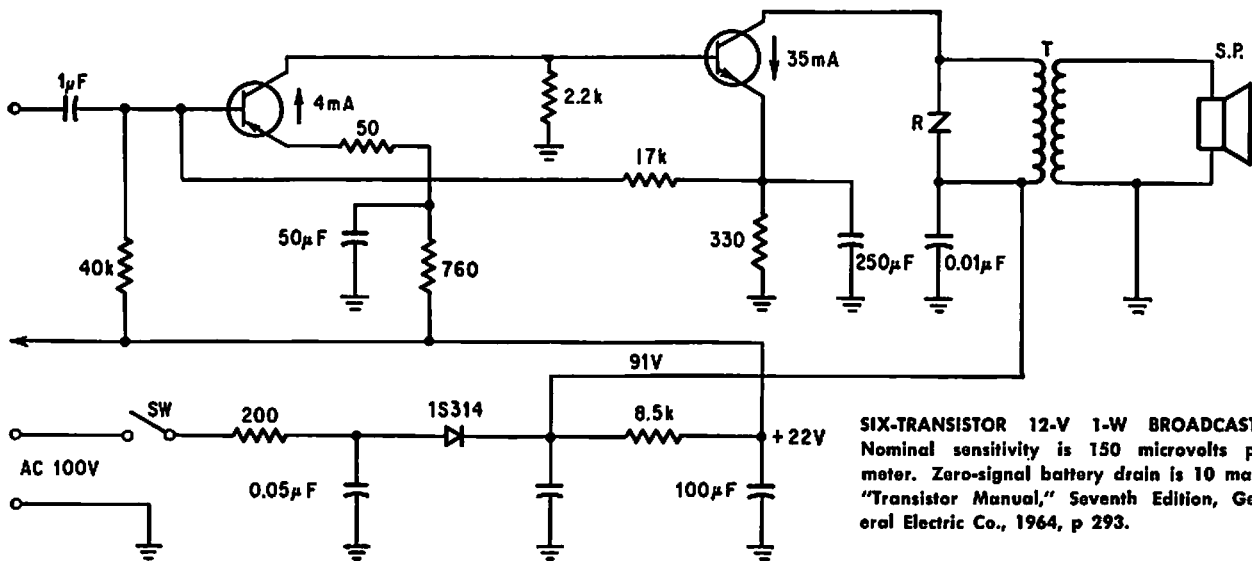
oscillator. I-f output is 10.7 Mc.—R. Elliott, *First Details on Mercury Spacecraft Command Receiver*, *Electronics*, 36:5, p 32-35.



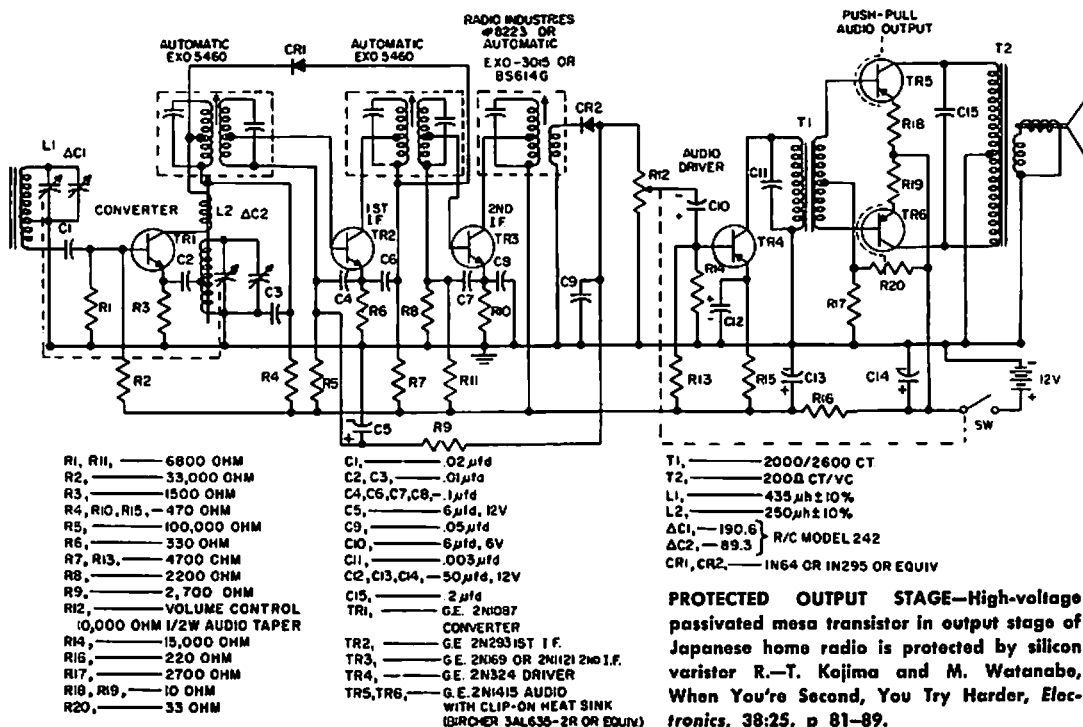
SSB PRODUCT DETECTOR—Square-law relationship between transconductance and drain

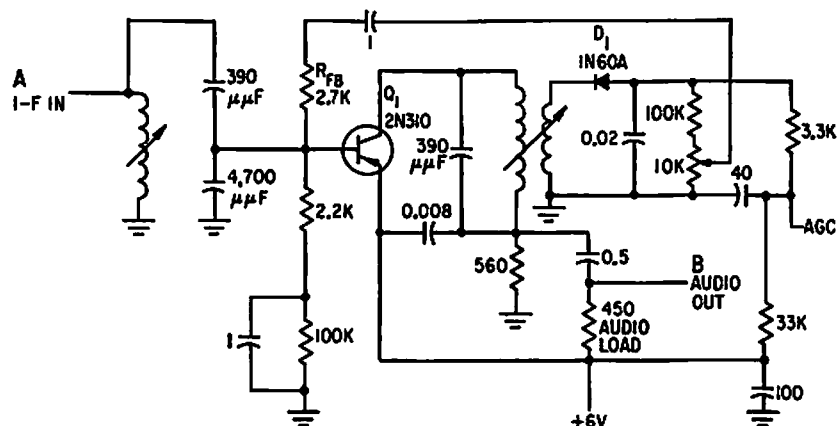
current makes mos fet ideal for product detector in ssb receivers.—G. G. Luettgenau and

S. H. Barnes, *Designing With Low-Noise MOS FETs: A Little Different But No Harder*, Electronics, 37:31, p 53-58.



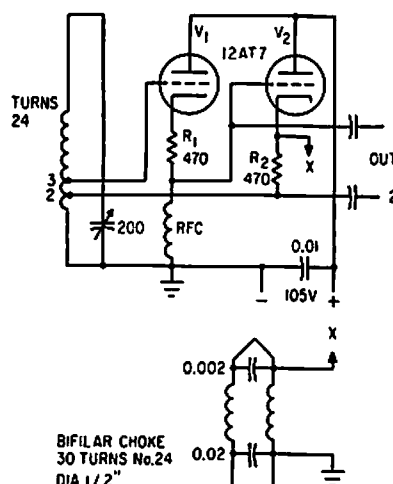
SIX-TRANSISTOR 12-V 1-W BROADCAST—Nominal sensitivity is 150 microvolts per meter. Zero-signal battery drain is 10 ma.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 293.



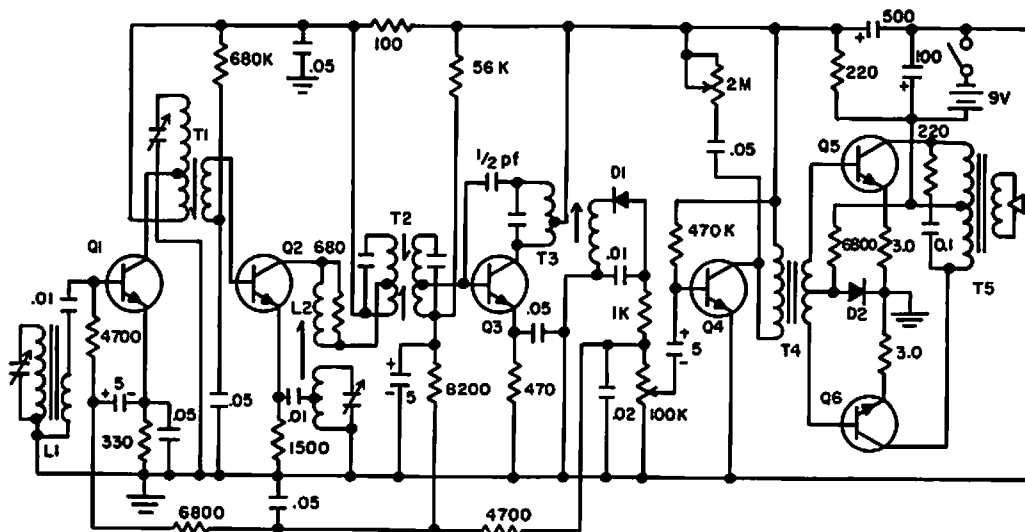


I-F/A-F REFLEX—Single transistor gives simultaneous amplification at intermediate and audio frequencies in economy radio. Careful design provides high gain and sufficient undistorted output power to drive audio output transistor at full rating without motorboating.

—J. Waring, *How To Design Reflexed Transistor Receivers*, *Electronics*, 32:19, p 70-72.



HIGH-STABILITY 3-3.5 MC OSCILLATOR—Modification of Lampkin circuit uses pair of cathode followers in cascade, with tube circuits being tapped across part of coil and excited from resonator through high impedance. Bifilar choke minimizes variations in heater-cathode capacitance of driver V2.—E. Roberson, R-F Oscillator has Improved Stability, *Electronics*, 36:32, p 62-63.



GENERAL ELECTRIC CO.

Q1 2N2715
Q2, Q3 2N2716
Q4 2N2924 OR 2N3392
Q5, Q6 2N2714 (WITH HEAT SINK)
D1, D2 1N4009

RADIO INDUSTRIES, INC.

T1	16412
T2	16414
T3	13964
L1	16413
L2	16411

RADIO CONDENSER CORP.
TUNING CONDENSER

MODEL 42-3A (CN 909991)

PERFORMANCE

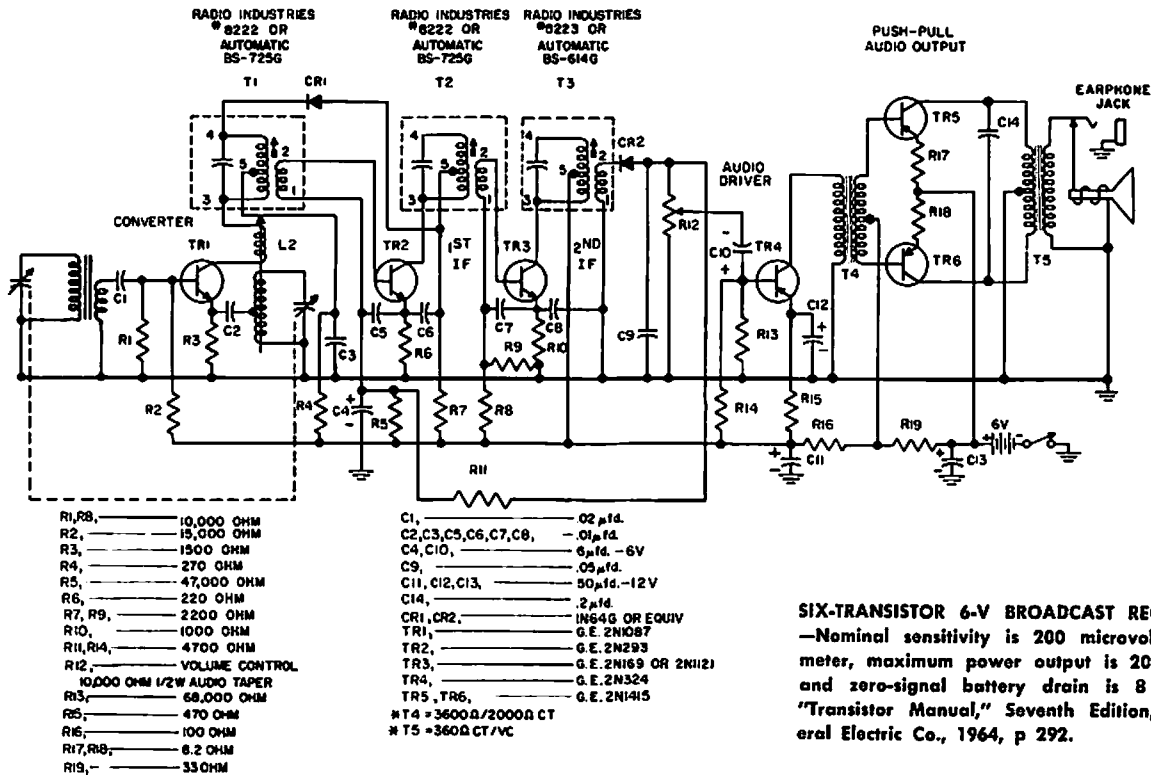
NOMINAL SENSITIVITY	30 μ V/m
RATED OUTPUT POWER	500 MW
BATTERY DRAIN	12.5 MA

OTHER COMPONENTS

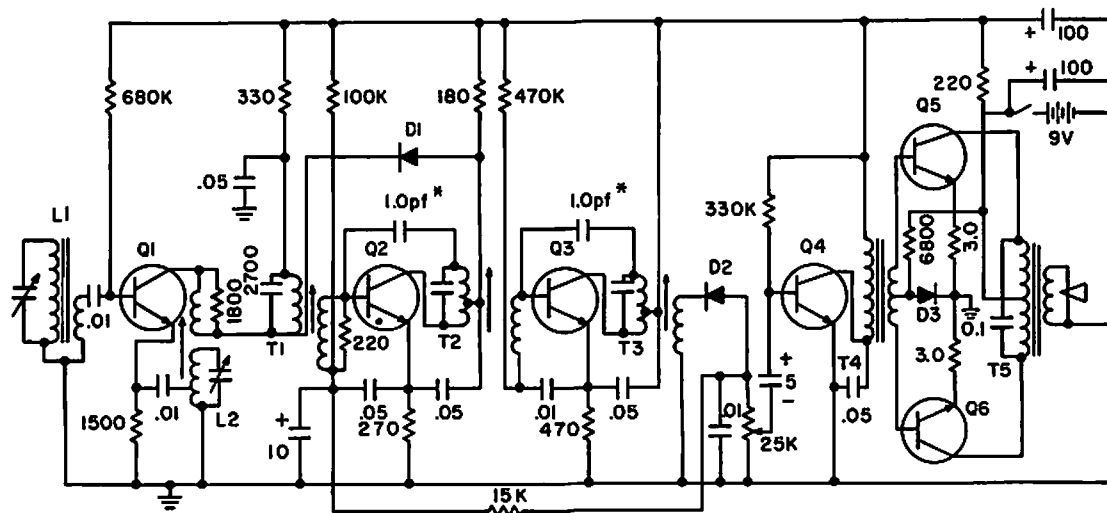
T4 5K/2K CT
T5 250 CT/VC

SIX-TRANSISTOR 9-V BROADCAST WITH TUNED R-F STAGE—Nominal sensitivity is 30 microvolts per meter, power output 500 mw, and battery drain 12.5 ma.—"Transistor Man-

ual," Seventh Edition, General Electric Co., 1964, p 296.



SIX-TRANSISTOR 6-V BROADCAST RECEIVER
—Nominal sensitivity is 200 microvolts per meter, maximum power output is 200 mw, and zero-signal battery drain is 8 ma.—
"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 292.



GENERAL ELECTRIC CO.

Q1, Q3 2N2926 (RED) OR 2N2715 OR 2N3394
Q2, Q4 2N2926 (ORANGE) OR 2N2716 OR 2N3393
Q5, Q6 2N2714 (WITH HEAT SINK)
D1 IN60 (GERMANIUM)
D2, D3 IN4009 (SILICON)

RADIO INDUSTRIES, INC.

T1 - 13964 - R1
T2, T3 - 13964
L1 16413
L2 16411
 Δ C MODEL 42-2A

PERFORMANCE

NOMINAL SENSITIVITY 20 μ v/m
RATED OUTPUT POWER 500 MW
BATTERY DRAIN 10 MA

OTHER COMPONENTS

T4 - 5K/2K CT
T5 - 250 Ω CT/VC

*USE 1.0pf WITH 2N2926 AND 2N3391 SERIES TRANSISTORS, 0.5pf WITH 2N2715 SERIES.

SIX-TRANSISTOR 9-V BROADCAST—Nominal power output 500 mw, and battery drain 10 ma.—"Transistor Manual," Seventh Edition,

General Electric Co., 1964, p 295.

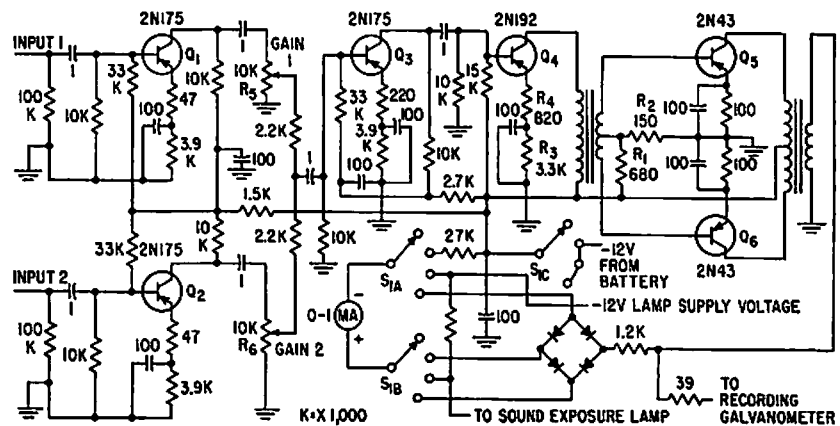


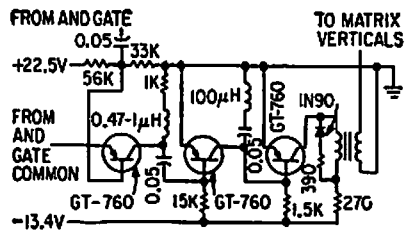
Synthesizer Replaces Sideband Converter, Electronics, 35:25, p 41-43.

CHAPTER 71

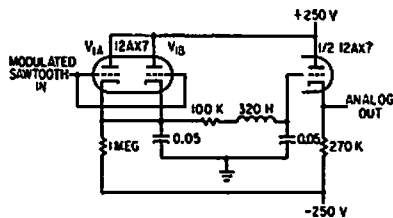
Recorder Circuits

SOUND TRACK DRIVE—Dual-input amplifier drives 10-ohm recording galvanometer for variable-area optical sound track of 16-mm sound-on-film camera. Can be mounted directly on camera. Requires only two 6-v nickel-cadmium cells.—E. M. Tink, Transistorizing 16-Mm Tv Remote Film Camera, *Electronics*, 32:3, p 58-59.

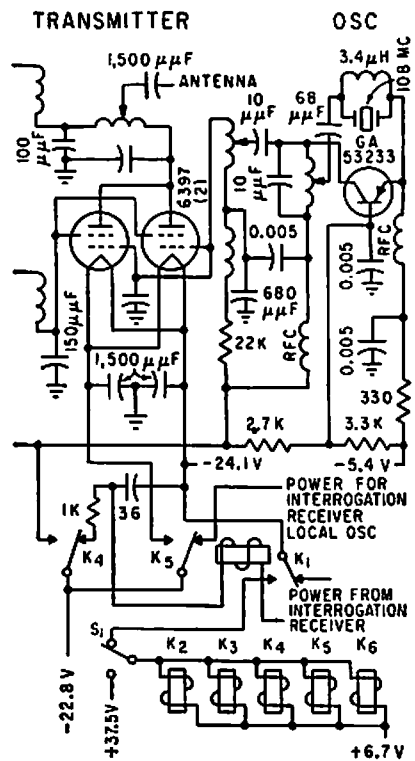




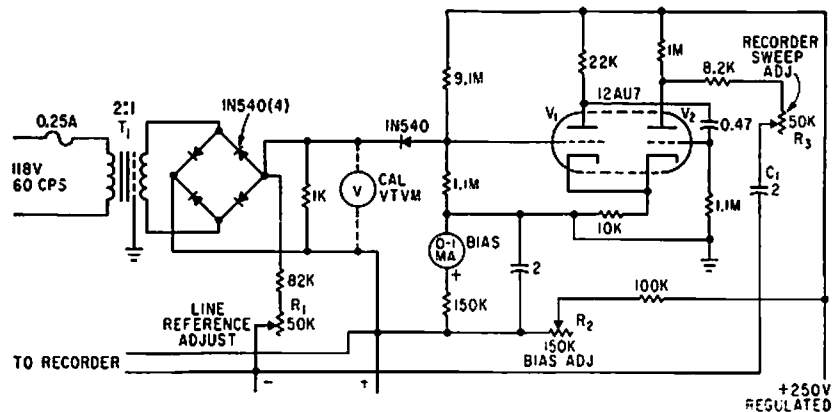
GATED AMPLIFIER DRIVES FERRITE CORES—Used in shockproof recorder in which each amplifier drives a line of six cores. Interrogation of cores releases stored information for processing.—C. P. Hedges, Digital Recorder Holds Data After Shock, *Electronics*, 32:12, p 60-62.



PEAK-READING CIRCUIT—Recovers analog voltage from modulated sawtooth waveform of magnetic-drum recorder.—H. L. Daniels and D. K. Sampson, *Magnetic Drum Provides Analog Time Delay*, *Electronics*, 32:6, p 44-47.

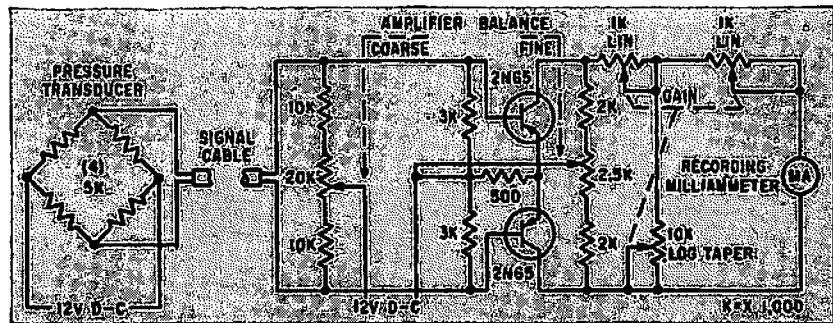


Cover Satellites, Electronics, 32:18, p 44-49.



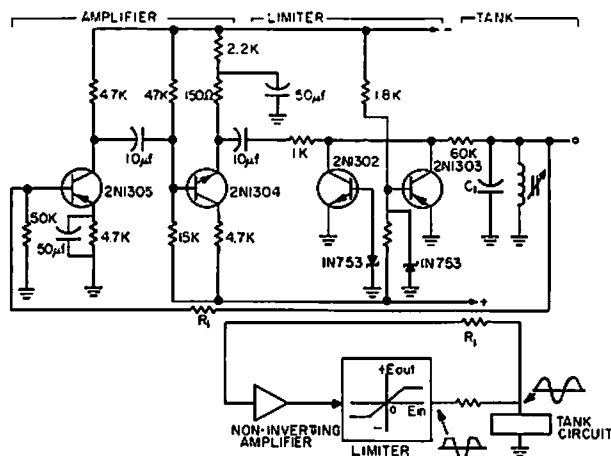
TRACING CAUSES OF LAB LINE TRANSIENTS
—Circuit responds to single pulse having rise time as short as 1 microsec, and records average value of line voltage. Transients greater than preset trigger level pass through

diode gate and trip mono, giving current pulse that drives chart recorder pin.—F. Trainor, Transient Recorder Monitors Power Lines to Protect Circuits, *Electronics*, 34:29, p 74-75.



RUGGED DESIGN FOR OCEANOGRAPHY—Can drive low-impedance recording galvanometer for long periods without auxiliary power. Bilateral symmetry of push-pull circuit using matched 2N65 transistors optimizes linearity

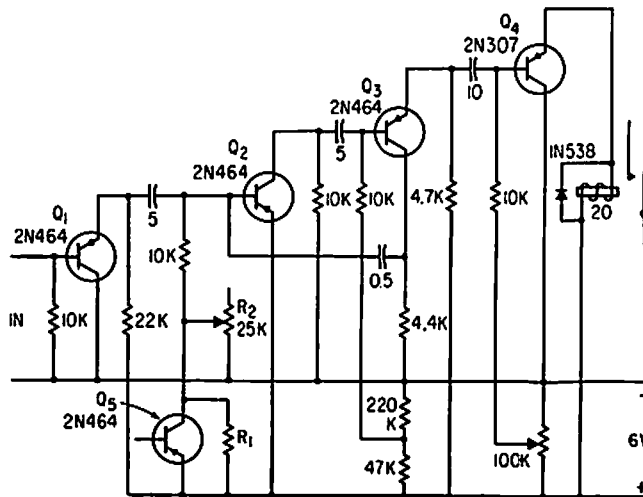
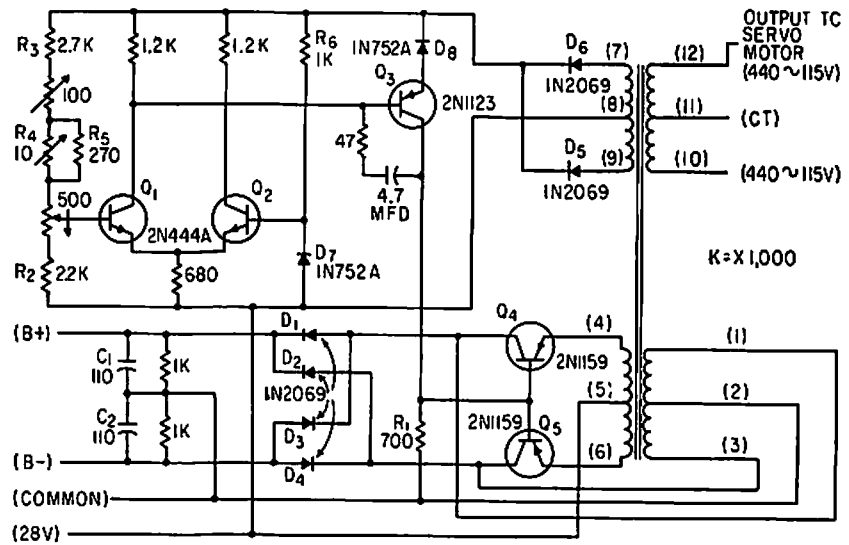
and thermal stability. Although designed for d-c operation, response is flat within 2 db up to 50 kc.—W. G. Van Dorn, Transistor D-C Amplifier for Rugged Use in Field, *Electronics*, 33:1, p 85.



TRANSDUCER EXCITER—Used in carrier amplifier of strip-chart recorder to provide amplitude-stable fixed excitation frequency for transducer. Two-stage amplifier with 360° phase shift oscillates when output is fed to

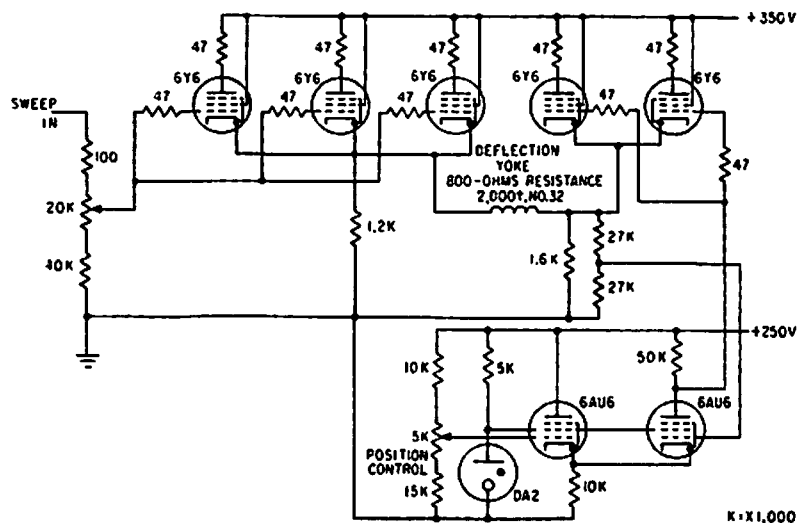
input, at frequency depending on loop parameters. Amplitude variation is held to 0.2% for 25°C change in ambient by simple zener and transistor limiter.—Amplitude-Stable Audio Oscillator, *EEE*, 11:8, p 87.

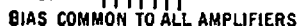
PRECISION FREQUENCY GENERATOR—Provides 440 cps at 115 v for timing motor of engraved-fail flight recorder. Uses saturating-transformer oscillator and auxiliary regulating circuits to maintain precise voltage and frequency.—H. E. Schauwecker, Data Recorder for Airplane Flight Analysis, *Electronics*, 33:48, p 118-120.



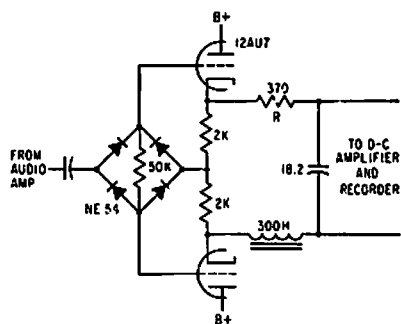
RAIL FAULT-DETECTING AMPLIFIER—Signals from inductive pickup near rail are amplified enough to drive sensitive relay of pen recorder. Amplifier does not block after being overloaded when pickup passes over rail joint.—H. W. Keovil, Transistor Pulse Amplifiers Detect Rail Faults, *Electronics*, 35:21, p 53-54.

FACSIMILE SWEEP AMPLIFIER—Provides power amplification for driving electronic high-definition facsimile recorder. Sweep input voltage is high enough to eliminate need for voltage gain in power amplifier stages.—E. W. VanWinkle, High-Precision Sweep Generator, *Electronics*, 33:50, p 88-90.

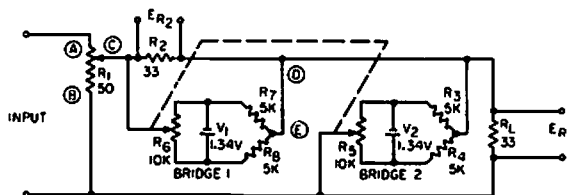




MAGNETIC DRUM READ AMPLIFIER—Amplifies phase-modulated step-modulated Manchester signal from magnetic drum read head and provides phase detection for recovery of stored information. Also used for synchronization.—A. J. Strassman and R. E. Kester, *Clock Track Recorder For Memory Drum, Electronics*, 32:41, p 74-76.

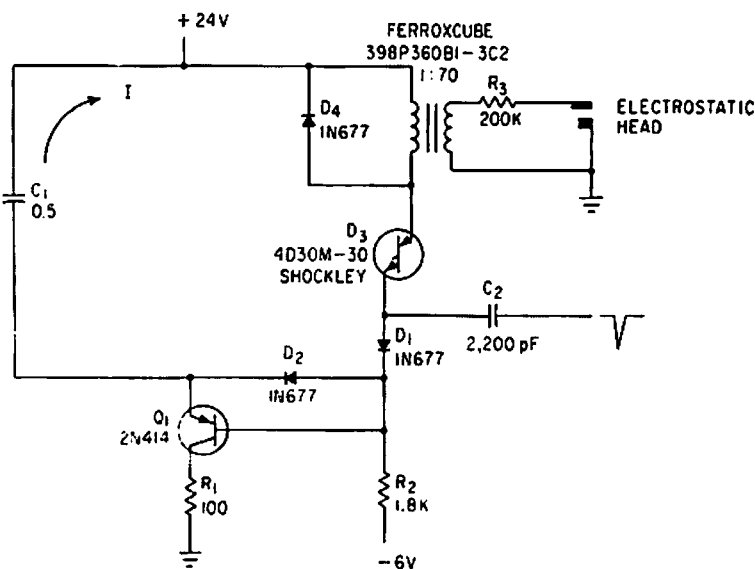


VU RECORDER—Used to provide permanent records of broadcast speech levels and for checking audio network circuits. Circuit has same rise time, overshoot, frequency response, and rectifier characteristics as standard vu meter.—D. H. McRae, Vu Recorder Has Standard Response, *Electronics*, 31:17, p 78-82.



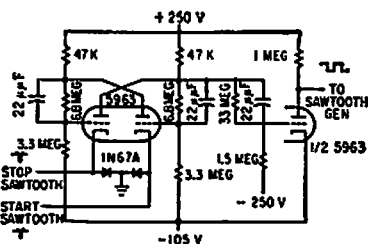
POSITIONING AND ATTENUATING CONTROL—Adds d-c positioning voltage to input signal of recording galvanometer. Magnitudes of input and positioning voltages can be varied independently without interaction. Cir-

cuit also has attenuating control for signal voltage.—N. Kassowitz, Non-Interacting Positioning and Attenuating Controls, *EEE*, 13:3, p 47.

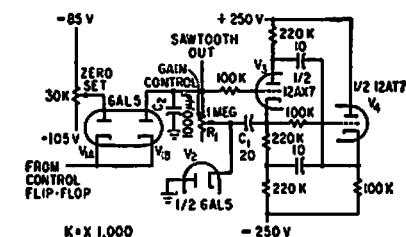


10,000 1-KV PULSES PER SECOND—Four-layer diode D3 discharges C1 through pulse transformer and transistor Q1 prevents diode from remaining in conducting state. Used in elec-

trographic recorder.—N. C. Hokimian and P. M. Schmitz, Four-Layer Diode Triggers High-Voltage Pulse Generator, *Electronics*, 34:26, p 84-85.



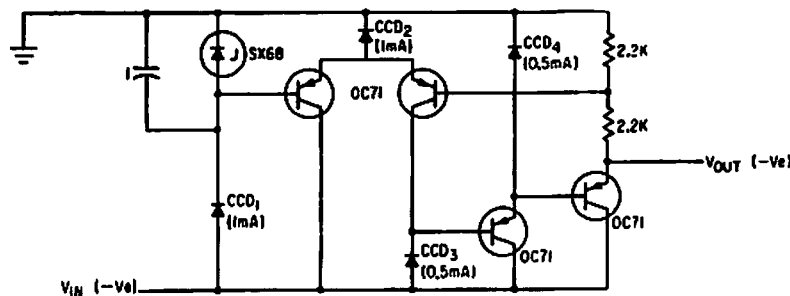
RECORDING SAWTOOTH-CONTROLLING FLIP-FLOP—Used for sawtooth generator of magnetic drum recording system.—H. L. Daniels and D. K. Sampson, *Magnetic Drum Provides Analog Time Delay*, *Electronics*, 32:6, p 44-47.



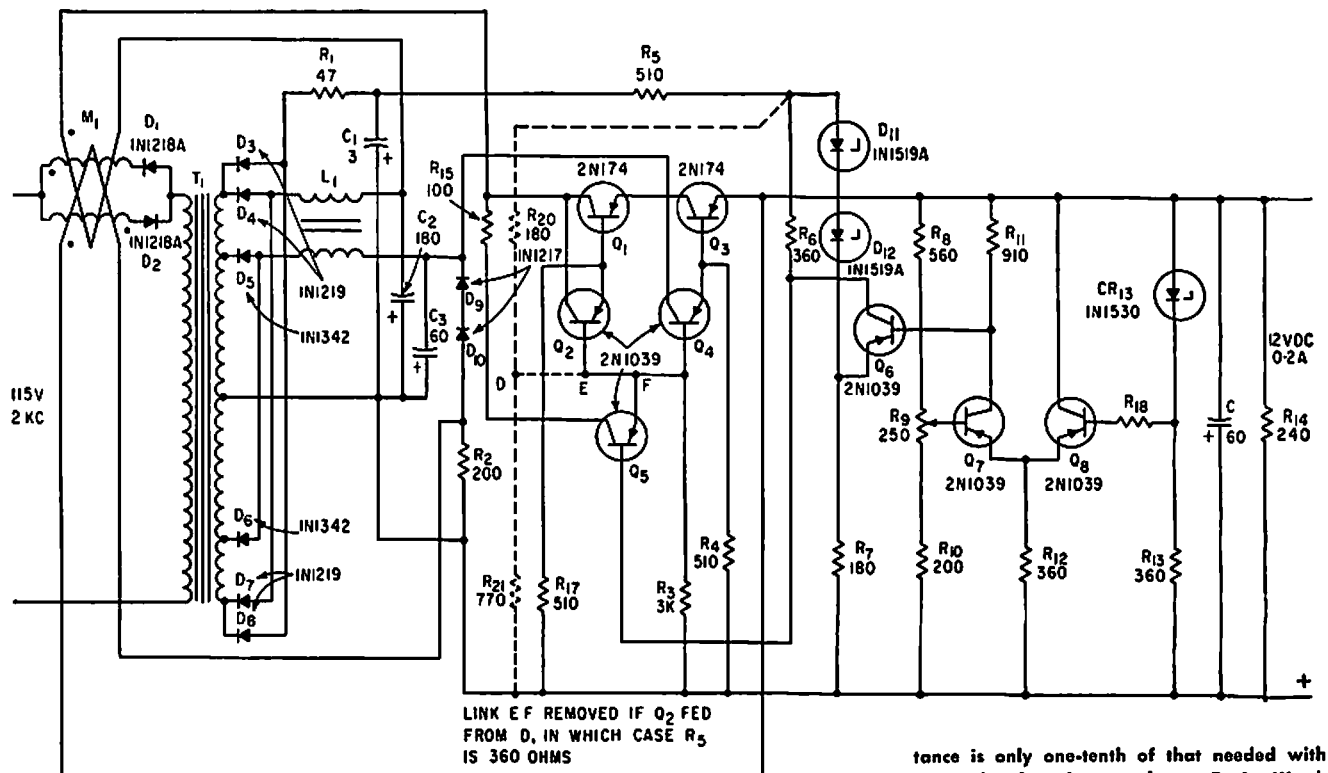
READING SAWTOOTH-CONTROLLING FLIP-FLOP—When flip-flop output is negative with respect to zero-set reference voltage, sawtooth output is dropped through diode gate to reference voltage. Start-sawtooth pulse makes output of flip-flop positive.—H. L. Daniels and D. K. Sampson, Magnetic Drum Provides Analog Time Delay, *Electronics*, 32:6, p 44-47.

CHAPTER 72

Regulated Power Supply Circuits



VOLTAGE STABILIZER USING FOUR CONSTANT-CURRENT DIODES—Value of stabilized output voltage can be adjusted by placing potentiometer in parallel with SX68 zener diode and connecting base of Q1 to slider. With this arrangement, magnitude and phase angle of output impedance are not affected by output voltage setting.—T. K. Hemingway, Applications of the Constant-Current Diode, *Electronics*, 34:42, p 60-63.

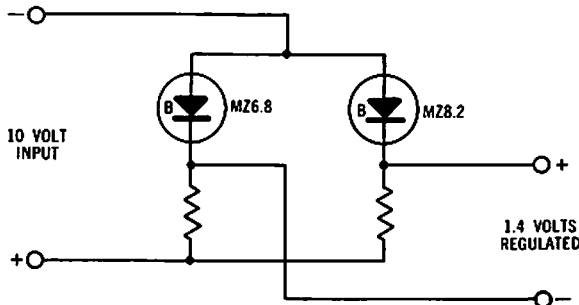
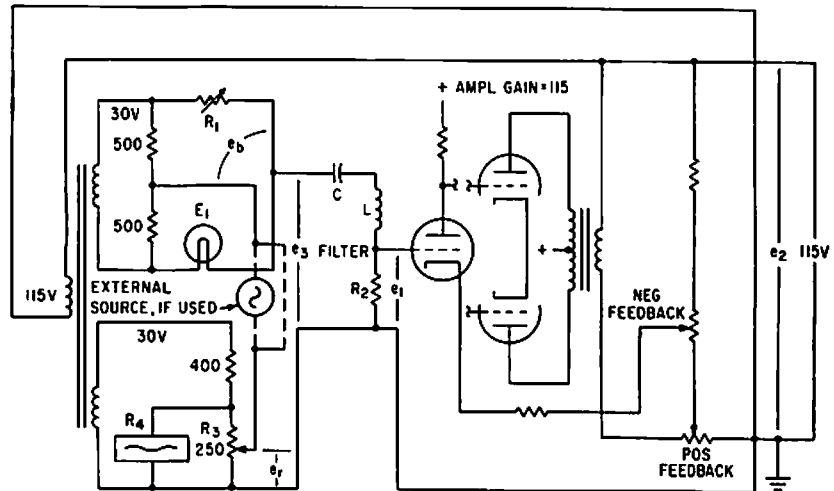


TWO-LEVEL REGULATION FOR 12 V AT 0.2 A—Transformer provides two voltages, one for

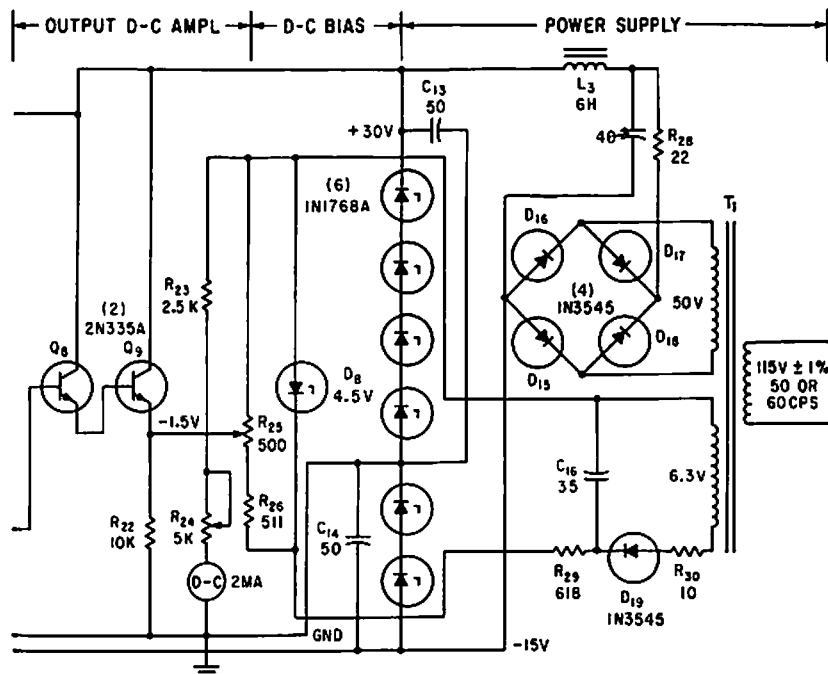
normal operation and the other to supply current during transients. Storage capaci-

tance is only one-tenth of that needed with conventional series regulator.—F. L. Ward, Novel Bi-Level Regulator Reduces Storage Capacitance, *Electronics*, 35:32, p 74-75.

VARIABLE-FREQUENCY A-C REGULATOR—Commercial ballast tube in thermal regulating bridge is used with feedback-stabilized amplifier and filter to regulate a-c voltage source to 0.1%. Used for instrument calibration. Triode oscillator circuit oscillates at series resonant frequency of LC filter, which can be tuned from 50 to 2,000 cps.—E. A. Gilbert, *Precision Variable Frequency Power Supply*, *Electronics*, 34:2, p 99-100.



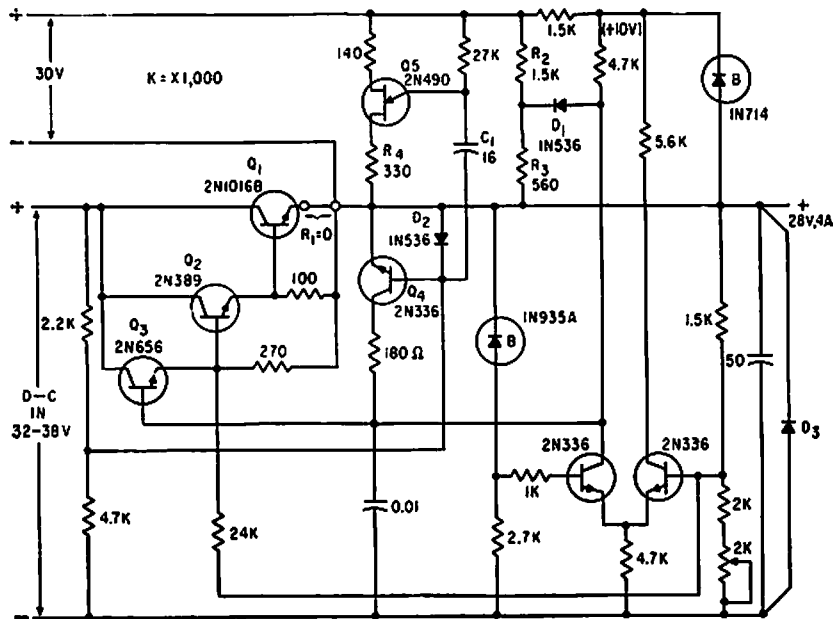
1.4 V TWO-ZENER REGULATOR—Used to deliver regulated voltage lower than is normally available with zener diodes. Difference voltage is used for output. Gives excellent temperature compensation because both diodes tend to drift in same direction.—"Zener Diode Handbook," International Rectifier Corp., 1960, p 54.



REGULATOR DIODE STRING—Six 5% silicon regulator diodes operated at 65 ma give +30 v at 90 ma and -15 v at 95 ma. Used

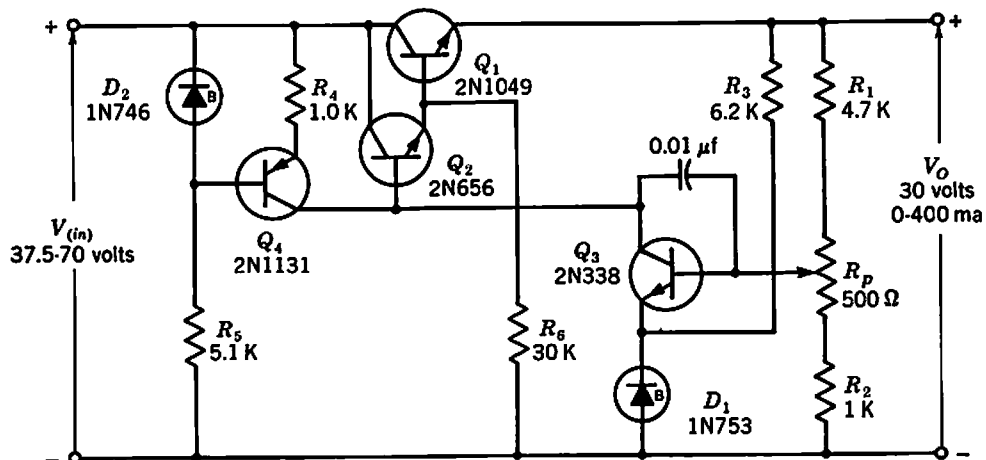
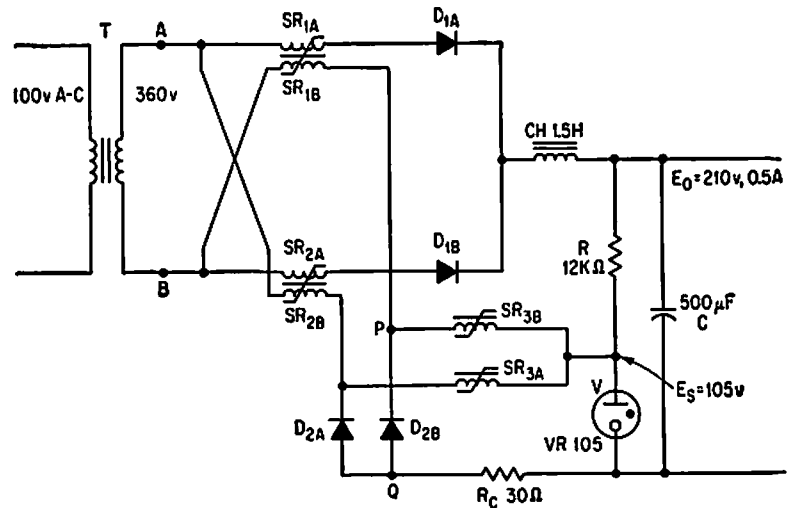
with vibration-measuring circuit whose peak-reading output drives d-c amplifier Q8-Q9 to give required output current of 2 ma for d-c

meter or recorder.—H. A. Harriman and W. M. Trenholm, *Vibration Measurements with Peak-Reading Circuit*, *Electronics*, 35:20, p 57-59.



SHORT-CIRCUIT PROTECTION—Series regulator has automatic pulsing-type short-circuit protection. D1, R2, and R3 form constant-current prelimiting circuit, and Q4 is shut-off transistor. Unijunction transistor Q5 pulses continuously. D2 completes discharge path of C1 through R4 when Q5 fires.—A. G. Lloyd, Overload Protection for Transistor Voltage Regulators, *Electronics*, 33:52, p 56-59.

TRANSDUCTORS STABILIZE HIGH-POWER RECTIFIER—Rectangular-loop saturable reactors SR in single-phase power supply hold output voltage constant within 1% at load currents of 0 to 20 amp and line voltage variations of 50%. Choice of components determines power capacity.—T. Kurimura and K. Yamamura, New Way to Use Saturable Reactors: Stabilizing High-Power Rectifiers, *Electronics*, 36:21, p 62-66.

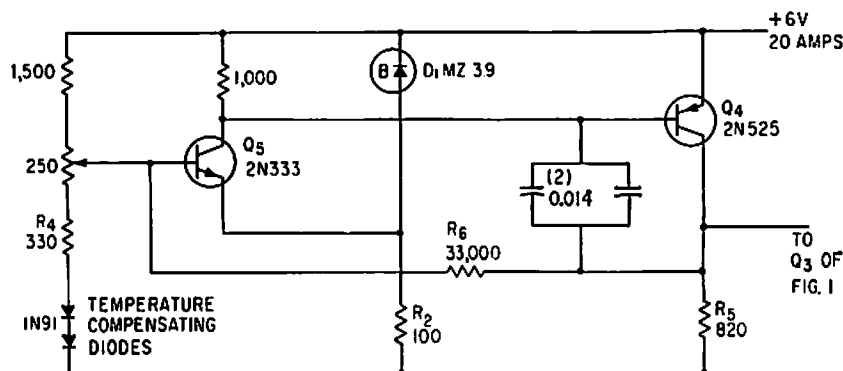
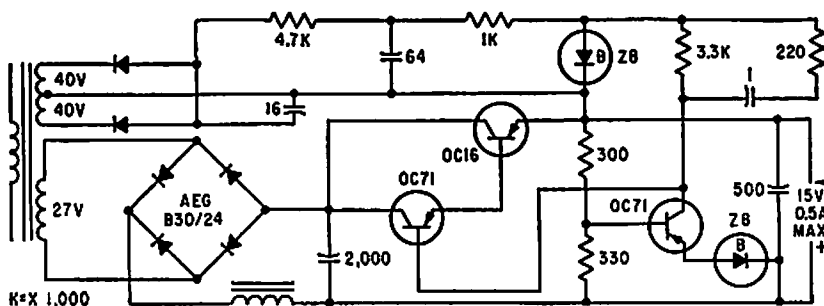


SERIES REGULATOR WITH TRANSISTOR PRE-REGULATOR—Design procedure is given to meet specification that regulation factor F range from 0.001 for no load to 0.00145 for

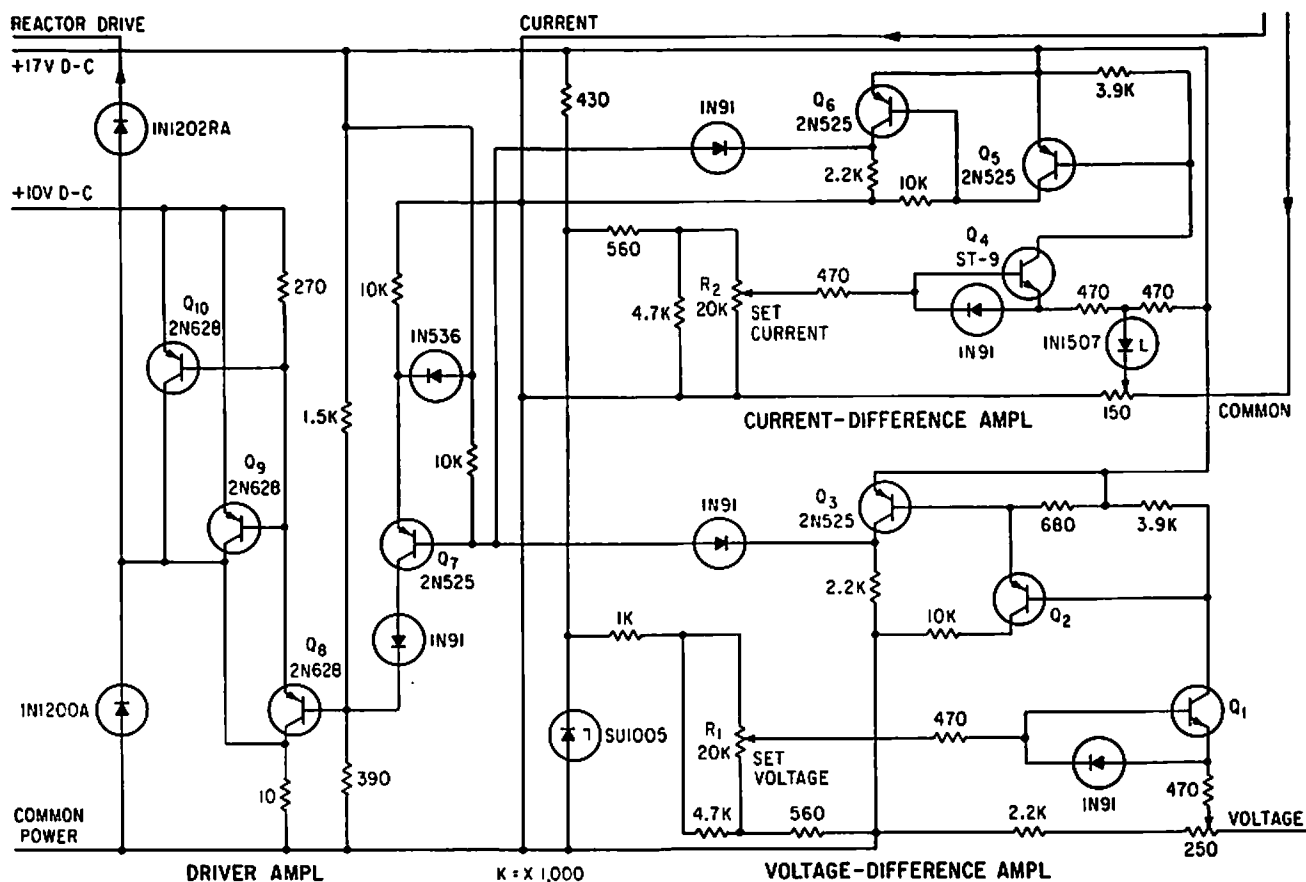
full load when input voltage varies over range specified. Output varies from 30.7 v to 31.1 over temperature range of -50 to +125°C.—Texas Instruments Inc., "Transistor

Circuit Design," McGraw-Hill, N.Y., 1963, p 160.

—Provides constant 15 v for moderately variable load, with temperature coefficient of 1 mv per degree C, 4 mv peak-to-peak ripple, and 0.5 amp maximum current.—E. Boldinger and W. Czoja, *Designing Highly Stable Transistor Power Supplies*, *Electronics*, 32:39, p 70-73.



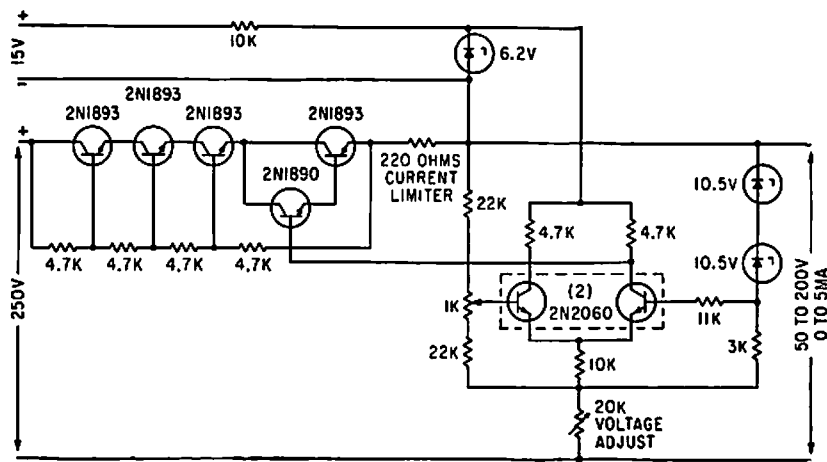
ZENER REFERENCE—Sensing circuit for 6-v constant-voltage transformer-regulated power supply develops error signal for controlling shunt transistors.—J. T. Keefe, Transformer and Shunt Transistors Regulate D-C Power Supply, *Electronics*, 34:20, p 99—101.



FORMING ELECTROLYTICS—Current-difference amplifier delivers constant current during initial forming of electrolytic capacitors, and voltage-difference amplifier holds voltage

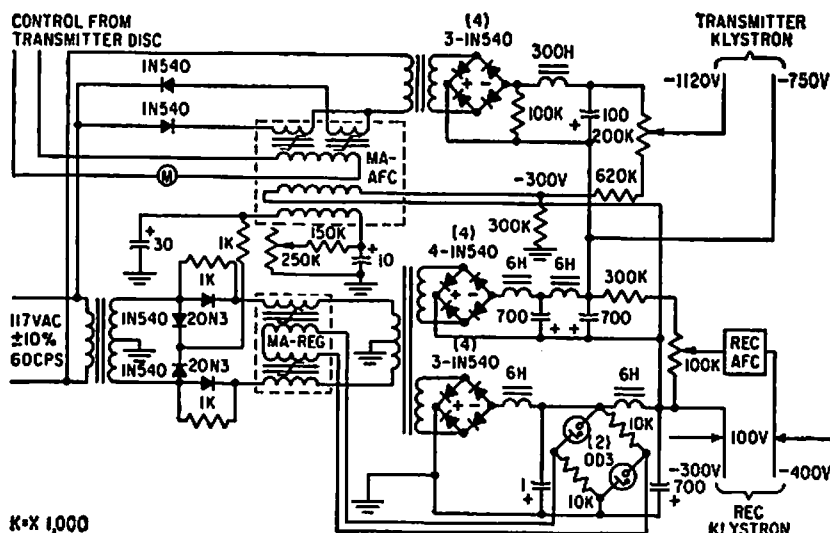
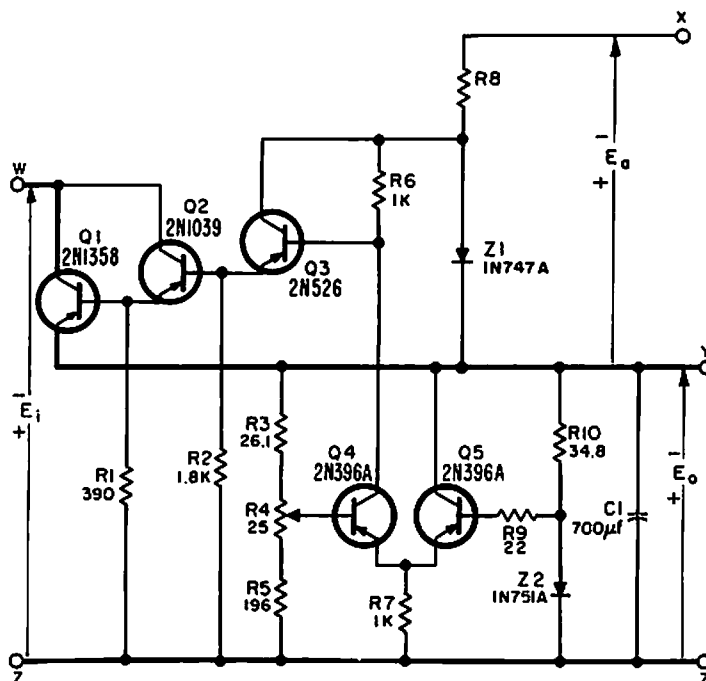
constant when final forming voltage is reached. Values of current and voltage can be adjusted over wide range of preset values for different sizes of capacitors.—J. W.

Martin and H. Liepins, Unique Power Supply Delivers Constant Voltage and Current, Electronics, 35:31, p 40-41.



MAGNETRON INJECTION ELECTRODE SUPPLY—Constant-voltage bridge floating on variable resistor feeds differential amplifier and series regulator. Rheostat setting determines value of regulated output voltage.—S. Prigozy, *Designing Special Power Supplies for Voltage-Tunable Oscillators*, *Electronics*, 35:44, p 48-50.

6-V D-C REGULATOR—Provides 4 amp at 6 v with 1% regulation for inputs of 7 to 50 v from unregulated source. Auxiliary source E_a must be minimum of 5 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 1, p 1-2.



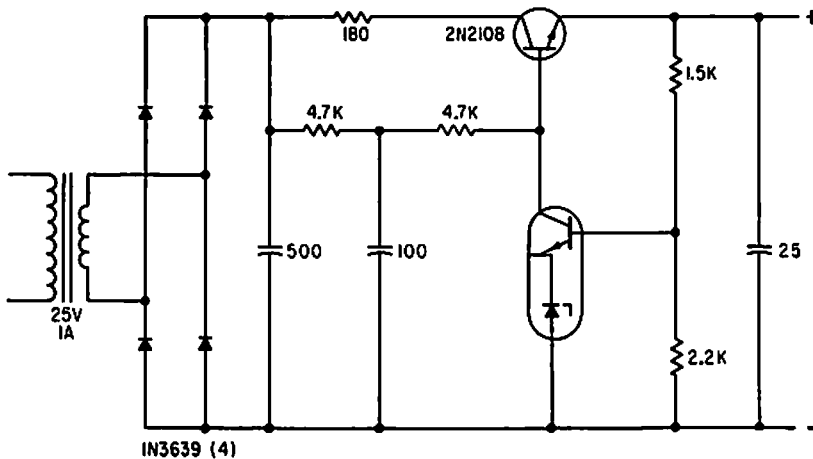
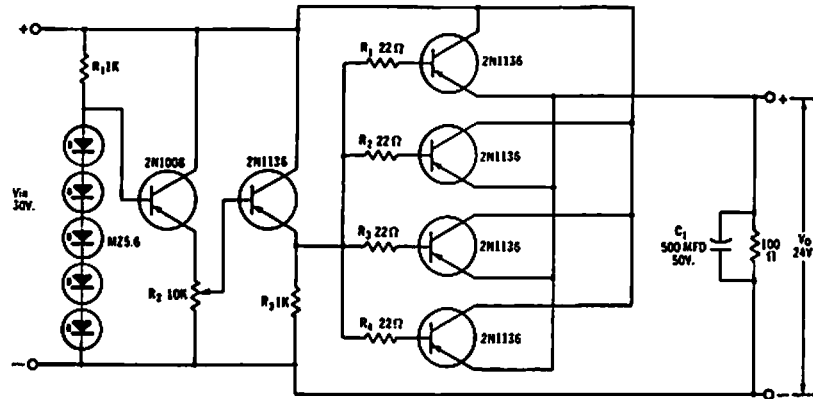
K $\times$ 1,000

MAGNETIC-AMPLIFIER REGULATOR—Provides stable operating voltages for transmitter and

receiver local-oscillator klystrons in 6,000-Mc microwave link.—M. C. Harp, *Nonvacuum*

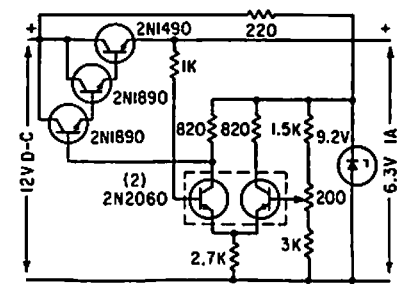
Devices Control Klystrons, *Electronics*, 32:7, p 68-70.

HIGH-CURRENT REGULATOR—Four parallel-connected transistors handle up to 240 w if mounted with heat sink. If output voltage is reduced, separate power supply must be provided for zener regulator to protect transistors.—"Zener Diode Handbook," International Rectifier Corp., 1960, p 57.

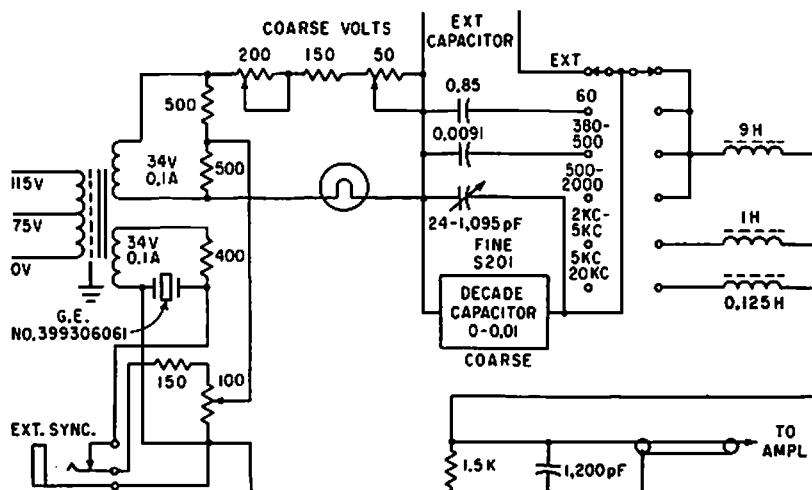


12-V REGULATED SUPPLY WITH REFERENCE AMPLIFIER—Integrated transistor and zener diode in reference amplifier act with transistor 2N2108 to hold 12-v d-c output voltage within 0.3% over a-c line voltage variations of 10% for load currents up to 100 ma.—T. P. Sylvan, New Device Simplifies Power Supply Design, *Electronics*, 36:20, p 39-43.

age within 0.3% over a-c line voltage variations of 10% for load currents up to 100 ma.—T. P. Sylvan, New Device Simplifies Power Supply Design, *Electronics*, 36:20, p 39-43.



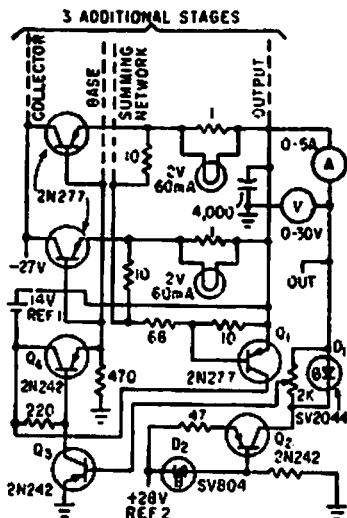
BACKWARD-WAVE OSCILLATOR FILAMENT SUPPLY—Provides constant voltage for filament of backward-wave oscillator. Output voltage is compared with fixed reference in d-c amplifier, and difference is used to control series pass element.—S. Prigozy, Designing Special Power Supplies for Voltage-Tunable Oscillators, *Electronics*, 35:44, p 48-50.



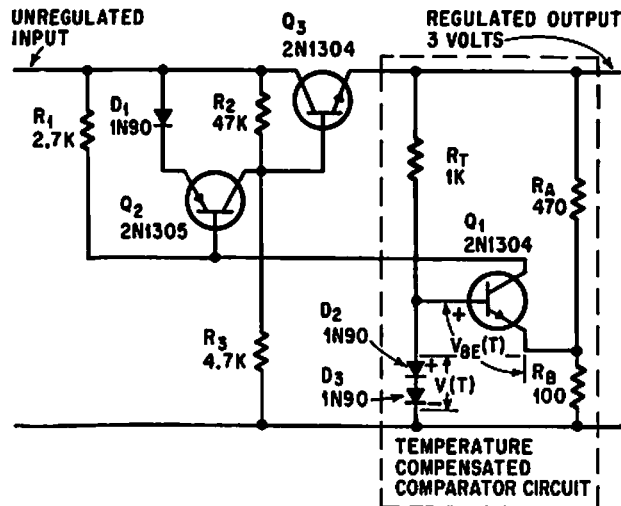
50 CPS-2 KC REGULATOR—Thermal bridge using ordinary iron-wire ballast tube is used with tunable filter of 200 v-a variable-frequency power supply for instrument calibration. Output stage (not shown) uses two 7378 pentodes in push-pull class AB1, with

positive feedback.—E. A. Gilbert, Precision Variable Frequency Power Supply, *Electronics*, 34:2, p 99-100.

positive feedback.—E. A. Gilbert, Precision Variable Frequency Power Supply, *Electronics*, 34:2, p 99-100.

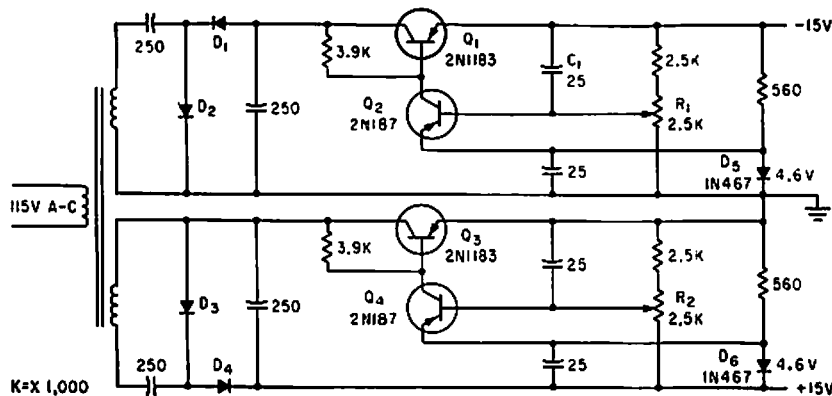


5 AMP AT 0 TO 20 V—Five regulator transistors in 5-amp power supply have indicator lamps at their emitters. Regulation is better than 0.1% at 20 v, and ripple is below 1 mv rms.—J. A. Wheeler and E. J. Currence, Fault-Indicating Series Regulator, *Electronics*, 34:4, p 60.

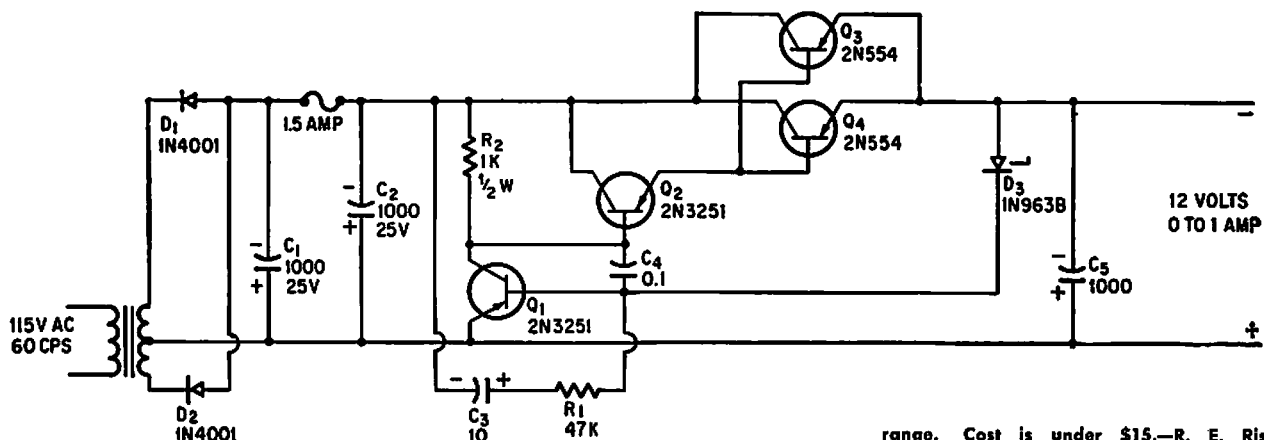


REGULATED 3-V SUPPLY—Junction diode D1 provides nonlinear voltage-current characteristic of zener diode, as required for reference level below 2 v. Poor temperature characteristics of junction diode are offset by base-emitter voltage variation of transistor

Q1 with temperature. Regulated output of 3 v within 2%, at 5 to 100 ma, is obtained from unregulated 4.8-v source over range of -16°C to $+50^{\circ}\text{C}$.—A. K. Scidmore, Junction Diode Regulates Low-Voltage Supply, *Electronics*, 37:27, p 55-56.



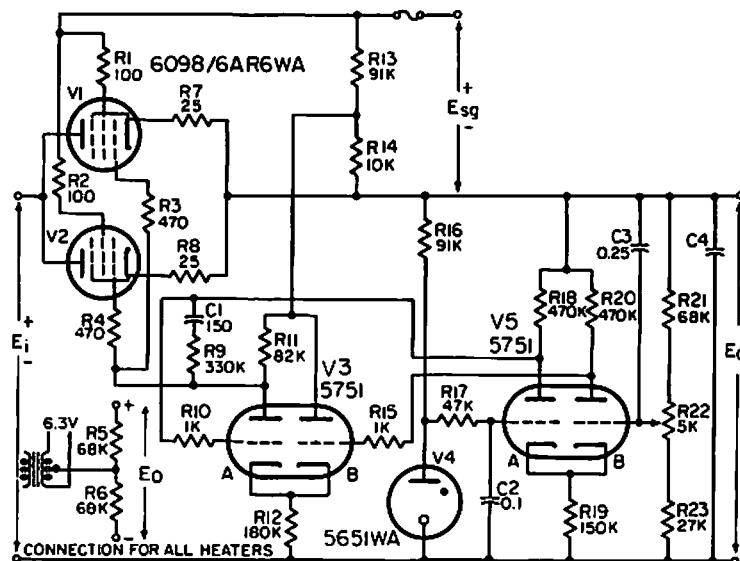
DUAL-POLARITY 15-V SUPPLY—Output voltage is adjustable from 11 to 15 v d-c and nearly constant from no load to 300 ma, or from 90 to 140 v a-c line voltage.—D. T. Birch and K. E. Chellis, Regulated Positive-Negative Supply Delivers Low-Voltage Direct Current, *Electronics*, 34:30, p 62.



LOW RIPPLE AT LOW COST—Ripple at output is used to control d-c resistance of series

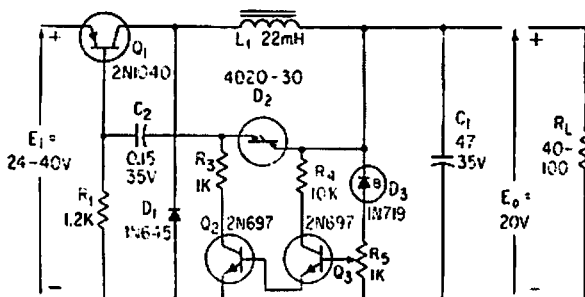
regulator transistors Q3-Q4, to keep ripple below 10 mv rms over wide temperature

range. Cost is under \$15.—R. E. Risely, Power Supply Reduces Ripple by Varying Series Resistance, *Electronics*, 39:2, p 74-75.



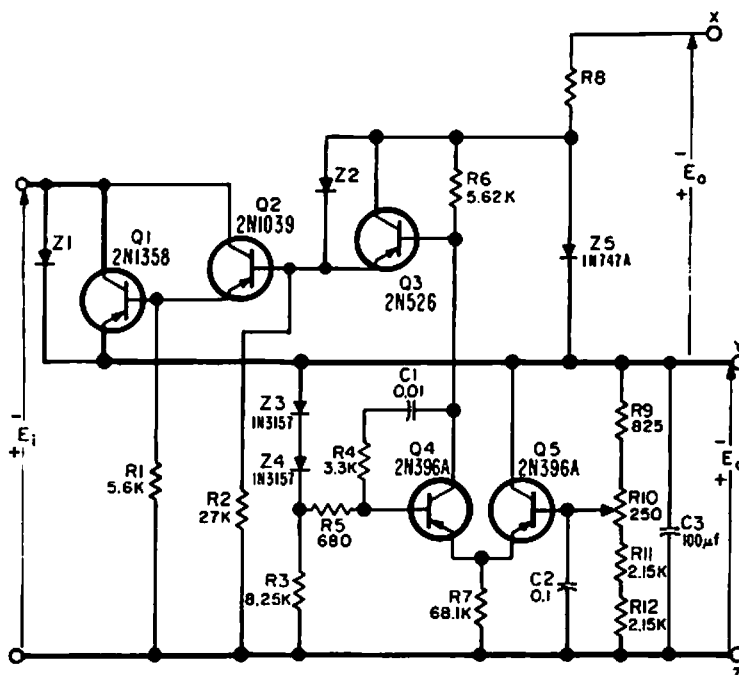
PREFERRED 0.1% REGULATION 300-V D-C—Provides either polarity, for applications requiring superior regulation and long-time stability. Minimum input is 340 v d-c, and minimum Esg is 150 v. Maximum load cur-

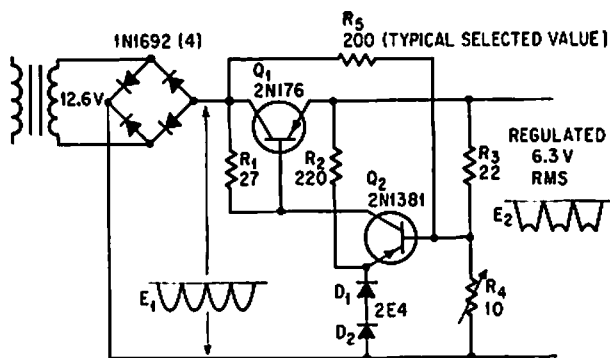
rent is 100 ma per series tube. C4 is minimum of 4 mfd.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 5, p 5-2.



D-C SWITCHED REGULATOR—Gives 0.5% regulation for input voltage range from -15% to +30%. Efficiency is 95%. Transistor is near-ideal switch, having low leakage when open and low voltage when closed.—A. A. Soranson, Solid-State D-C Switched Regulators, *Electronics*, 33:48, p 121-123.

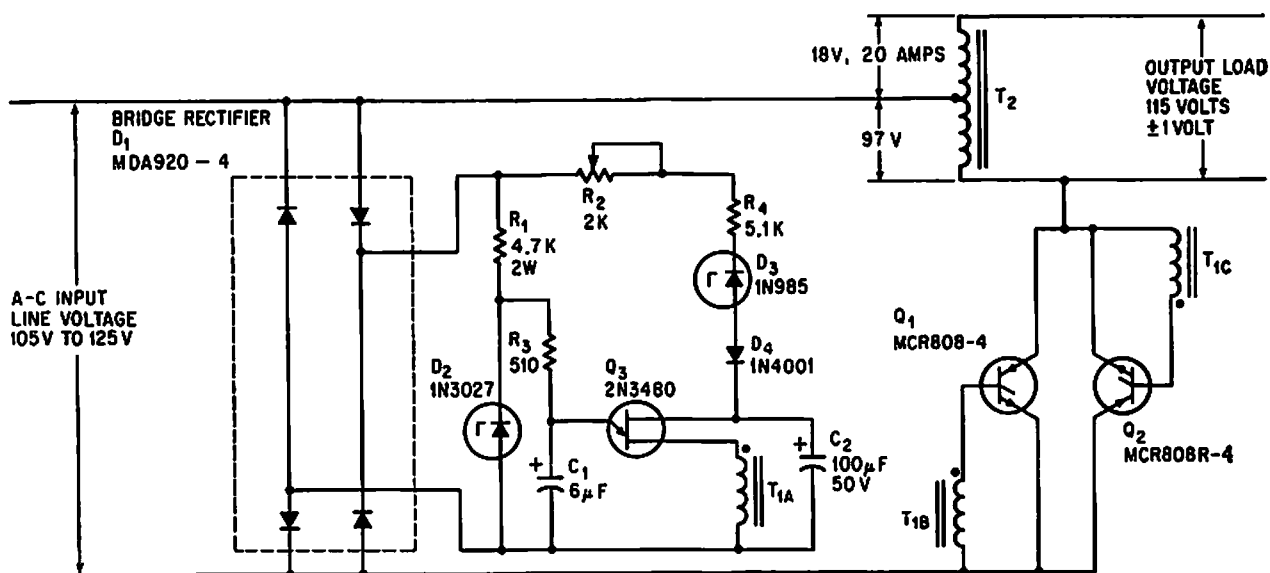
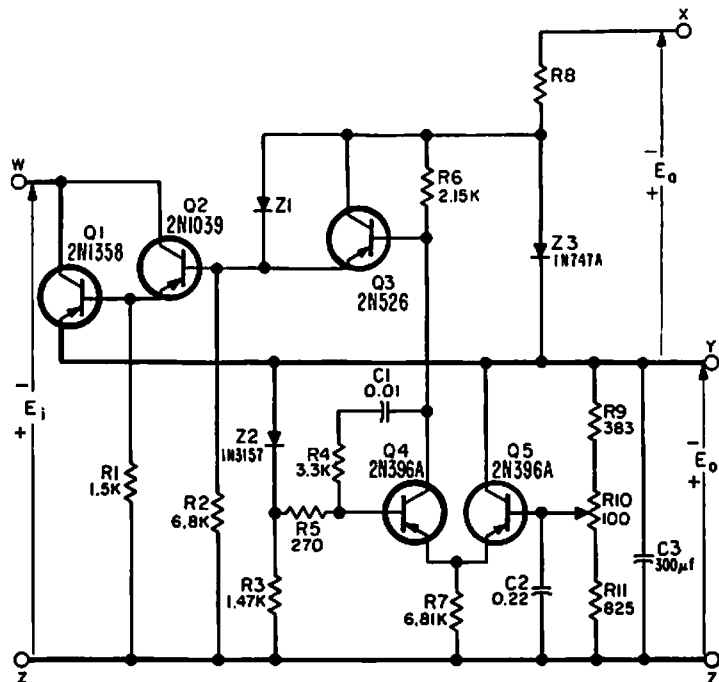
100-V D-C REGULATOR—Provides up to 400 ma at 100 v with 1% regulation for inputs of 101 to 150 v from unregulated source. Auxiliary source Ea must be minimum of 5 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 5, p 5-2.





HEATER VOLTAGE REGULATOR—Clipping action is combined with depression of flat-top portion of output waveform in proportion to input voltage change, to hold rms output voltage constant within 0.2% of voltage determined by value of R_5 .—J. D. Wells, Low-Cost Adjustable Regulator Consumes Little Power, *Electronics*, 38:23, p 109-110.

25-V D-C REGULATOR—Provides up to 1.5 amp at 25 v with 1% regulation for inputs of 26 to 50 v from unregulated source. Auxiliary source E_a must be minimum of 5 v. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 3, p 3-2.



T_{1A} - 30 TURNS OF AWG NO. 22

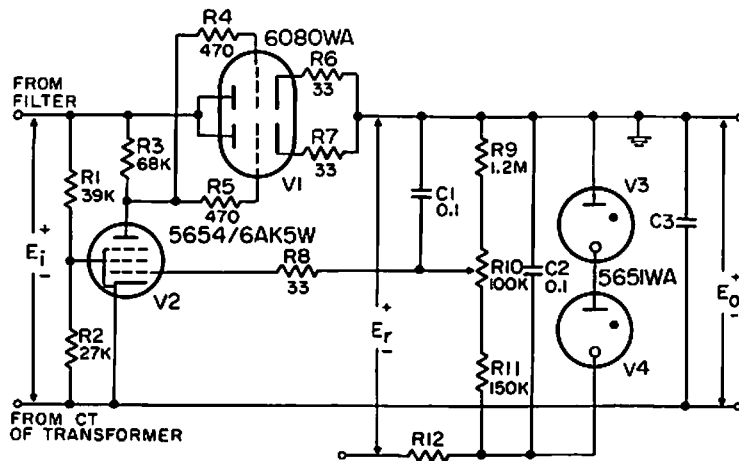
T_{1B} & T_{1C} - 45 TURNS OF AWG NO. 22

T_1 CORE - FERROXCUBE 203F181 - 3C, OR EQUIVALENT

LINE VOLTAGE REGULATOR—Line voltage controls frequency of relaxation oscillator Q3,

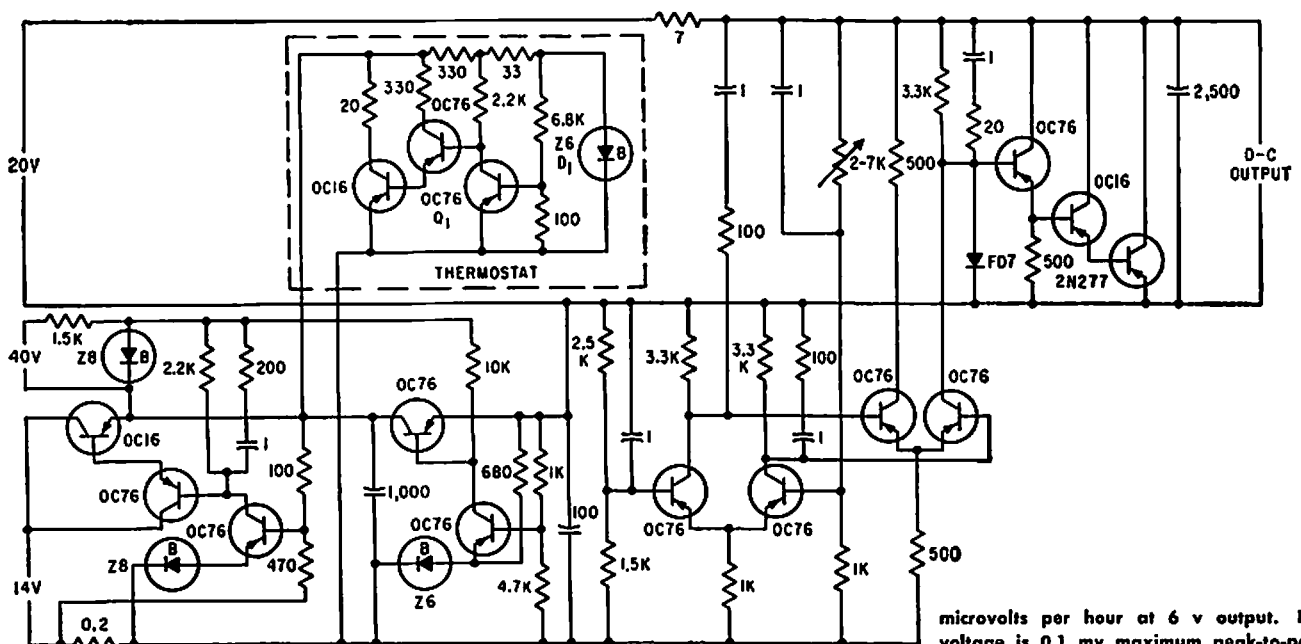
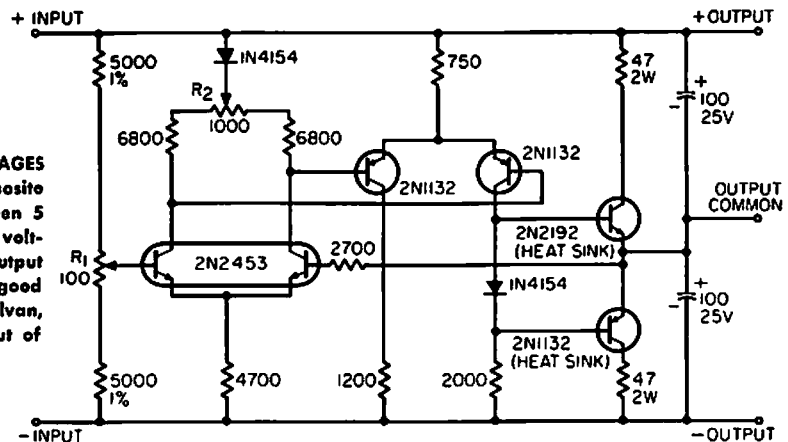
which in turn changes triggering of scr's to keep load voltage essentially constant.—R.

Wechsler, Scr's Regulate A-C Line Voltage, *Electronics*, 38:3, p 61-62.



PREFERRED —150-V D-C REGULATOR—Provides 1% output voltage regulation under normal line and load variations of military equipment. Minimum value of C3 is 2 mfd. Value of R12 depends on reference voltage E_r , which should be minimum of -300 v d-c. Two reference tubes, V3 and V4, are used in series so negative bias for V2 can be obtained with grounded reference supply. Minimum input voltage is 200 v d-c.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 2, p 2-2.

EQUAL POSITIVE AND NEGATIVE VOLTAGES—Single supply provides equal and opposite output voltages at desired value between 5 and 25 v, at up to 100 ma, for input voltages from 10 to 50 v. R1 balances output voltage, while R2 is adjusted to give good tracking of output voltage.—T. P. Sylvan, Regulator Makes Two Power Supplies Out of One, EEE, 14:5, p 117.

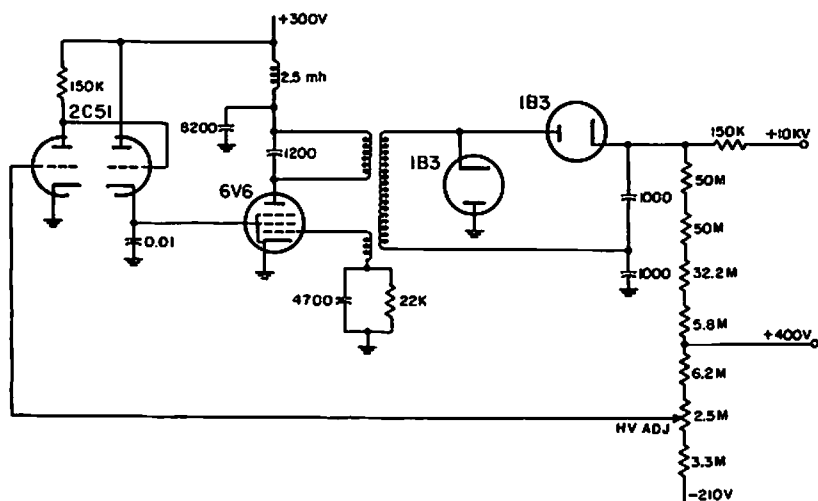
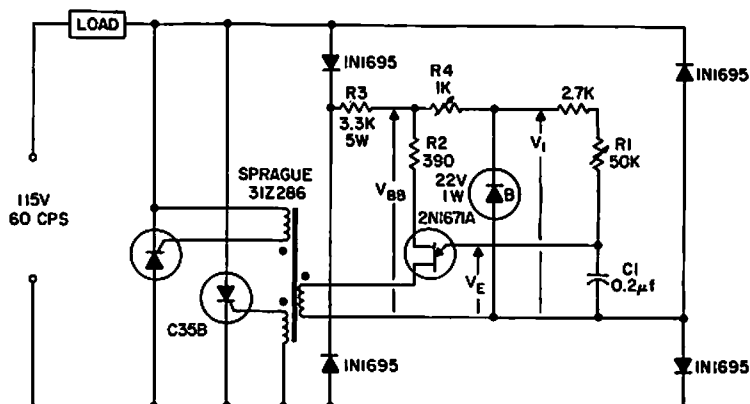


TEMPERATURE-STABILIZED 1 TO 17 V—Shunt-type supply uses pre-stabilizer to control temperature controller and stabilizing circuit that serves as voltage reference. After reaching equilibrium temperature in 5 hours, overall drift over 15-hour period is only 36

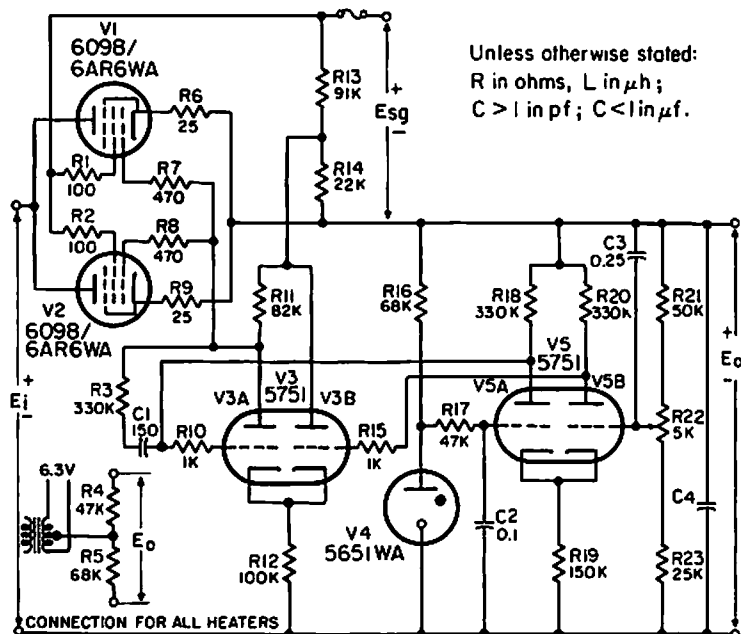
microvolts per hour at 6 v output. Ripple voltage is 0.1 mv maximum peak-to-peak.—E. Baldinger and W. Czaja, Designing Highly Stable Transistor Power Supplies, Electronics, 32:39, p 70-73.

microvolts per hour at 6 v output. Ripple voltage is 0.1 mv maximum peak-to-peak.—E. Baldinger and W. Czaja, Designing Highly Stable Transistor Power Supplies, Electronics, 32:39, p 70-73.

UJT-SCR REGULATED A-C SUPPLY—Component values shown give optimum regulation at 25 v rms output, with less than 0.1 v variation for change in line voltage from 115 v to 100 v. For wider range of output voltage than 10 to 30 v, R1 and R4 can be ganged pot.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 334.



10-KV R-F OSCILLATOR-TYPE CRT SUPPLY—Associated regulator controls oscillator output. Considered less desirable than a-f oscillator supplies, which have no r-f radiation problem.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-3.

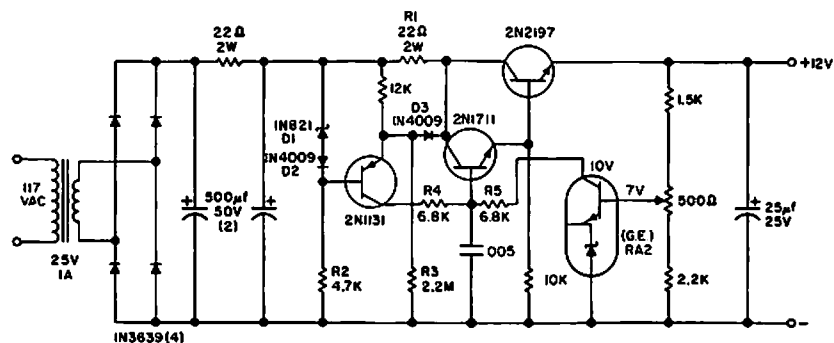


Unless otherwise stated:
R in ohms, L in μ h;
C > 1 in pf; C < 1 in μ f.

PREFERRED 0.1% REGULATION 250-V D-C—
Provides either polarity, for applications requiring superior regulation and stability. Min-

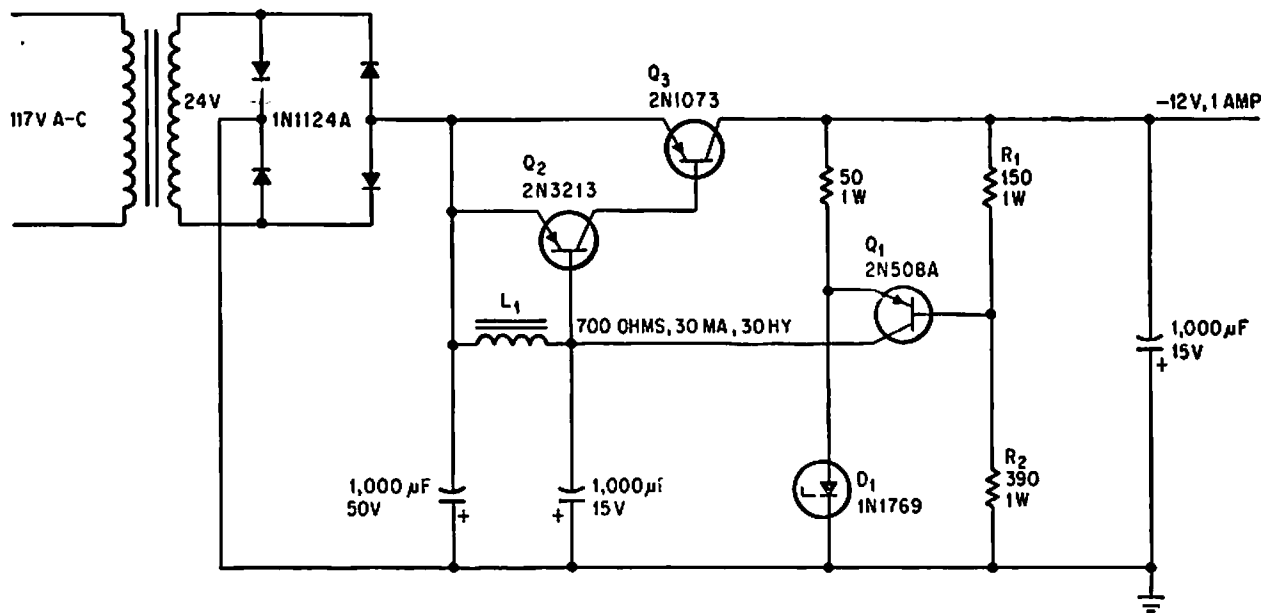
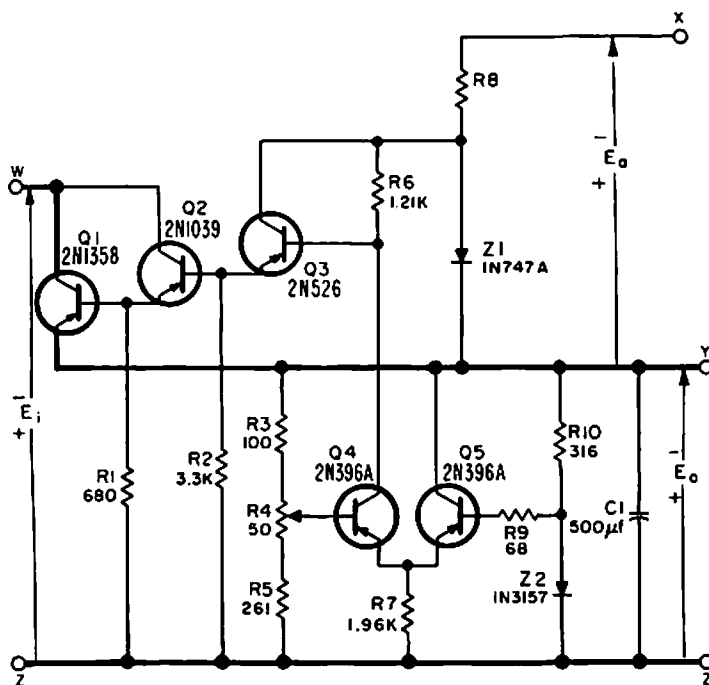
imum input is 290 v d-c, and minimum Esg is 150 v d-c. Maximum output is 100 ma per series tube. C4 is minimum of 4 mfd.—NBS.

**"Handbook Preferred Circuits Navy Aero-
nautical Electronic Equipment," Vol. 1, Elec-
tron Tube Circuits. 1963. PC 8, p 8-2.**



PRECISION 12-V 200-MA SUPPLY—Regulation is less than 0.001% for 10% change in line voltage. Sharp current limiting at 300 ma is provided by R1 and D3. Darlington connection for series regulator gives current gain of 10,000 at 100 ma, so normal variation of reference amplifier collector current is only 10 microamp over full range of output current.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 232.

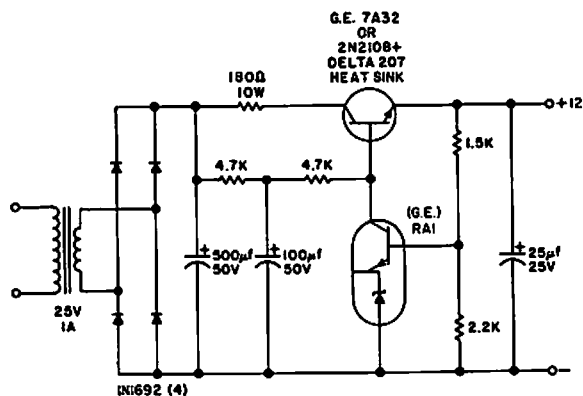
12-V D-C REGULATOR—Provides up to 3 amp at 12 v with 1% regulation for inputs of 13 to 50 v from unregulated source. Auxiliary source E_a must be minimum of 5 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 2, p 2-4.



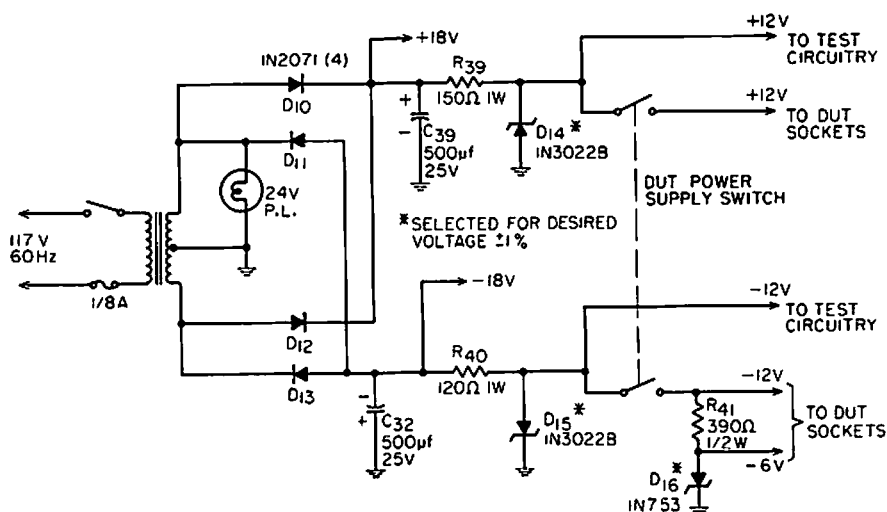
FEEDBACK CHOKE CUTS RIPPLE—Choke L1, placed in feedback path from Q1 to Q2, holds down ripple in current supplied to

load through Darlington amplifier Q2-Q3. Choke acts as if it were in series with load even though carrying only a fraction of

load current.—J. T. Quate, Feedback Choke Reduces Power Supply Ripple, *Electronics*, 39:13, p 74.



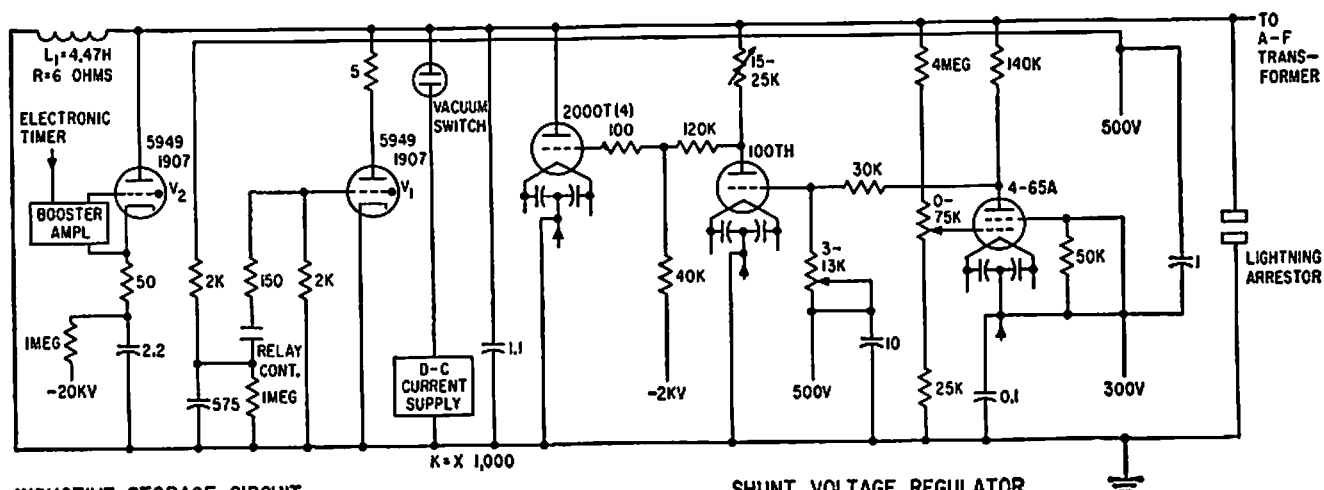
REFERENCE-AMPLIFIER 12-V REGULATED SUPPLY—Uses integrated device consisting of zener diode and npn transistor in single pallet, to serve dual function of voltage reference element and error voltage amplifier. Provides up to 100 ma. 180-ohm series resistor provides short-circuit protection by limiting output current to less than 200 ma. Output regulation is better than 0.3% for line voltage variations of 10%.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 231.



SIMPLE 5-VOLTAGE SUPPLY—Provides -6 v and both positive and negative 12 and 18 v outputs, each regulated by zeners, for linear

integrated-circuit tester and for integrated circuits under test. Transformer has center-tapped 24-v secondary. Lamp across half of

secondary operates at 12 v to extend life.—J. N. Giles, How to Measure Linear-IC Performance, EEE, 14:8, p 62-68 and 161.



INDUCTIVE STORAGE CIRCUIT

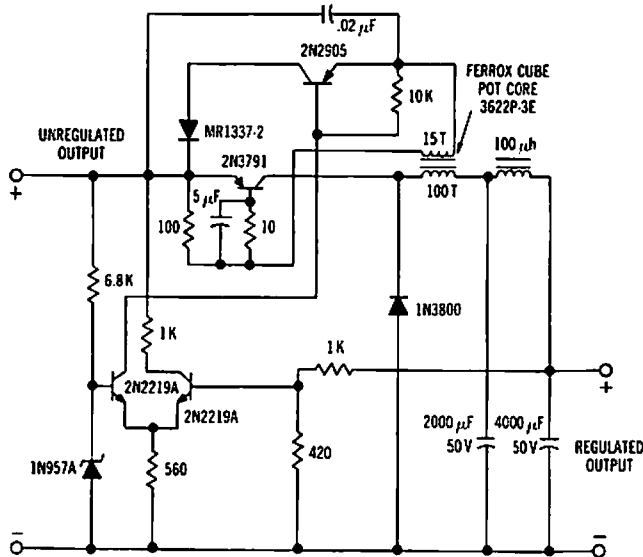
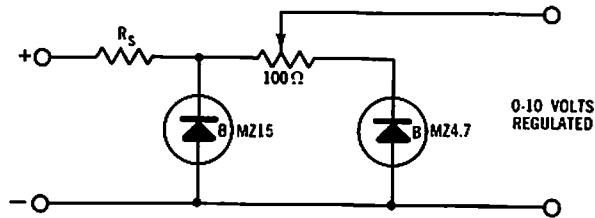
20,000-V INDUCTIVE-STORAGE SUPPLY—Consists of shunt-regulated electronically switched inductive energy storage system in which coil L1 is charged through vacuum switch. When

high voltage is needed, V2 is fired to de-ionize V1. Cathode capacitor of V2 is then charged to 20,000 v by coil current, at which time electronic feedback regulator in shunt

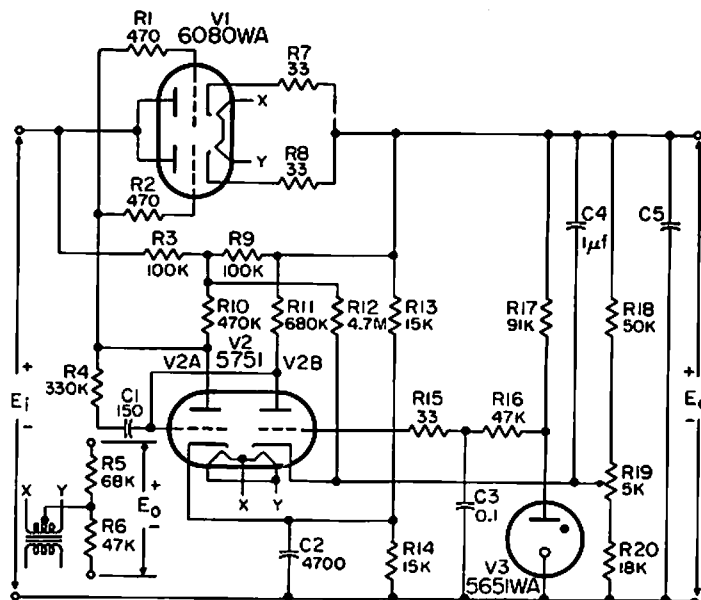
SHUNT VOLTAGE REGULATOR

with L1 draws current to maintain constant output voltage.—R. L. Gamblin, Ohmic Heating Circuits for Plasma Physics, Electronics, 32:41, p 57-59.

0-10 V TWO-ZENER—Simple arrangement provides source of well-regulated adjustable voltage. First zener diode tends to act as pre-regulator, improving dynamic regulation. —"Zener Diode Handbook," International Rectifier Corp., 1960, p 54.



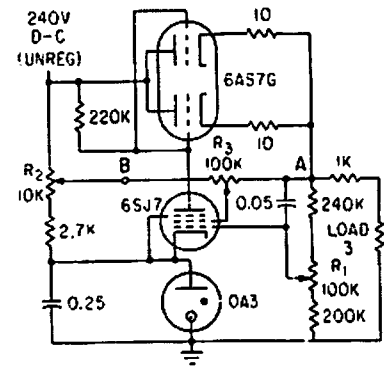
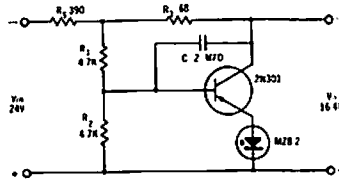
BLOCKING-OSCILLATOR SWITCHING VOLTAGE REGULATOR—Efficiency is improved greatly by having current of 2N3791 transistor flow through load. Differential-amplifier voltage-sensing arrangement controls action of oscillator to maintain constant output voltage. Will regulate 24-v output to within 1% over load range of 100 ma to 2 amp. Oscillator frequency is 6 kc.—H. Weber, "Two Unique Switching Voltage Regulators Using Blocking Oscillators," Motorola Application Note AN-163, Aug. 1965.



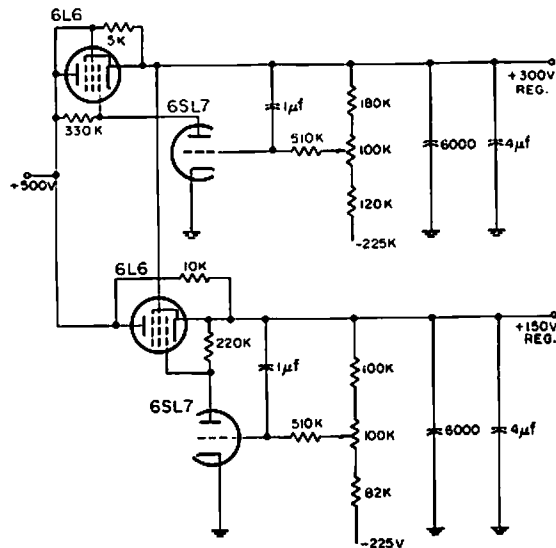
PREFERRED 300-V D-C REGULATOR—Provides either polarity of output with 1% regulation, from minimum of 350 v d-c input. Maximum output current is 125 ma for single series tube section and 100 ma per triode

section when two or more are paralleled. Minimum value of C5 is 4 mfd.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 3, p 3-2.

SHUNT REGULATOR—Used when output voltage must be higher than zener voltage. Ripple is less than 10 mv when regulator is supplied by full-wave rectifier having 20 mfd capacitance.—“Zener Diode Handbook,” International Rectifier Corp., 1960, p 55.

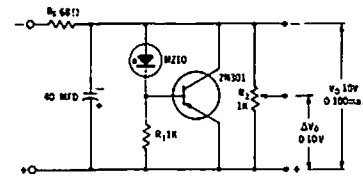


THERMOCOUPLE VACUUM-GAGE HEATER—Simple regulator for 240-v d-c supply provides 140 ma with 0.1% regulation. Uses regulator triode, pentode-connected d-c amplifier, and series-connected reference regulator tube.—W. V. Loebenstein, *Regulated Power Supply for Instruments, Electronics*, 33:48, p 132.

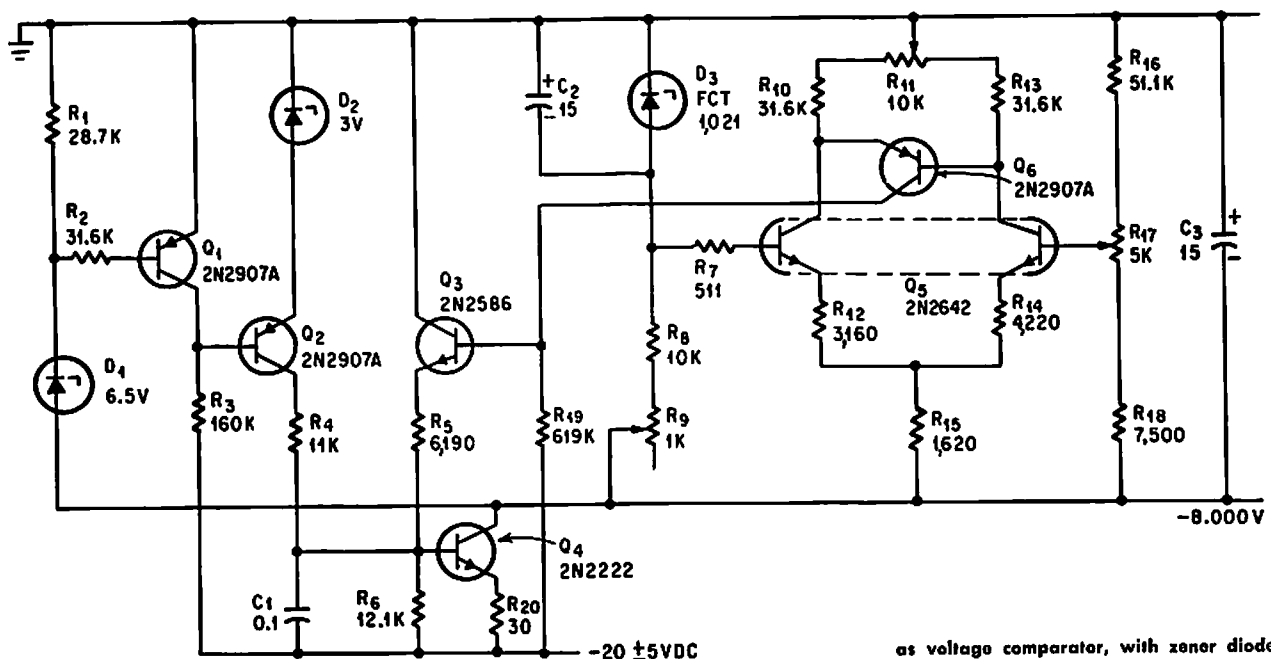


150 AND 300 V SERIES-TUBE REGULATOR—Uses simple triode as regulator amplifier. Series tube for 300-v supply is conventional triode-connected pentode, but series tube that regulates 150 v has its screen fed from

output of 300-v regulated supply, for pentode operation.—NBS, “Handbook Preferred Circuits Navy Aeronautical Electronic Equipment,” Vol. 1, *Electron Tube Circuits*, 1963, p N2-11.



SHUNT REGULATOR—Used when output can be less than zener voltage.—“Zener Diode Handbook,” International Rectifier Corp., 1960, p 55.

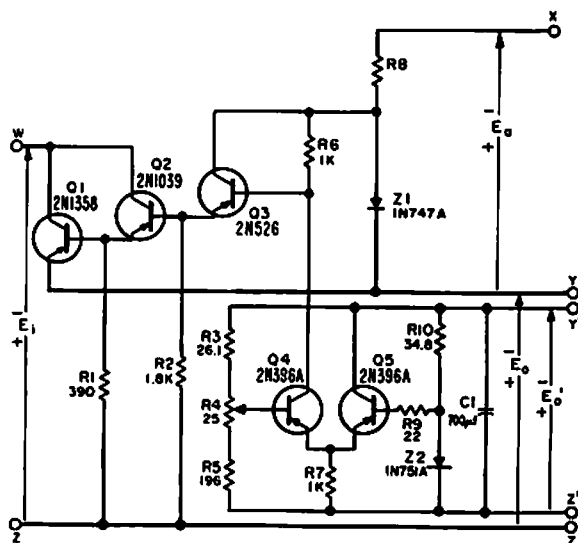
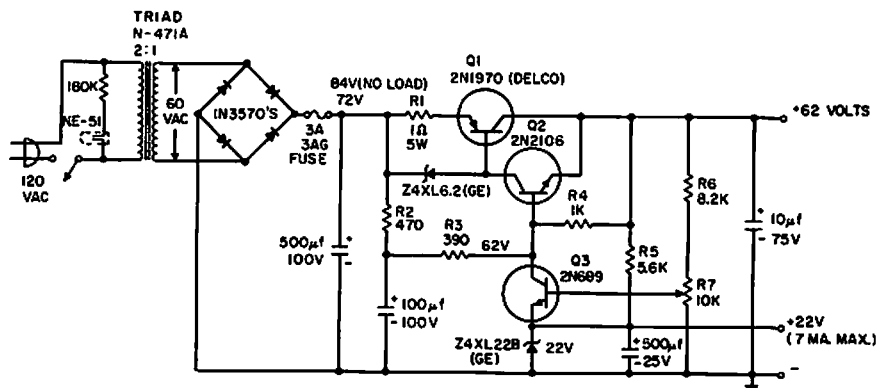


8-V VOLTAGE REGULATOR—Output voltage is held within 0.1% despite 5-v variations in

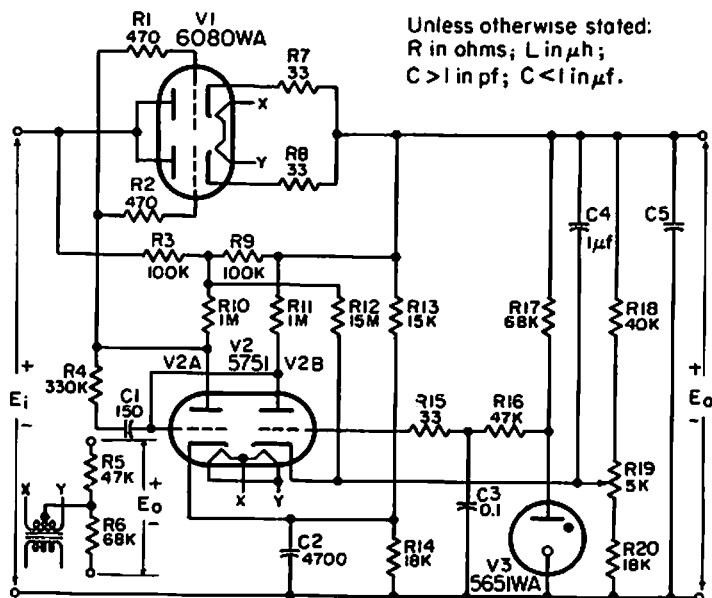
20-v input to regulator that itself consumes only 1.5 ma. Differential amplifier Q5 acts

as voltage comparator, with zener diode D3 as reference.—A. Dargis, *A High Performance Voltage Regulator, Electronics*, 37:13, p 75.

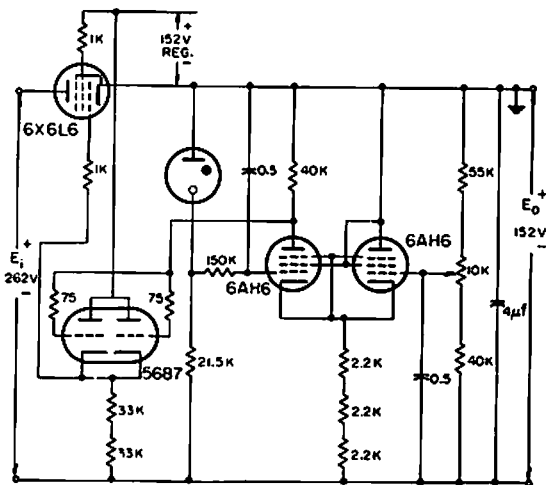
DARLINGTON-CONNECTED SERIES REGULATOR—Designed for output currents up to 2 amp average or 3.5 amp peak. Output voltage can be adjusted from 45 to 65 v by R7. Ripple is less than 1 mv rms at no load, increasing to 60 mv peak-to-peak at 2 amp. Regulation is 2.1% at 2 amp and 0.72% at 1 amp.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 228.



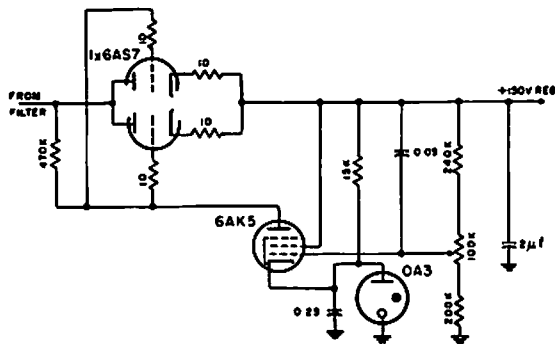
REMOTE-SENSING 6-V REGULATOR—Used when small lead resistance between regulator and load is physically impossible. Voltage E_o' is essentially voltage that appears directly across load. Differential amplifier senses and corrects for changes in this voltage rather than for changes in E_o at regulator output terminals.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 1, p 1-11.



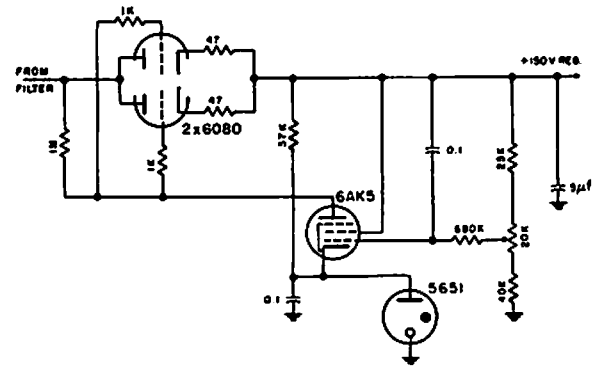
PREFERRED 250-V D-C REGULATOR—Provides either polarity of output with 1% regulation, from minimum of 300 v d-c. C5 is minimum of 4 mfd.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 7, p 7-2.



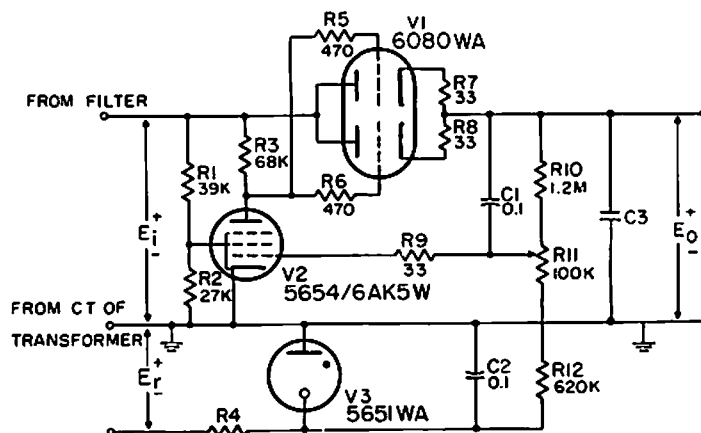
152-V PENTODE SERIES-TUBE REGULATOR—Has excellent frequency response, but this performance could also be obtained if cathode follower were amplifier using negative feedback for frequency compensation, along with better regulation and lower d-c resistance.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-11.



POSITIVE-REFERENCE REGULATOR—Has self-contained 150-v positive reference potential for pentode regulator, but gives only marginal operation.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-4.

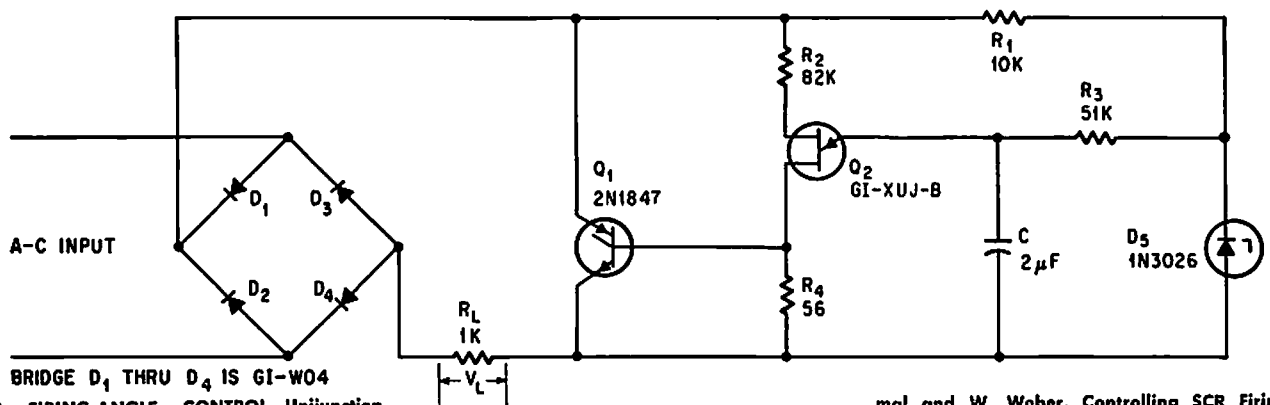


POSITIVE-REFERENCE REGULATOR—Has self-contained 150-v positive reference potential for pentode regulator, but gives only marginal operation.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-4.



PREFERRED +150-V D-C REGULATOR—Provides 1% output voltage regulation under normal line and load variations of military equipment. Minimum value of C3 is 2 mfd. Value of R4 depends on reference voltage

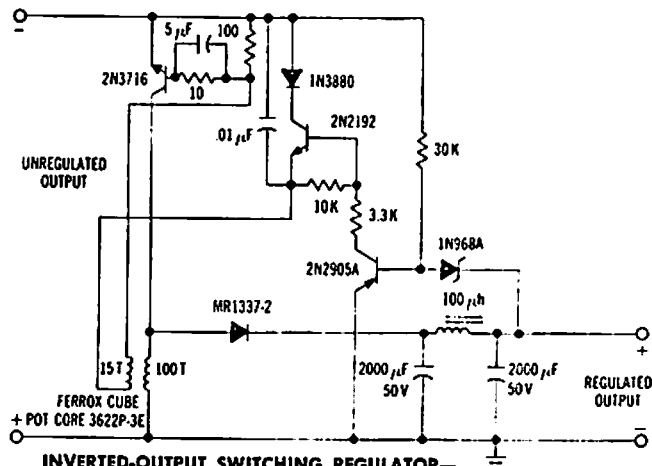
E_r , which should be minimum of -150 v d-c. Minimum input voltage is 200 v d-c.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 1, p 1-2.



BRIDGE D₁ THRU D₄ IS GI-W04
SCR FIRING-ANGLE CONTROL—Unijunction transistor trigger circuit fires scr at specific time in each half-cycle of a-c input to keep

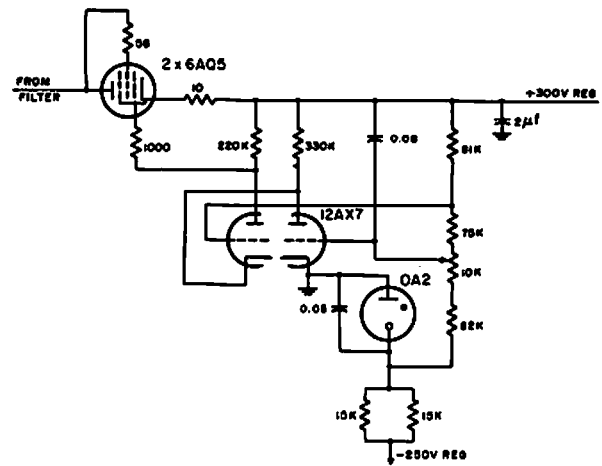
average load voltage constant over line voltage range of 100 to 200 v a-c.—R. S. Krech-

mal and W. Weber, Controlling SCR Firing Angle Regulates D-C Load Voltage, *Electronics*, 38:16, p 80-81.

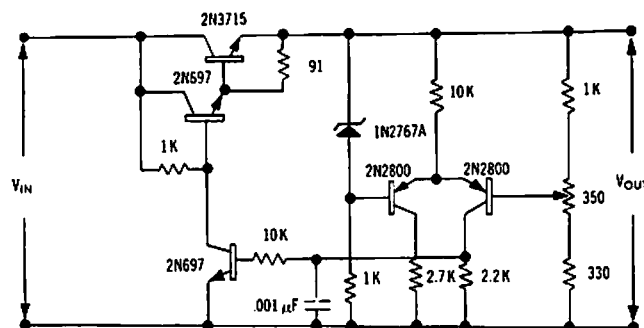


INVERTED-OUTPUT SWITCHING REGULATOR—

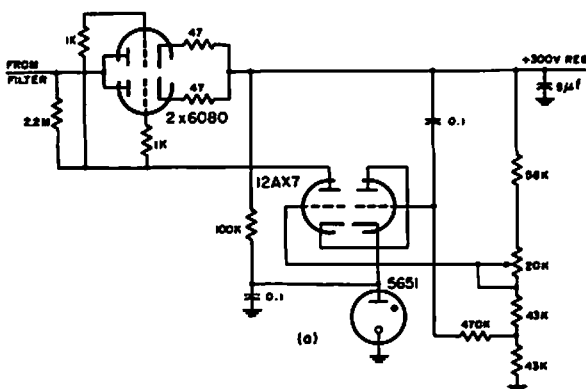
Simple 6-kc blocking oscillator circuit serves both for sensing and duty cycle control. Arrangement is more efficient than conventional series-pass regulators. Inverted polarity is added feature. Will regulate 20-v output within 1% overload range of 50 ma to 1 amp. —H. Weber, "Two Unique Switching Voltage Regulators Using Blocking Oscillators," Motorola Application Note AN-163, Aug. 1965.



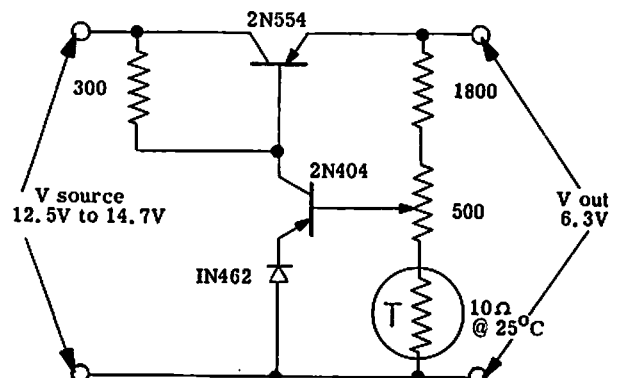
MODIFIED TWIN-TRIODE CASCODE—Plate resistor for lower-potential triode parallels top triode, which is plate load for true cascode. This increases gain of circuit by increasing average plate current and thereby transconductance of bottom triode.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-2.



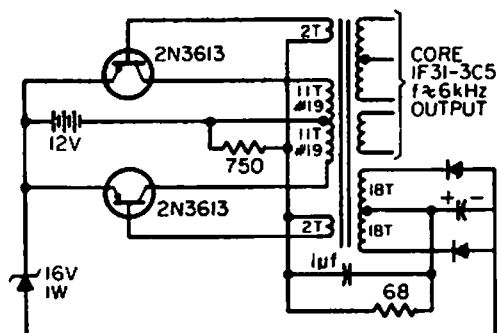
BASIC SERIES-PASS REGULATOR—Output voltage is regulated by 2N3715 series-pass silicon power transistor having rise and fall times below 0.5 microsec at 5 amp. Transistor has wide safe-area range, but circuit otherwise has no overload protection.—J. Takesuye and H. Weber, "Silicon Power Transistors Provide New Solutions to Voltage Control Problems," Motorola Application Note AN-163, Aug. 1965.



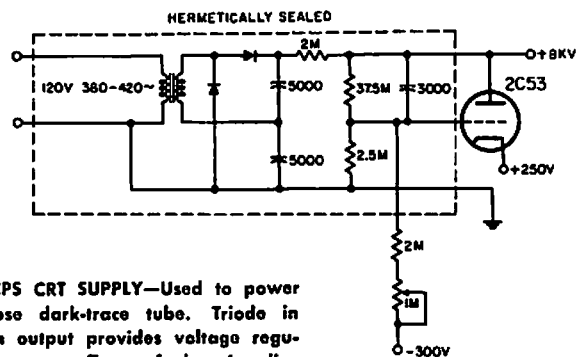
TRUE TWIN-TRIODE CASCODE—Use of 5-mfd capacitor across regulated output reduces adverse effect of 2.2-meg plate load resistor on frequency response. Cascode circuit is used when required gain is too high for single triode, because it avoids need for second d-c supply that would be required for screen if pentode were used.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-2.



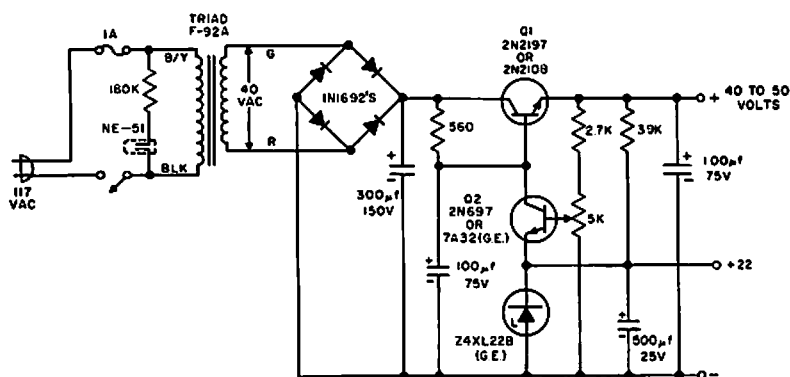
LOW-COST VOLTAGE REGULATOR—Costs 5 to 7 times less than zener regulator having same power rating. Can be set at precise voltage value required, whereas zener has 5 or 10% tolerance. Uses 1N462 silicon diode for reference. Total cost (in quantity) is less than two dollars. Thermistor makes circuit perform from -55 to 71°C. Input voltage source is sea-water-activated battery.—M. E. Gavin, Low Cost Transistor Voltage Regulator, EEE, 10:8, p 28-29.



BATTERY VOLTAGE REGULATOR—Used in battery-powered instruments to compensate for wide range of battery voltages. Converter serves to provide required variety of operating voltages and isolate equipment from supply. Will hold output within 0.5 v of 16 v for input range of 11.5 to 19 v.—C. D. Lindsay, Combined Battery Converter-Regulator Power Source, *EEE*, 14:3, p 61.

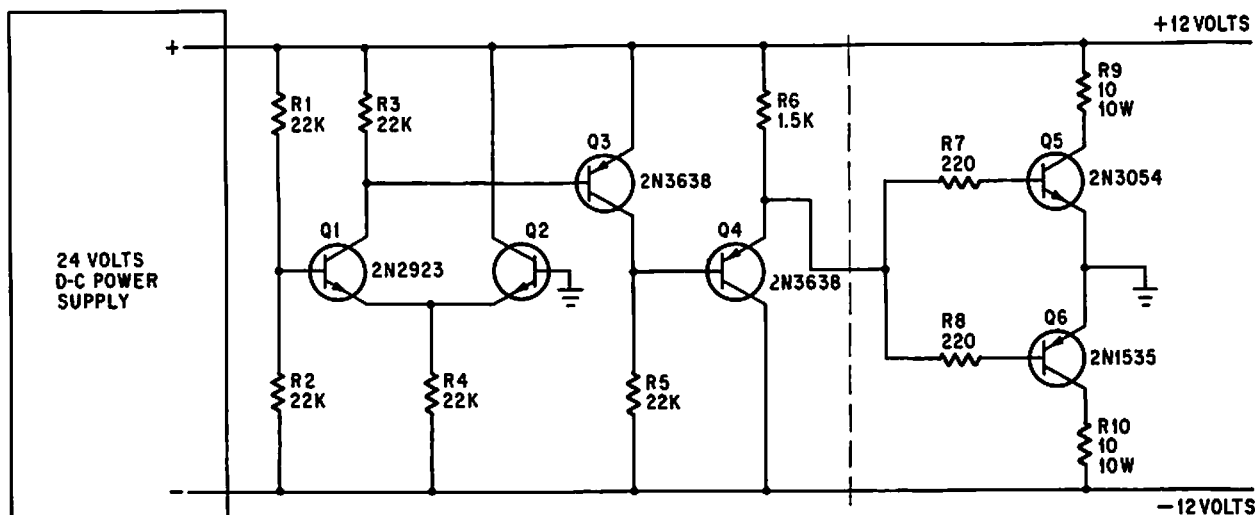


14-KV 400-CPS CRT SUPPLY—Used to power special-purpose dark-trace tube. Triode in parallel with output provides voltage regulation to overcome effects of changing line voltage, improve output ripple, and improve output impedance characteristics.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N14-1.



BASIC SERIES REGULATOR—Provides voltage regulation within 2% at 400 ma, with peak-to-peak output ripple below 0.3 v. Output

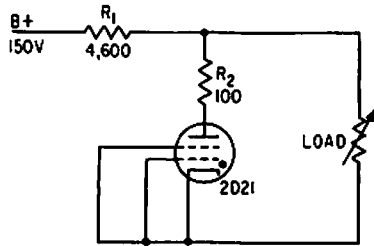
impedance is less than 2 ohms from d-c to 20 cps.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 227.



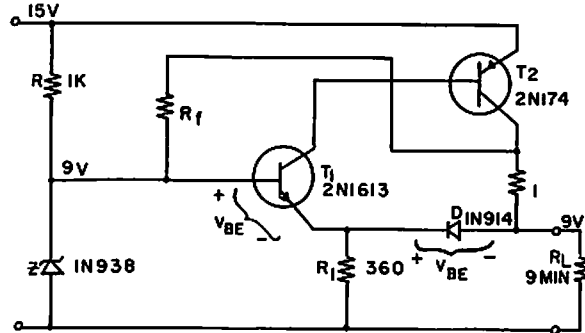
VOLTAGE SPLITTER-REGULATOR—Provides regulated +12 and -12 v from 24-v supply.

Negative feedback loop permits circuit to furnish unbalanced currents up to 700 ma in either direction without changing output

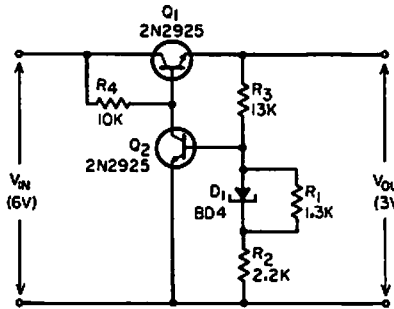
voltages more than 10 mv.—J. M. Kasson, Voltage Splitter Balances Floating Power Supply, *Electronics*, 39:6, p 96.



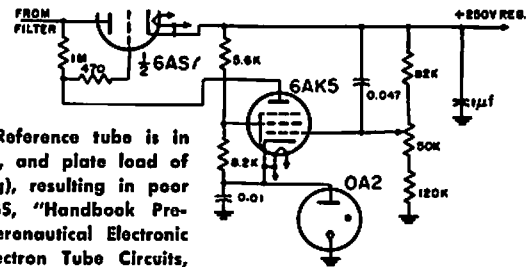
THYRATRON REGULATOR—Output of 12 to 16 v is regulated within 1% for loads of 6 to 22 ma. Since 2021 can handle 100 ma continuously, circuit is easily modified to regulate higher current values.—W. D. Fryer, *Thyratron Regulates Supply*, *Electronics*, 31: 25, p 88.



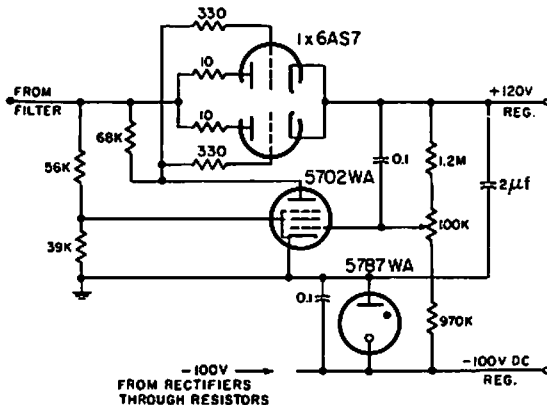
ZERO-IMPEDANCE VOLTAGE REGULATOR—Uses two transistors and controlled positive feedback along with temperature compensation to reduce output resistance to zero while holding output voltage constant. Also gives some current overload protection. Values shown provide 1 amp at 9 v.—G. Duggan, *Zero Impedance Voltage Regulator*, *EEE*, 11:5, p 91-92.



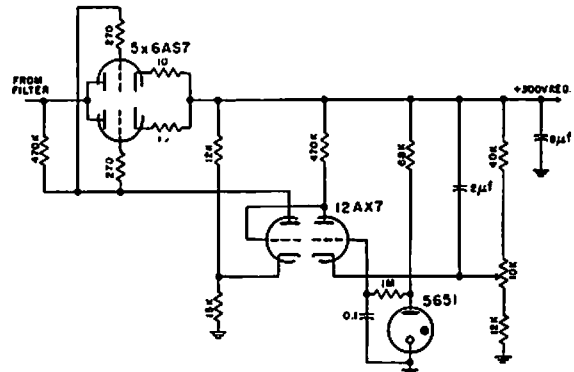
SERIES REGULATION at 3 V—Combination of backward diode and resistor network serves as reference for regulated outputs below 6 v, for which temperature-compensated zener diodes are not available. Provides input regulation of 100:1 over 10% change in input voltage, with output impedance of 0.04 ohm.—T. P. Sylvan, *Backward-Diode Power-Supply Reference Elements*, *EEE*, 13:11, p 46-48.



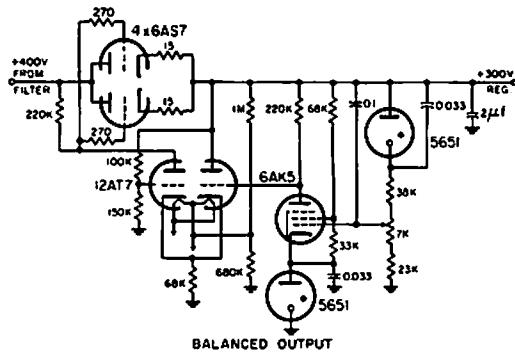
PENTODE REGULATOR—Reference tube is in cathode circuit of 6AK5, and plate load of pentode is high (1 meg), resulting in poor frequency response.—NBS, *Handbook Preferred Circuits Navy Aeronautical Electronic Equipment*, Vol. 1, *Electron Tube Circuits*, 1963, p N2-2.



150-V REGULATOR WITH EXTERNAL REFERENCE—Permits wider supply voltage range and better operation than arrangements using self-contained reference.—NBS, *Handbook Preferred Circuits Navy Aeronautical Electronic Equipment*, Vol. 1, *Electron Tube Circuits*, 1963, p N2-4.

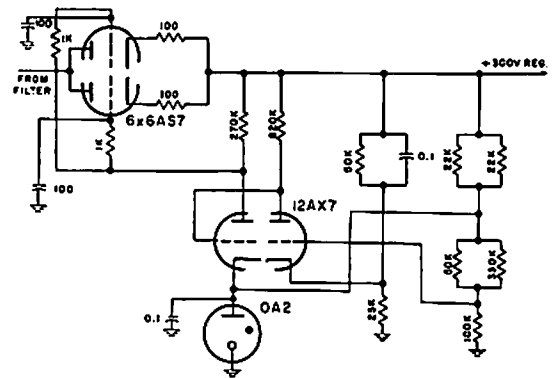


TWIN-TRIODE CASCADE—Has self-contained reference voltage, and does not load reference tube. Is theoretically capable of highest possible gain obtainable with single-envelope d-c amplifiers.—NBS, *Handbook Preferred Circuits Navy Aeronautical Electronic Equipment*, Vol. 1, *Electron Tube Circuits*, 1963, p N2-3.

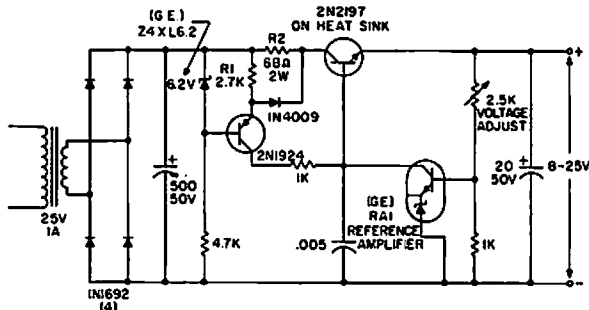


BALANCED-OUTPUT PENTODE TWIN-TRIODE

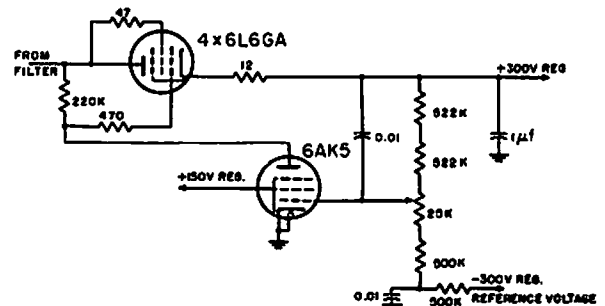
—Arrangement gives high gain, approaching 10,000, along with more ripple reduction than is generally required.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-4.



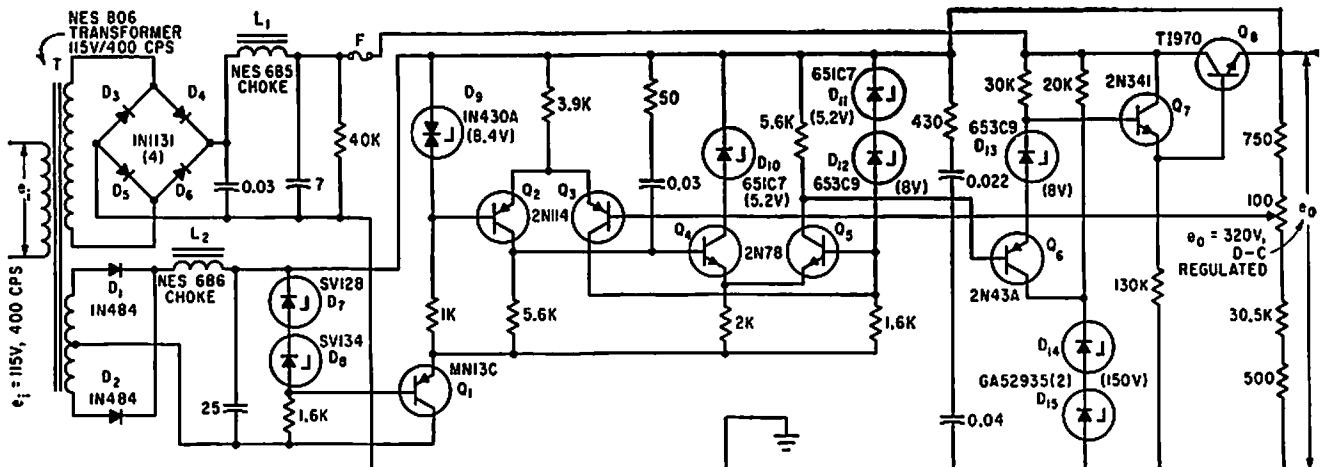
TWIN-TRIODE CASCADE—Smaller load resistor improves frequency response. Both reference tube and comparison divider are loaded.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-3.



CURRENT-LIMITING SUPPLY WITH REFERENCE AMPLIFIER—Variable 8 to 25-v supply limits output to 100 ma for protection against short-circuits. Regulation is 0.02% for line voltages from 105 to 130 v.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 232.



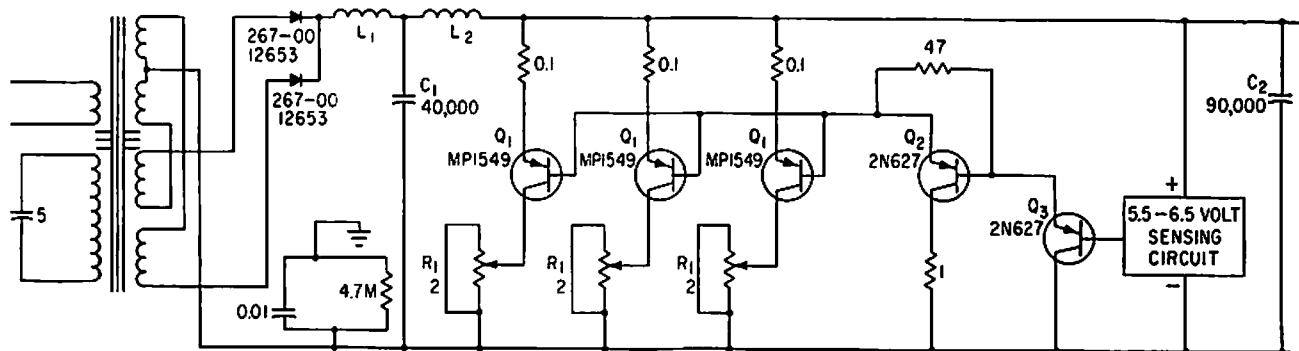
PENTODE WITH CONSTANT REFERENCE CURRENT—Screen is fed directly from regulated voltage of shunt-regulating supply using VR-105 and VR-150 reference tubes to give —300 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-2.



320 V AT 60 MA—Silicon diodes in full-wave bridge feed seven-transistor regulator. Temperature-compensated silicon-junction zener

diode is basic reference element. Output is constant within 50 mv, for use with digital-analog converter.—N. Aron, Precise Con-

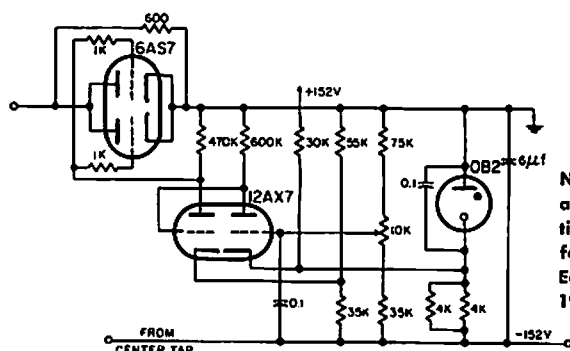
verter takes Current Analog of Digital Voltage Pulses, *Electronics*, 35:32, p 68-71.



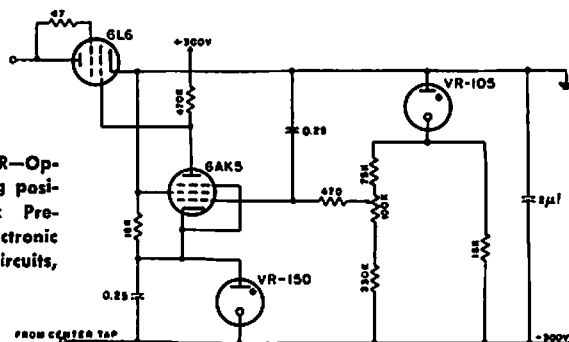
6 V at 20 A—Constant-voltage ferroresonant transformer with full-wave silicon rectifiers is supplemented by shunt transistors

driven by error signal from zener-reference sensing circuit.—J. T. Keefe, Transformer and Shunt Transistors Regulate D-C Power Supply,

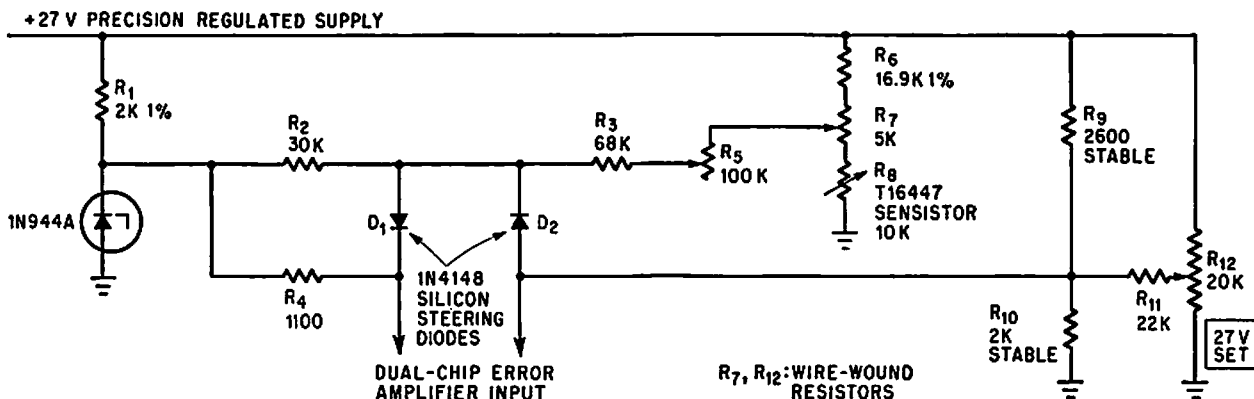
Electronics, 34:20, p 99–101.



NEGATIVE-OUTPUT 152-V REGULATOR—Operation is comparable to corresponding positive-output circuit.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-5.



NEGATIVE-OUTPUT 300-V REGULATOR—Operation is comparable to corresponding positive-output circuit.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N2-5.



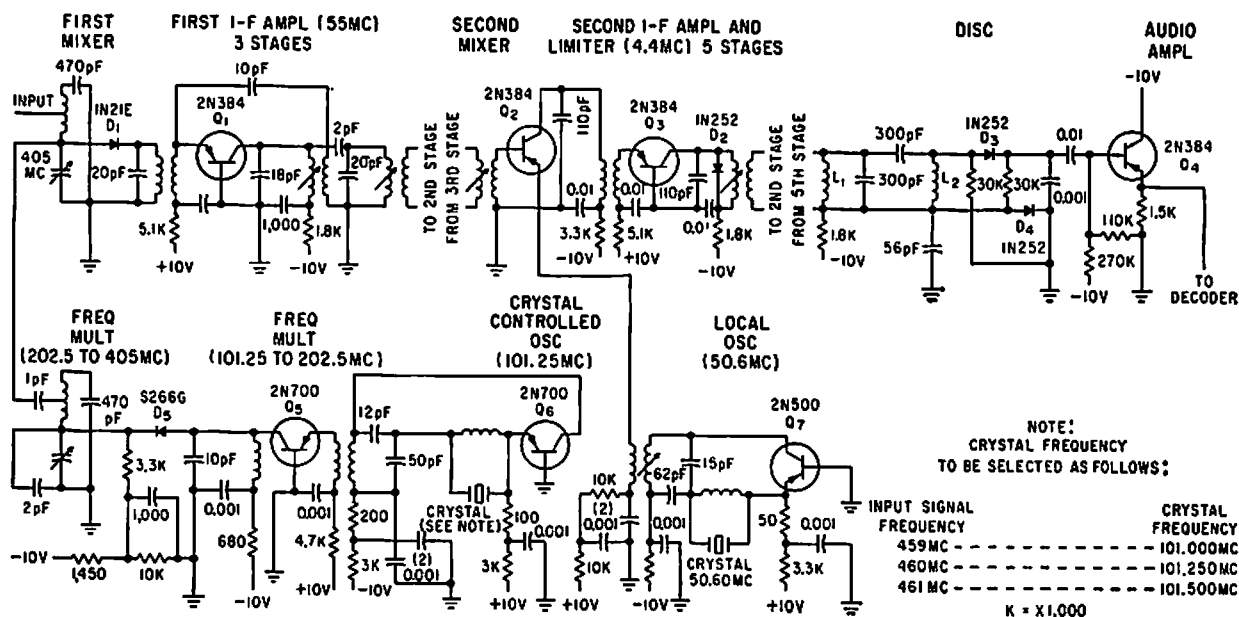
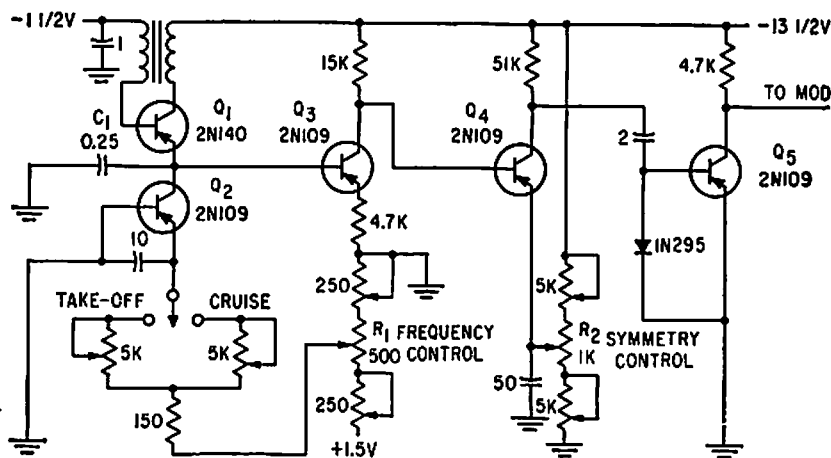
TEMPERATURE-COMPENSATED ZENER—Reference voltage is compared to 27-v output by dual-chip transistor serving as error ampli-

fier. Unique end-compensation circuit using sensistor generates voltage that rises with temperature.—C. H. Moulton, Light Pulse Sys-

tem Shrinks High-Voltage Protection Device, *Electronics*, 38:11, p 71–75.

Remote Control Circuits

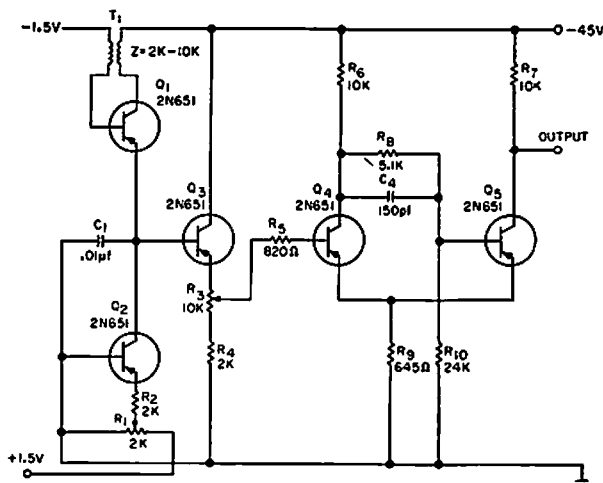
TRANSMITTER CONTROL FOR DRONE—Pulse repetition rate and pulse symmetry control serves that drive rudder and elevator. Pulses modulate transmitter carrier, which is picked up and detected by superregenerative receiver in target drone.—G. B. Horzog, *Transistors Simplify Control of Target Drone, Electronics*, 32:18, p 52-54.



460-MC F-M COMMAND RECEIVER—Transistorized double-conversion f-m superhet, tunable by crystal substitution in 457-462-Mc band, has 6-microvolt sensitivity for 20 db of noise quieting. Camera start and timing

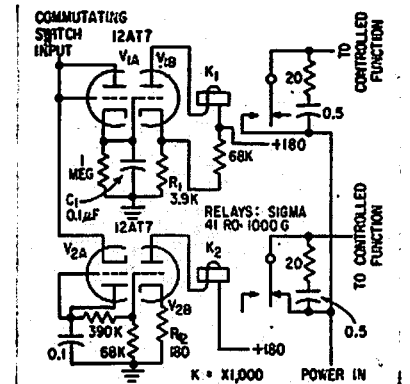
pulses are amplitude-modulated onto 3.5 and 12-kc carriers. After signal is detected, subcarriers are separated and pulses are re-constituted by decoder. Start pulses operate camera relays, and timing pulses flash neon

Lamps.—F. M. Gardner and L. R. Hawn, Camera Control System for Rocket Sled Tests, *Electronics*, 33:14, p 63-65.

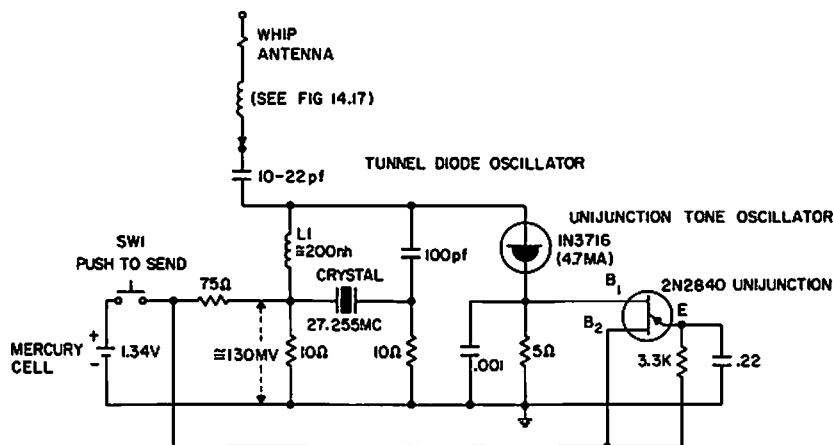


RECTANGULAR WAVEFORM GENERATOR—Provides variable frequency and symmetry without interaction of functions. Supply voltage can be -15 to -45 v. Frequency range

is variable from 60 cps to 7 kc. Can be used to modulate small transmitter for remote control purposes.—L. E. Spadt, Rectangular Waveform Generator, *EEE*, 10:6, p 33-34.

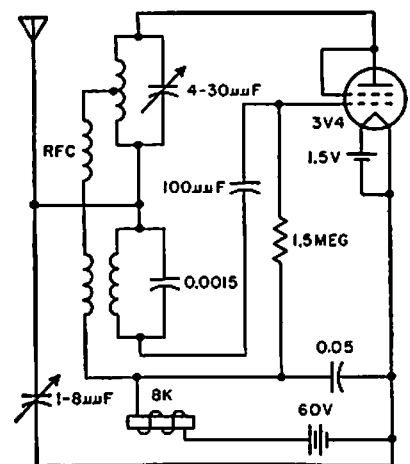


POLARITY-SENSING ON-OFF CONTROL—Remote switching circuits are sensitive to positive and negative inputs, thereby doubling number of control channels available from commutating switches of remote control system for robot that performs jobs in dangerous radioactive areas. All functions requiring independent operation are connected to positive input circuits only.—D. A. Campbell, Multiplex Circuits for Control of a Robot, *Electronics*, 33:4, p 46-48.

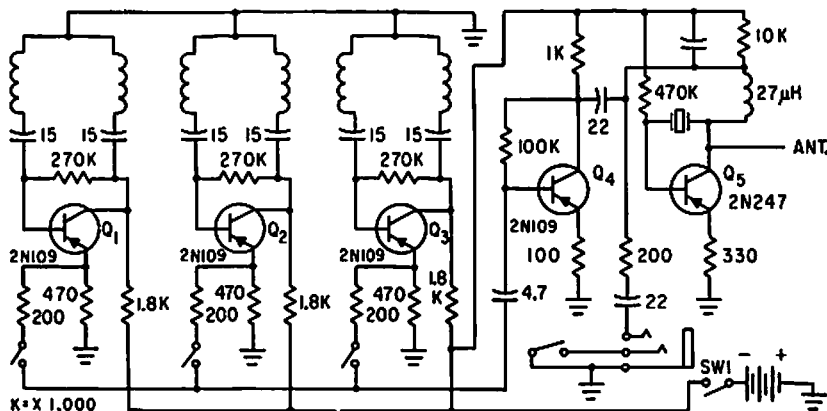


UJT-TD GARAGE-DOOR CONTROL TRANSMITTER—Unijunction tone oscillator modulates 27.255-Mc crystal-controlled tunnel-diode oscillator. Has adequate range for remote control of toys, window displays, garage doors,

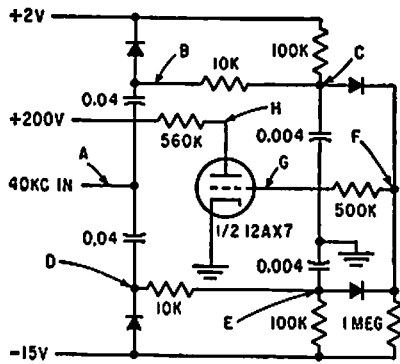
etc. When voice-modulated, can be used for short-range communication, as in shopping centers and bowling alleys.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 355



MILLER SUPERREGENERATIVE RECEIVER—Well-known in model-control field for its reliability. With self-quenching, optimum performance is obtained when receiver is in weak oscillatory state and incoming signal causes oscillation every third quench cycle. Provides large decrease in plate current when signal arrives.—S. J. Neshyba and F. E. Brooks, Jr., Stable Receiving Circuits for Remote Control, *Electronics*, 31:31, p 74-76.

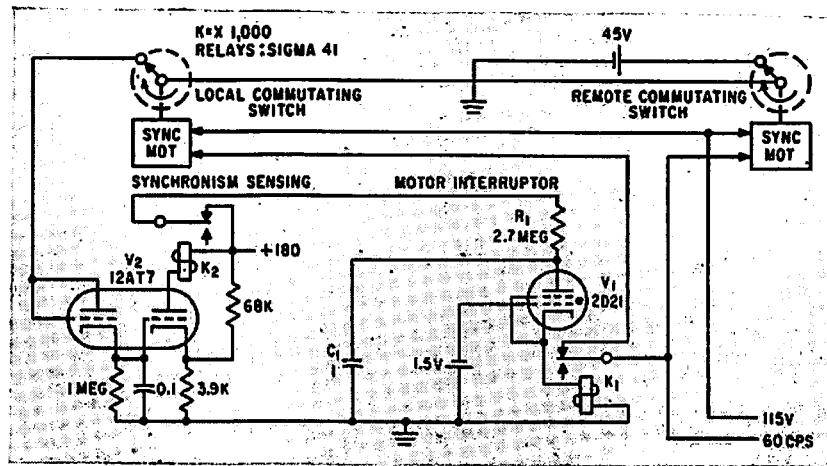


THREE-TONE H-F CONTROLS TRANSMITTER—Tone-modulated ground transmitter can be tone-modulated by three different tones, each corresponding to a particular read of receiving relay and balloon. Consists of three stable audio oscillators (between 200 and 500 cps) and low-power crystal-controlled transmitter in h-f band between 3 and 18 Mc.—R. W. Frykman, Radio Command Set for High-Altitude Balloons, *Electronics*, 33:35, p 54-55.



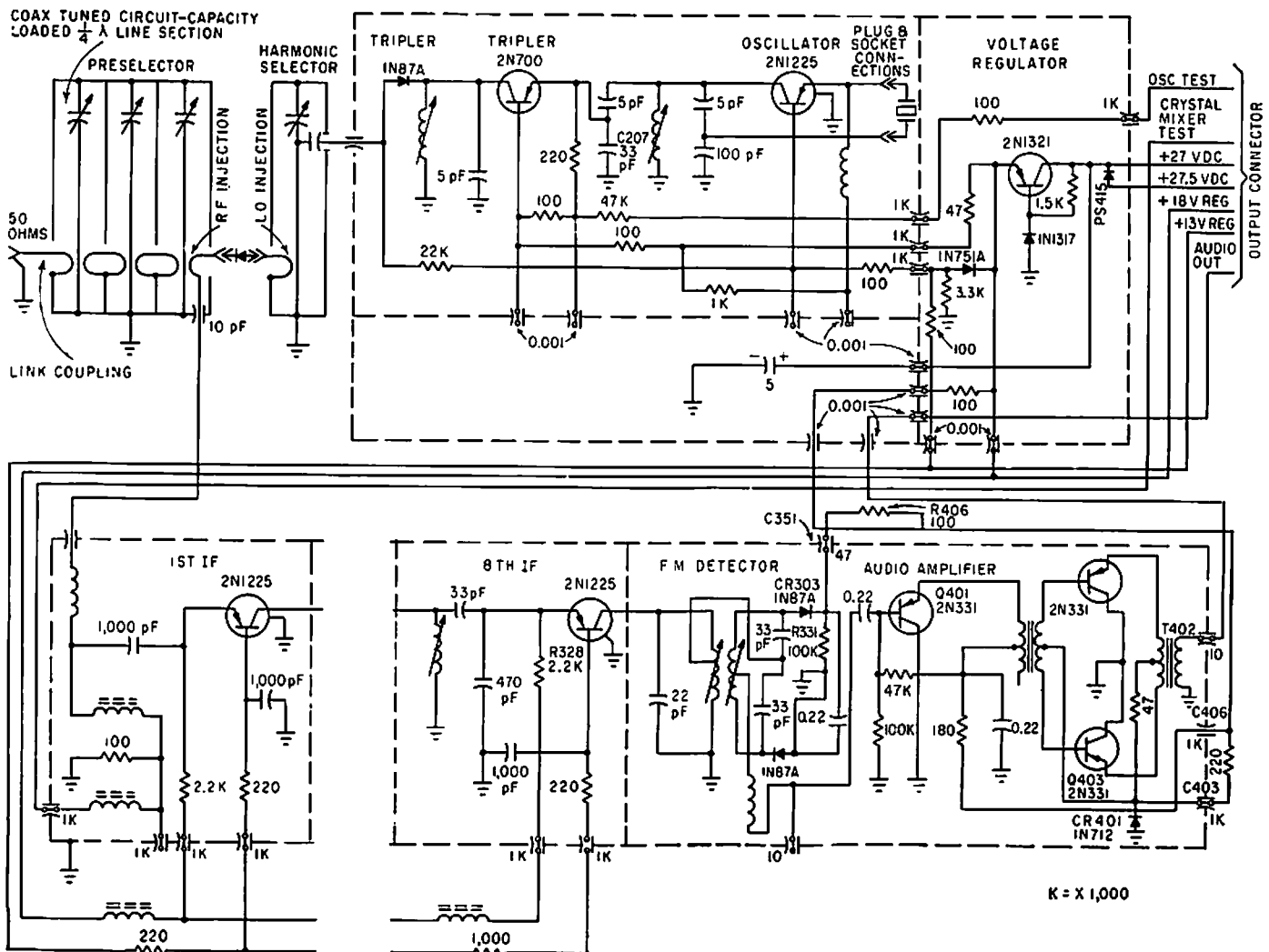
0.1 SEC $\leq \tau \leq$ 0.5 SEC
OUTPUT PULSE WIDTH INDEPENDENT OF
INITIAL AMPLITUDE

TIME-CONSTANT DETECTOR FOR TV CONTROL—Produces output pulse whose width is proportional to time constant of exponentially damped ultrasonic signal, in range of 0.1 to 0.5 sec, independent of input amplitude.—K. R. Cross and R. O. Whitaker, *Time-Constant Detectors Control Tv Sets, Electronics*, 32:36, p 62-67.



COMMUTATOR SYNCHRONIZER—Automatic synchronizing circuit consists of motor interrupter and synchronism-sensing circuit. Commutator drive motor at control transmitter is interrupted until it syncs with commutator drive motor in robot that performs jobs in

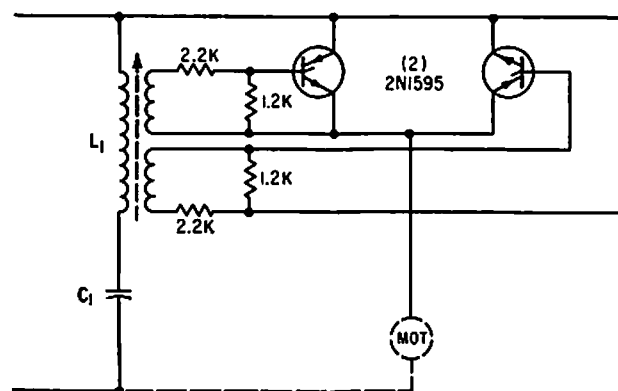
dangerous radioactive areas. Each interrupt makes motor drop back 90°, so that only up to three interruptions are required to achieve synchronism.—D. A. Campbell, *Multiplex Circuits for Control of a Robot, Electronics*, 33:4, p 46-48.



WIDEBAND F-M RADIO CONTROL LINK—Covers 406 to 549 Mc. Used in missiles and missile-target aircraft to receive up to 20 tone

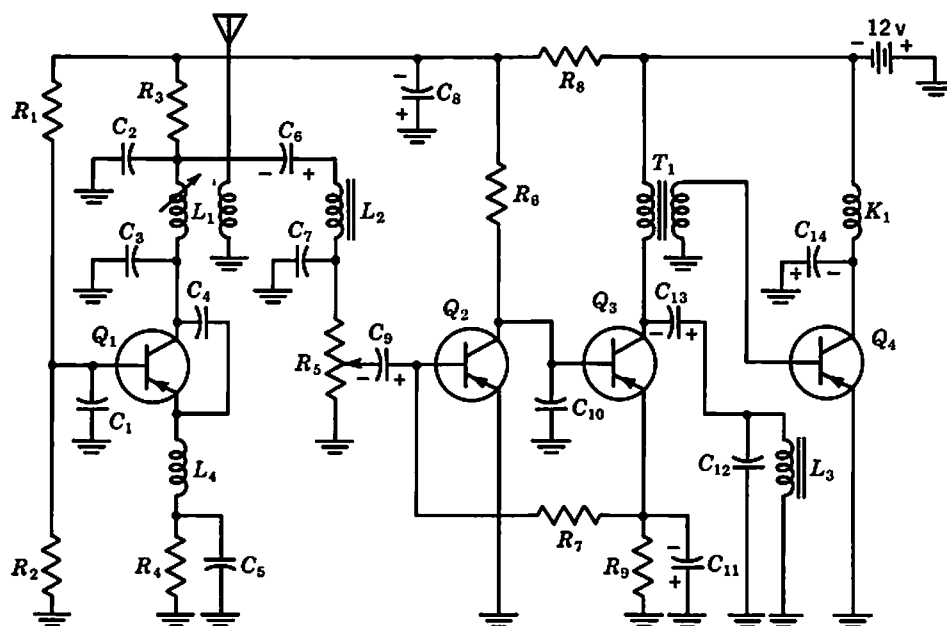
channels and provide demodulated audio output to decoding equipment. Second through seventh i-f channels are essentially same as

eighth.—T. L. Fischer, *Wideband F-M Receiver for Remote Aircraft Control, Electronics*, 33:40, p 85-87.



TOY TRAIN CONTROL—With L_1 - C_1 tuned to one of five r-f channels (100, 140, 180, 220 and 255 kc), pair of scr's drives train either forward or in reverse, depending on polarity of r-f signal pulse applied to rails by control station. Five-channel transmitter permits sim-

ultaneous control of five different trains, each having receiver tuned to different frequencies. Scr characteristics eliminate jackrabbit starts. —S. B. Gray, *Appliances and Housewares, Electronics*, 36:20, p 46-49.



Parts List

Resistors	Kilohms	Watt
R_1	22	$\frac{1}{2}$
R_2	2.2	$\frac{1}{2}$
R_3, R_4	1	$\frac{1}{2}$
R_5	10	Potentiometer
R_6	2.7	$\frac{1}{2}$
R_7	10	$\frac{1}{2}$
R_8	270 ohms	$\frac{1}{2}$
R_9	150 ohms	$\frac{1}{2}$

Transistors	
Q_1	2N2188
Q_2, Q_3, Q_4	2N1274

Miscellaneous

K_1 Typical: Sigma 11F-2300-G/SIL or equivalent

Transformer

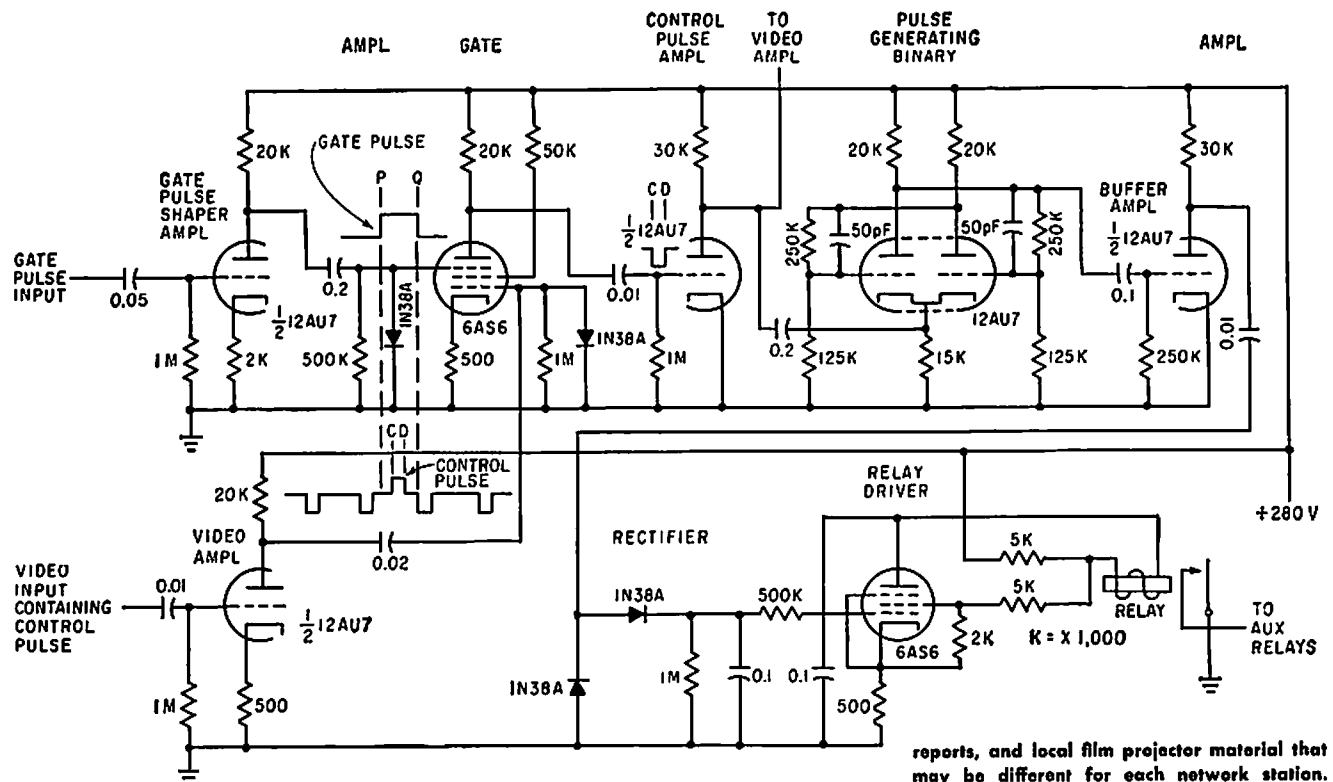
T_1 10-2 kilohms (Thordorson TR7 or equivalent)

Capacitors	
C_1	0.001- μ f disk
C_2, C_7, C_{10}	0.05- μ f disk
C_3	15- μ f disk
C_4	18-pf disk
C_5, C_{12}	0.002- μ f disk
C_6, C_9, C_{13}	5- μ f electrolytic
C_8, C_{14}	100- μ f electrolytic
C_{11}	40- μ f electrolytic

Inductors	
L_1	Adjustable RF coil (J. W. Miller 4403 or equivalent). Add 2 turns of No. 24 enameled wire on cold end.
L_2	30-mh choke (Bud CH 1227 or equivalent).
L_3	8.5 henrys (Stancor C1279 or equivalent).
L_4	RF coil, 15 μ h (Delevan 1537-44 or equivalent).

27.255-MC REMOTE-CONTROL RECEIVER—

Output of superregenerative detector consists of 200-kc quench signal and 1,000-cps tone modulation from incoming signal. Quench filter passes only audio signal to amplifier. Amplified audio is detected and resulting direct current used to operate relay K_1 .—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 363.



TV STATION CONTROL—Control pulses transmitted in blanking interval by tv network transmitter are decoded by receiver circuits

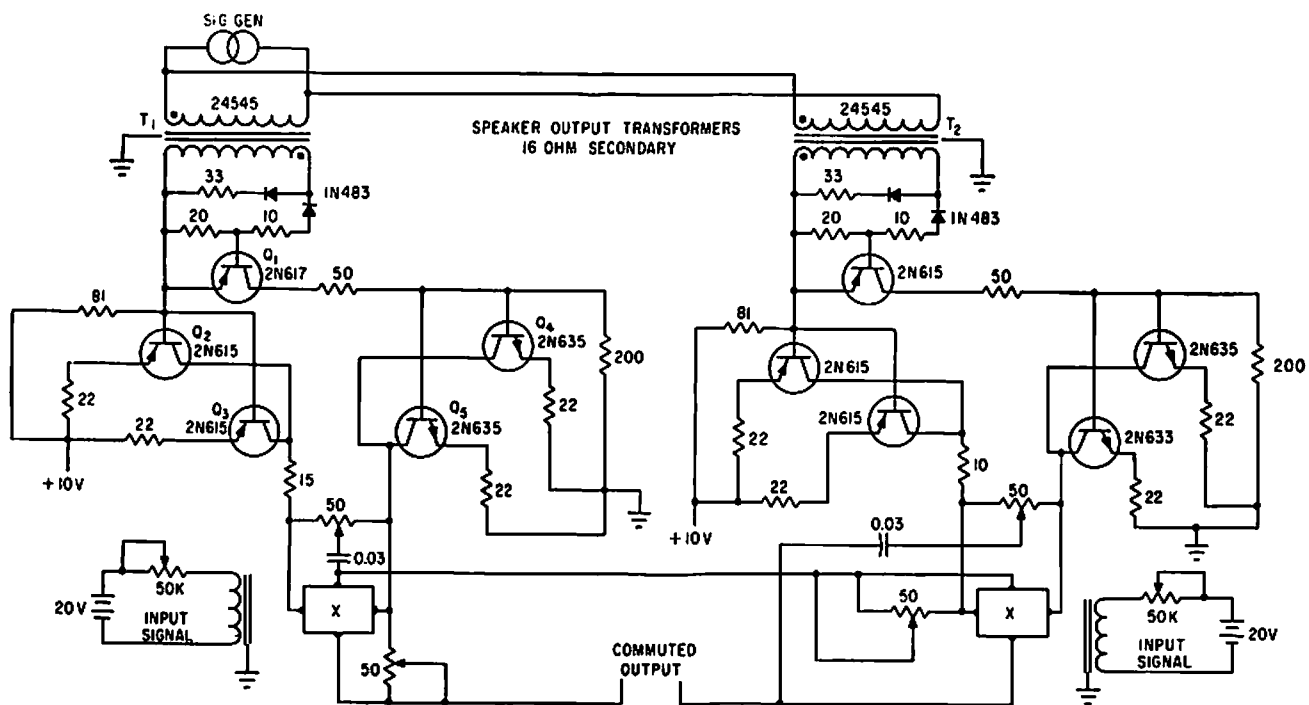
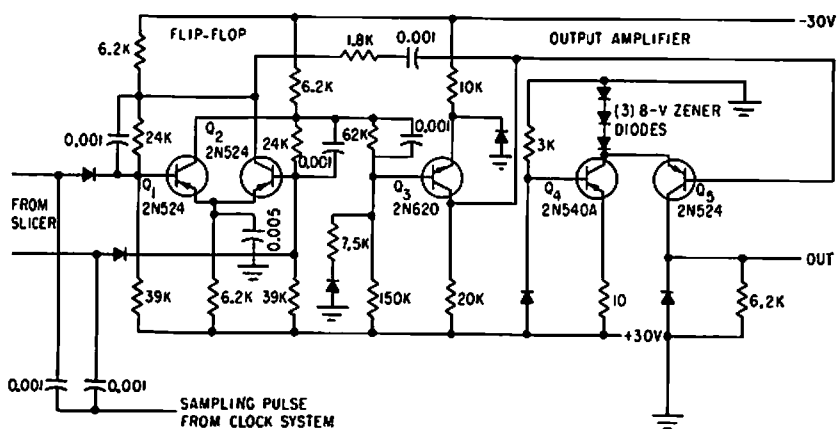
shown and translated into six different switching actions used to introduce special program matter such as commercials, weather

reports, and local film projector material that may be different for each network station. —K. Kazama and T. Ishino, *Remote Tv Control by Blanking-Interval Pulses*, *Electronics*, 33:20, p 79-81.

CHAPTER 74

Sampling Circuits

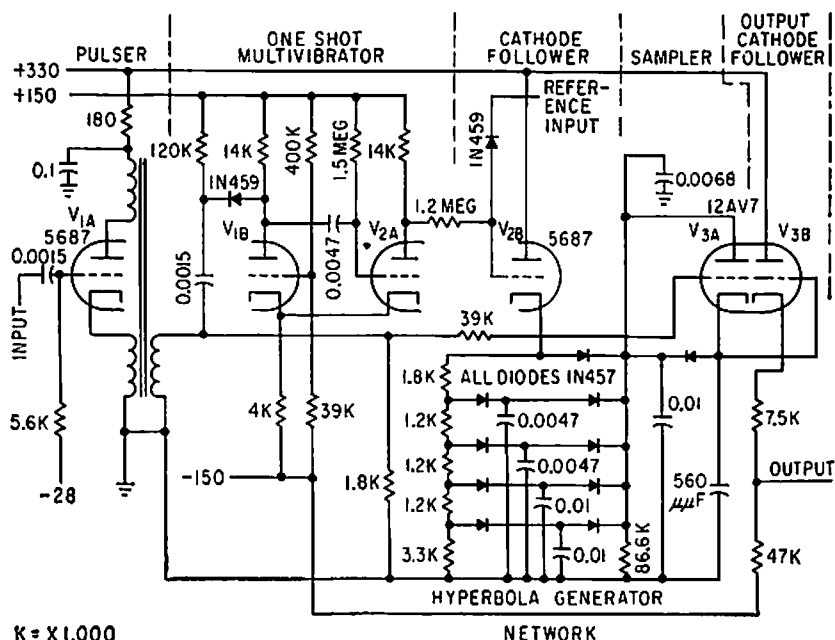
SYNCHRONOUS SAMPLER—Time jitter of digital receiver output pulse is eliminated by synchronous sampling of detected signal. Each bit is sampled by local clock pulses that trigger flip-flop Q1-Q2. Two outputs of slicer, 180° out of phase, are applied to bases of Q1 and Q2. Output of flip-flop is regenerated information, free of jitter.—J. L. Hollis, Sending Digital Data Over Narrow-Band Lines, *Electronics*, 32:23, p 72-74.



30-KC HALL-GENERATOR SAMPLING SWITCHES—Control current circuits of series-connected Hall generators X are pulsed alternately for

switching d-c input signal. Triggering for identical circuit at right occurs on opposite half-cycles of signal generators.—T. J. Marcus,

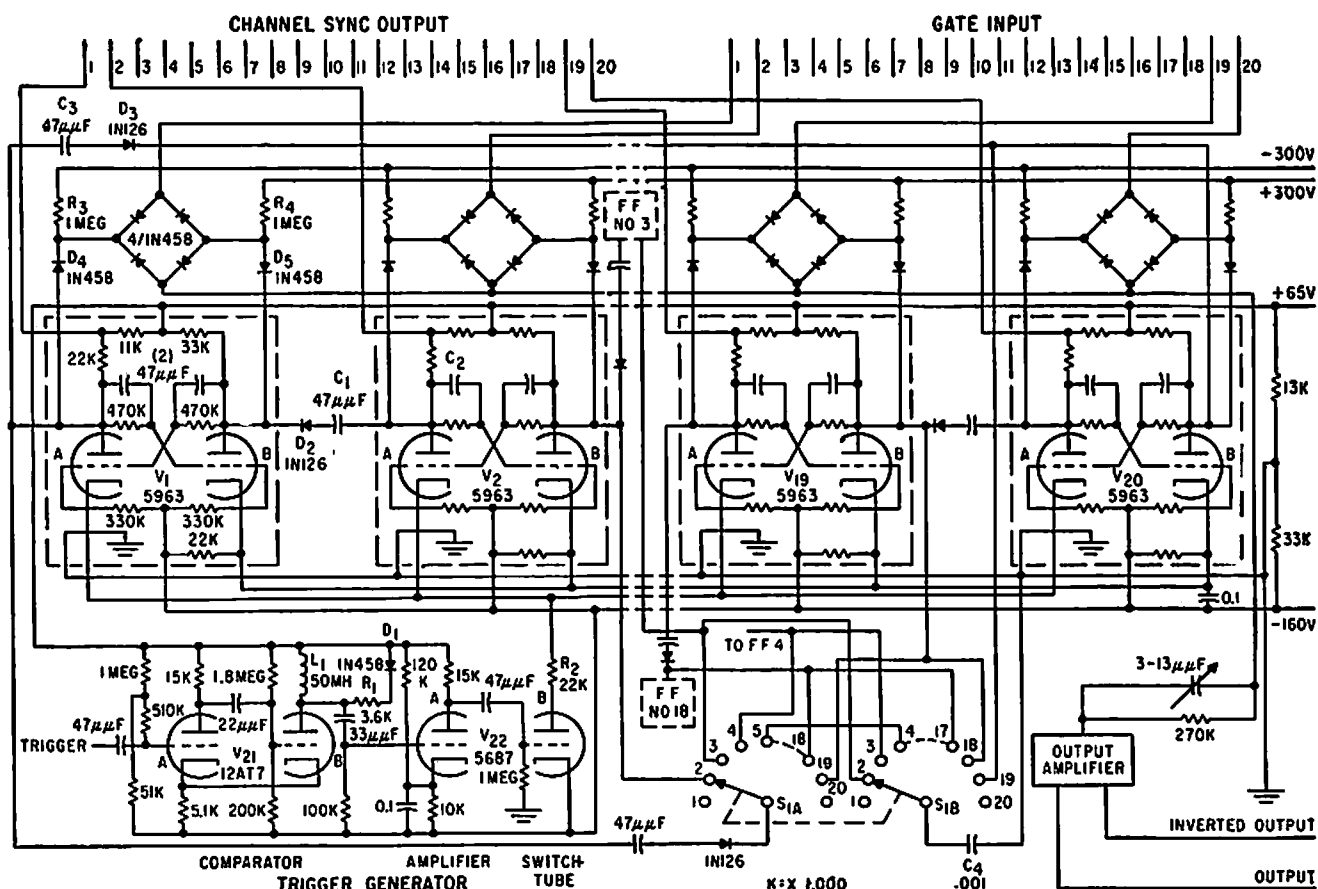
Using Hall Generators as Contactless Commutators, *Electronics*, 35:4, p 43-45.



MAGNETIC-TAPE DATA SAMPLER—Used in playback system for discrimination of f-m signal from magnetic tape. Compares data channel signals with recorded reference fre-

quency, to make output independent of tape speed. Tube V2B is cathode follower with hyperbola generator network as cathode impedance, to create curve for average area

of voltage block at any time during data period for a given blockwidth (1.4 millsec). —P. S. Bongston, *Sampling Discriminators for Data Reduction, Electronics*, 32:13, p 70-72.

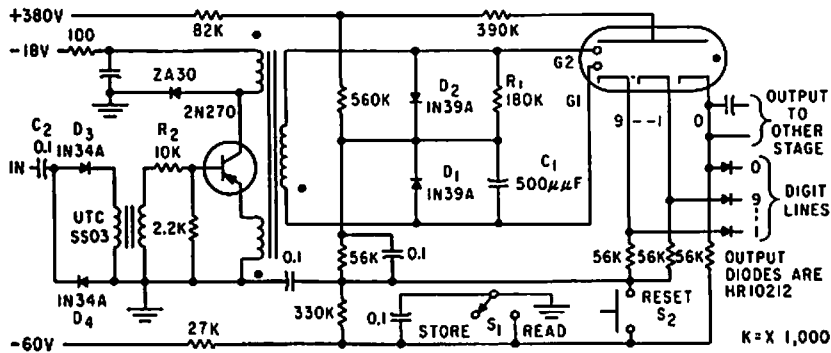
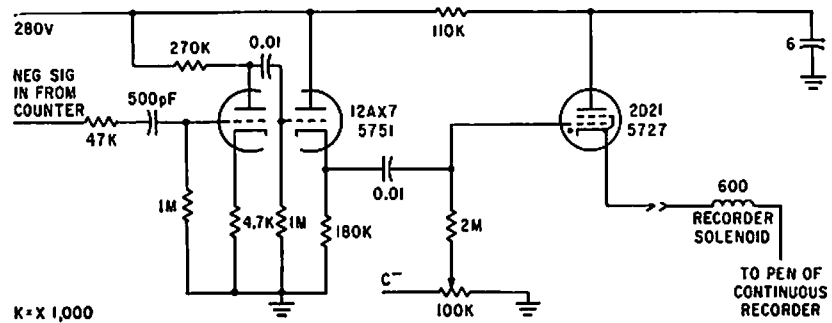


CLOSED-RING COUNTER—Serves as 20-channel electronic switch for sampling voltage sources at rates of up to 50,000 cps. Flip-

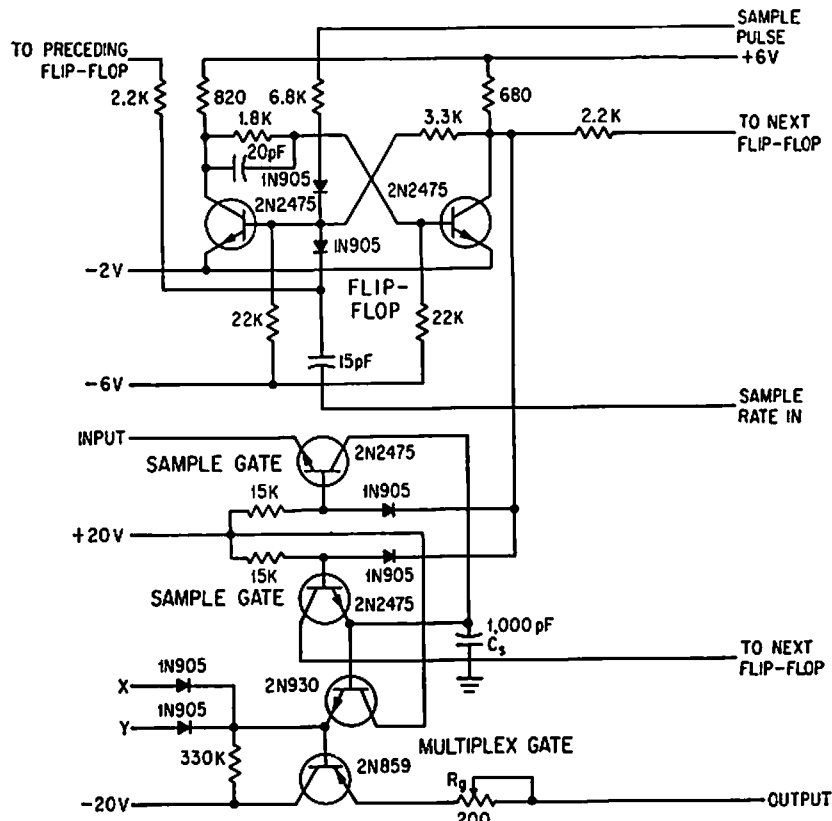
flop ring actuates diode gates in step with trigger pulses. Output can be used to drive recorders or feed data processing equipment.

—K. L. Berns and B. E. Bishop, *High-Speed Multiplexing With Closed-Ring Counters, Electronics*, 32-26, p 48-50.

PULSE-COUNT SAMPLE-TIME RECORDER— Amplifier and thyatron trigger feed pulse to solenoid pen of recorder, to record time of end of counting period, corresponding to instant at which counter delivers negative pulse.—C. F. Miller, *New Phototransistor Tachometers Measure Missile Spin*, *Electronics*, 35:25, p 33-35.



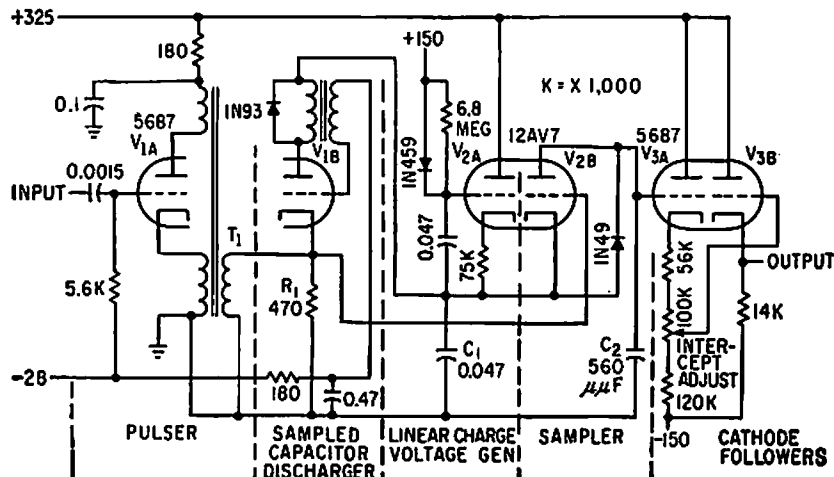
COLD-CATHODE SAMPLING COUNTER— Transistor-blocking oscillator drives cold-cathode counter tube to give long-life decade counter having low power consumption. Used in automatic recorder for data from several hundred radioactive samples per day. Maximum repetition rate is 200 pps.—H. Sadowski and M. E. Cassidy, *How Transistor Drives Cold-Cathode Counter*, *Electronics*, 32:38, p 46-47.



FLIP-FLOPS CONTROL SAMPLE-AND-HOLD— Sampled slices of incoming radar pulse are converted to binary digital form at 10-Mc rate, using flip-flops to connect sample-and-

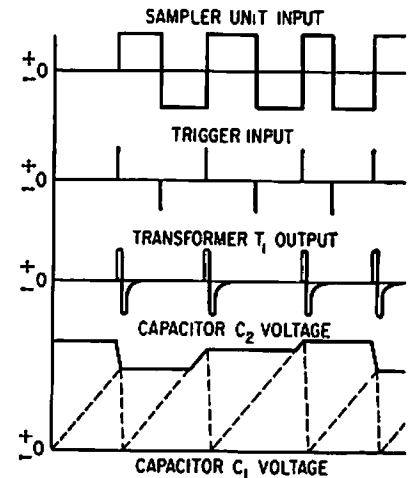
hold capacitor C_s to signal amplifiers. Effective aperture time of sample gate is 20 nsec. Multiplex gate feeds sampled signal values to analog-digital converter at proper time as

selected by multiplex counter of system.—A. Hakimoglu and R. D. Kulvin, *Sampling Ten Million Words a Second*, *Electronics*, 37:8, p 52-57.



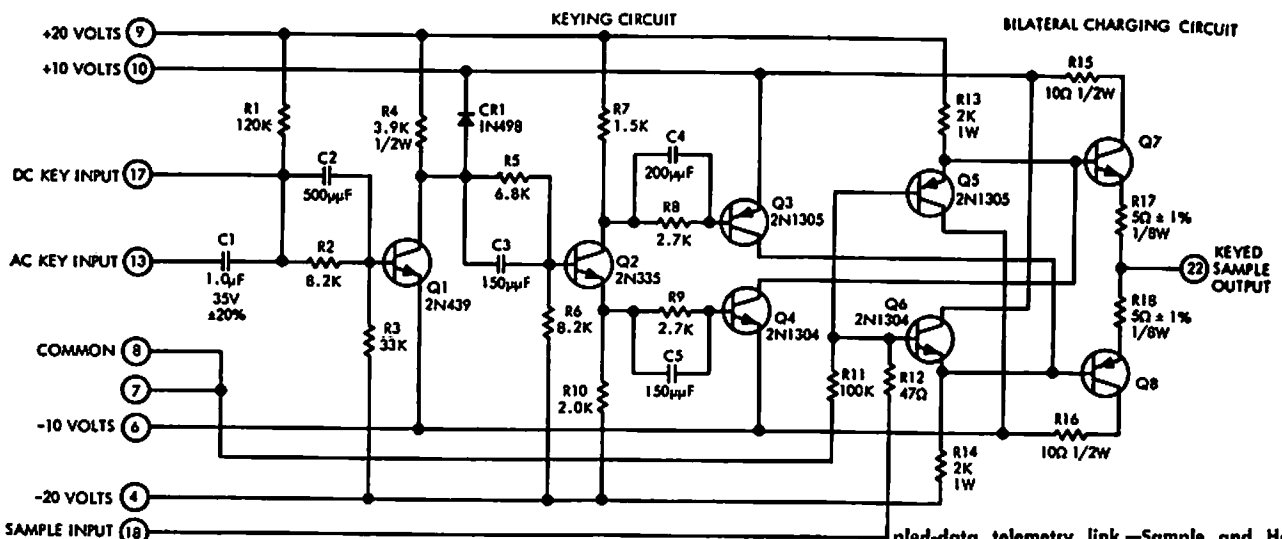
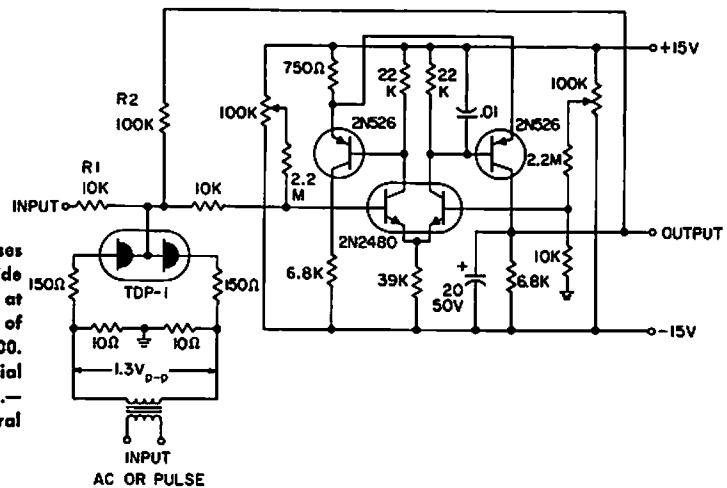
REFERENCE-FREQUENCY SAMPLER—Improves discrimination of f-m signals from magnetic tape, with fastest possible response to wide-deviation frequency-modulated transients. Constant reference frequency is recorded on

one tape channel. When playback output voltage is made proportional to quotient of data and reference frequencies, output is independent of tape speed, and wow and flutter components are cancelled. Reference



discriminator shown provides voltage output proportional to period of preceding cycle. —P. S. Bangston, Sampling Discriminators for Data Reduction, *Electronics*, 32:13, p 70-72.

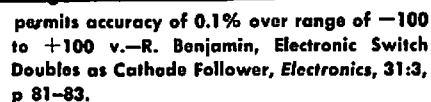
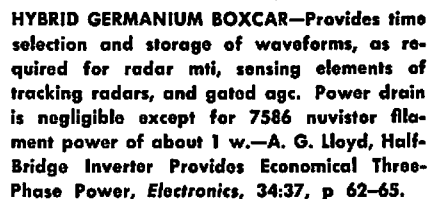
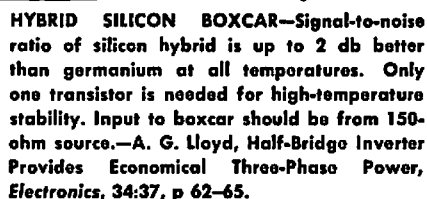
OPERATIONAL-AMPLIFIER SAMPLER—Uses tunnel-diode pair with amplifier to provide output that is proportional to input signal at instant corresponding to leading edge of sampling pulse. Voltage gain of circuit is 100. Can also be used to measure differential peak point current of tunnel diode pairs.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 372.

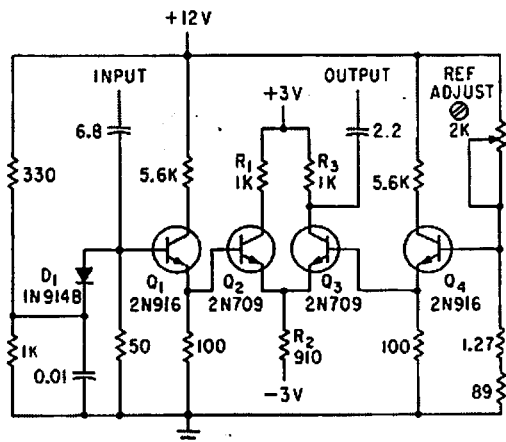


SAMPLE AND HOLD—Uses bilateral charging to increase energy content of series of low-duty-cycle amplitude-modulated pulses result-

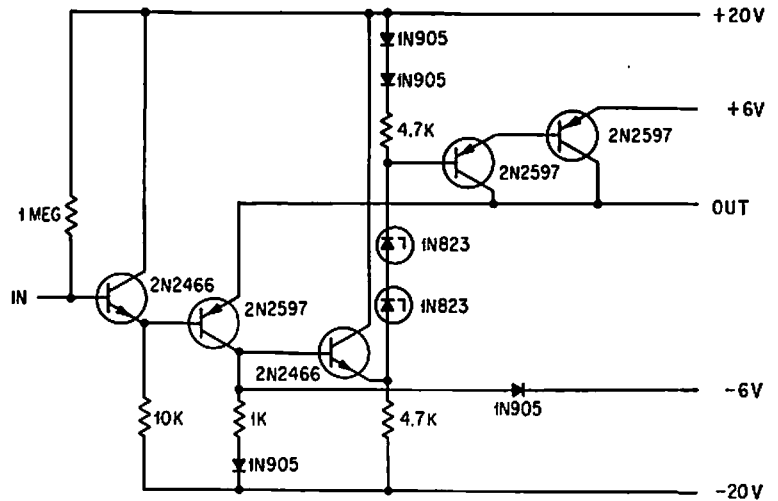
ing from demultiplexing one channel of pam pulse train. Designed for sampling and holding 0 to 5 v information received via sam-

pled-data telemetry link.—Sample and Hold Circuit with Bilateral Charging, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 131.

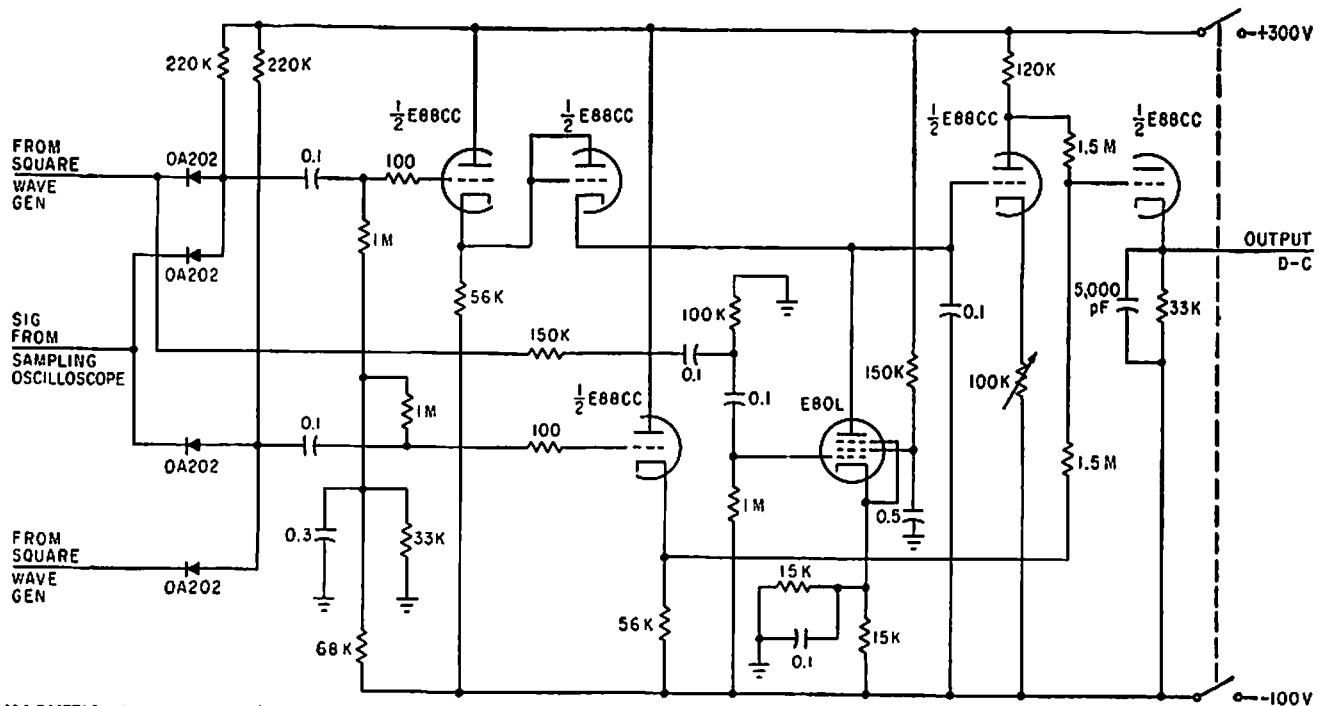




HEIGHT SAMPLING GIVES NANOSECOND RESOLUTION—Eight identical four-transistor difference amplifiers in parallel divide input signal into eight levels for sampling. In each, input signal is compared to reference signal in Q2-Q3.—A. A. Fleischer and E. Johnson, New Digital Conversion Method Provides Nanosecond Resolution, *Electronics*, 36:18, p 55-57.



UNITY-GAIN VIDEO SAMPLING AMPLIFIER—Five-transistor amplifier delivers high bidirectional current, with bandwidth of 10 Mc, for amplifying incoming radar or other pulse prior to binary conversion.—A. Hakimoglu and R. D. Kulvin, Sampling Ten Million Words a Second, *Electronics*, 37:6, p 52-57.



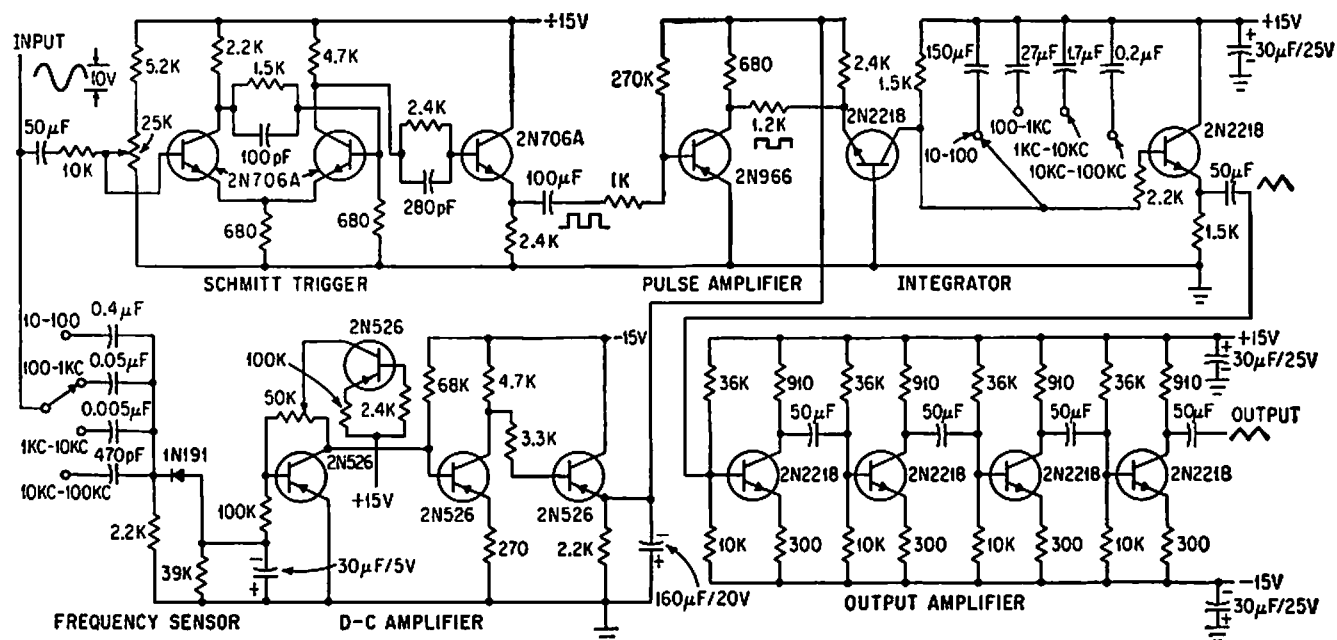
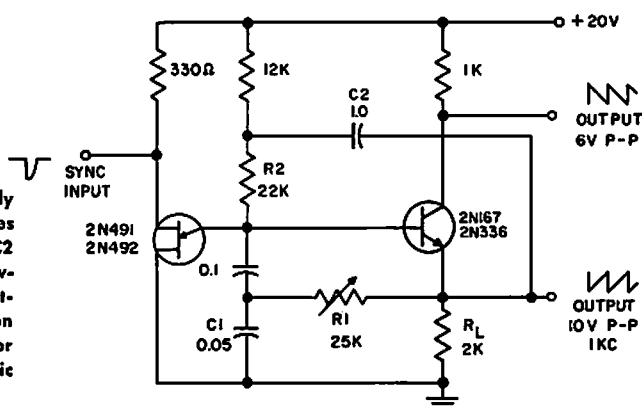
MAGNETIC FILM—Circuit subtracts one sampled waveform from the next, to reduce noise disturbances by factor of 10 in equipment using strip transmission line for determining polarity reversal time of thin magnetic films to be used as switching elements

in computers.—W. Dietrich and W. E. Proebster, Measuring Switching Speed of Magnetic Films, *Electronics*, 33:23, p 79-81.

CHAPTER 75

Sawtooth Generator Circuits

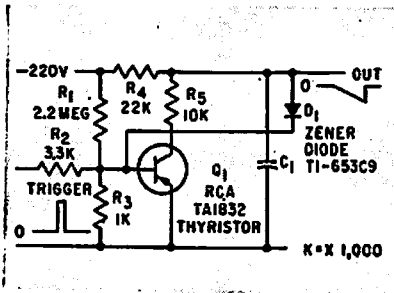
HIGH-LINEARITY SAWTOOTH—Requires only single positive supply. Npn transistor serves as output buffer amplifier, while R2 and C2 in bootstrap circuit improve linearity of sawtooth. R1 and C1 act as integrating network to provide second-order compensation for nonlinearity of waveform.—“Transistor Manual,” Seventh Edition, General Electric Co., 1964, p 319.



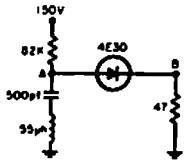
20-100,000 CPS TRIANGULAR-WAVE GENERATOR—Sinusoidal frequency changes are converted into proportional d-c voltage and fed into pulse amplifier and integrator to generate constant-amplitude triangular waveform

for measuring dynamic linearity of amplifier as function of frequency. Schmitt trigger converts input sine wave to constant-amplitude square wave. Frequency sensor produces d-c voltage proportional to frequency to serve as

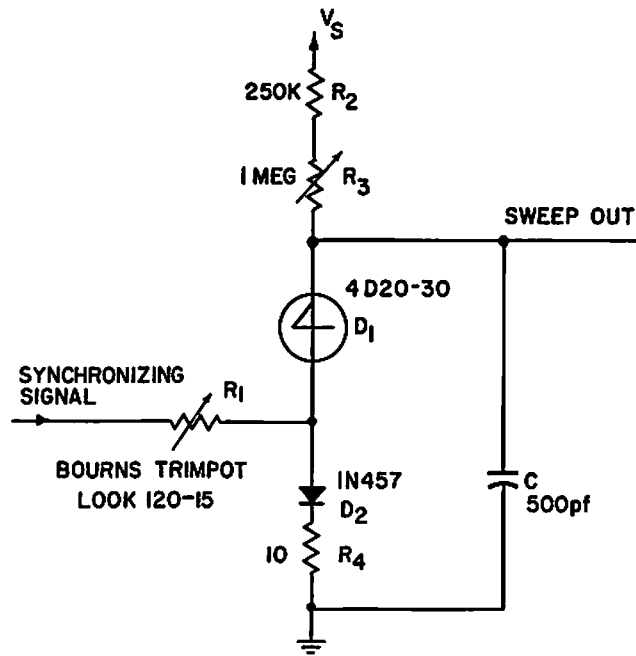
d-c source for pulse amplifier and integrator. —D. E. Cottrell, Frequency Sensor Stabilizes Triangular-Wave Generator, *Electronics*, 37:9, p 38-40.



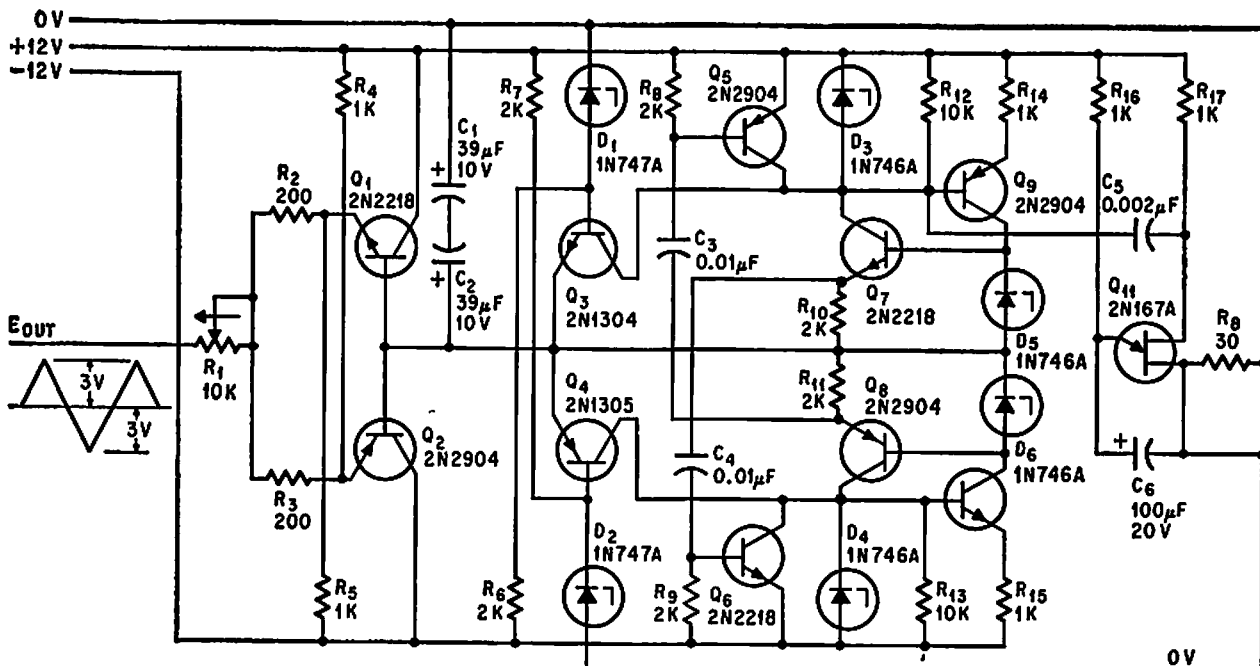
SIMPLE SAWTOOTH—Uses semiconductor switch Q1, whose amplitude is controlled by zener diode D1. Operation on only small part of R-C charging curve helps make output pulse widths, amplitudes, and waveform timing independent of active elements in circuit.—C. A. Von Urff and R. W. Ahrens, How to Generate Accurate Sawtooth and Pulse Waves, *Electronics*, 32:50, p 64-66.



BOOSTING SAWTOOTH FREQUENCY—Inductor causes ringing and thereby extends operating frequency of sawtooth oscillator using four-layer diode. Will operate well above 100 kc.—P. Emilio, Jr., Inductor Raises Useful Sawtooth Frequency, *EEE*, 12:7, p 28.

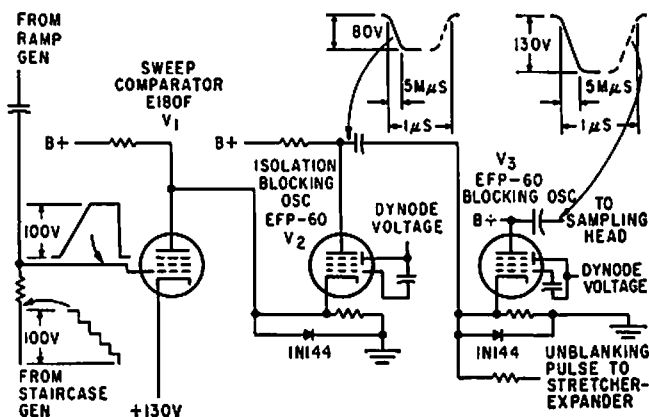


SYNCHRONOUS SWEEP—Produces linear 20-v sawtooth with four-layer diode and six other components. Maximum sweep rate can reach 100 kc. Provides synchronous operation with good linearity and sufficiently fast retrace to eliminate need for blanking in oscilloscope applications.—4-Layer Diode Sweep (Synchronous), "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 173.



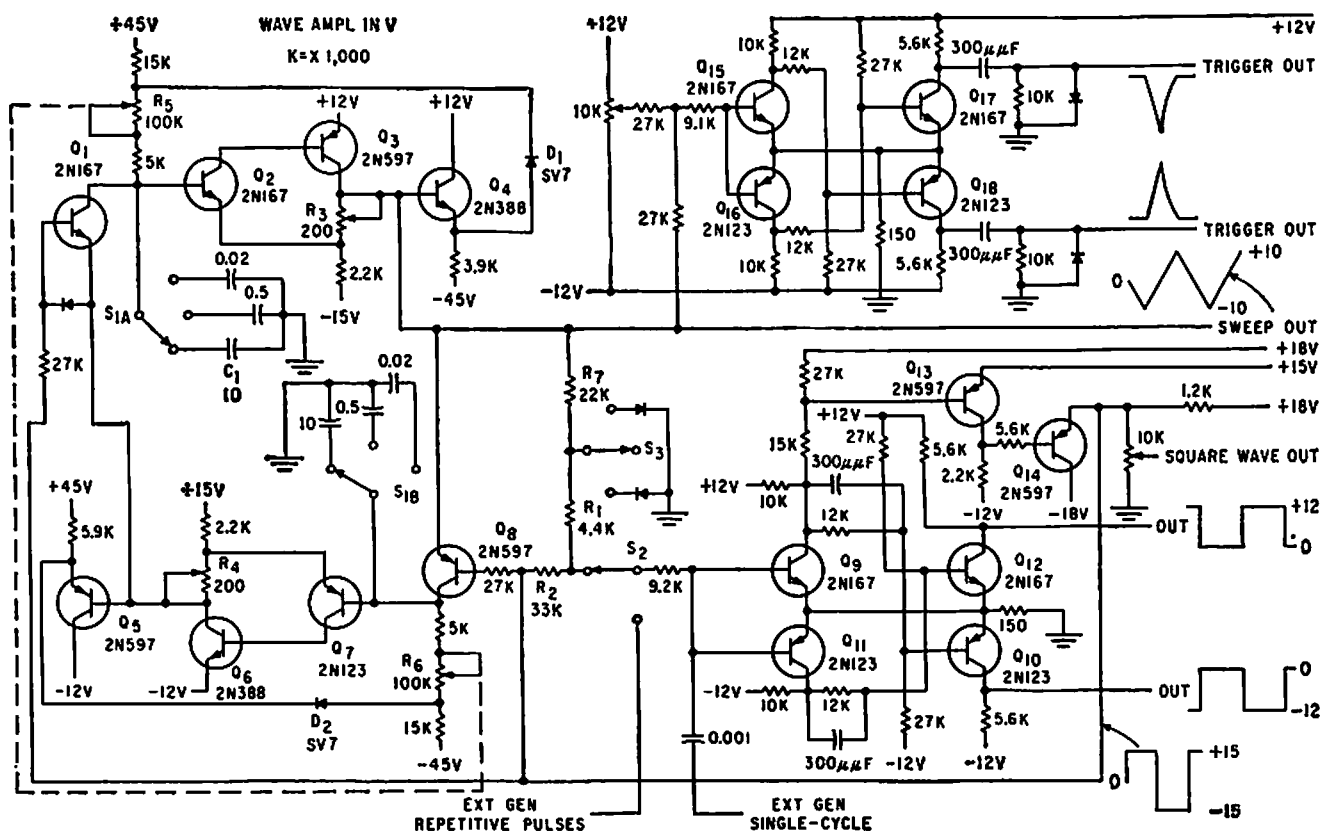
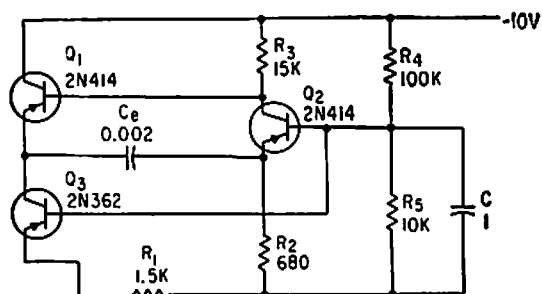
TRIANGLES—Peaks, slopes, and frequency of triangular waves can be varied independently. R10-R11 control positive slope, R11-R15 negative slope, and C1-C2 both slopes.

Zener voltages of D1-D2 determine peaks.—R. Zane, Triangle Generator Adjusts Output Slopes and Peaks, *Electronics*, 38:12, p 85-86.



STROBE GENERATOR—Sweep comparator V1 mixes output of ramp generator and staircase generator to give sampling or strobe pulse. Instantaneous d-c level of ramp, corresponding to a step, fixes time at which strobe signal is generated.—W. E. Bushor, *Sample Method Displays Millimicrosecond Pulses*, *Electronics*, 32:31, p 69-71.

LINEAR SAWTOOTH—Q1 and Q2 in emitter-coupled mvbr and constant-current generator Q3 produce sweep having linearity comparable to that of vacuum-tube circuits.—B. Rakovic, *One More Transistor Makes a Linear Sawtooth*, *Electronics*, 35:49, p 50-51.

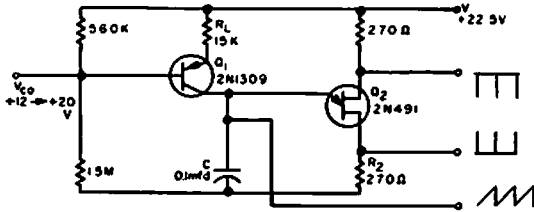
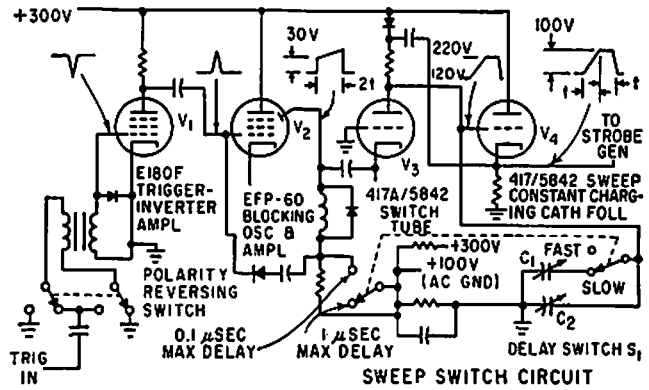


MULTI-WAVEFORM GENERATOR—Uses double-bootstrap sweeps to generate triangular

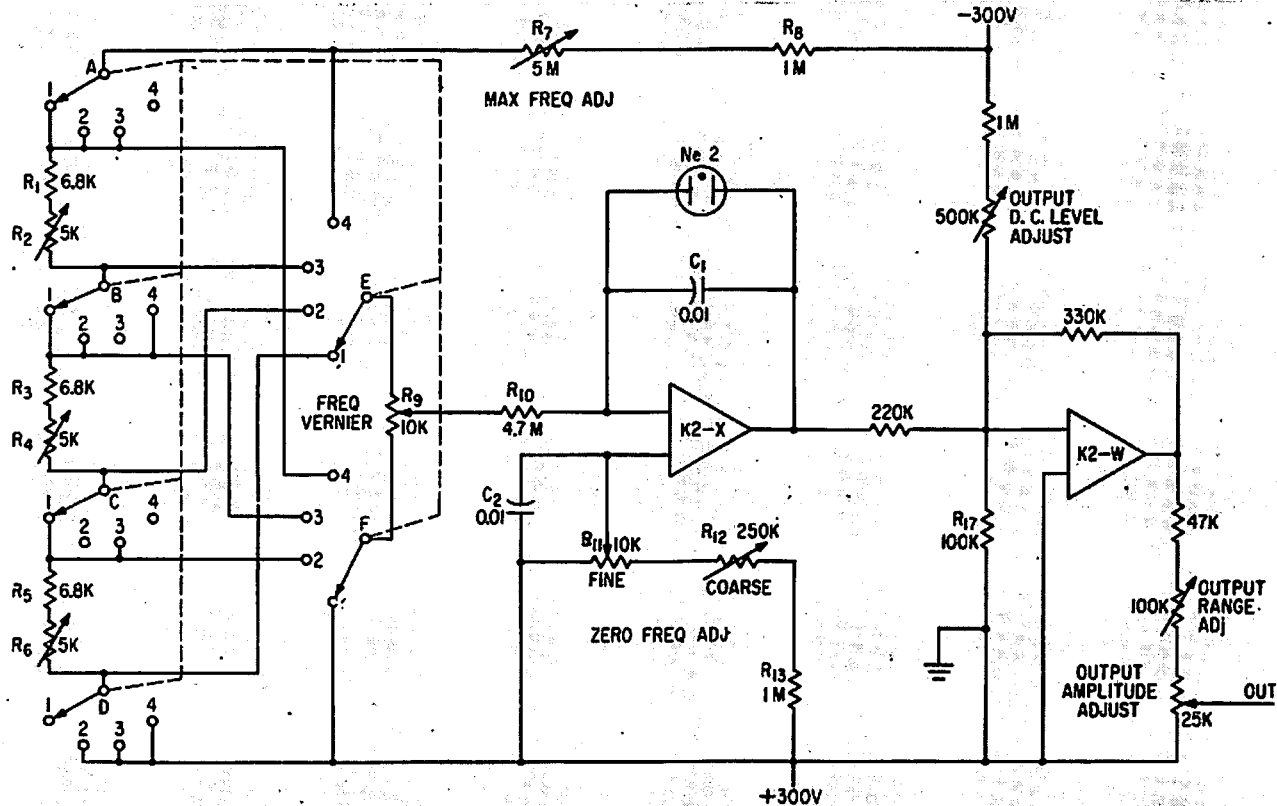
wave. Can be free-running or driven by external generator. Polarity-sensitive trigger

circuit controls sweeps.—J. E. Curry, *Multi-Waveform Generator*, *Electronics*, 32:46, p 83.

RAMP GENERATOR—Produces positive-going ramp with 100-v amplitude when input trigger pulse is applied. Time duration of ramp can be set at 0.1 or 1 microsec. Time stability is better than 0.1 millimicrosec for long time intervals.—W. E. Bushor, *Sample Method Displays Millimicrosecond Pulses*, *Electronics*, 32:31, p 69-71.



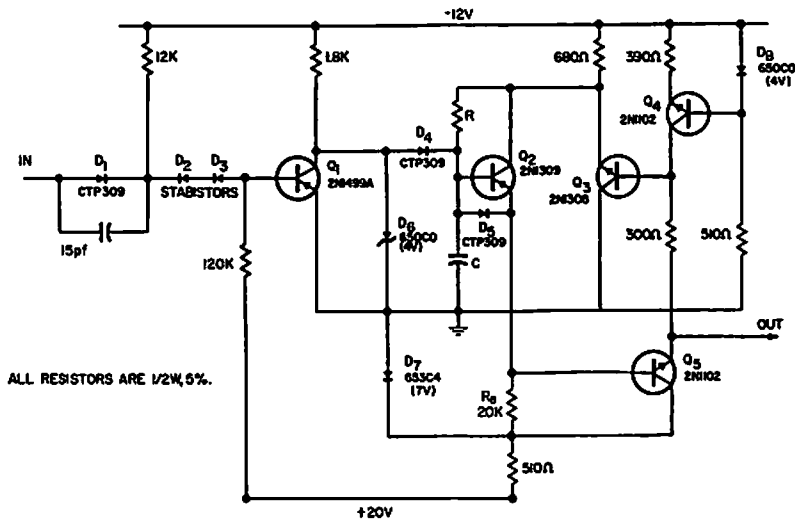
VOLTAGE-CONTROLLED RAMP/TRIGGER—Provides ramp output with or without positive and negative trigger pulses over 6:1 linear range of frequency control. For values of C from 0.001 to 10 mfd, frequency range is 10 cps to 20 kc.—M. S. Tatch, *Voltage-Controlled Ramp/Trigger Generator*, *EEE*, 12:3, p 71.



0.2 TO 18 CPS—Provides low-frequency 5-v sweeps of high linearity, to complement con-

ventional signal generators having maximum accuracy at higher sweeps.—A. Angelone,

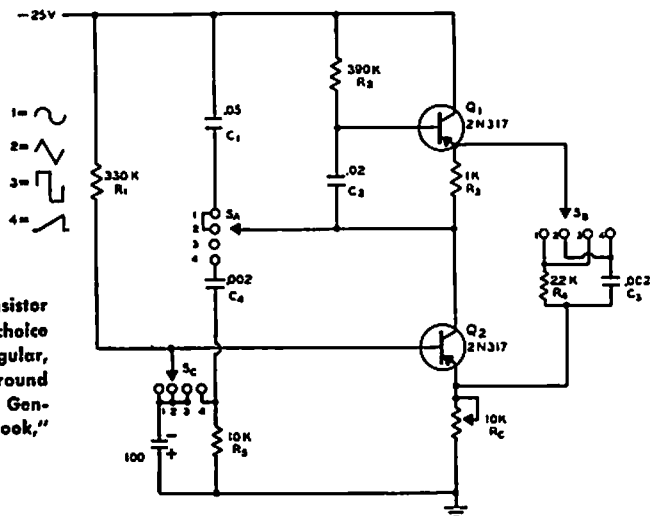
Subaudio Sawtooth Generator Gives One-Percent Linearity, *Electronics*, 34:48, p 42-43.



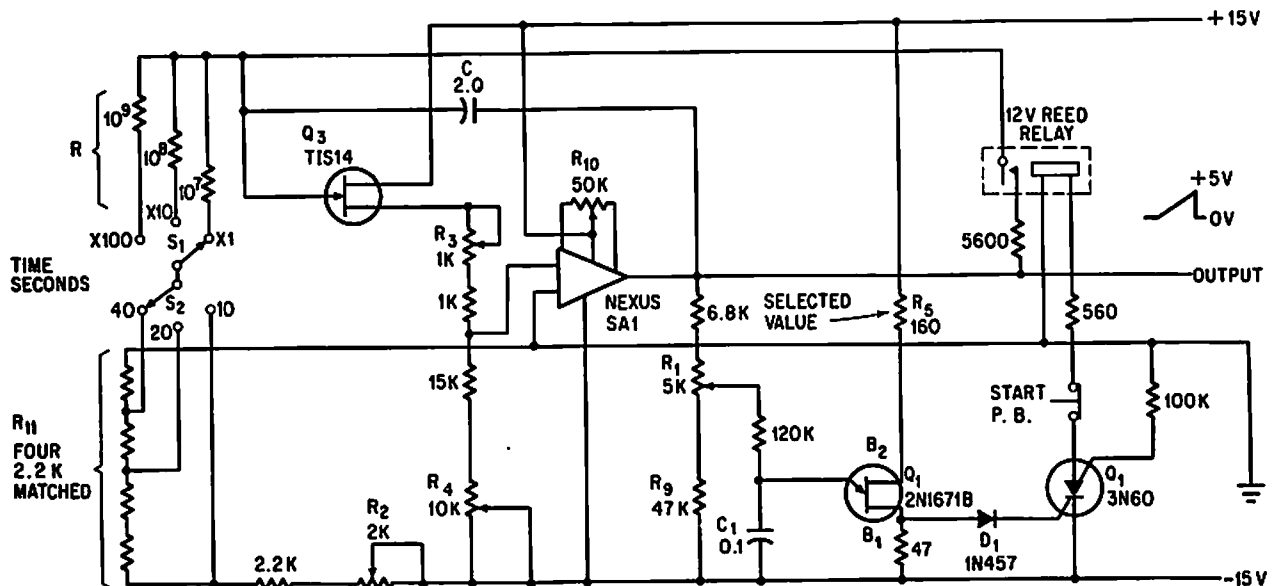
ALL RESISTORS ARE 1/2W, 5%.

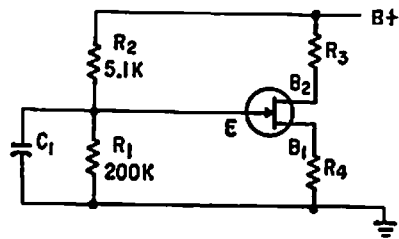
WIDE-RANGE LINEAR BOOTSTRAP TIME BASE

—Delivers highly linear ramps at repetition rates up to 5 Mc, for input pulses from 0.1 microsec to several seconds wide. Nonlinearity is 5% for slow ramps, and improves to 0.05% for fast ramp. Measures pulse width accurately when used in combination with voltage comparator. Can also be used for sampling and for testing amplitude response of linear amplifiers.—T. Molliga, A Wide-Range, Linear Time Base, *EEE*, 10:8, p 56-59.

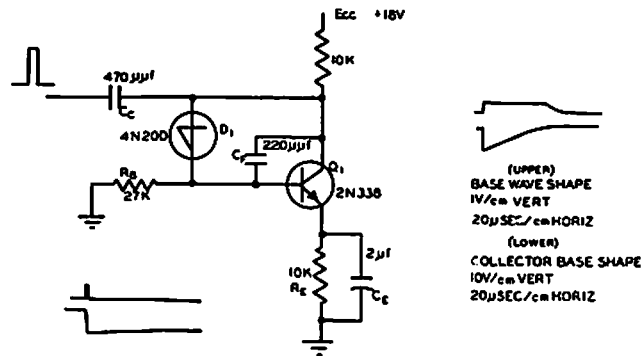


ALL-WAVEFORM GENERATOR—Two-transistor circuit with function switch provides choice of four different waveforms: sine, triangular, square, and sawtooth. Frequency is around 450 cps.—Transistorized All-Waveform Generator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 168.



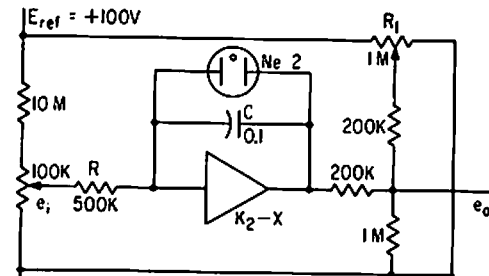


UNI-JUNCTION SAWTOOTH—Uses 2N14 unijunction transistor. R1 represents input impedance of conventional emitter-follower having nominal 5,000-ohm impedance in emitter circuit. R3 is 3K and R4 is 330 ohms. —M. Rosen, Subaudio Swept Signal Generator, *Electronics*, 33:17, p 67-68.

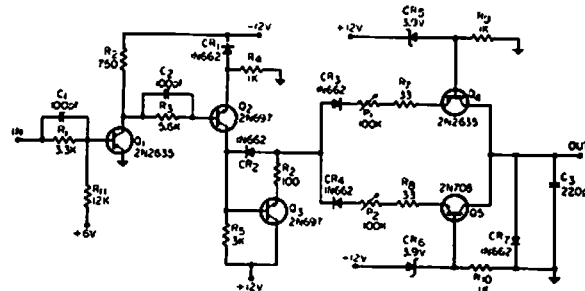


TRIGGERED SAWTOOTH—Uses Shockley four-layer diode and transistorized integrating circuit. Ramp starts with quick drop, then rises back to steady-state condition. Pulse width

is 0.2 microsec at 400 pps.—Triggered Sawtooth Generator, "Electronic Circuit Design Handbook," MacIver Pub. Corp., N.Y., 1965, p 169.

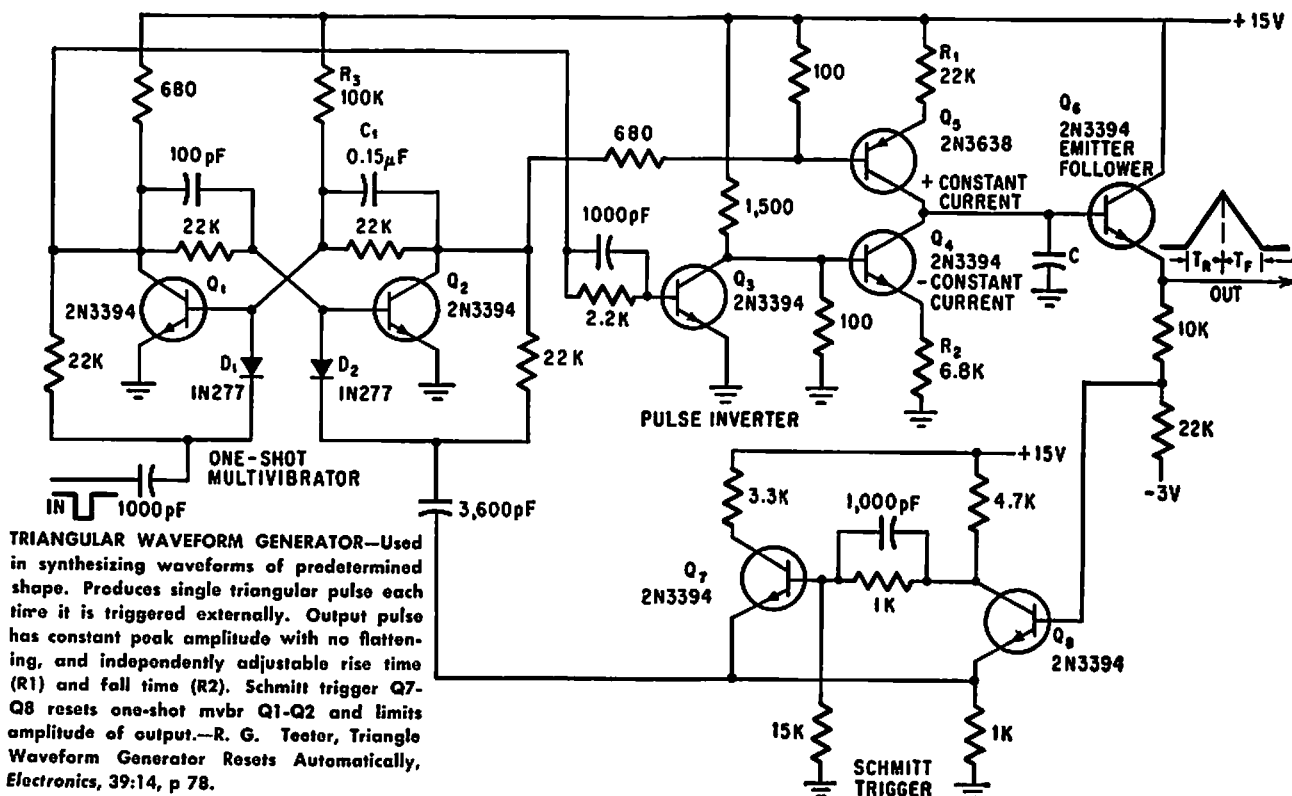


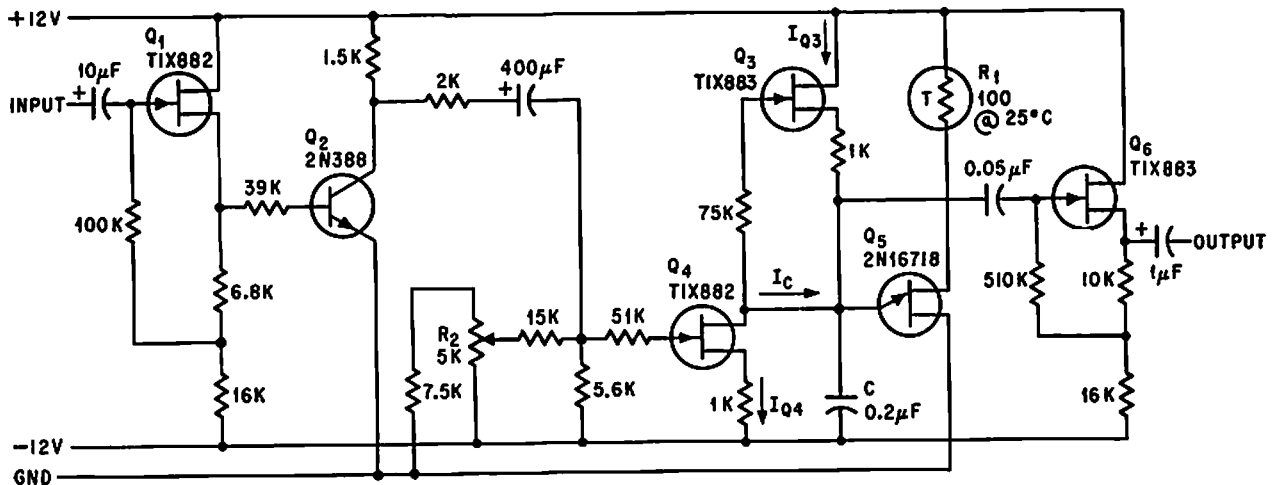
SIMPLE SUBAUDIO SWEEP—Uses operational amplifier in integrating circuit, neon lamp as automatic switch, and resistance network that allows output to be varied around level set by R1. Used to generate sweeps with high linearity up to 18 cps.—A. Angelone, Subaudio Sawtooth Generator Gives One-Percent Linearity, *Electronics*, 34:48, p 42-43.



POSITIVE OR NEGATIVE SLOPE—Generates linear ramps, either negative or positive, by switching two current sources on and off

during charging of C3.—G. Marosi, Positive or Negative Slope Generator, *EEE*, 13:5, p 43.

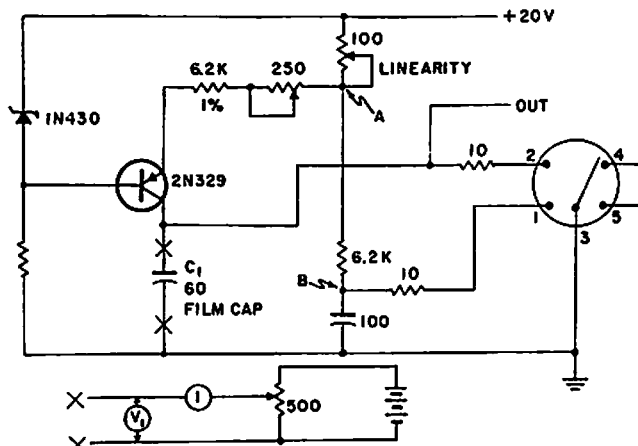




LINEAR RAMP—Output voltage varies linearly with frequency within 98%, while peak voltage is constant at 0.6 v. Thermistor R1 pro-

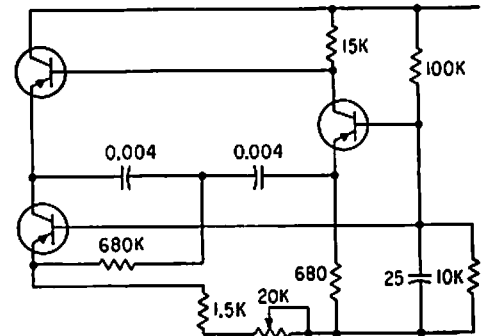
vides temperature stability and source follower Q6 reduces loading.—D. D. Brooks and C. F. Johnson, *Sawtooth Generator Uses FET*

as Constant Current Source, *Electronics*, 38:18, p 87.

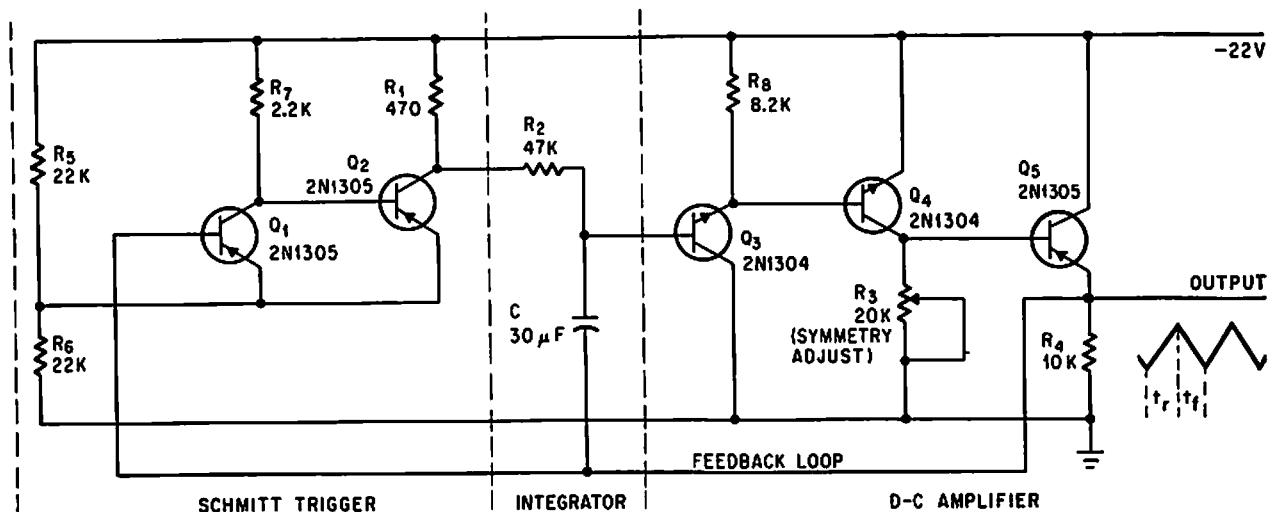


ULTRALINEAR RAMP GENERATOR—Used in high-accuracy low-speed voltage-to-pulse width converter. Linearity is better than 0.02% between 10 and 90% points of ramp.

Test circuit below is substituted for C1 when adjusting linearity control.—Ultra Linear Ramp Generator, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 166.



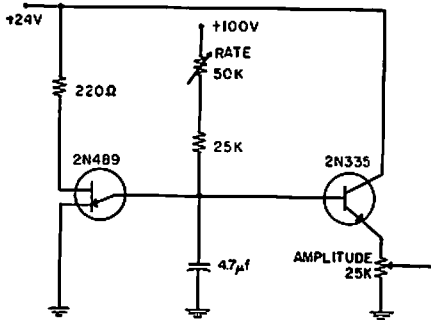
LINEAR SAWTOOTH WITH SPLIT TIMING CAPACITOR—To compensate for linearity deterioration, timing capacitor of emitter-coupled mvbr is split into two equal parts, and feedback resistor is connected between center point and emitter of constant-current generator Q3.—B. Rakovic, *One More Transistor Makes a Linear Sawtooth*, *Electronics*, 35:49, p 50-51.



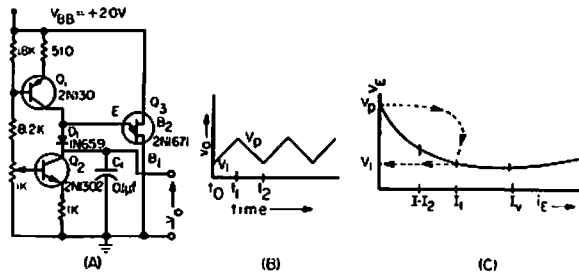
TRIANGULAR—Deviation is less than 1% from linearity at any frequency from 400 cps to 1 cycle per hour. Frequency is changed by

varying R1, R2 or C; values shown give 20-second period. Used to sweep magnetic field across air gap of electromagnet and for test-

D-C AMPLIFIER—ing servos and vlf amplifiers.—J. F. Delpach, *Triangular Waveforms Have 1% Linearity*, *Electronics*, 38:7, p 86-87.

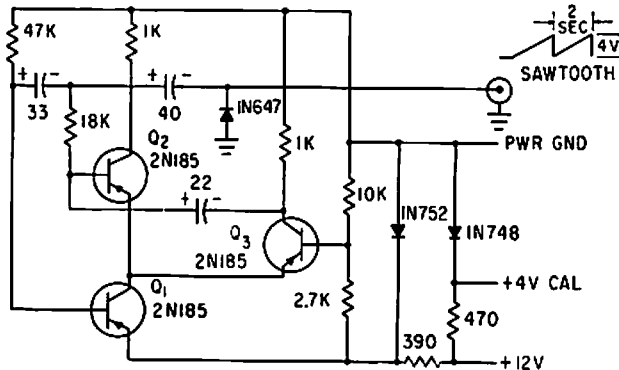


SIMPLE UJT SWEEP—Generates variable-frequency sawtooth directly, with emitter-follower 2N335 serving only for isolation. Sawtooth frequency can be varied without affecting output amplitude.—Unijunction Sweep, *EEE*, 11:7, p 86.



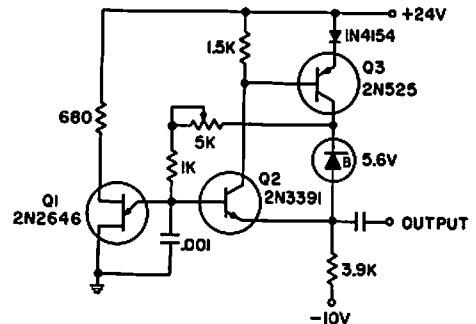
UJT TRIANGULAR-WAVE GENERATOR—Two current generators produce triangular wave by alternately charging and discharging C_1 .

Ujt and diode serve as switch to reverse slope of ramp.—R. Dean, Unijunction Triangular Wave Generator, *EEE*, 12:4, p 59.

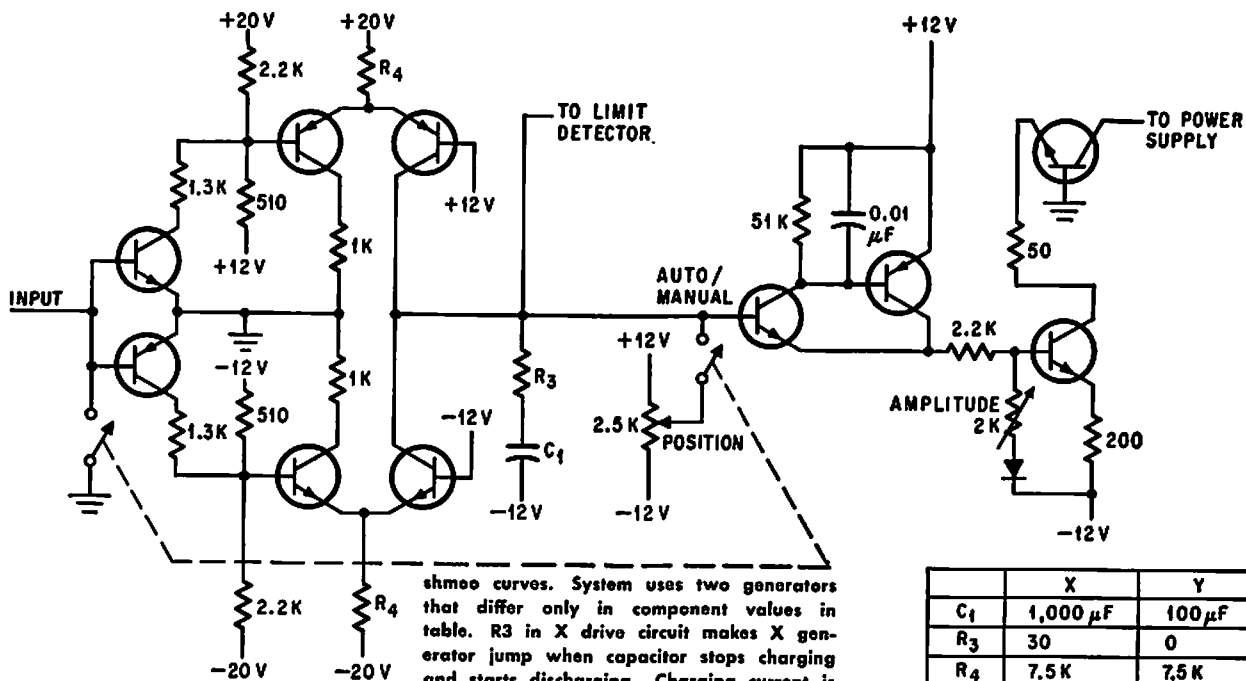


LINEAR SAWTOOTH—Develops signal with 4-v amplitude and 2-sec period.—O. C. Haycock and K. D. Baker, *Measuring Antenna*

Impedance in the Ionosphere, *Electronics*, 34:2, p 88-92.



50-KC SAWTOOTH—Uses bootstrap charging circuit, with constant voltage maintained across charging resistor by zener diode and emitter-follower amplifier Q2, so capacitor charging current is constant over complete cycle.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 319.

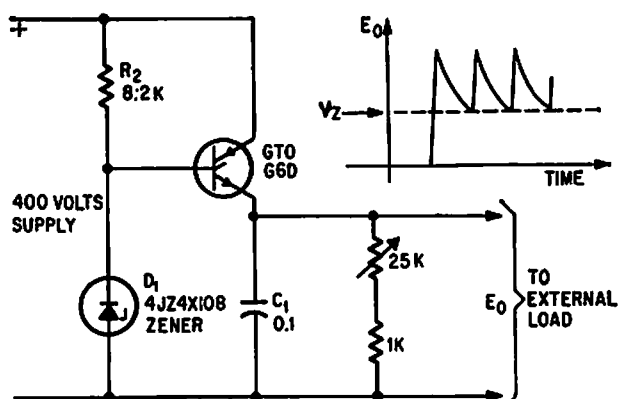


LINEAR RAMP GENERATOR—Used in tester that shows computer memory performance under marginal drive currents by plotting

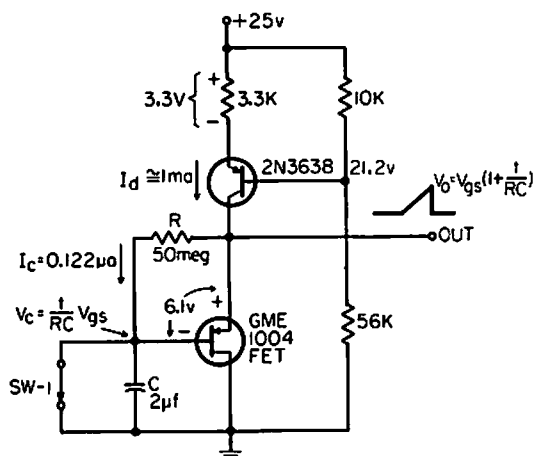
shmoos curves. System uses two generators that differ only in component values in table. R_3 in X drive circuit makes X generator jump when capacitor stops charging and starts discharging. Charging current is positive or negative depending on input voltage polarity. Npn transistors are 2N706, pnp transistors are 2N1132, and diodes are

	X	Y
C_1	1,000 μF	100 μF
R_3	30	0
R_4	7.5 K	7.5 K

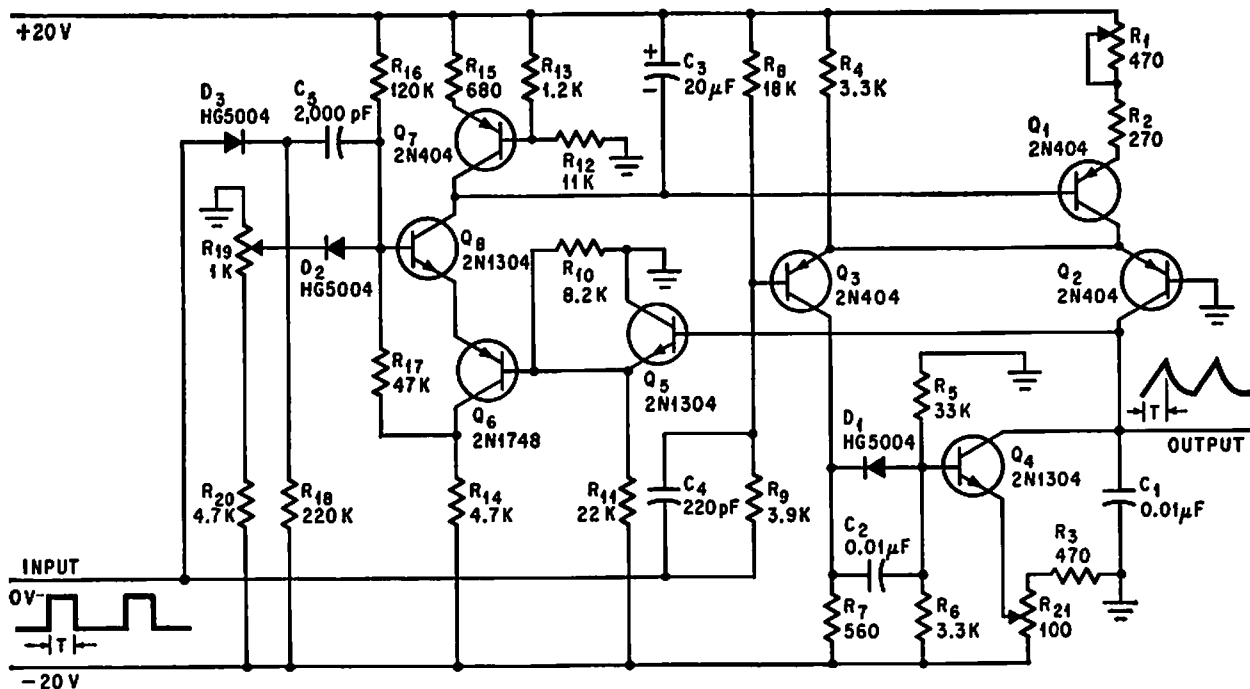
1N921.—J. E. Gersbach, The Great Shmoos Plot: Testing Memories Automatically, *Electronics*, 39:15, p 127-134.



FREE-RUNNING HIGH-VOLTAGE SAWTOOTH GENERATOR—When power is applied, gate-turnoff scr triggers and applies 400 v to C1. When voltage across C1 rises above avalanche voltage of D1, GTO turns off and C1 discharges until scr conducts again.—D. R. Grafham, Now the Gate Turnoff Switch Speeds Up D-C Switching, *Electronics*, 37:12, p 64-71.



FET RAMP GENERATOR—Use of mos fet gives very slow rate-of-rise linear ramp generator (less than 0.1 v/soc). Longer durations can be obtained by using larger values for R and C.—J. M. Phalan, MOS FETs Give Long Time-Constant Ramps, *EEE*, 14:4, p 46.



VARIABLE-SLOPE RAMP GENERATOR—Slope is determined by rate at which C1 is charged by constant-current generator Q1-R4 through

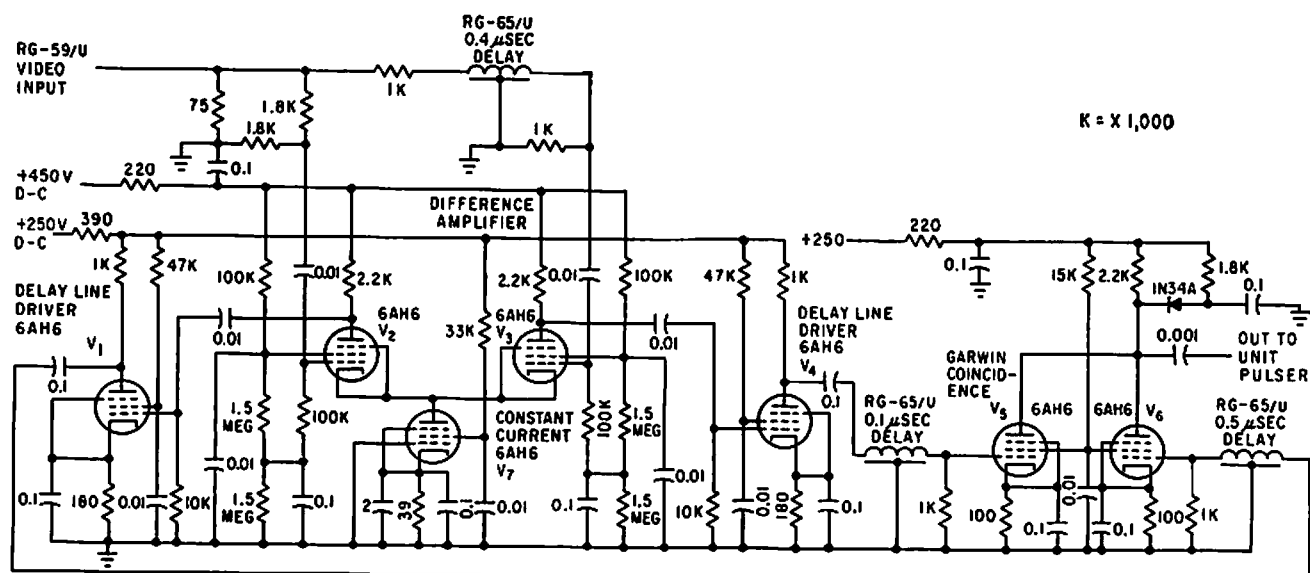
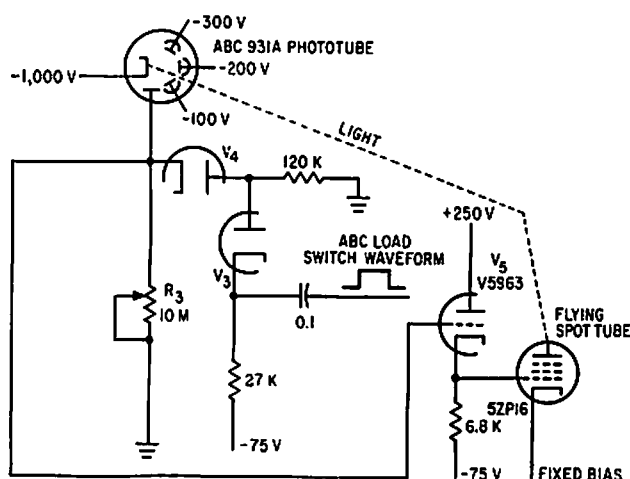
Q2. Peak of ramp is determined by R19. Circuit will synchronize over 3:1 frequency range centered on 70 kc.—D. J. Grover, Ca-

pacitor Charging Controls Variable Ramp Generator, *Electronics*, 39:11, p 91-92.

CHAPTER 76

Scanner Circuits

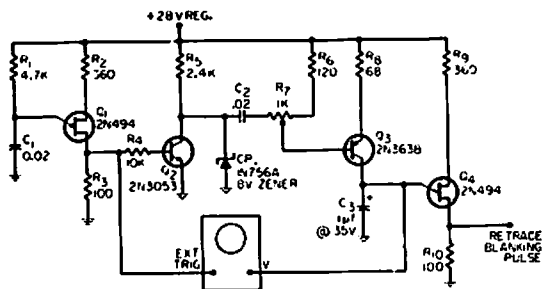
AUTOMATIC BRIGHTNESS CONTROL—Automatic brightness control circuit intensifies scanning spot when sweep is triggered, and holds intensity constant. When spot is quiescent, output of 931A is applied to cathode follower that determines bias on 5ZP16. Phototube then sees only 10-meg load because V4 is cut off. When mark is sensed by scanner, load switching action makes V4 conduct to reduce phototube load to 120,000 ohms.—A. C. L. Brown, Flying Spot Inspects TV Rating Records, *Electronics*, 35:9, p 31-34.



NUCLEAR TRACK COUNTER—Recognition system scans nuclear emulsion strips coated on glass, using image orthicon with microscope. Straight or moderately curved tracks in emul-

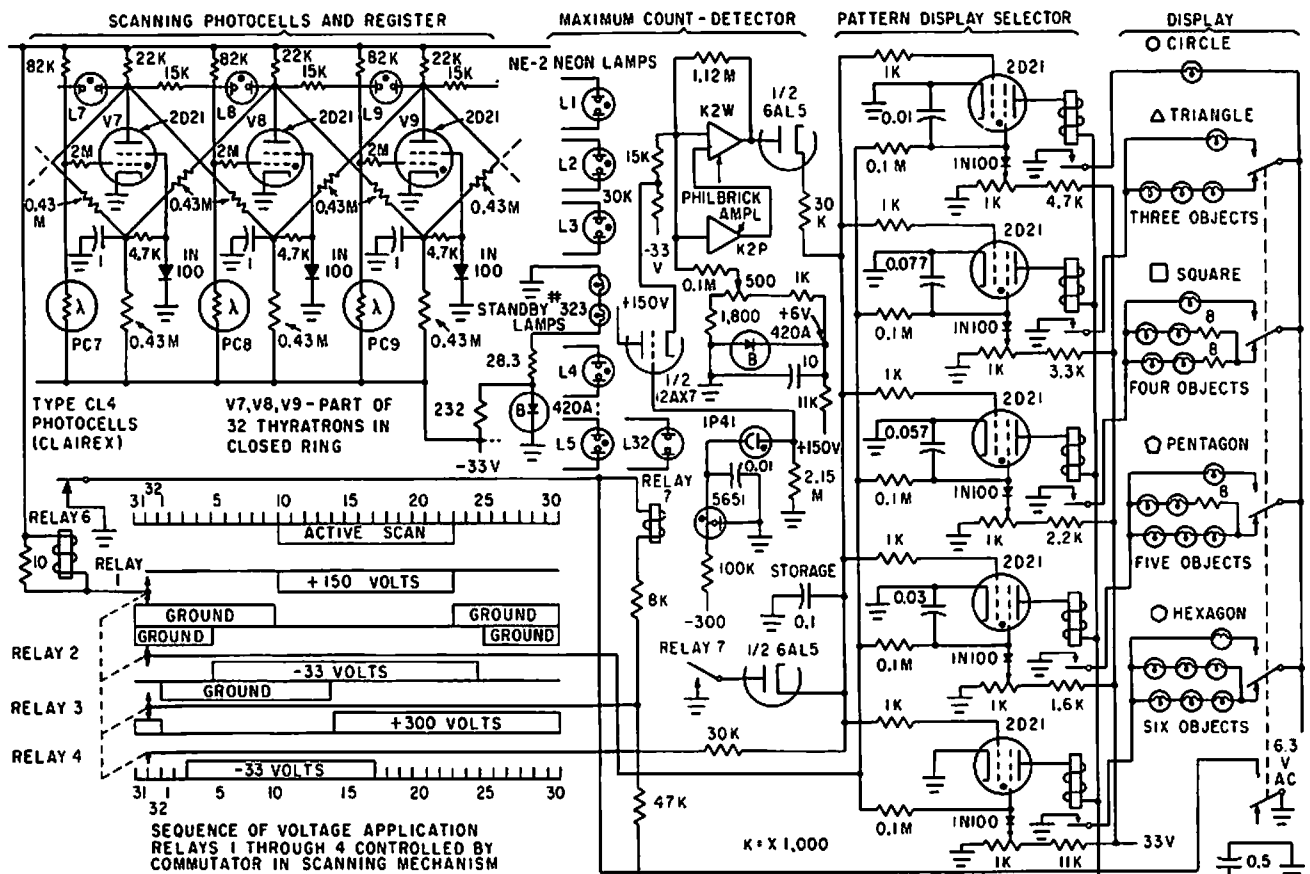
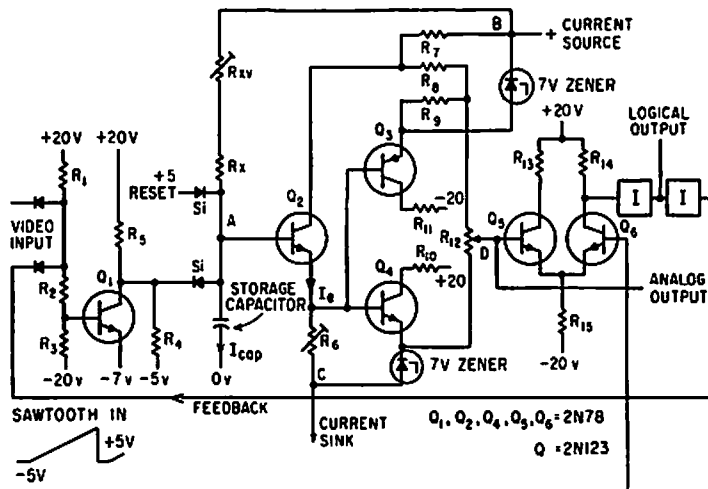
sion, produced by nuclear particles, are recognized and counted by scanner that used video screening circuit shown. Opaque emulsion regions that meet narrowness criteria

produce output pulses.—P. V. C. Hough, J. A. Koenig, and W. Williams, Scanner Recognizes Atomic Particle Tracks, *Electronics*, 32:13, p 58-61.



UJT RASTER GENERATOR—Developed for use in low-cost transistorized flying-spot scanner. Can also be adapted for closed-circuit tv cameras and monitors. Ujt Q1 is relaxation oscillator at desired horizontal sweep rate of 10 kc. Interlaced scanning is easily obtained.—F. Stevens, Low-Cost UJT Raster Generator, *EEE*, 13:12, p 65-66.

VERTICAL LOCATOR FOR CHARACTER READER—Sawtooth input is compared with earliest video of each vertical sweep of typed character being scanned, to charge capacitor and derive logical output related to bottom of typed line.—J. Bauldreay and E. Milbradt, Solving Registration Problems in Optical Character Recognition, *Electronics*, 35:1, p 77-81.

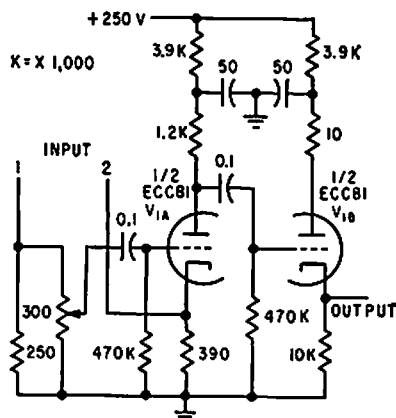


SHAPE RECOGNITION—Use of dilating circular scan resolves some of problems for general-purpose reading machine. Technique

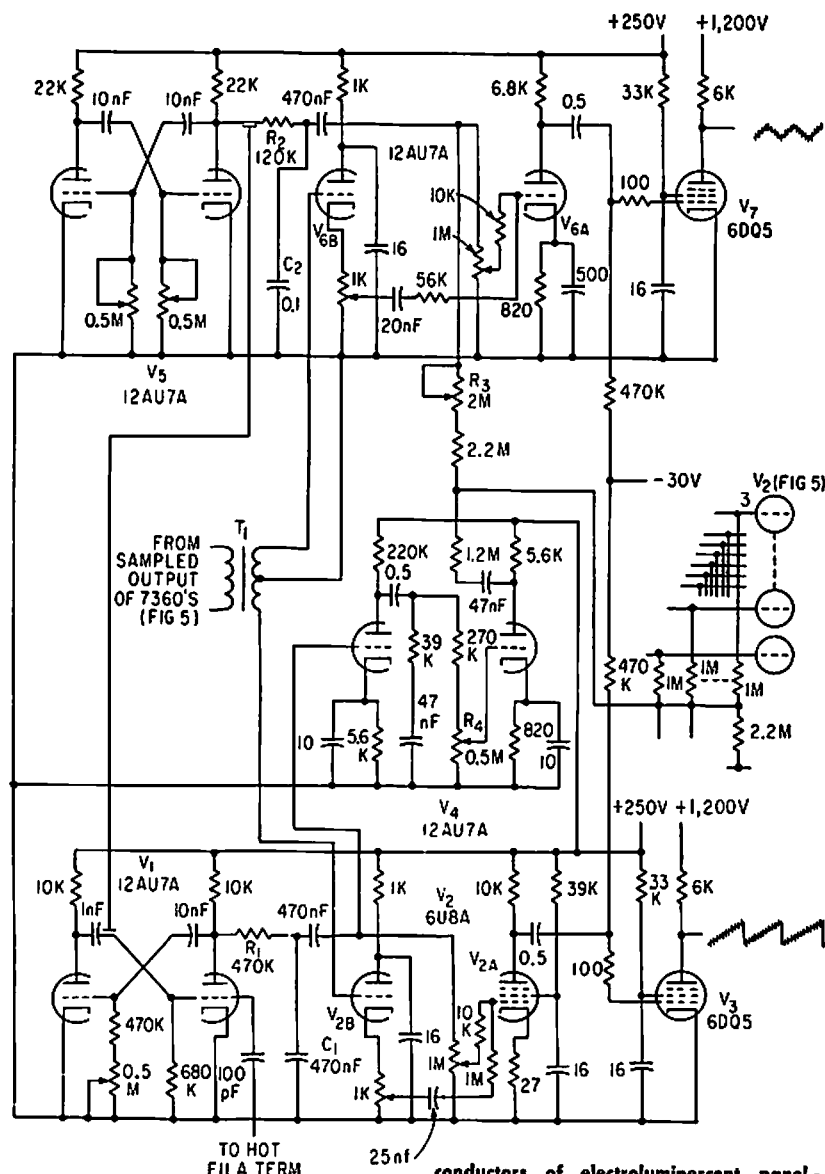
can be applied to automatic recognition of letters and numbers in variety of styles.—

L. D. Harmon, Line-Drawing Pattern Recognizer, *Electronics*, 33:36, p 39-43.

STABILIZING PHOTOMULTIPLIER GAIN—Feed-back loop provides automatic gain stabilization for color film scanner.—R. M. Farber and K. M. St. John, *Scanner Analyzes Color Content of Movie Film*, *Electronics*, 34:48, p 38-41.

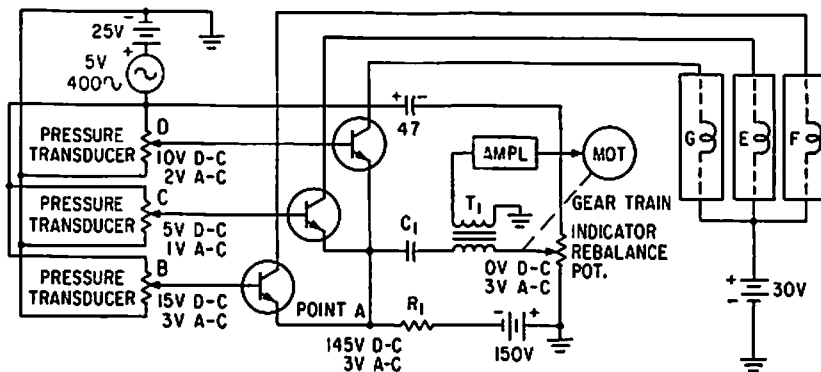


FLAT-TUBE SCANNER—V1 thru V3 generate sawtooth wave and V5 thru V7 triangular wave for driving horizontal and vertical



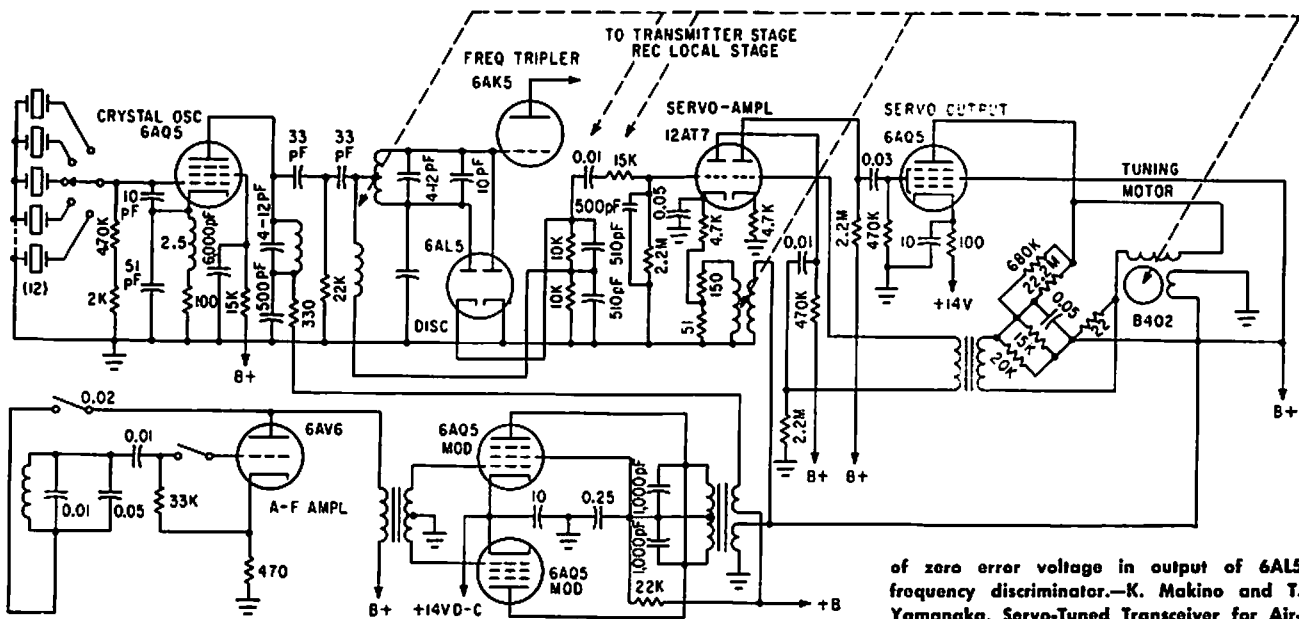
conductors of electroluminescent panel.—B. Binggelli and E. Fatuzzo, Solid-State Panels: Will They Bring Flat-Display TV? *Electronics*, 35:26, p 67-70.

Servo Circuits



TRANSDUCER SCANNER WITH INDICATORS—Transducers monitored in parallel mode actuate a-c servo when any one goes beyond predetermined range. Transistor then turns

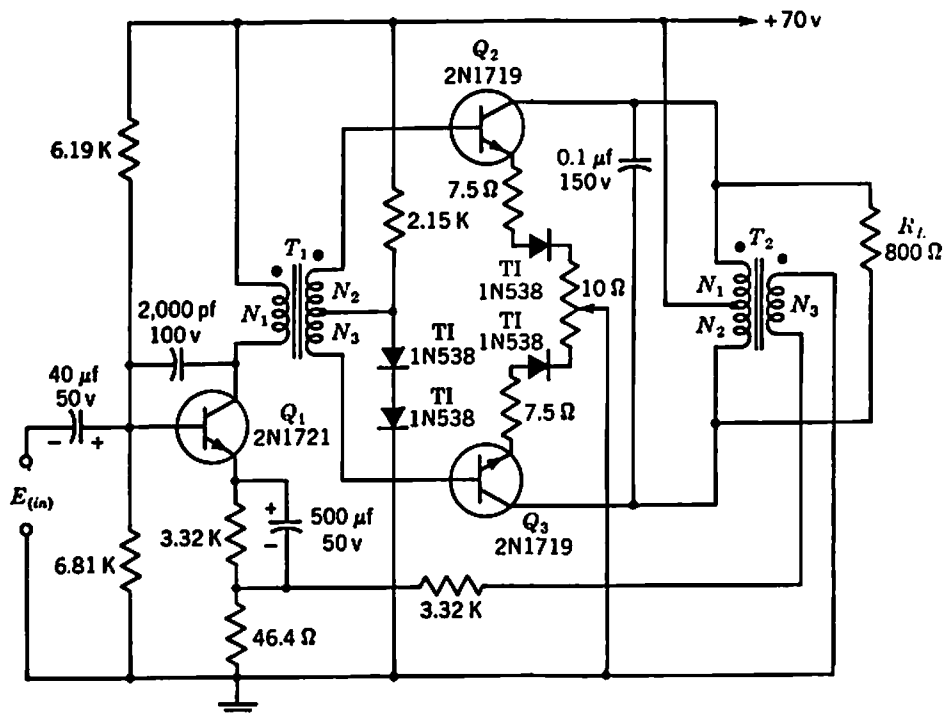
on lamp to identify transducer whose output has actuated the servo.—S. Thaler, Solid-State Parallel-Mode Scanner Reads System Physical Parameters, *Electronics*, 34:19, p 78-80.



TWELVE-FREQUENCY SERVO TUNER—Negative feedback voltage proportional to servo

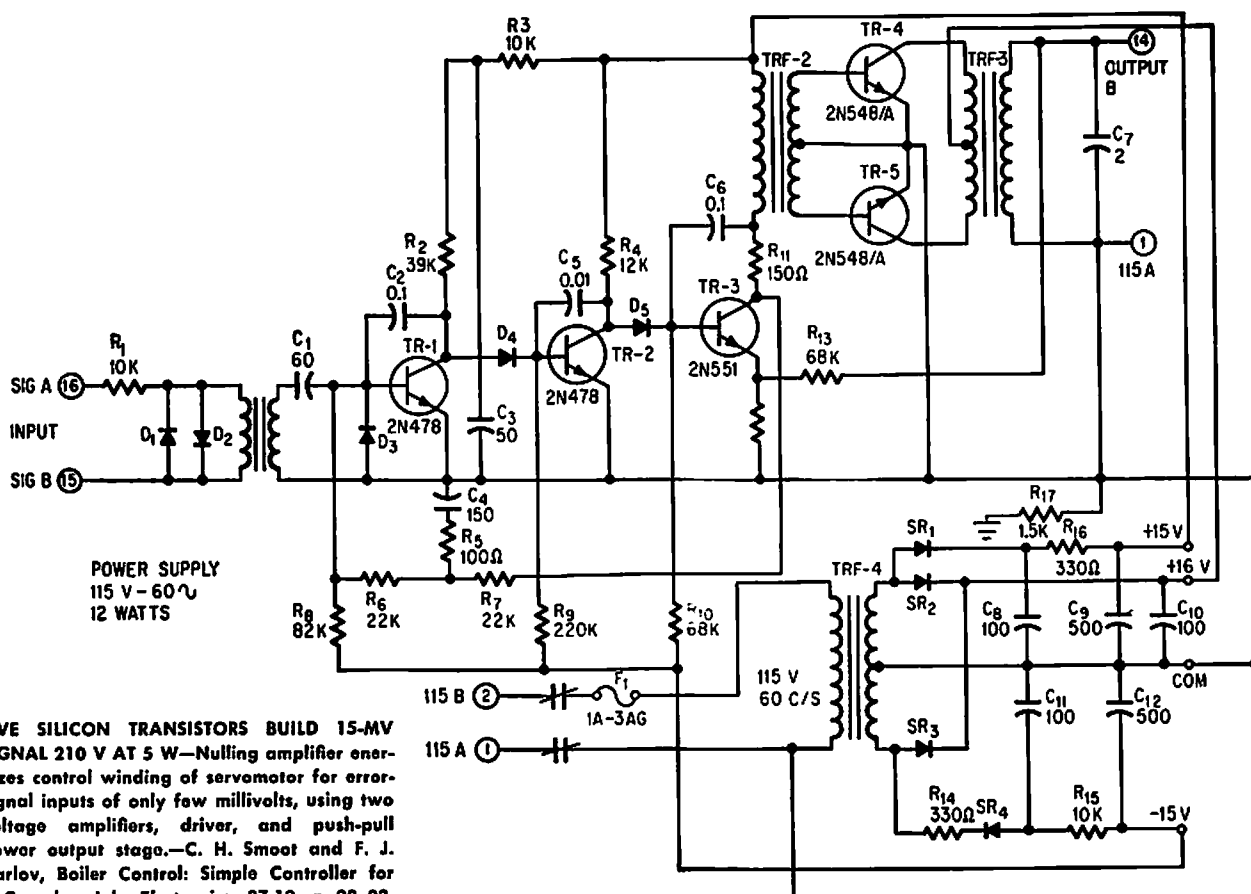
speed is picked off winding of servomotor to improve stability in stopping at point

of zero error voltage in output of 6AL5 frequency discriminator.—K. Makino and T. Yamanaka, Servo-Tuned Transceiver for Airborne VHF Communications, *Electronics*, 35:1, p 82-85.



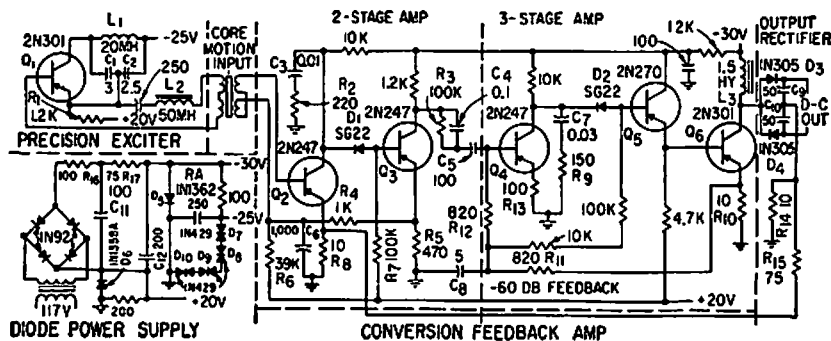
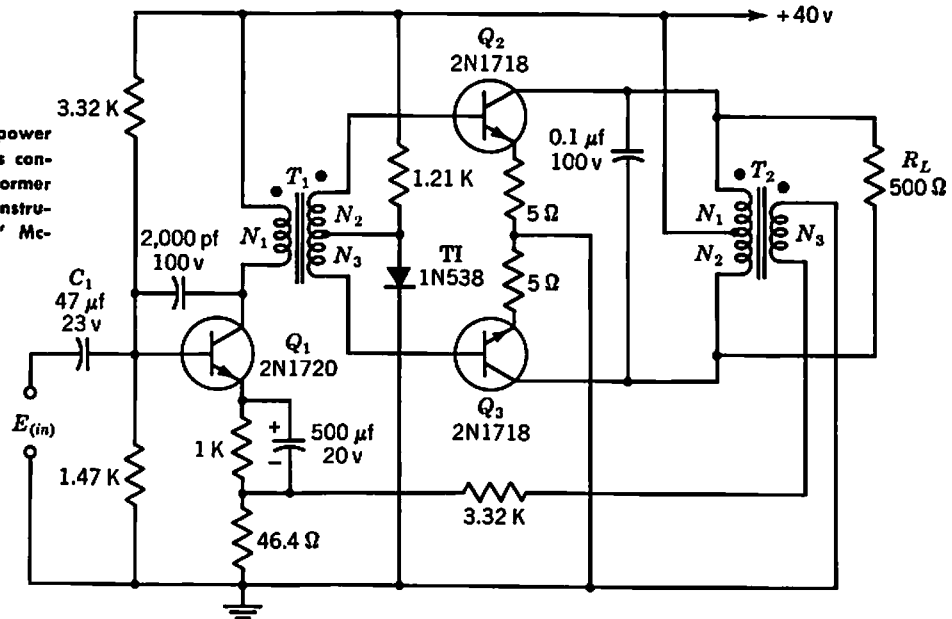
7.5-W CLASS B SERVO AMPLIFIER—Gives power gain of 45 db. Voltage amplification is constant within 2 db of 44 db. Trans-

former data is for 400-cps operation.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 242.

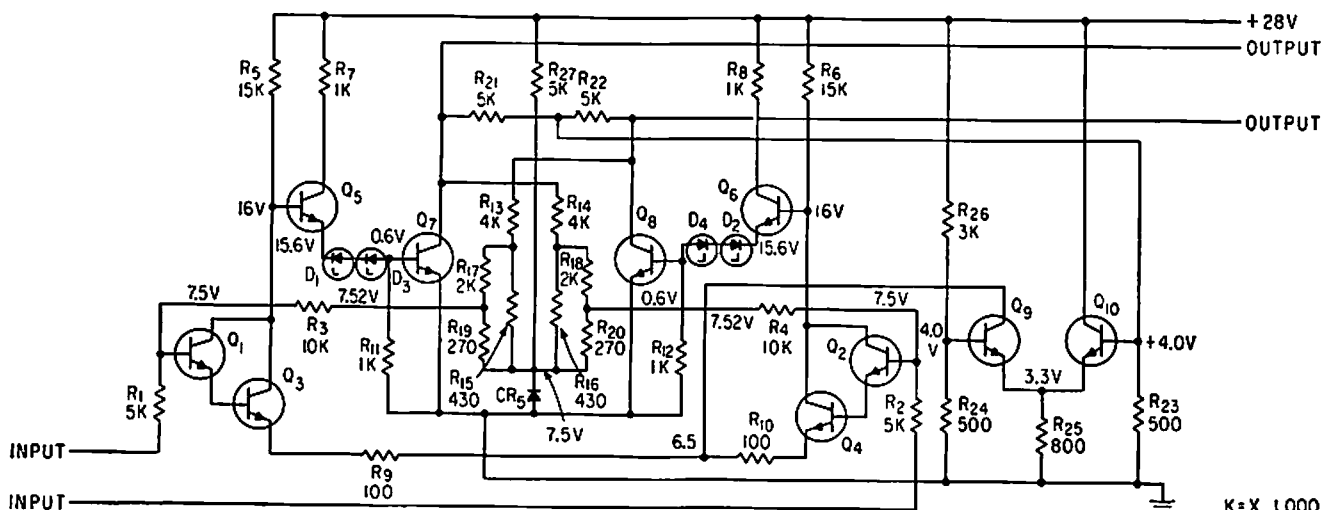


FIVE SILICON TRANSISTORS BUILD 15-W SIGNAL 210 V AT 5 W—Nulling amplifier energizes control winding of servomotor for error-signal inputs of only few millivolts, using two voltage amplifiers, driver, and push-pull power output stage.—C. H. Smoot and F. J. Karlov, Boiler Control: Simple Controller for a Complex Job, *Electronics*, 37:18, p 88-93.

4-W CLASS B SERVO AMPLIFIER—Gives power gain of 42 db. Voltage amplification is constant within 2.5 db of 42.5 db. Transformer data is for 400-cps operation.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 241.



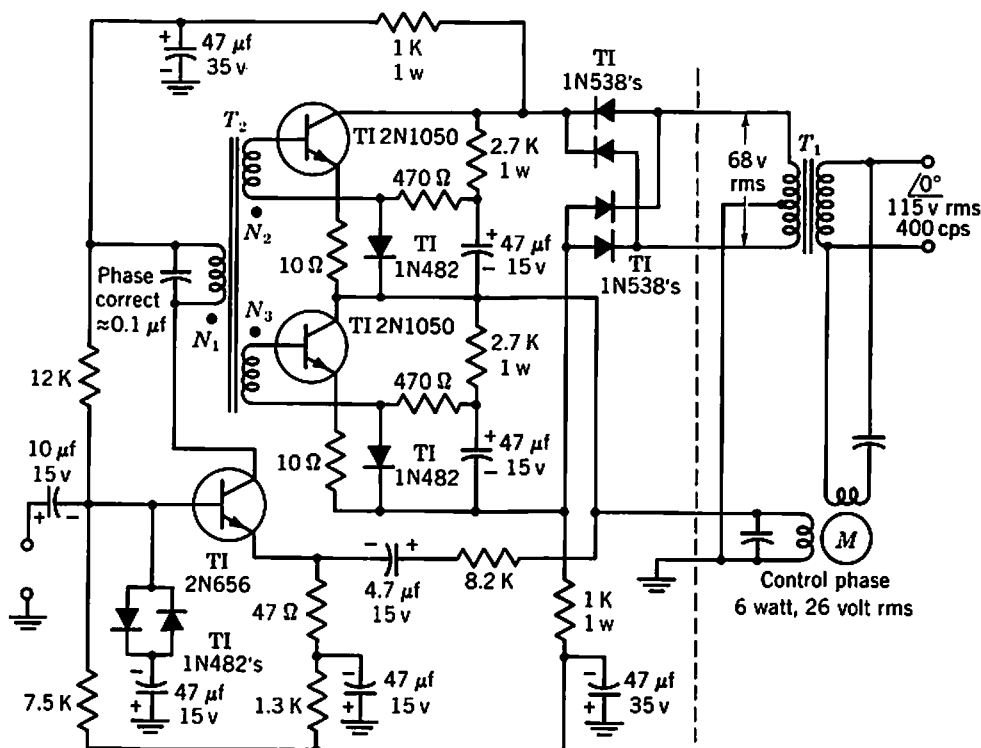
DIFFERENTIAL-TRANSFORMER TRANSDUCER—Detects and responds with 0.1% linearity to core displacement. Low-level a-c transformer output is converted to 10 to 50 ma d-c transmission signal with 1 w maximum power by high-input-impedance feedback amplifier. Precision exciter consists of constant-voltage 1-kc oscillator and high-Q swamping choke. Gain is stabilized by using separate d-c feedback loop for each group of d-c coupled transistors.—L. H. Dulberger, Constant-Current Technique Cuts Servo Response Time, *Electronics*, 32:28, p 52-54.



DARLINGTON-PAIR SERVO AMPLIFIER—Open-loop gain of differential forward amplifier Q1 through Q8 is over 2,000 and closed-loop gain is 200. Signal across output of common-

mode feedback amplifier Q9-Q10 is differentially summed by R21-R22 to cancel a-c components, while d-c component is amplified and applied to emitters of differential-input

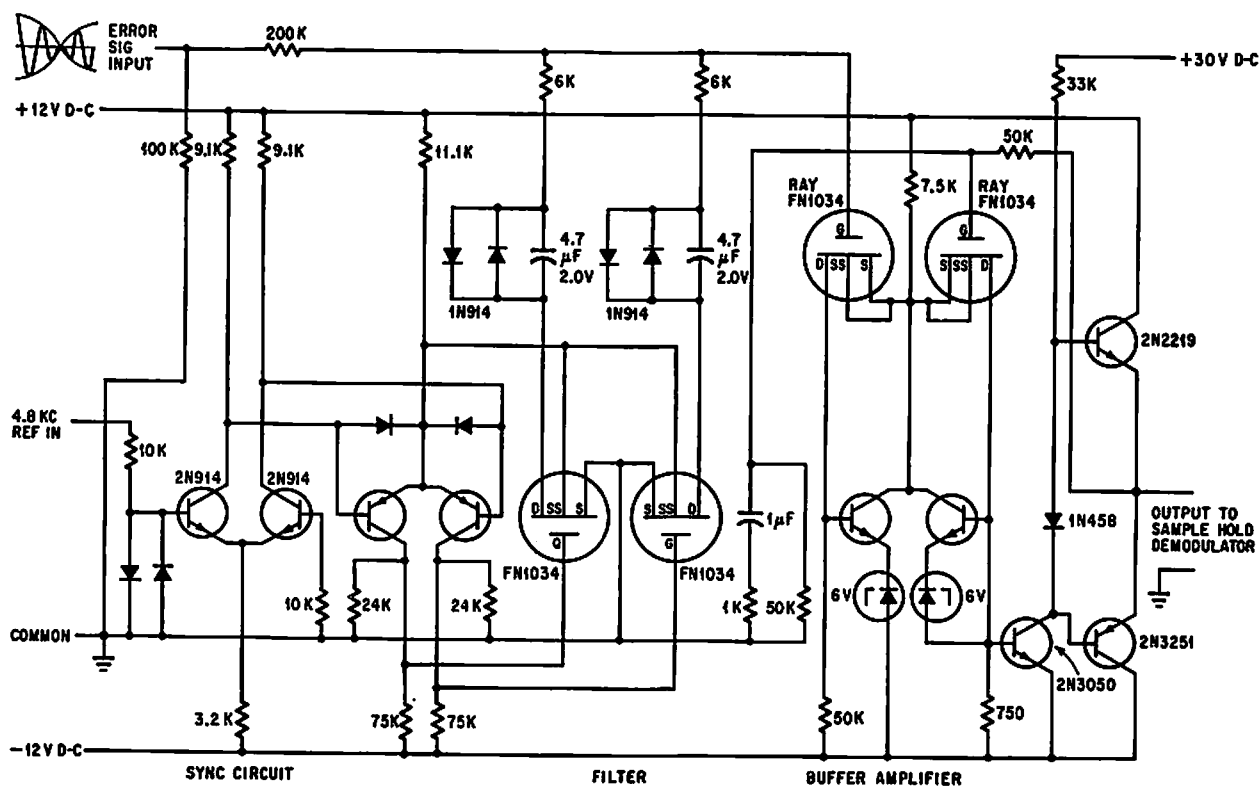
Darlington pair.—M. W. Aarons, Putting a Servo Amplifier on a Small Silicon Wafer, *Electronics*, 35:52, p 33-35.



6-W HIGH-EFFICIENCY AMPLIFIER—Overall efficiency is 55%. Design equations are given. —Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 249.

Transformers

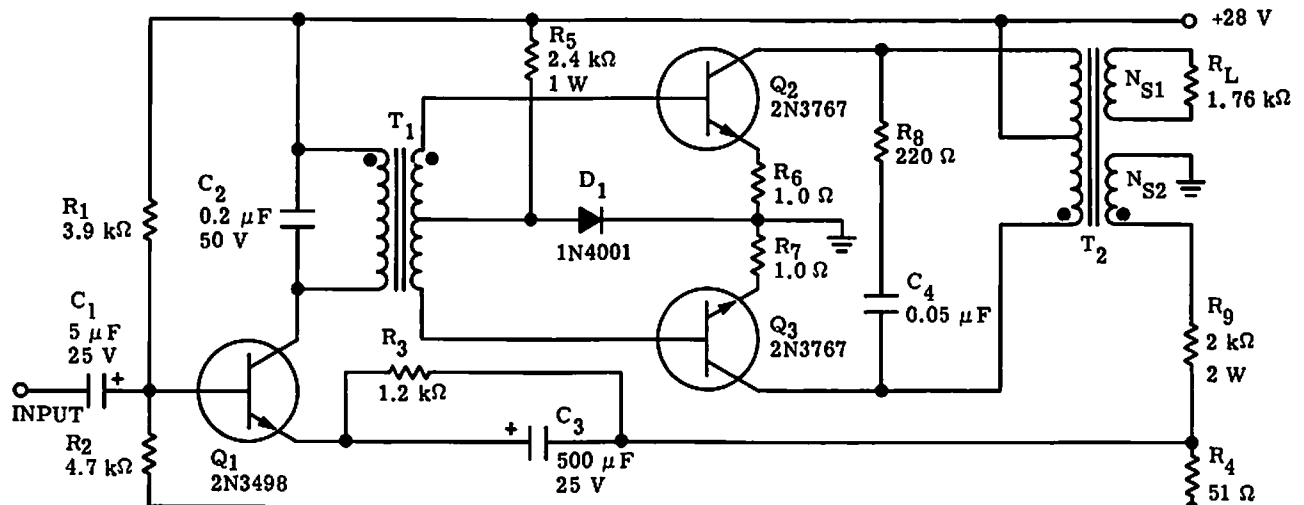
T_1 400 cps 12-watt power transformer step-down 115 volt to 68 volt c.t.
 T_2 400 cps 65-mw driver transformer. Turns ratio $N_1 : N_2 : N_3 = 2 : 1 : 1$
Primary current = 10 ma d-c. Primary inductance = 1.5 hy.



SERVO FREQUENCY COMPENSATION—Performs frequency compensation in servo system by operating on modulation envelope of amplitude-modulated suppressed-carrier sig-

nal. Hybrid construction, replacement of linear circuits with switching circuits, and substitution of active filters for large L-C filters reduce size and weight.—F. A. Plam-

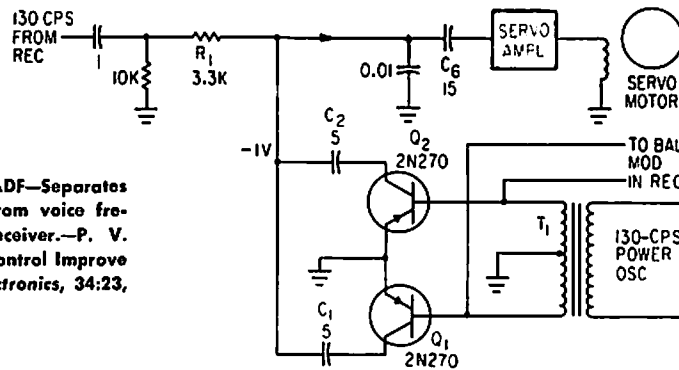
enos, The Packaging Revolution, Part VI: Converting to Microelectronics, Electronics, 39:4, p 103-109.



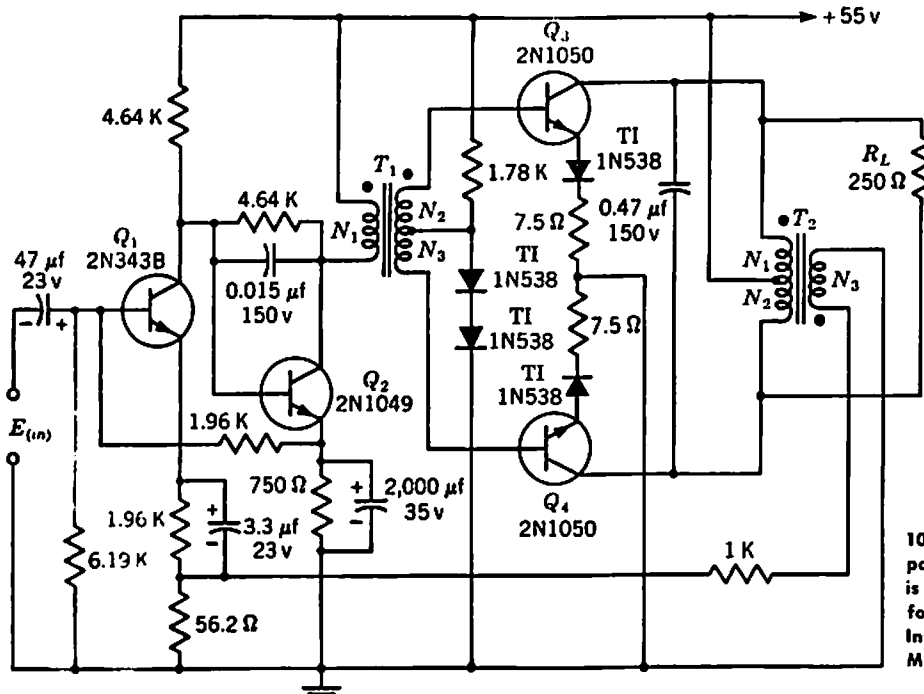
TRANSFORMER-COUPLED SERVO AMPLIFIER—Three transistors provide stable voltage gain of 40 db and power gain of 37 db.

Maximum output is 115 v rms into 1,760-ohm load, for 7.5 w.—N. Freyling, "High Performance All Solid-State Servo Amplifiers,"

Motorola Application Note AN-225, Jan. 1966.

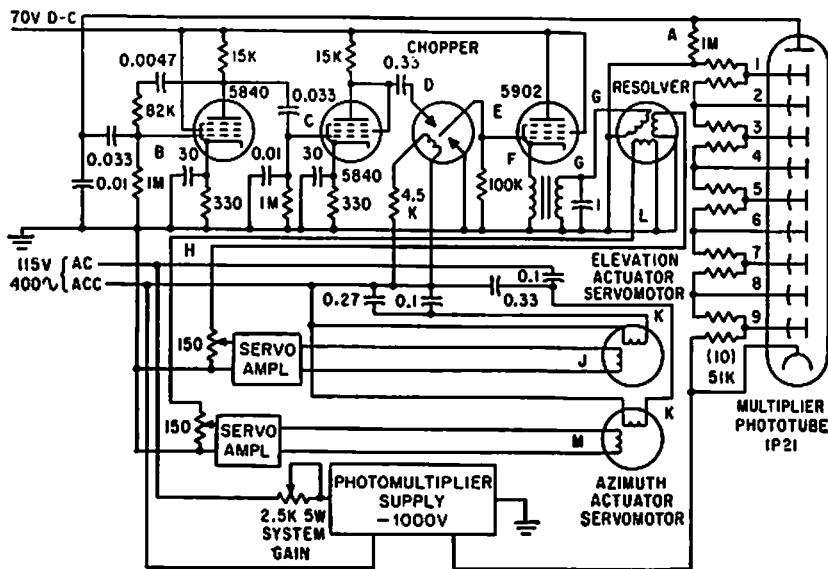
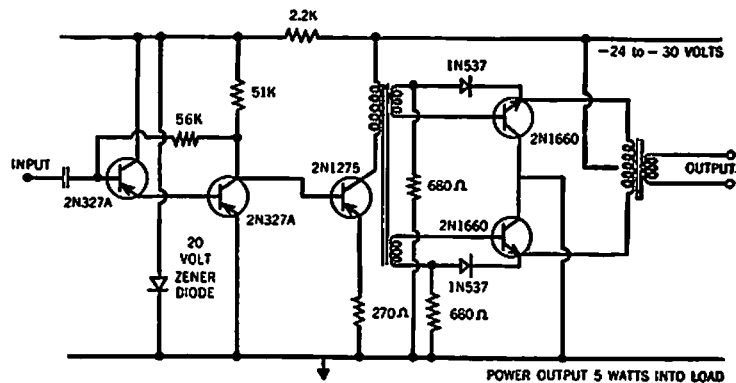


SYNCHRONOUS FILTER FOR ADF—Separates 130-cps motor drive voltage from voice frequencies in output of adf receiver.—P. V. Sparks, Servo Filter and Gain Control Improve Automatic Direction Finder, *Electronics*, 34:23, p 110-113.



10-W CLASS B SERVO AMPLIFIER—Gives power gain of 39 db. Voltage amplification is constant within 1 db of 32.5 db. Transformer data is for 400-cps operation.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 243.

LOW-POWER DRIVER—Pair of high-voltage, high-gain silicon power transistors gives 5 w output from -55 to $+125^{\circ}\text{C}$ when driven by 250-mw 2N1275 transistor.—New High Voltage, High Gain Transistors (Raytheon Ad), *Electronics*, 33:35, p 42.



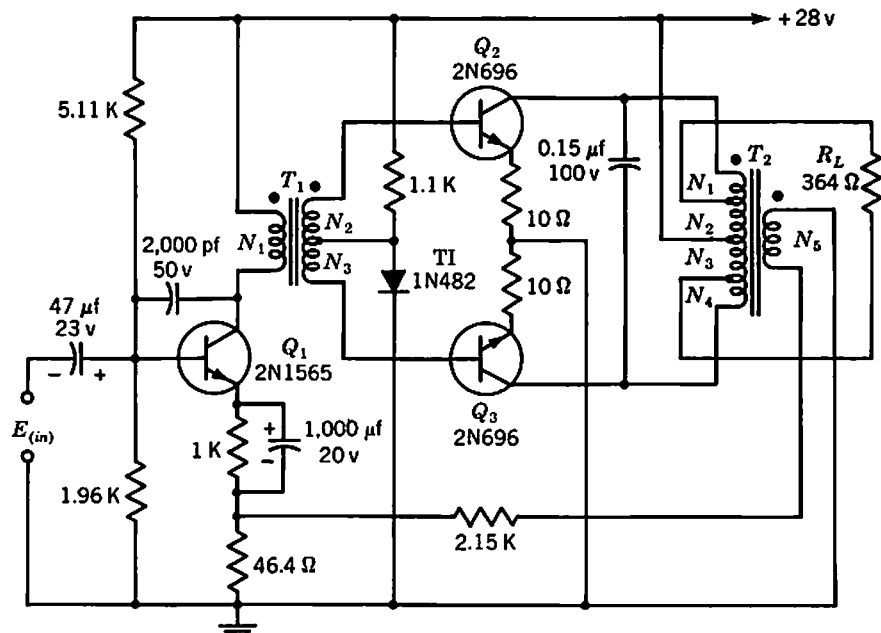
SERVO-CONTROLLED GAIN—Gain is controlled by varying photomultiplier input voltage, permitting photoelectric system to track brightness range from remote stars to moon.—W. J. Wichman and M. M. Birnbaum, *Servo System Design for Balloon-Borne Star Trackers*, *Electronics*, 34:35, p 43-46.

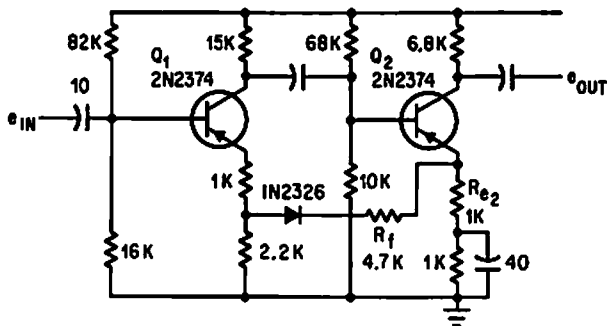
Transformer data

T_1 — $N_1 = 2050$ turns No. 35 AWG. $N_2 = N_3 = 466$ turns No. 29 AWG, bifilar wound. Core: Magnetic Metals 75 EI, SL-14, or equivalent, butt-jointed.

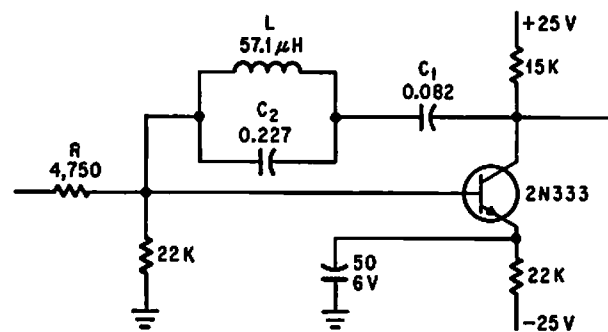
T_2 — $N_1 = N_4 = 90$ turns No. 29 AWG. $N_2 = N_3 = 433$ turns No. 29 AWG, bifilar wound. $N_5 = 303$ turns No. 38 AWG. Core: Magnetic Metals Carpenter 49, 0.006-in. 375 EI or equivalent, 8×8 interleaved.

1.5-W CLASS B SERVO AMPLIFIER—Gives power gain of 38 db. Voltage amplification is constant within 1.5 db of 40.5 db. Transformer data is for 400-cps operation.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 240.

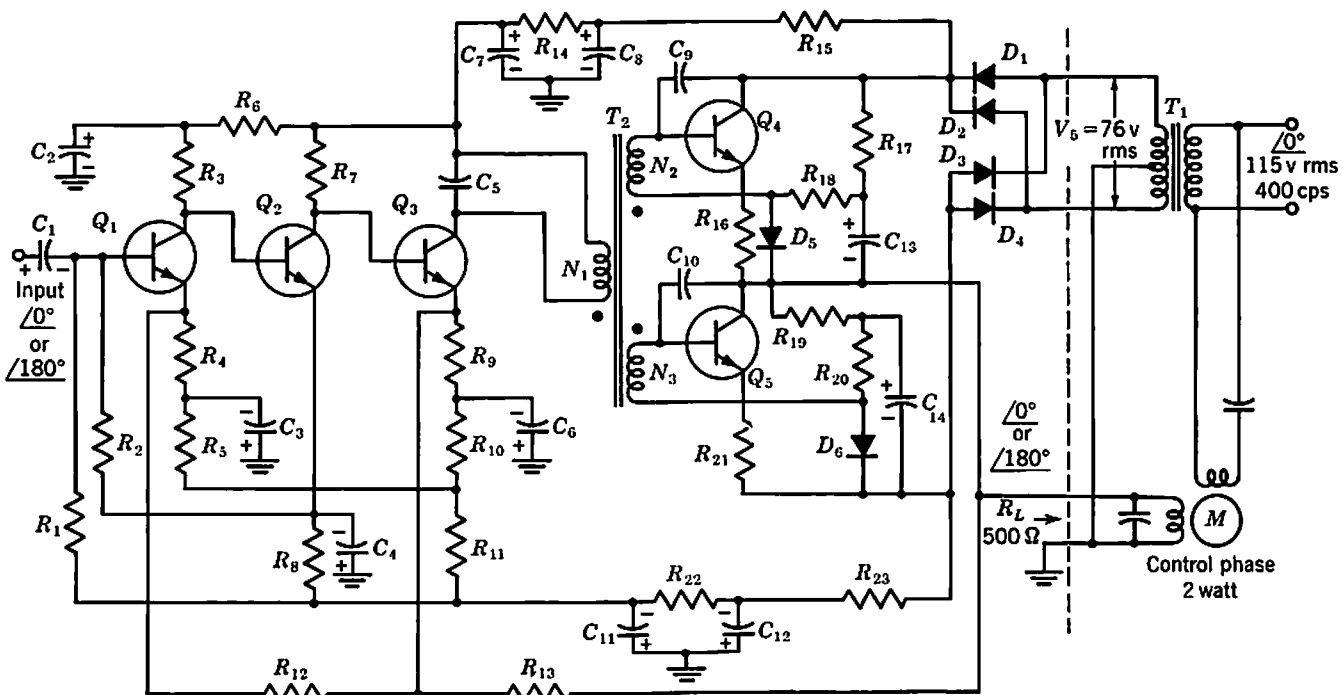




COMBINED NEGATIVE-POSITIVE FEEDBACK—Two-stage common-emitter amplifier permits use of positive feedback to R_f along with negative feedback, to give stability factor of 5 and overall gain of 47 db with input impedance of 10,000 ohms for small-pulse amplification in servo system.—N. A. Wade, Combined Feedback Stabilizes Amplifier, *Electronics*, 37:15, p 76.



400-CPS SERVO AMPLIFIER SUPPRESSES THIRD HARMONIC—Single-transistor operational amplifier for 400-cps servosystems gives accurate 90-deg phase shift at carrier frequency and open-loop gain of 34 db. Circuit resonance at 1,200 cps keeps third harmonic 20 db down.—M. Schmidt, Operational Amplifier Suppresses Third Harmonic, *Electronics*, 35:13, p 74.



Parts List

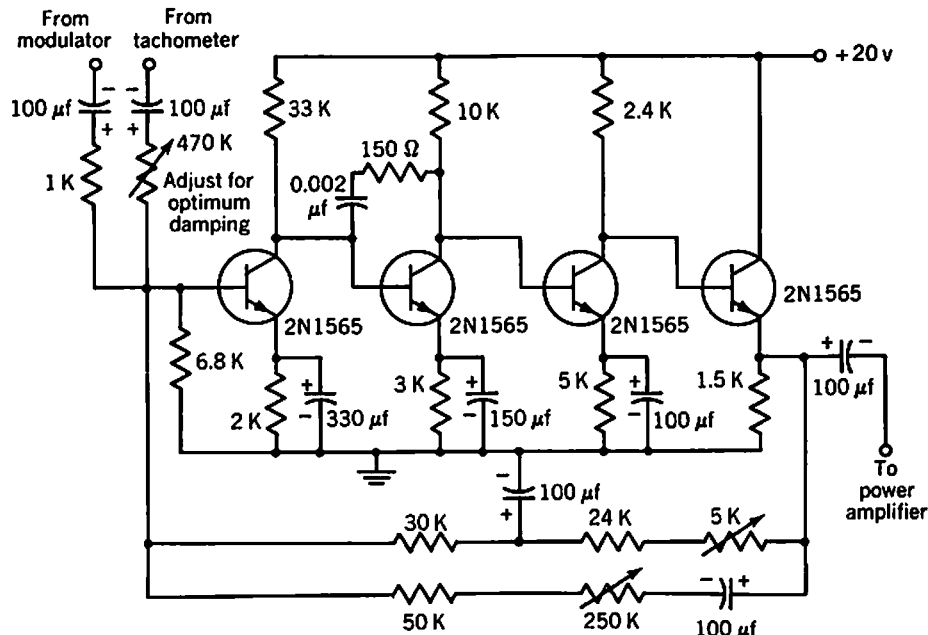
Resistors	Ohms	Watts	Resistors	Ohms	Watts	Transistors	Transformers
R_1	68 K	1/4	R_{10}, R_{15}, R_{23}	1.5 K	1/2	Q_1, Q_2	T_1 400-cps 4-watt power transformer.
R_2, R_3	22 K	1/4	R_{11}	680	1/4	Q_3	Step-down 115-76-volt center-tapped.
R_4	39	1/4	R_{13}	4.7 K	1/4	Q_4, Q_5	T_2 400-cps 50-mw driver transformer:
R_5	2.2 K	1/4	R_{14}, R_{22}	390	1/4		Turns ratio: $N_1:N_2:N_3 = 3.7:1:1$.
R_6, R_{12}	15 K	1/4	R_{16}, R_{21}	15	1/4		Primary current = 5 ma d-c. Primary inductance ≈ 1.5 henrys.
R_7	10 K	1/4	R_{17}, R_{19}	3.3 K	2		
R_8	5.1 K	1/4	R_{18}, R_{20}	470	1/4		
R_9	100	1/4					
						Capacitors	Diodes
						C_1	D_1, D_2, D_3, D_4 1N645
						C_2, C_3, C_4, C_6	D_5, D_6 1N482
						C_5^*	
						C_7, C_8, C_{11}, C_{12}	
						C_9, C_{10}	
						C_{13}, C_{14}	

* Value depends on primary inductance of T_2 .

COMPLETE 2-W SERVO AMPLIFIER—Includes direct-coupled preamplifier and driver stages, with considerable d-c feedback to stabilize

bias conditions. Voltage gain of amplifier with feedback loop closed is 10,000. Overall efficiency is 50%. Input impedance is 10,000

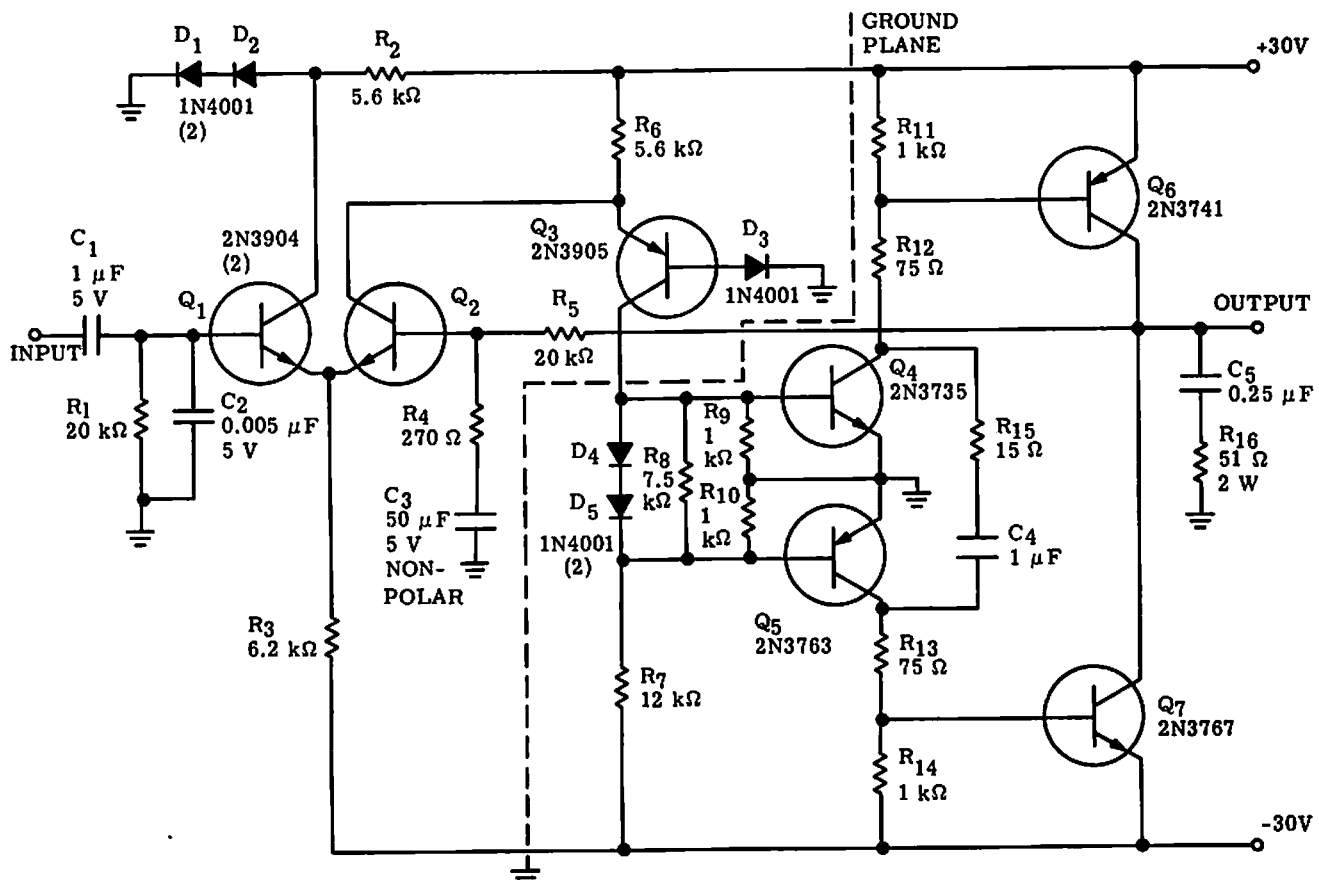
ohms and output impedance is 150 ohms.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 247.



OPERATIONAL PREAMPLIFIER—Used to sum modulator and tachometer outputs and provide signal for power amplifier that drives

split-phase motor. Adjustable overall d-c feedback insures equal clipping when amplifier is overloaded, so squared output will

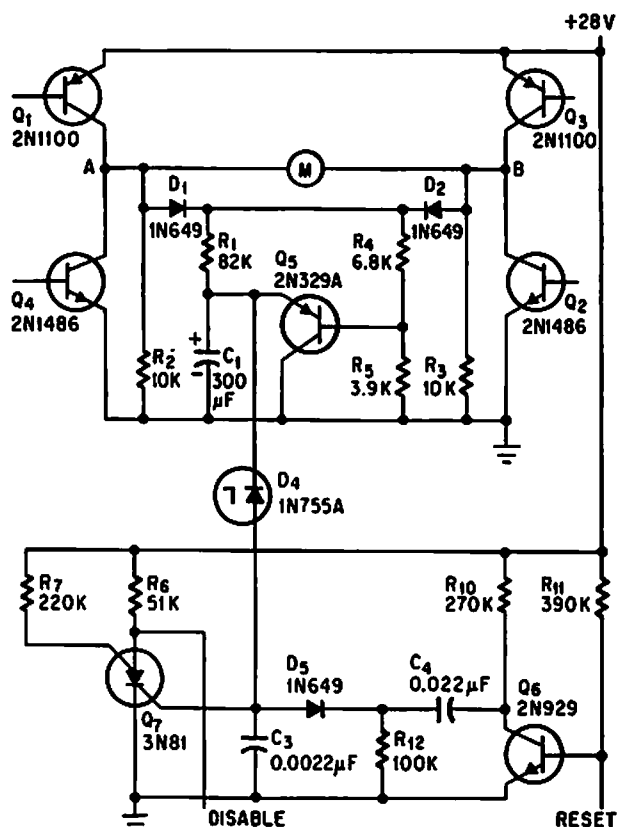
have equal mark-space ratio.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 493.



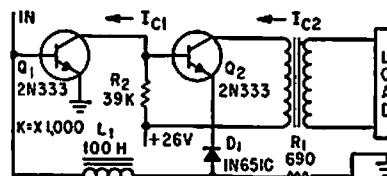
COMPLEMENTARY-TRANSISTOR SERVO AMPLIFIER—Use of direct coupling eliminates transformers. For d-c loads, C1 and C3

must be shorted. Will drive 20 v rms into 40-ohm load, giving 10 w. Voltage gain is 37 db and power gain is 60 db.—N. Frey-

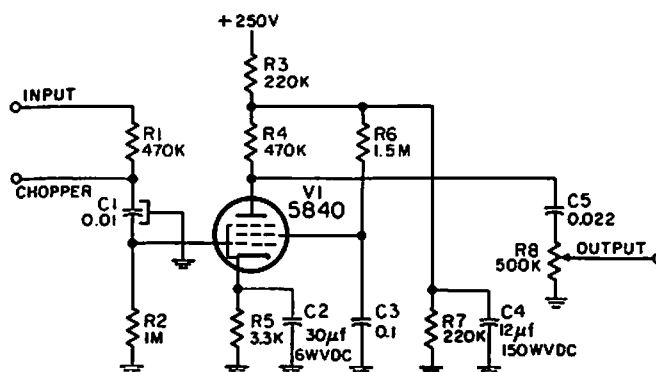
ling, "High Performance All Solid-State Servo Amplifiers," Motorola Application Note AN-225, Jan. 1966.



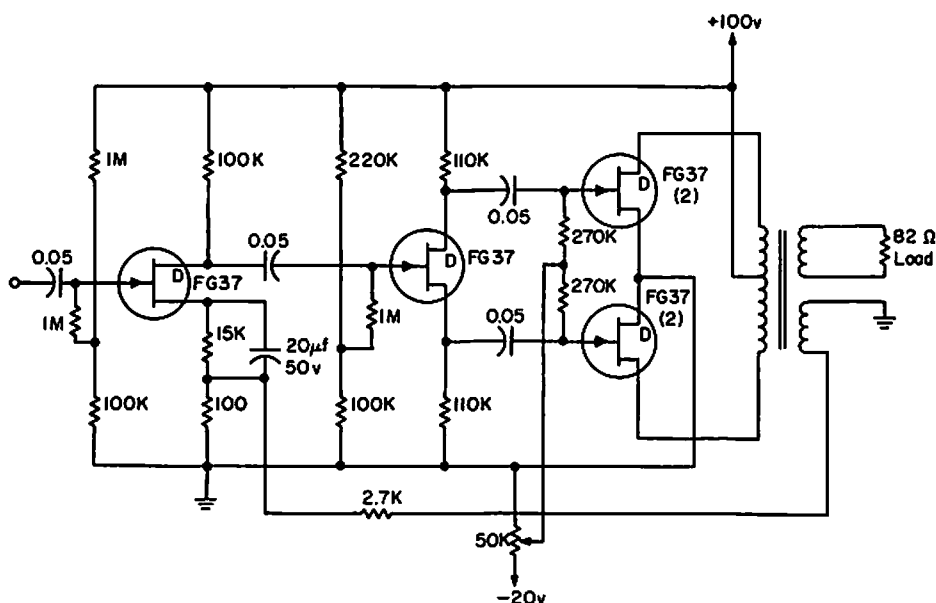
STALLED SERVO MOTOR SHUTOFF—Silicon controlled switch Q7 in timing circuit turns on each time servo motor is actuated, and removes power from motor if it remains on more than 15 sec, indicating a stall.—D. Perlman, *Silicon Switch Turns Off Stalled Servomotors*, *Electronics*, 39:10, p 90-91.



DIRECT-COUPLED PREAMP—Direct-coupled silicon-transistor amplifier uses zener diode to provide constant voltage, and has adequate d-c stability even with transistors having beta range of 3:1.—A. N. Desautels, *Servo Preamplifiers Using Direct-Coupled Transistors*, *Electronics*, 32:20, p 74.

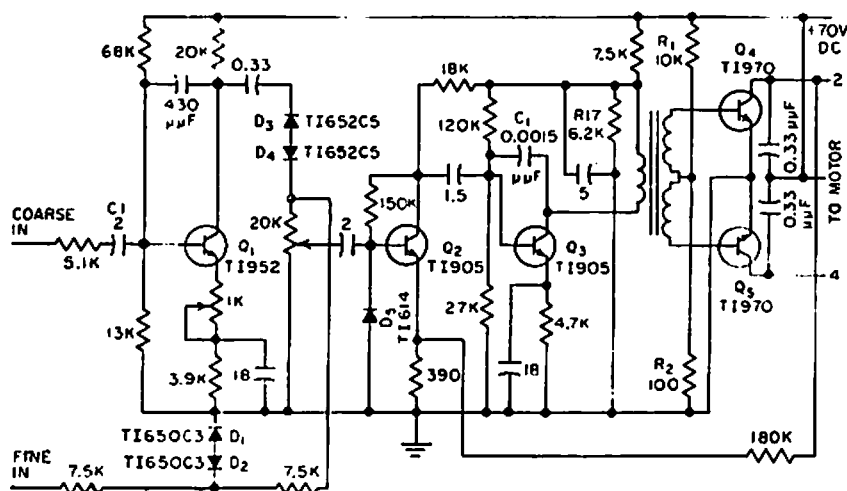


PREFERRED AMPLIFICATION-70 PREAMPLIFIER—Used with instrument servo motor controller to increase available gain. Chopper is used with d-c inputs only. Frequency range is 380 to 420 cps.—NBS, *Handbook Preferred Circuits Navy Aeronautical Electronic Equipment*, Vol. 1, *Electron Tube Circuits*, 1963, PC 72, p 72-2.

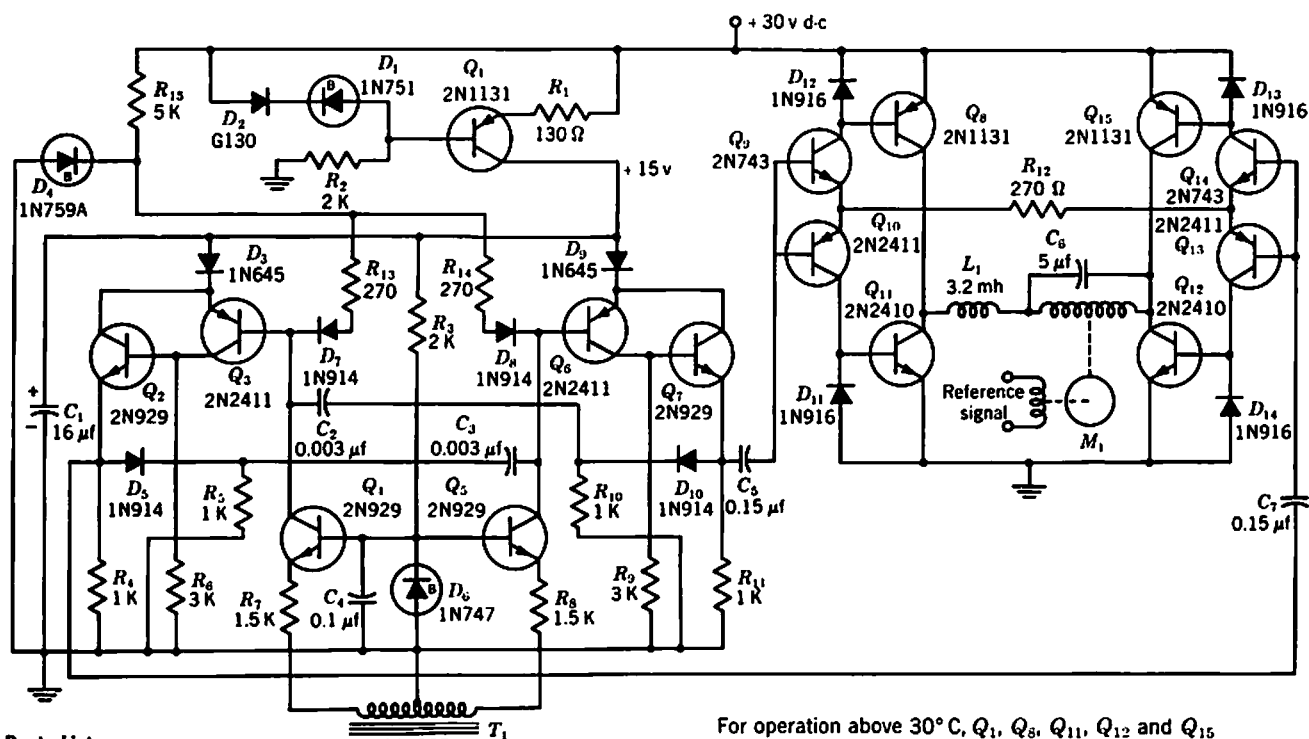


FET SERVO AMPLIFIER—Servo amplifier uses medium-power fet's for 1.5 w output. Circuit has no driver transformer for power stage, and only one electrolytic. Power gain

is 70 db, voltage amplification 30 db, input resistance 1 meg, and maximum efficiency 56%.—L. J. Sevin, Jr., *Field-Effect Transistors*, McGraw-Hill, N.Y., 1965, p 100.



TWO-SPEED SERVO—Directly drives size 11 motor, eliminating need for output transformer. Zener diodes provide switching between fine and coarse signals for two speeds. Maximum coarse and fine signal input is 26 v rms at 400 cps. Voltage gain of coarse amplifier Q1 is 25, and gain of feedback amplifier Q2-Q3-Q4-Q5 is 460.—B. E. Orr, Direct Drive Amplifier for Two-Speed Servos, *Electronics*, 31:11, p 146-147.



Parts List

Resistors

	Kilohms
R ₁	130 ohms
R ₂ , R ₃	2
R ₄ , R ₅ , R ₁₀ , R ₁₁	1
R ₆ , R ₉	3
R ₇ , R ₈	1.5
R ₁₂ , R ₁₃ , R ₁₄	270 ohms
R ₁₅	5

Transistors

Q ₁ , Q ₈ , Q ₁₅	2N1131
Q ₂ , Q ₄ , Q ₅ , Q ₇	2N929
Q ₃ , Q ₆ , Q ₁₀ , Q ₁₃	2N2411
Q ₉ , Q ₁₄	2N743
Q ₁₁ , Q ₁₂	2N2410

Diodes

D ₁	1N751
D ₂	G130
D ₃ , D ₉	1N645
D ₄	1N759A
D ₅ , D ₇ , D ₈ , D ₁₀	1N914
D ₆	1N747
D ₁₁ , D ₁₂ , D ₁₃ , D ₁₄	1N916

Transformer

T ₁	Step-down 117-12.6-volt
----------------	-------------------------

For operation above 30°C, Q₁, Q₈, Q₁₁, Q₁₂ and Q₁₅ must be placed in heat sinks.

R₁ should be adjusted to bring the collector of Q₁ to +15 volts.

Capacitors

	μf
C ₁	16
C ₂ , C ₃	0.003
C ₄	0.1
C ₅ , C ₇	0.15
C ₆	5

Inductor

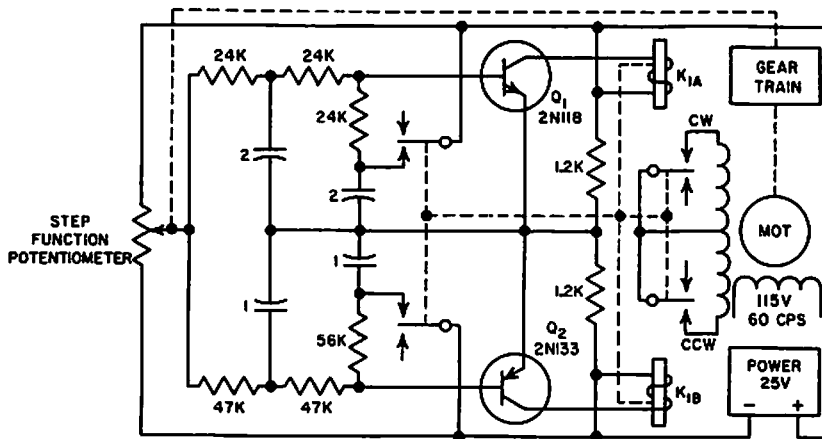
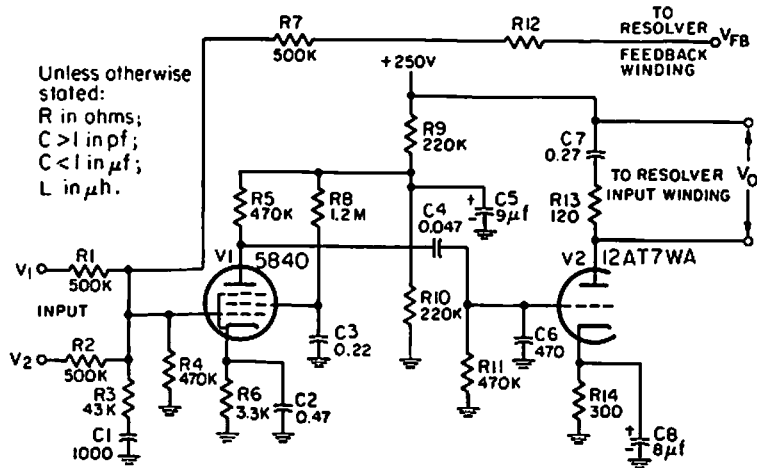
L ₁	3.2 mh
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Miscellaneous

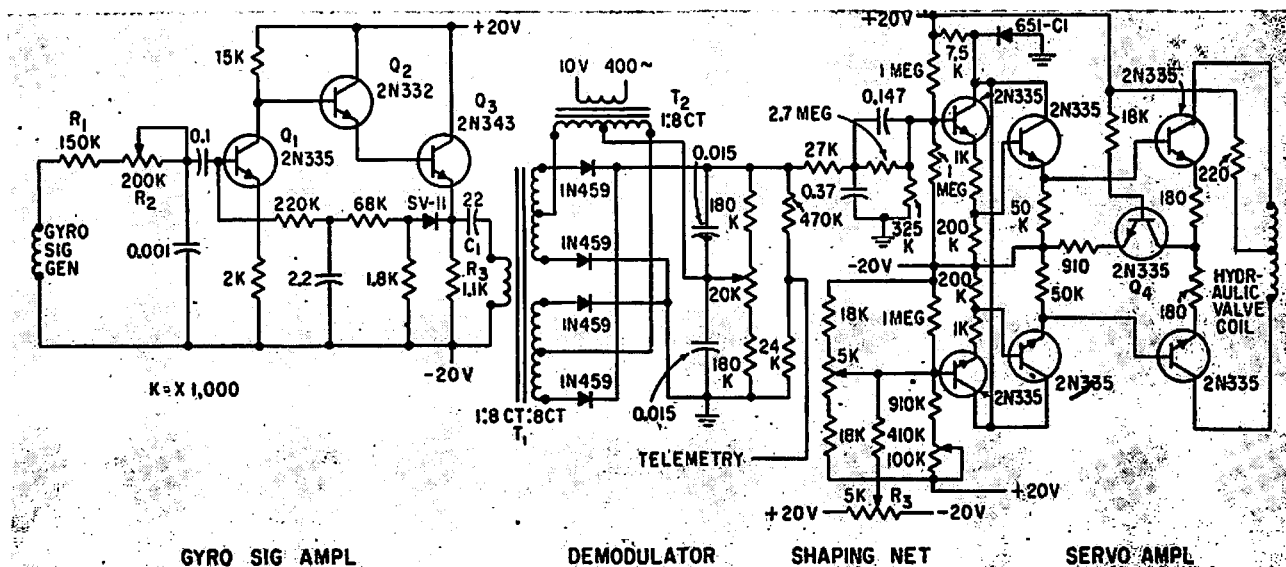
M ₁	John Oster Manufacturing Co. type 11-5101-03
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SWITCHING-MODE SERVO AMPLIFIER—Input signal is used to control duration of relatively constant repetition-rate pulse signal, to give pulse-duration modulation. Repetition rate is 10 kc. Switching-mode output stage supplies power to control winding of two-phase servo motor.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 483.

PREFERRED RESOLVER DRIVER—A-c operational amplifier is used as isolation amplifier, employing feedback that includes compensating winding of a-c resolver being driven. Useful as computing element for coordinate conversion, coordinate rotation, and resolution of vectors when accuracy of 0.5% is sufficient. Component values given are for 500 cps, with Mark 4 Mod 0 resolver.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 79, p 79-2.



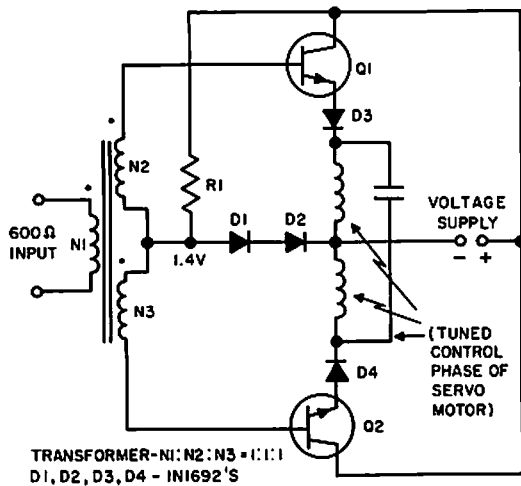
ON-OFF RELAY SERVO—Step-function potentiometer provides on-off characteristic of null detector. Easily adjusted damping is applied through differential relay contacts to eliminate oscillations. Fast response to small angular displacements assures close following. Tapping positive voltage off step-function potentiometer causes current flow through Q1, operation of relay K1A, and upward movement of arms. Negative voltage moves arms downward, reversing motor travel.—S. Shenfeld, Transistors Reduce Relay Servo Size, *Electronics*, 31:33, p 74-77.



PITCH AND YAW CHANNELS—Identical channels amplify, demodulate, and shape gyro signal to feed direct-coupled differential

servo amplifier whose output differential current goes to dual-coil hydraulic control valve in rocket.—R. E. King and M. Low, *Solid-State*

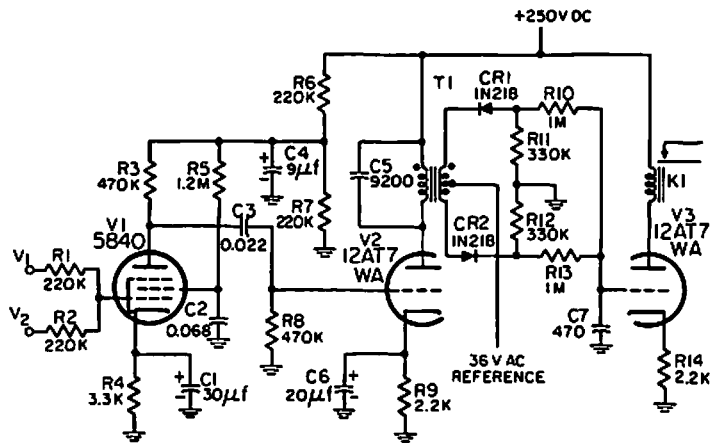
Guidance For Able-Series Rockets, *Electronics*, 33:5, p 60-63.



4-W SERVO MOTOR DRIVE—Emitter-follower (common collector) push-pull amplifier gives stable output stage gain along with low-impedance drive for 1 to 4-w servo motors. Forward bias of 1.4 v is developed across D1 and D2, while D3 and D4 protect transistors from inductive load generated voltages that exceed emitter-base breakdown. Efficiency is better than 60%.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 223.

Q1 = Q2 = G.E. 2N2202
2N2203
2N2204
2N2196
2N2197
7F2
OR 7F4

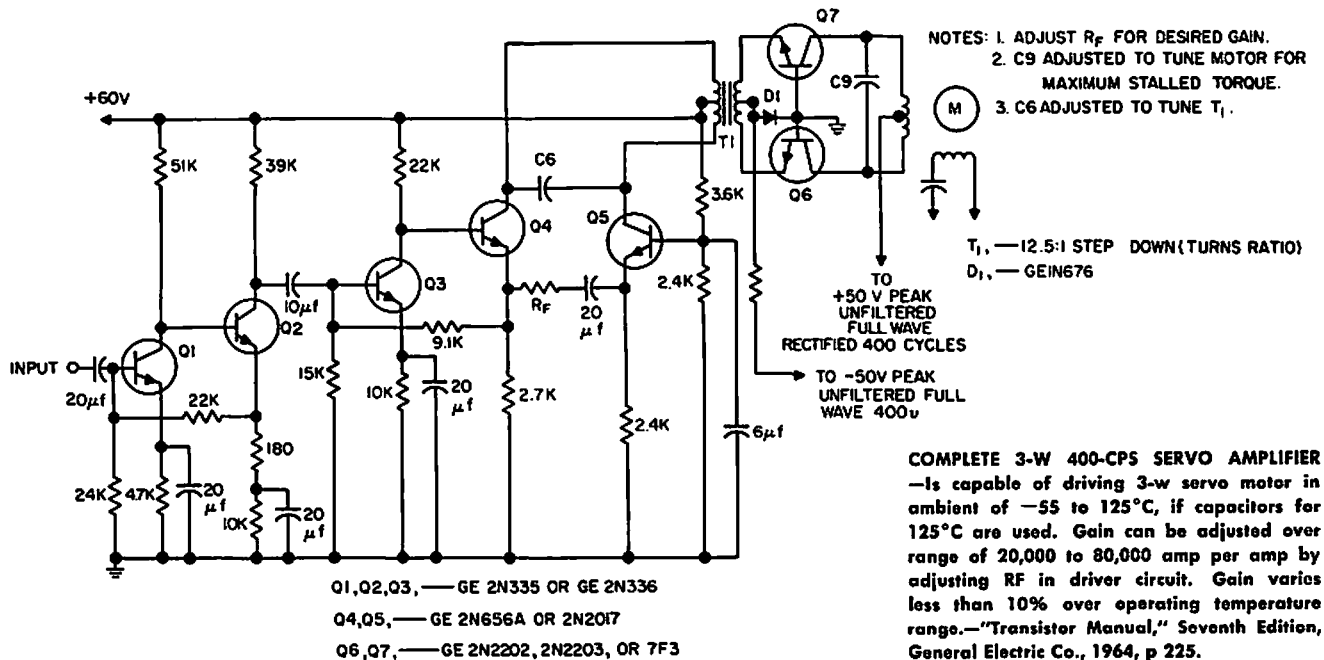
PREFERRED PHASE-SENSITIVE NULL DETECTOR
—Operates d-c relay when sum of input currents is zero. Circuit can be adjusted for any operating frequency from 300 to 1,000 cps by selection of C5 and C7; values given are for 500 cps. Consists of a-c amplifier, phase-sensitive detector, and relay control tube.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 78, p 78-2.



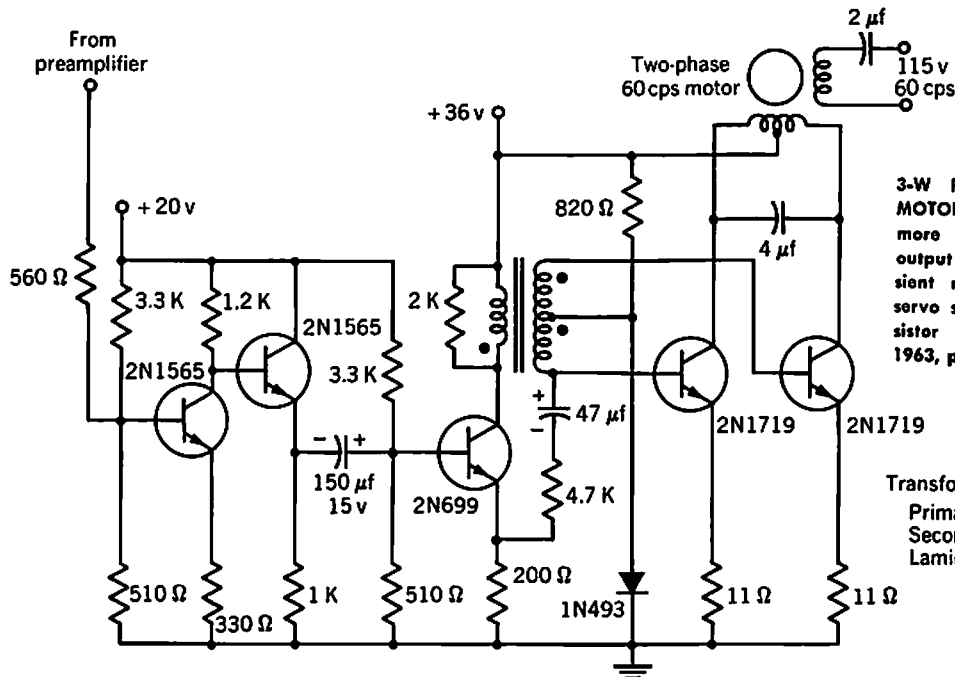
PREAMPLIFIER

DRIVER

OUTPUT



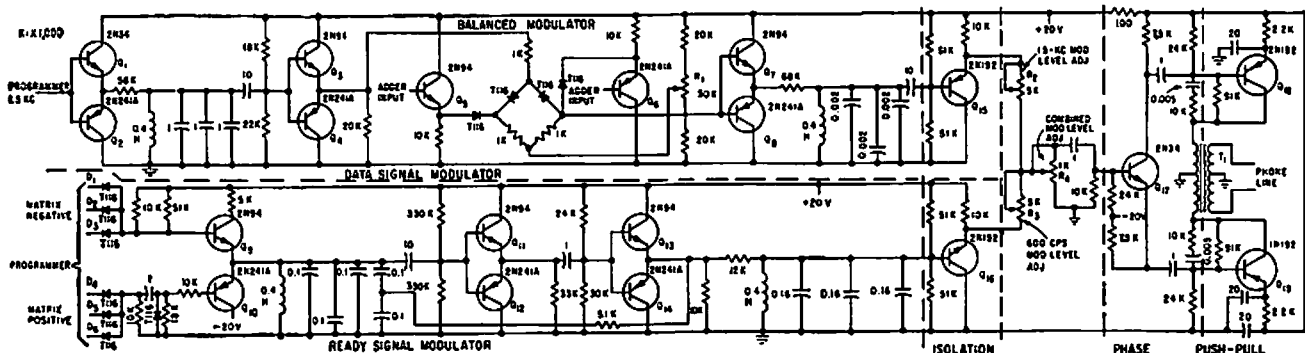
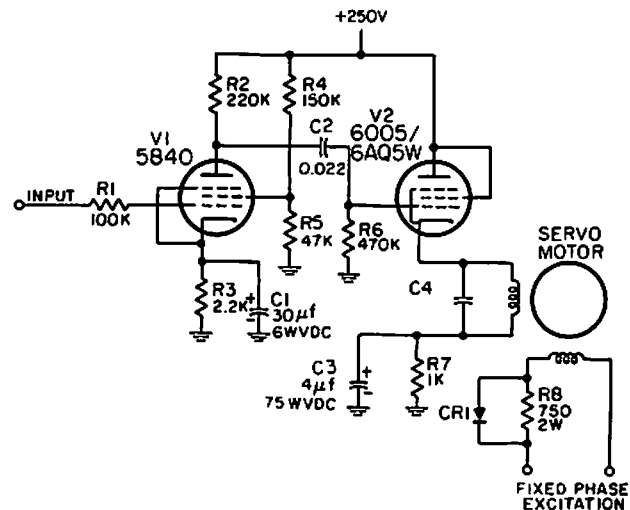
COMPLETE 3-W 400-CPS SERVO AMPLIFIER
—Is capable of driving 3-w servo motor in ambient of -55 to 125°C , if capacitors for 125°C are used. Gain can be adjusted over range of 20,000 to 80,000 amp per amp by adjusting R_F in driver circuit. Gain varies less than 10% over operating temperature range.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 225.



3-W POWER AMPLIFIER FOR SPLIT-PHASE MOTOR—Capacitor tunes motor to make load more resistive. Feedback is used to lower output impedance and thereby improve transient response of motor. Used in digital servo system.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 493.

Transformer:
Primary 1630 T #35
Secondary 1020 T #29 Bifilar wound
Laminations EI 75 SL14 5x5 interleaved

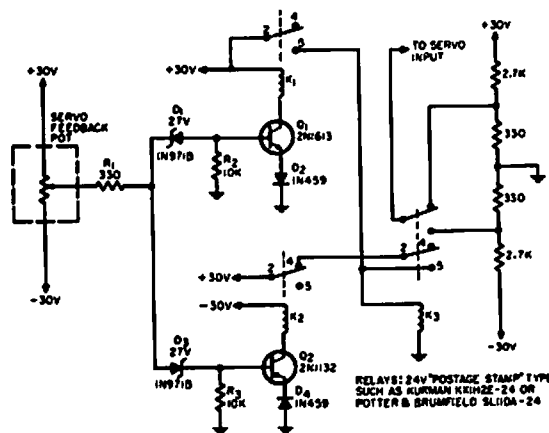
PREFERRED INSTRUMENT SERVO MOTOR CONTROLLER—Used to excite control winding of 2-phase Mark 7 Mod 1 and Mark 14 Mod 0 servo motors. Delivers nominal output of 1 w to loads with effective resistance between 2,000 and 4,000 ohms. Maximum output is 50 v. CR1 is 75-ma silicon rectifier with 70-v reverse working voltage.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 70, p 70-2.



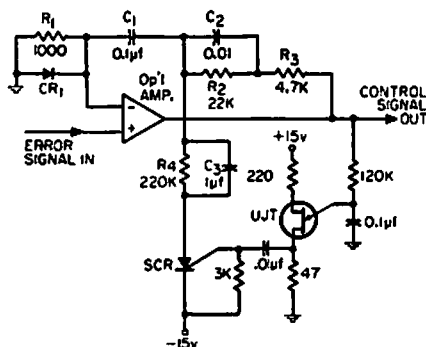
SHAFT POSITION MODULATOR—Used for modulating and mixing 1.5-kc data carrier and 600-cps ready signal in system for trans-

mitting digitally encoded master shaft positions over phone line at 750 bits per second.—R. B. Palmiter, Digital System Positions

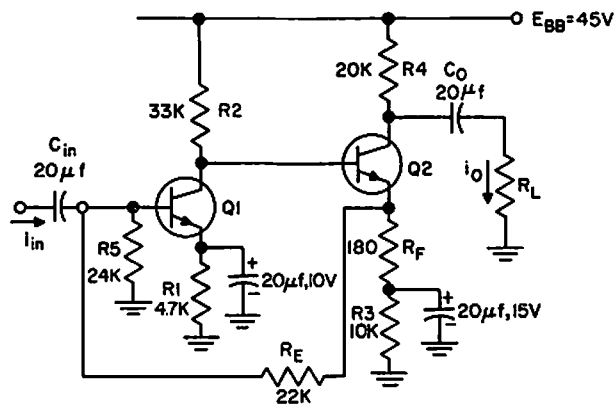
Shafts Over Phone Line, *Electronics*, 32:7, p 62-66.



INSTRUMENT SERVO CYCLING—Used to cycle instrument servo units from stop to stop for extended periods of time, as for determining wear characteristics and friction level changes. Motor drive is applied so servo pot arm is driven toward +30 v. When 27-v breakdown of D1 is exceeded, it conducts and turns on Q1; K1 pulls in, energizing K3, and motor drive reverses. As pot arm approaches -30 v, reversing action occurs again. —P. J. Stein, Instrument Servo Cycling Circuit, *EEE*, 12:9, p 61.



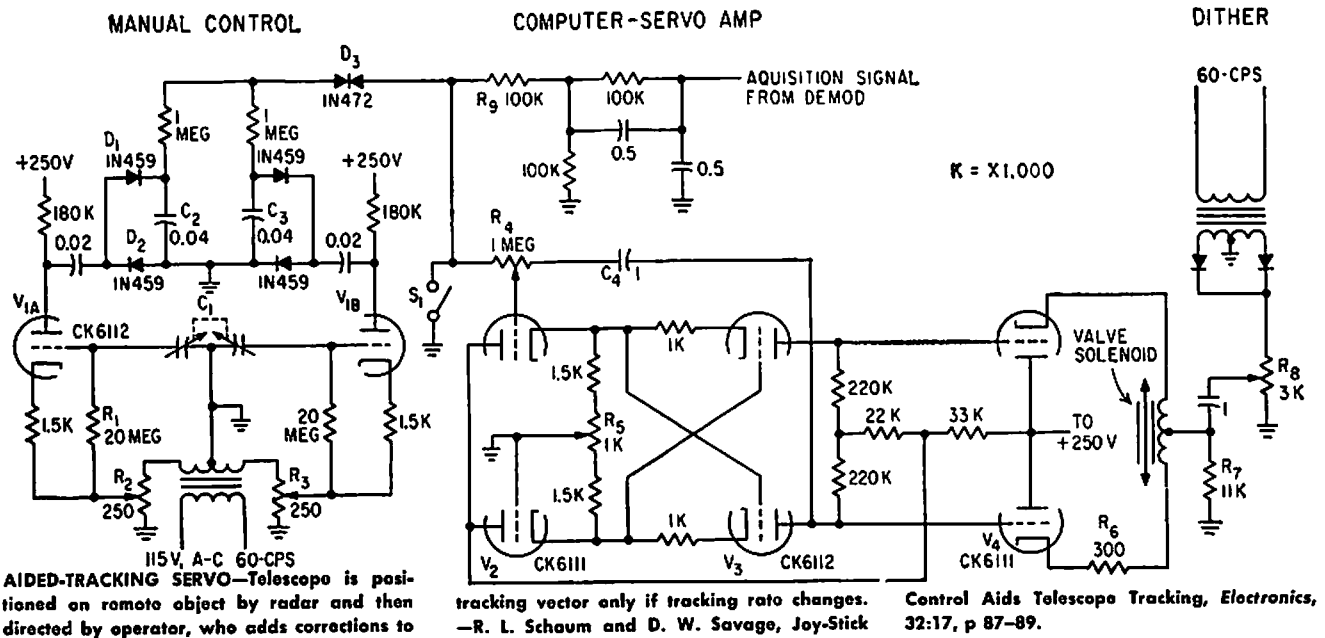
AUTOMATIC SEARCH AND CONTROL—Used in servo control systems when automatic acquisition and linear search are desired, as in afc and phase-lock controls. Basic circuit was used in phase-lock microwave systems having 300-kc bandwidth. Active integrator is used as linear search generator as well as control system integrator. —W. H. Schuette, Automatic Search and Control Circuit for Servo Loop, *EEE*, 12:11, p 67-68.



Q1 = Q2 = G.E. 2N335, 4C30, 4C31, OR 2N336

400-CPS PREAMP FOR TWO-PHASE SERVO MOTOR—Bias point and gain are stable over wide temperature range, from -55 to 125°C. No selection of transistors is required. Bias

design procedure and design equations are given. —"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 218.

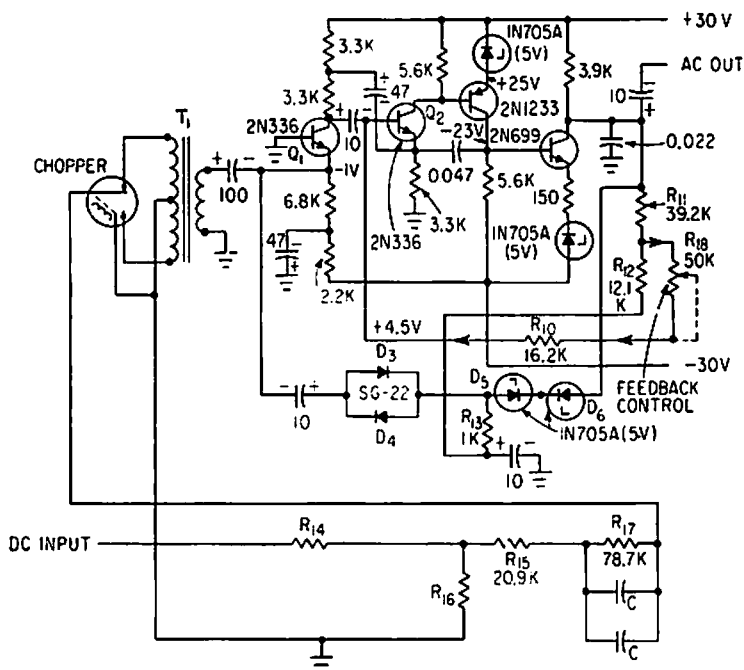
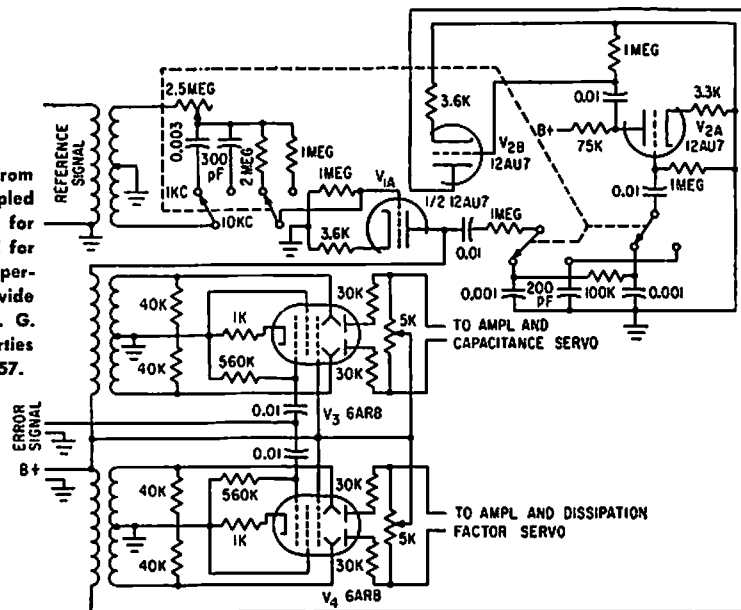


AIDED-TRACKING SERVO—Telescope is positioned on remote object by radar and then directed by operator, who adds corrections to

tracking vector only if tracking rate changes. —R. L. Schuam and D. W. Savage, Joy-Stick

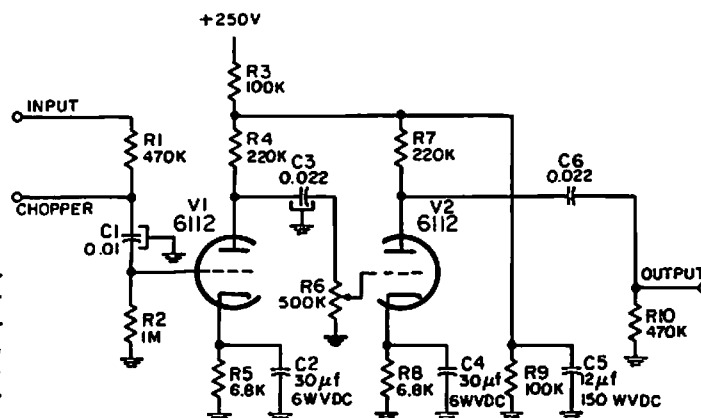
Control Aids Telescope Tracking, *Electronics*, 32:17, p 87-89.

BRIDGE BALANCER—Reference signal from capacitance bridge is transformer-coupled into phase-shifting circuit to compensate for phase shifts in bridge and amplifier used for automatic measurement of dielectric properties. Shunt beam tubes V3 and V4 provide gating action for rebalancing servos.—P. G. Frischmann, *Measuring Dielectric Properties Automatically*, *Electronics*, 33:32, p 56-57.



ERROR-RATE COMPENSATION AMPLIFIER—Chopped d-c input is fed to Q2 through T1 and Q1. Base of Q2 is amplifier summing point and receives feedback signals. Gain is 2.73 v rms per volt d-c.—E. R. Schlosinger, *Aiming a 3-Ton Telescope Hanging from Balloon*, *Electronics*, 36:6, p 47-51.

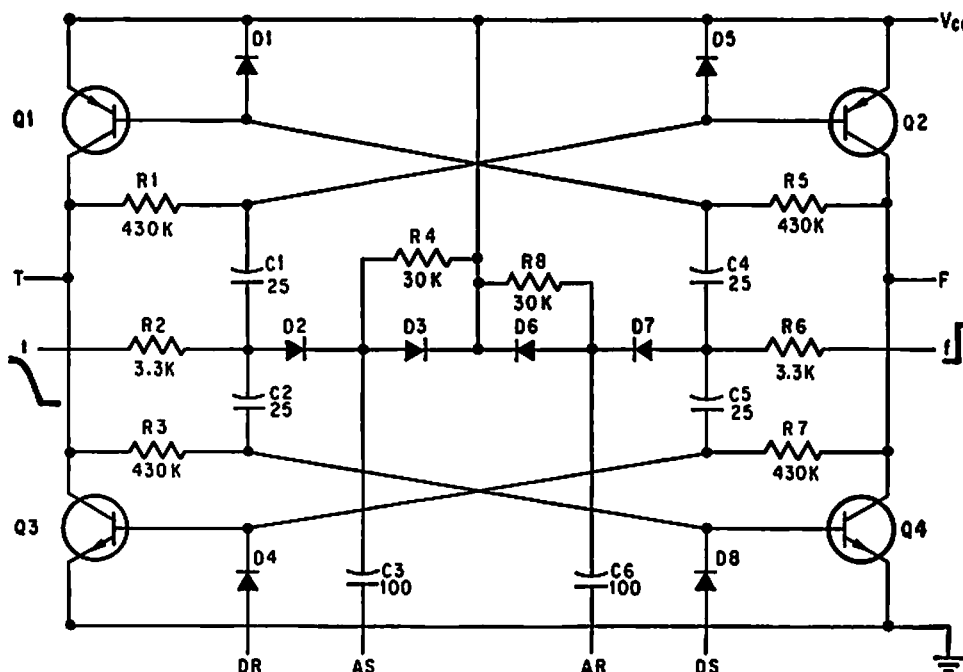
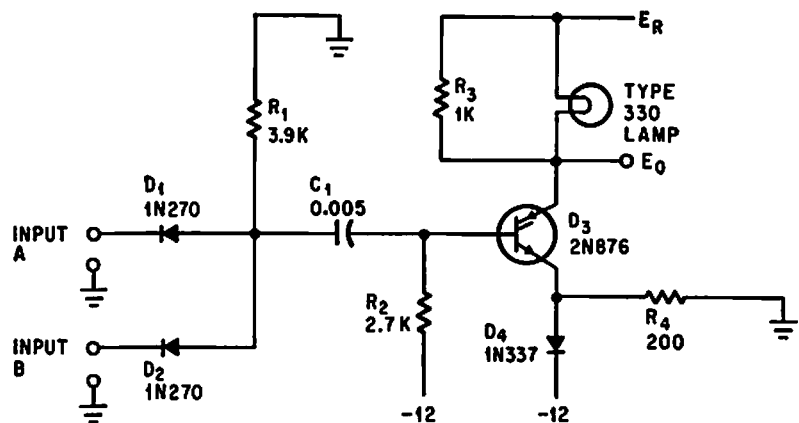
PREFERRED AMPLIFICATION-1,200 PREAMPLIFIER—Used with instrument servo motor controller to increase available gain. Chopper is used with d-c outputs only.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, *Electron Tube Circuits*, 1963, PC 74, p 74-2.



CHAPTER 78

Shift Register Circuits

SCR AND LAMP DISPLAY CONTENTS OF REGISTER—Computer register to be sampled is connected to input A, and input B is fed with 10-microsec, 12-v positive pulse. When output of register is at its low level of -12 v, diode D1 conducts but D2 does not, so scr D3 is nonconducting and lamp is off. When register output is high level (ground potential), coincident positive voltages applied to base of scr make it conduct and turn lamp on.—J. J. Collins, Displaying the Contents of a Computer Register, *Electronics*, 37:21, p 72.



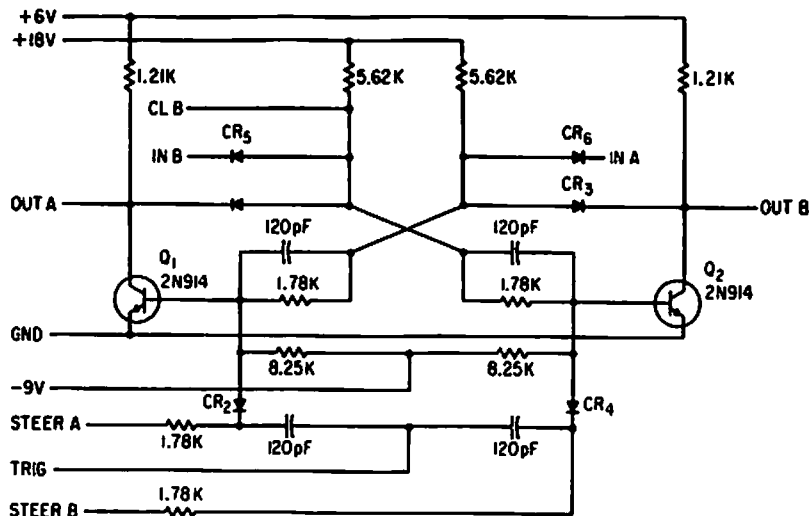
POINT	FUNCTION
Vcc	6V
T & F	OUTPUT
I & I	CONNECT TO T AND F FOR BINARY OPERATION
DS	DC SET
DR	DC RESET
DS & DR	CONNECT TO GROUND FOR BINARY OPERATION
AS	AC SET
AR	AC RESET

1. Q1 & Q2: SELECTED 2N3251.
2. Q3 & Q4: COMPLEMENT TO 2N3251
3. D1-D8: 1N3206 OR 1N914

LOW-DISSIPATION 1-MC FLIP-FLOP—Complementary-pair circuit keeps power dissipation below 2 mw, with standby power of only 150 microwatts. Consists of two interconnected flip-flops, sharing common diode

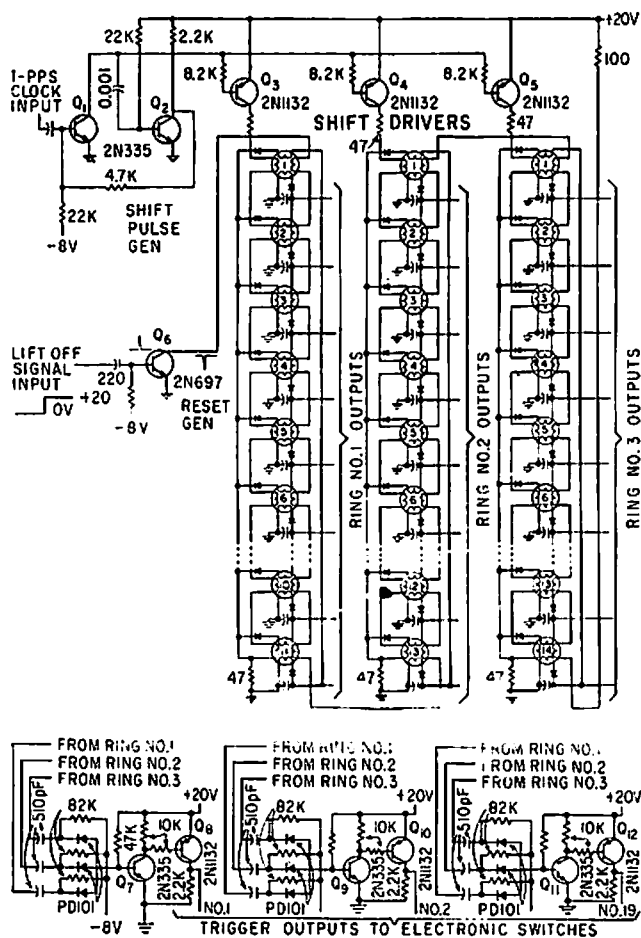
steering network at input. Diodes D1, D4, D5, and D8 protect emitter-based junctions from breakdown and also increase switching speed by clamping back bias levels at base of transistor. May be used for binary opera-

tion, as scalar, or as shift register.—M. E. McGee and J. H. Wujek Jr., One-Megahertz Flip-Flop Saves Standby Power, *Electronics*, 39:12, p 106-107.

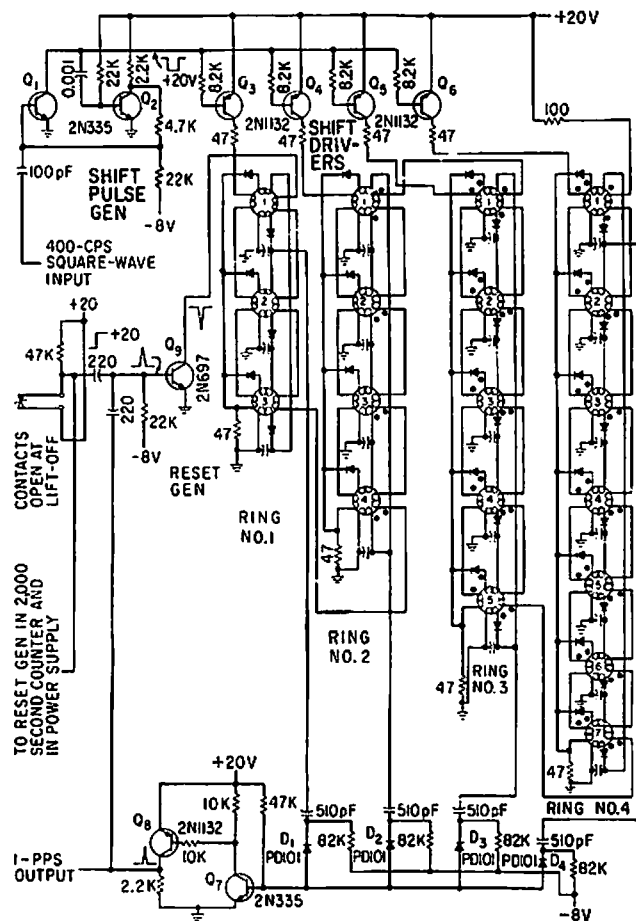


MICROMODULE FOR 1.6-MC CLOCK RATE—Flip-flop arrangement of two standard gates, with capacitor-resistor-diode gates tied to bases for trigger input, operates under worst-

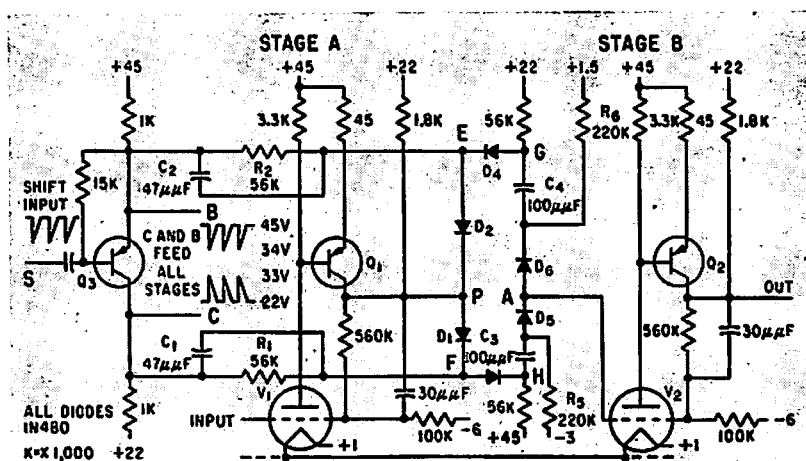
case temperature conditions with two standard gate loads.—A. S. Rettig, *Computers in the Front Lines: Micromodules Make it Possible*, *Electronics*, 36:1, p 77-81.



2,000-SEC COUNTER—Up to 20 and gate inputs can be supplied by each closed ring element. 1-pps clock triggers operate 2,000-sec counter pulse generator using unique magnetic shift register elements.—J. H. Porter, *Miniaturized Autopilot System for Missiles*, *Electronics*, 33:43, p 60-64.

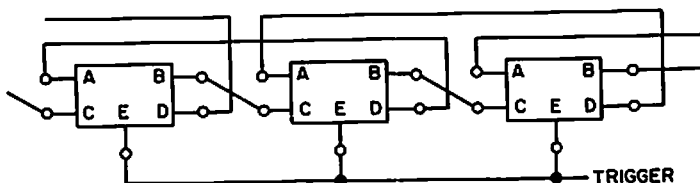
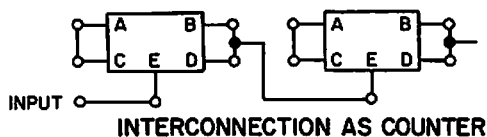
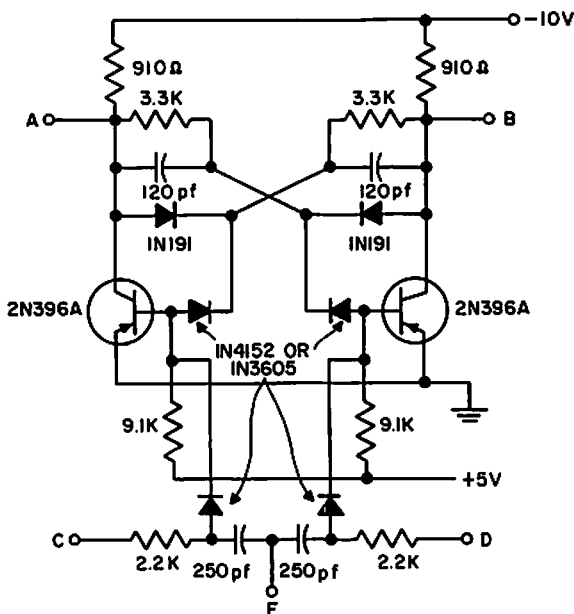


1-PPS CLOCK GENERATOR—Uses unique magnetic shift register in which elements are connected in rings, with output of last element connected to input of first. Each ring has own driver, all operated from same 400-cps pulse generator.—J. H. Porter, *Miniaturized Autopilot System for Missiles*, *Electronics*, 33:43, p 60-64.

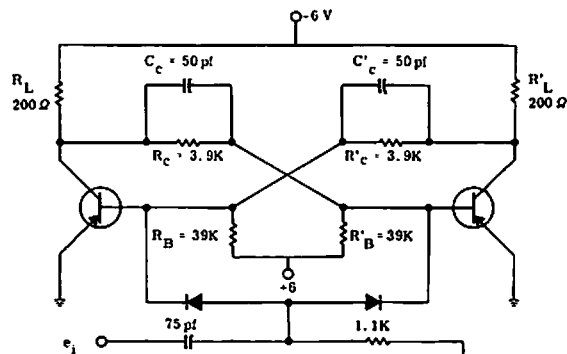


SELF-INDICATING REGISTER—Combines two self-indicating flip-flops with phase splitter that converts single-polarity shift pulses to positive and negative-going pulses for all

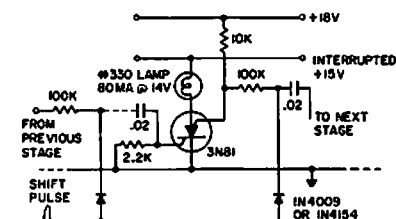
stages. Indicator triodes are Ampere 6977. —H. Rodrigues de Miranda and I. Rudich, Indicator Triode for Direct Data Readout, *Electronics*, 33:6, p 52-54.



500-KC SHIFT REGISTER—Basic nonsaturated flip-flop, using 52-step design procedure given, serves as building block for 500-kc counter and shift register.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 190.

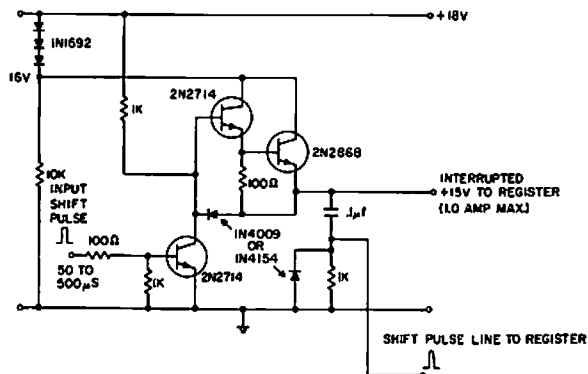
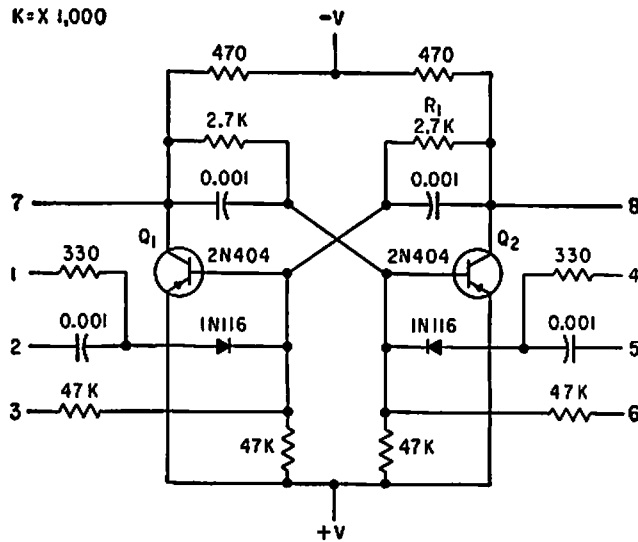


SYMMETRICAL SATURATED FLIP-FLOP—Developed for inexpensive 2N711 germanium pnp mesa switching transistors, to serve as building block for high-speed computer applications. Two or more flip-flops can be cascaded to form counter, or used as shift register by separating inputs. Close regulation is required for -6 v supply.—P. A. McInnis, "Low-Cost Computer Circuits," Motorola Application Note AN-130, Nov. 1965.

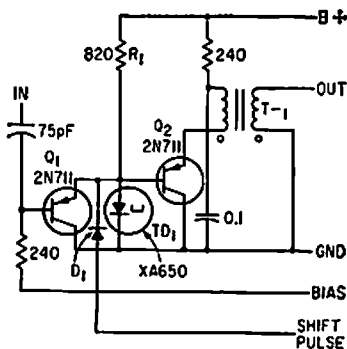


BASIC SHIFT-REGISTER STAGE—If silicon controlled switch stage is off, shift pulse (less than 15 v) will not be coupled to next stage. Anode supply is interrupted just before shift pulse, to turn off all stages. Stored capacitor charge then determines which stages will be retriggered.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 432.

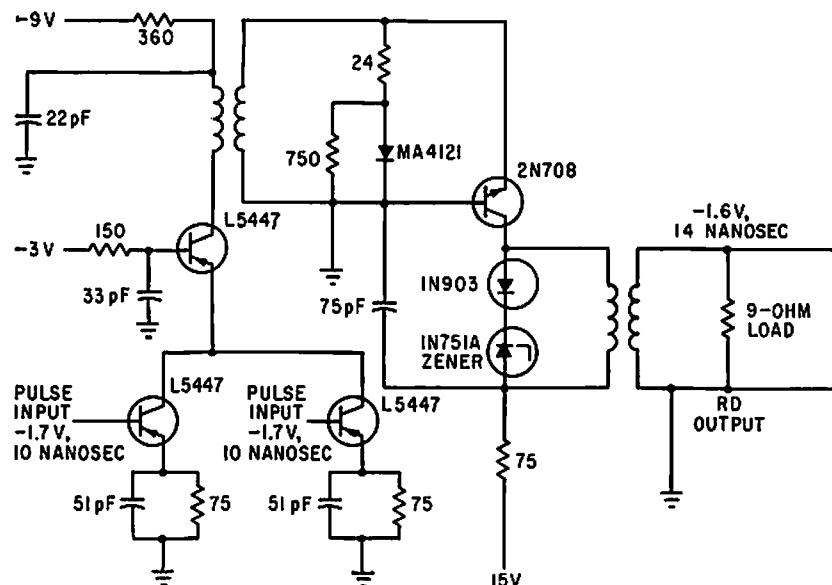
BASIC BISTABLE MODULE—Can be used as flip-flop by connecting 1 to 7 and 4 to 8, then using 2 and 5 as inputs and 7 and 8 as outputs. Becomes binary counter stage when 2 and 5 are tied together for same arrangement. Other combinations of connections give one-shot mvr, pulse generator, shift register, square-wave generator, or flip-flop. —A. I. Perlin, *Selective Calling for Data Link Systems*, Electronics, 33:18, p 108-110.



SHIFT REGISTER DRIVER—Shift pulse input saturates 2N2714, depriving Darlington combination of base drive. Resulting negative pulse generated on 15-v line is differentiated to produce positive trigger pulse at its trailing edge. —"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 432.

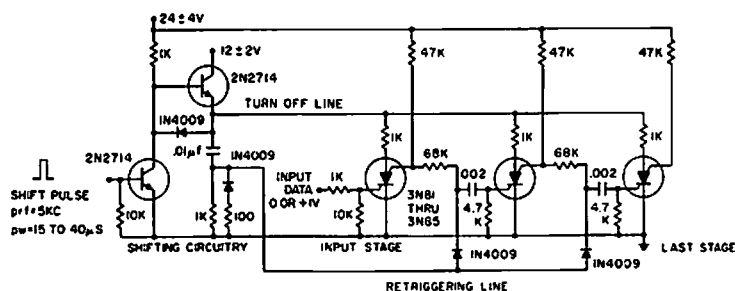


TUNNEL-DIODE SHIFT REGISTER—Incorporates tunnel-diode steering by Q1 and D1. T1 provides necessary phase reversal. —W. V. Harrison and R. S. Foote, *Tunnel Diodes Increase Digital-Circuit Switching Speeds*, Electronics, 34:32, p 154-156.

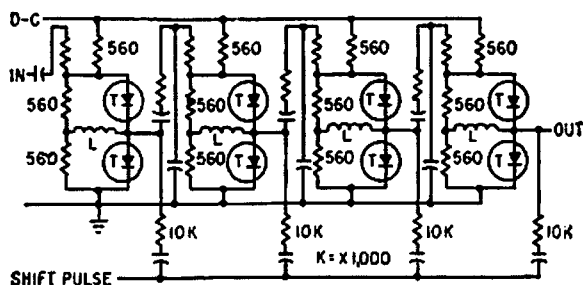


REGISTER-DRIVER—Handles 10-nsec pulses for 50-megapulse computer. Can drive eight 75-ohm lines. —K. H. Konkle and J. E. Laynor, *Key*

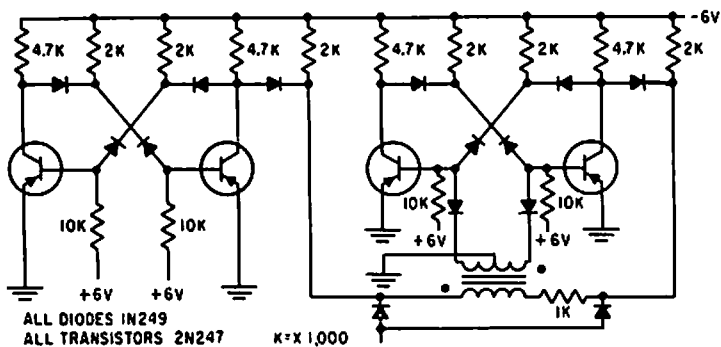
to Faster Computers: Ten-Nanosecond Amplifier, Electronics, 35:50, p 39-41.



SCS SHIFT REGISTER—Shift pulse turns off all silicon controlled switches. Trailing edge of turnoff pulse is differentiated for turning on appropriate stages. 2N2714 will easily drive ten scs stages.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 432.



FLIP-FLOP BUILDING BLOCKS—Inductively coupled flip-flops are put together to form shift register. Both signal and shift pulses are positive. Operates reliably over wide ranges of input pulse amplitude and circuit parameters.—M. M. Perugini and N. Lindgren, Recent Progress in Solid State Technology, *Electronics*, 33:10, p 39-43.

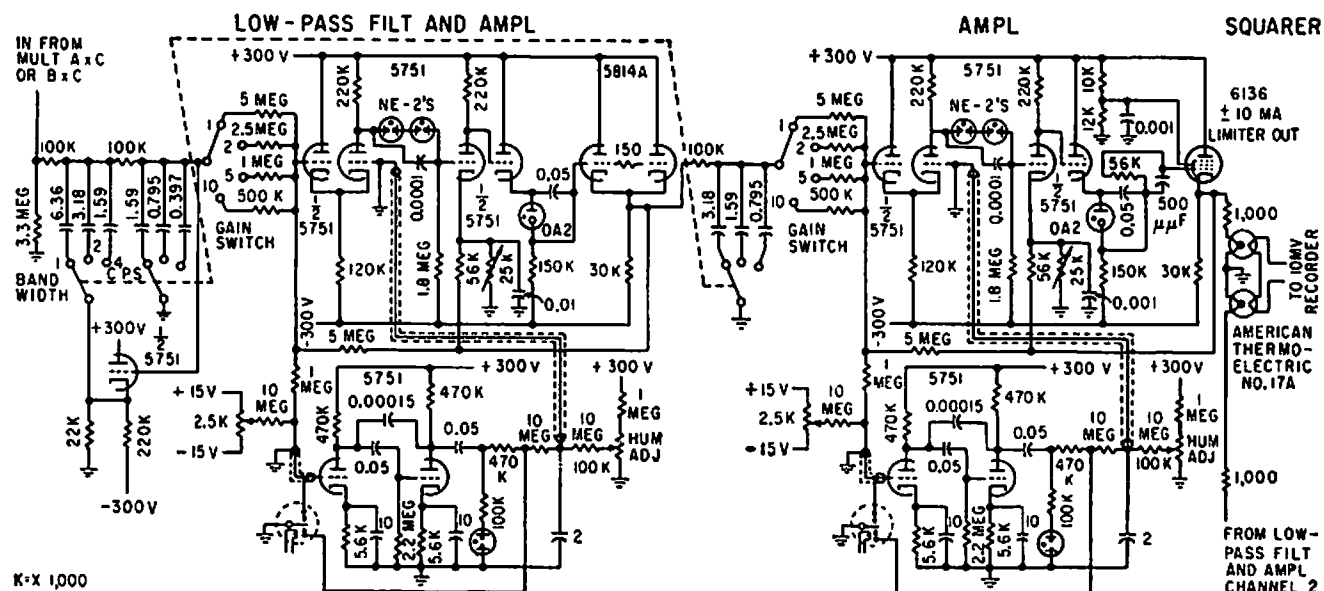
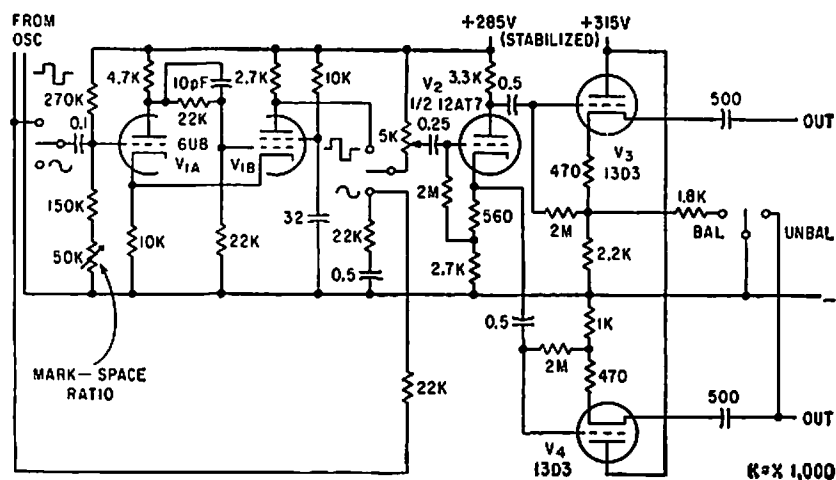


TRANSFORMER-CONTROLLED SHIFT REGISTER—Can be built to shift forward, backward, or in n dimensions. With series of flip-flops, stored information can be rearranged arbitrarily, in single pulse.—W. M. Carey, Using Inductive Control in Computer Circuits, *Electronics*, 32:38, p 31-33.

CHAPTER 79

Signal Generator Circuits

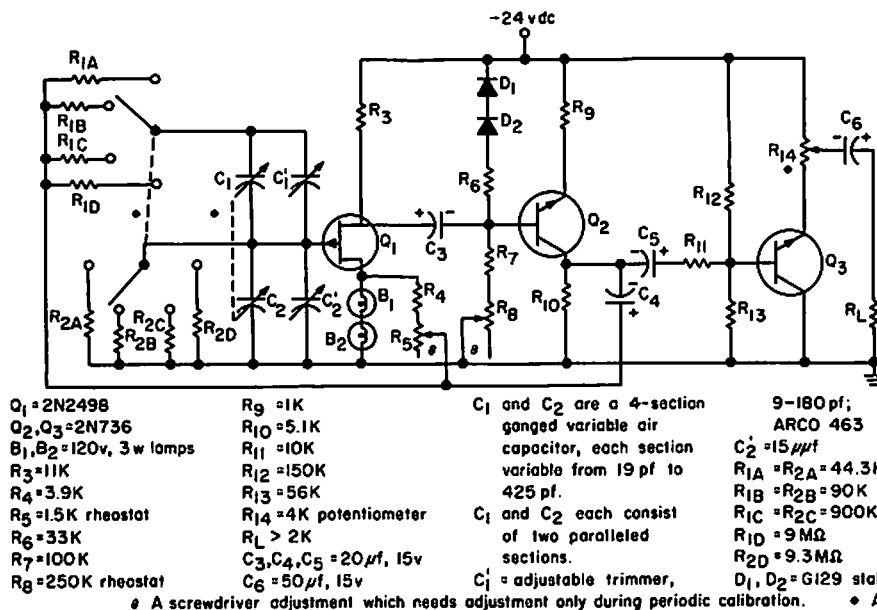
OSCILLATOR OUTPUT STAGE—Used with incremental-tuning precision R-C oscillator for testing and aligning equipment having sharp resonances. Oscillator signal is fed through phase splitter V2 to cathode followers V3 and V4 in push-pull, to provide symmetrical output when required. Will feed either 600-ohm unbalanced load or two 300-ohm outputs balanced with respect to ground, with 10 mw maximum output.—J. H. Reyner, Precision Oscillator with Incremental Tuning, *Electronics*, 33:16, p 76-78.



SPECTRUM ANALYZER FILTER—Designed to study dynamic data recorded as frequency-modulated signals on magnetic tape. Filter circuit values give 1, 2, and 4-cps bandwidth

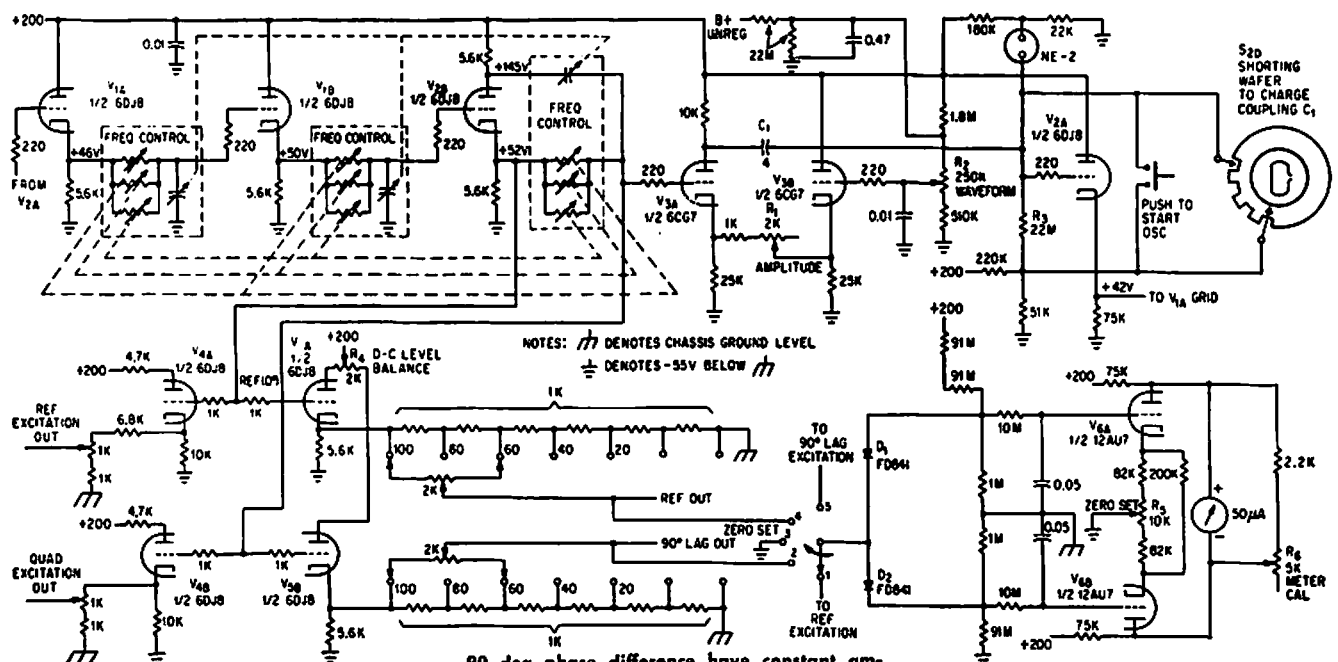
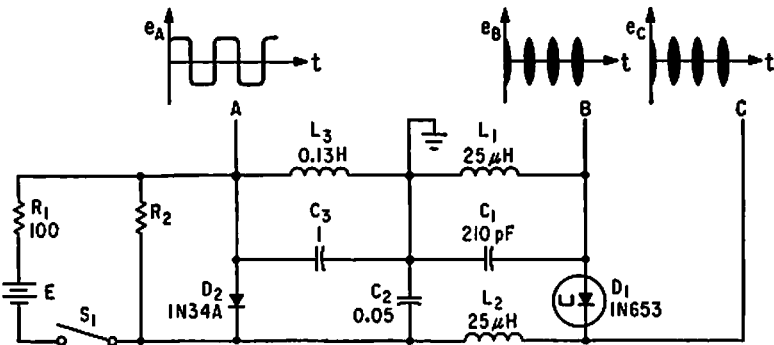
in analyzer. Thermocouple in squarer has time constant of 1 sec so output is filtered as well as squared. Outputs of thermocouples are in series to provide summing. D-c ampli-

fiers prevent loading of filters.—T. B. Fryer, Frequency Analyzer Uses Two Reference Signals, *Electronics*, 32:18, p 56-57.



FET WIEN-BRIDGE OSCILLATOR—Used where good amplitude stability is required for wide frequency variations. Two-stage R-C coupled class A amplifier has positive feedback loop that causes oscillation, and negative feedback loop that stabilizes amplitude of oscillation. Frequency ranges are 20 to 200 cps, 200 cps to 2 kc, 2 to 20 kc, and 4 to 40 kc. —L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 113.

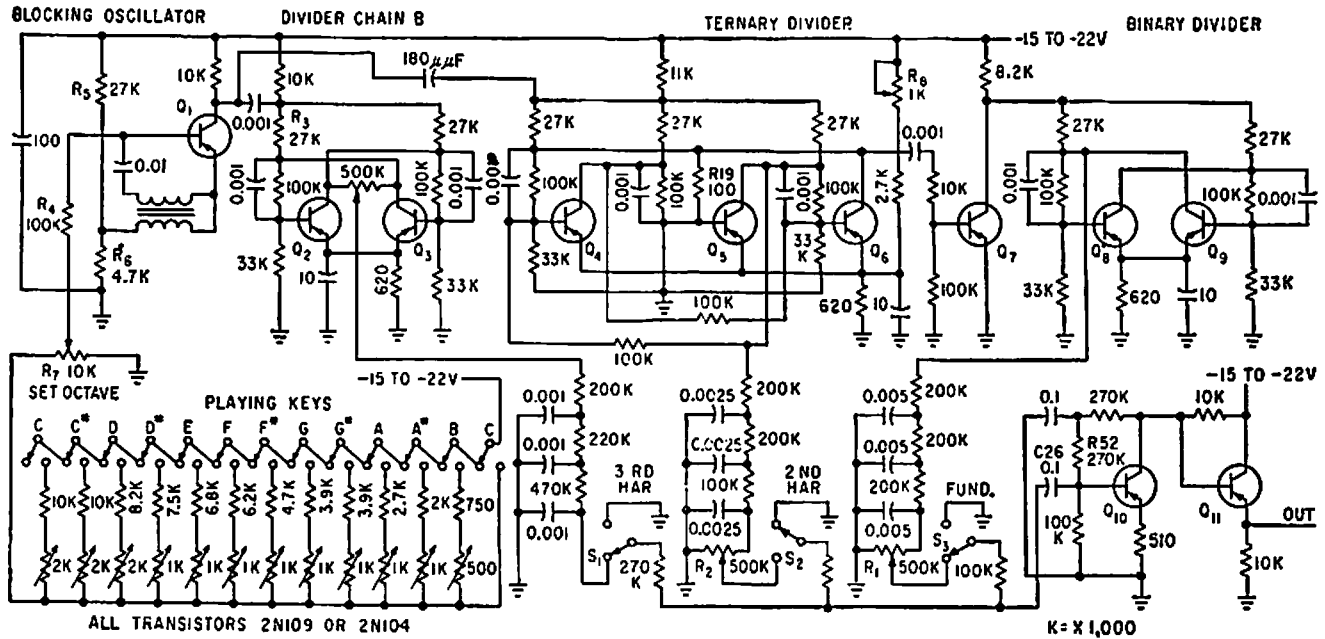
COMBINED R-F AND A-F OSCILLATOR—Used for checking a-m receivers. Generates r-f at 0.6 Mc, determined by L1-C1, and relaxation-type audio output of 400 cps at A.—W. H. Ko, Tunnel-Diode Oscillator Delivers R-F and Audio, *Electronics*, 35:41, p 56.



0.1-1,000 CPS DECADE-SWITCHING TWO-PHASE OSCILLATOR—Simultaneous outputs at

90 deg phase difference have constant amplitudes over entire range. Direct coupling between stages avoids phase error.—Y. P.

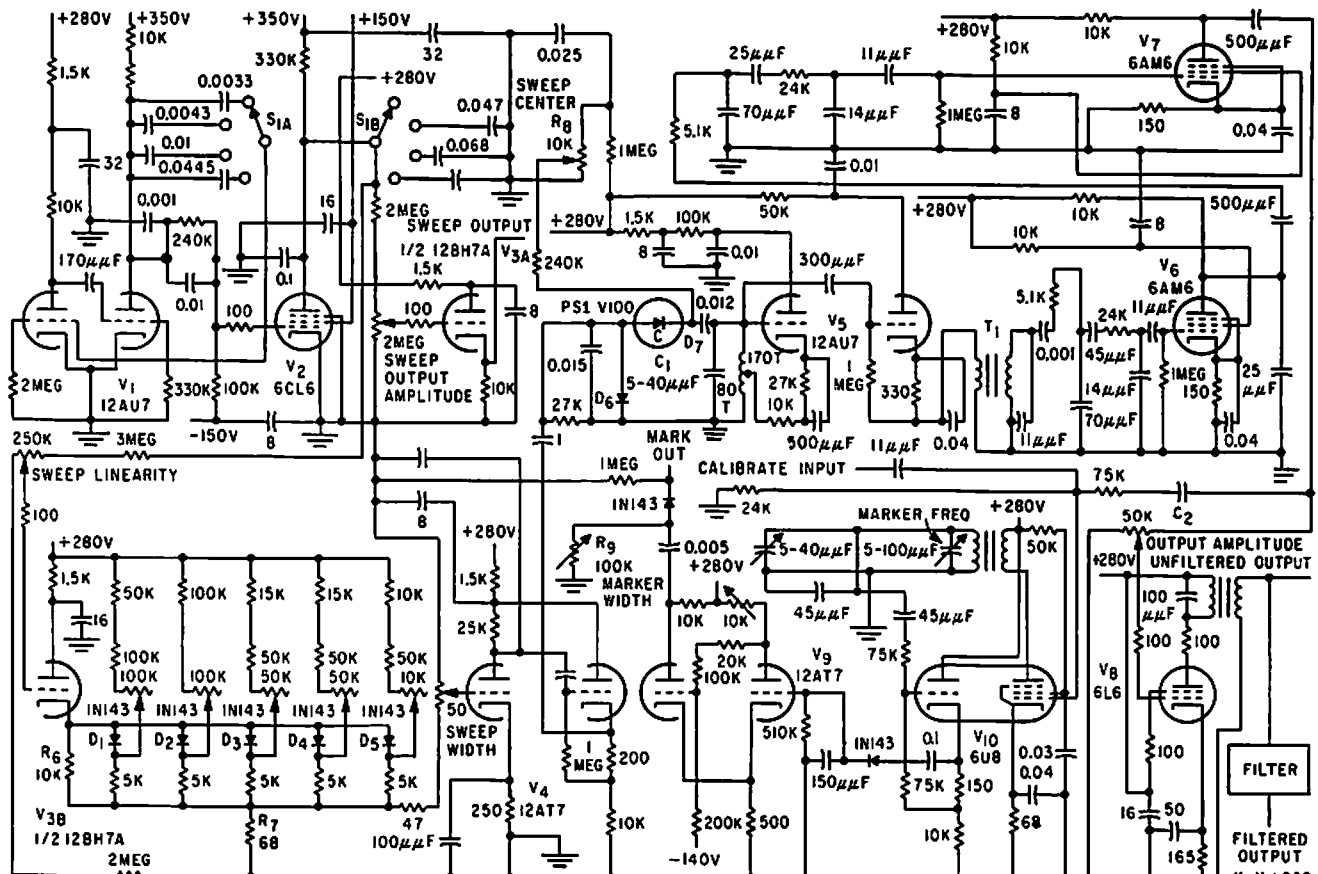
Yv, Two-Phase Oscillator Covers 0.1 to 1,000-CPS, *Electronics*, 36:40, p 27-29.



TONE TIMBRE DEMONSTRATOR—Demonstrates principles of Fourier synthesis of musical tone, for one octave. Lowest fundamental is 250 cps. Switches S1, S2, and S3 add or remove third harmonic, second har-

monic, or fundamental components from output signal to change tone quality. Master oscillator has range of 1.5 to 3 kc. Blocking oscillator Q1 is tuned through one octave by varying voltage to which R4 is returned, by

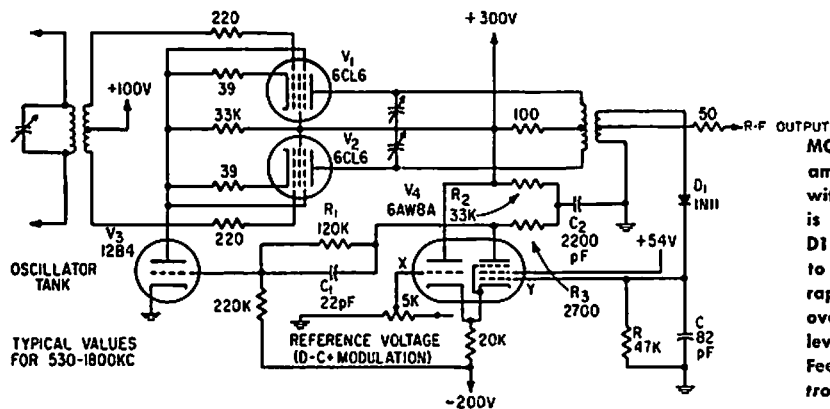
switching resistors in series with playing keys.—W. S. Pike and C. N. Hoyler, Synthesizing Timbre for Electronic Musical Tones, *Electronics*, 32:22, p 92-94.



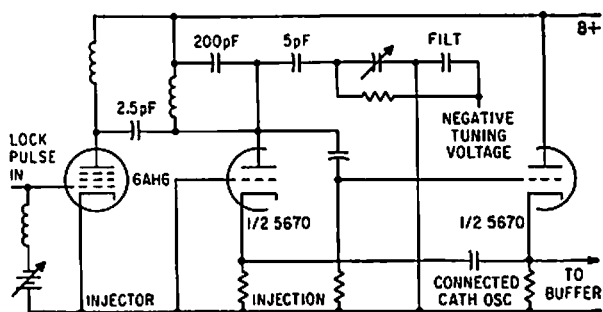
Frequency is swept from 400 to 600 kc electronically by using reverse-biased pn junction diode C as variable capacitor in oscil-

lator V5. Frequency markers are provided. Output is amplified and filtered to give 6 w into 150 ohms with high purity of waveform.—M. M. Brady, Oscillator Design Using

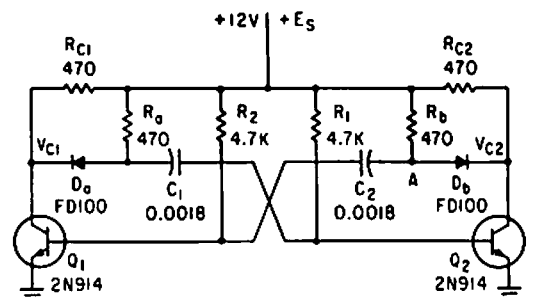
Voltage-Variable Capacitors, *Electronics*, 32:34, p 38-40.



MODULATOR WITH FEEDBACK—Automatic amplitude stabilization of r-f test signals, within 1 db over 1,300 to 1 frequency range, is achieved by demodulating r-f output with D1 and feeding demodulated voltage back to grid Y of differential amplifier V4. Permits rapid and accurate response measurements over wide range without resetting signal level to input of device under test.—A. Fong, *Feedback Stabilizes Signal Generator*, *Electronics*, 33:29, p 71-73.

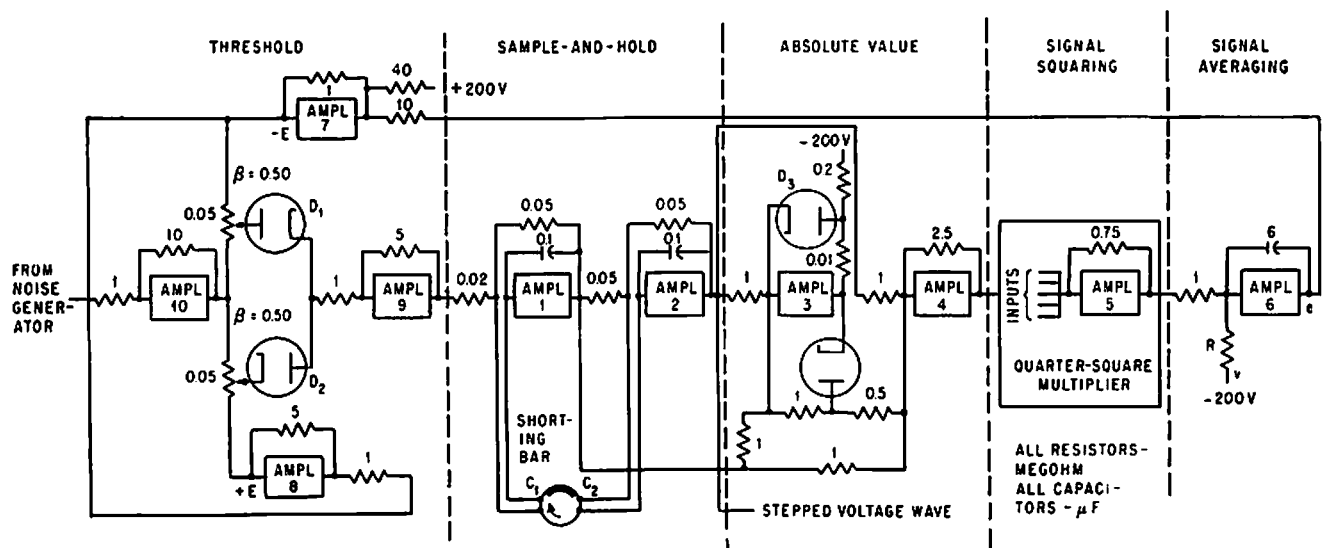


COHO—Connected-cathode coherent oscillator has compromise between good short-term frequency stability and good locking ability, as required for measuring pulse-to-pulse phase variation in pulsed r-f systems.—R. H. Holman and R. B. Shields, *Measuring Frequency Stability of Pulsed Signals*, *Electronics*, 34:16, p 61-65.



VALUES FOR 100KC

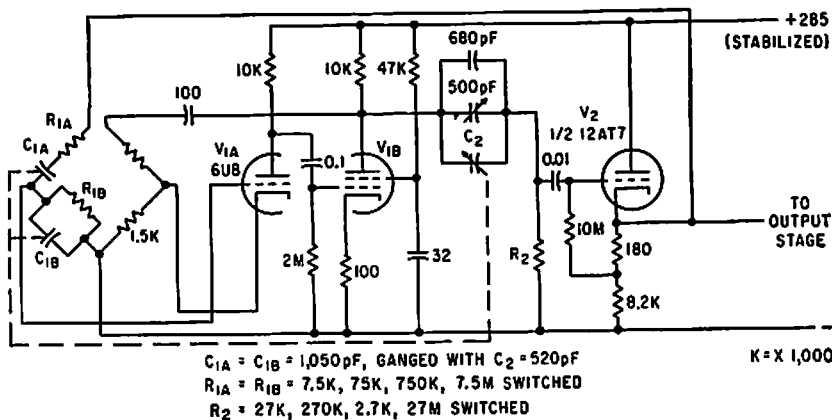
SQUARE WAVES FROM MVBR—Use of four additional components (two resistors and two diodes) with basic free-running mvbr changes its output to clean square wave. Operating range is from several cps to several Mc.—R. O. Gregory and J. C. Bowers, *Simple Square-Wave Generator*, *Electronics*, 35:51, p 47.



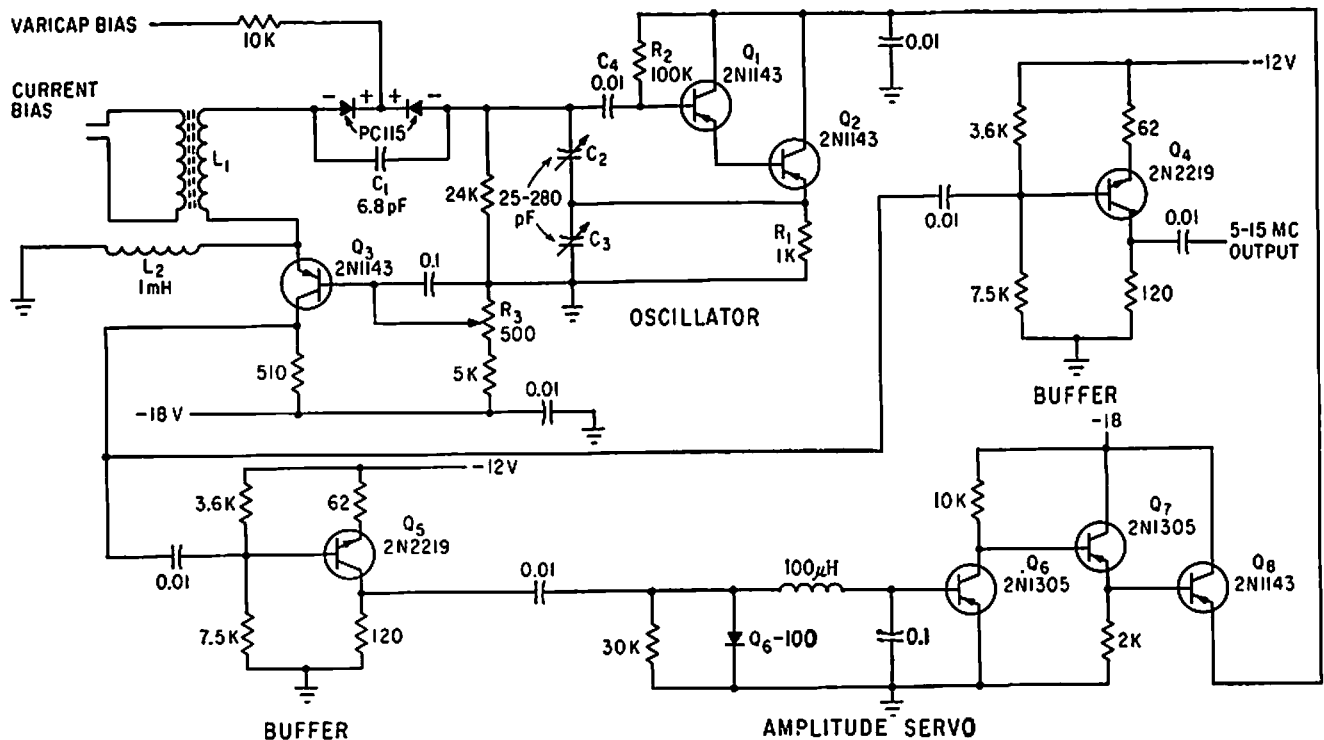
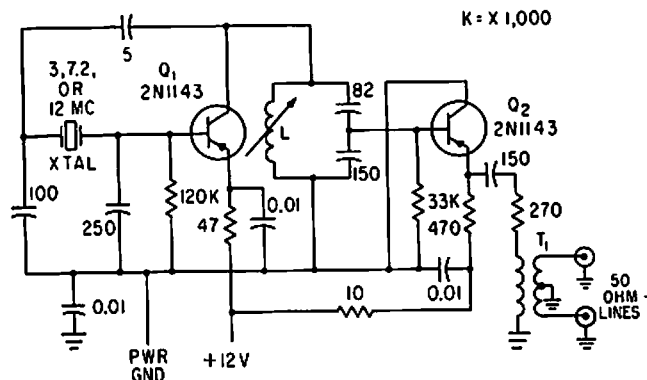
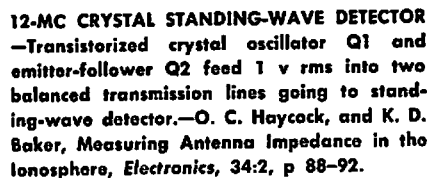
SPECTRUM-INVARIANT RANDOM FUNCTION GENERATOR—Operational amplifiers of analog computer produce periodically stepped

waves by clipping and sampling raw noise signal. Feedback maintains desired power density spectrum.—N. D. Diamantides and

C. E. McCray, *Generating Random Forcing Functions for Control-Systems Simulation*, *Electronics*, 34:33, p 60-63.



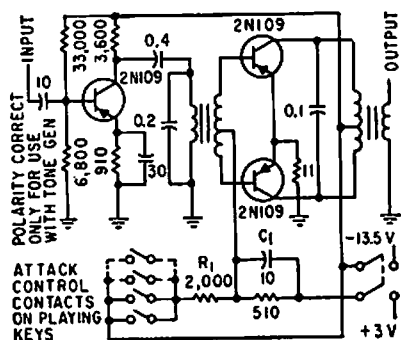
PRECISION R-C OSCILLATOR—Used in signal generator for testing systems by varying frequency over very small limits, as in aligning filters having sharp resonance curves. Cathode follower V2 is included in feedback loop to reduce loading on bridge network. Covers 25 cps to 250 kc in four ranges, with incremental control giving increment of 2% of maximum frequency in each range.—J. H. Reynor, Precision Oscillator with Incremental Tuning, *Electronics*, 33:16, p 76-78.



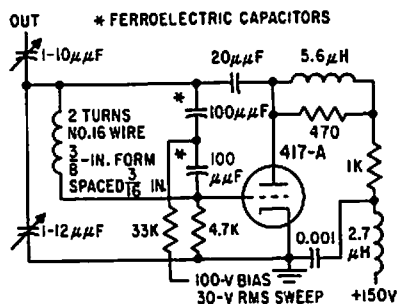
SWEEP-FREQUENCY CLAPP OSCILLATOR—
Tank circuit L1-C1-C2-C3 sweeps frequency of transistor Clapp oscillator over range of 5 to 15 Mc when current through bias wind-

ing of L1 is varied from zero to 800 ma. Collector voltage is servoed to maintain constant output amplitude.—R. E. Daniels and A. D. Cook, Advanced Clapp Oscillator

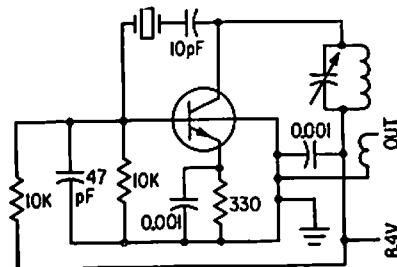
Features 3-to-1 Dynamic Range, Electronics,
36:8, p 60-61.



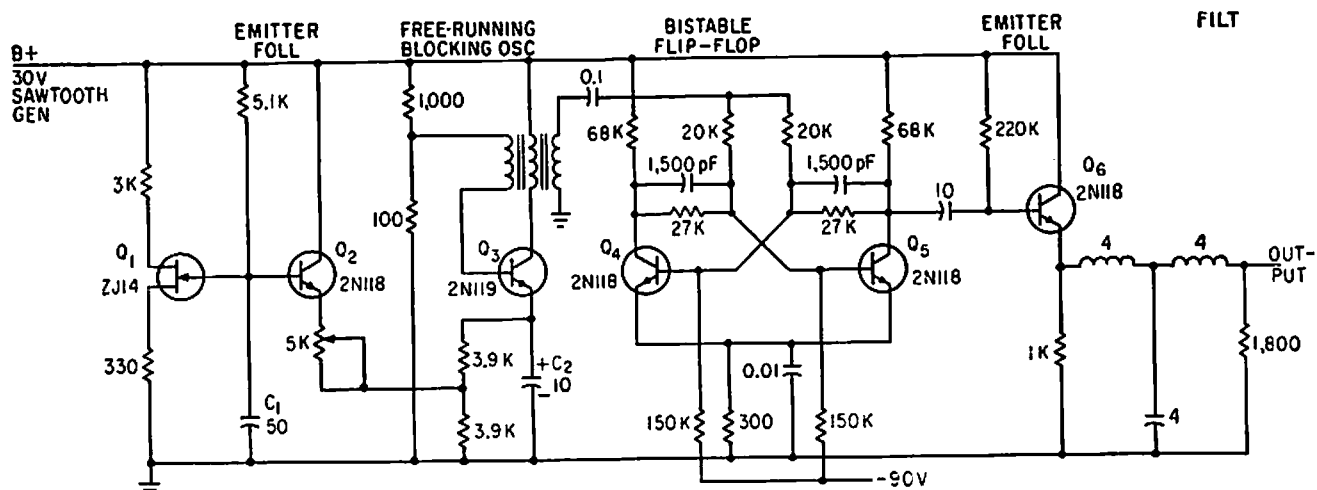
ATTACK CONTROL AMPLIFIER—Used with tone timbre generator to provide gradual attack for electronic music demonstration.—W. S. Pike and C. N. Hoyler, Synthesizing Timbre for Electronic Musical Tones, *Electronics*, 32:22, p 92-94.



VHF SWEEP OSCILLATOR—Voltage-tunable ferroelectric capacitors give tuning ratio of about 2 to 1 from 20 to 250 Mc and 1.5 to 1 from 250 to 400 Mc.—T. W. Butler, Jr., Ferroelectrics Tune Electronic Circuits, *Electronics*, 32:3, p 52-55.



PIERCE TETRODE-TRANSISTOR—Tuned to third overtone of crystal fundamental. Fifth harmonic of oscillator is used as calibration frequency for c-w receiver of radio direction finder.—A. T. Lloyd, Direction Finder Helps Recover Discoverer Capsule, *Electronics*, 34:9, p 42-45.



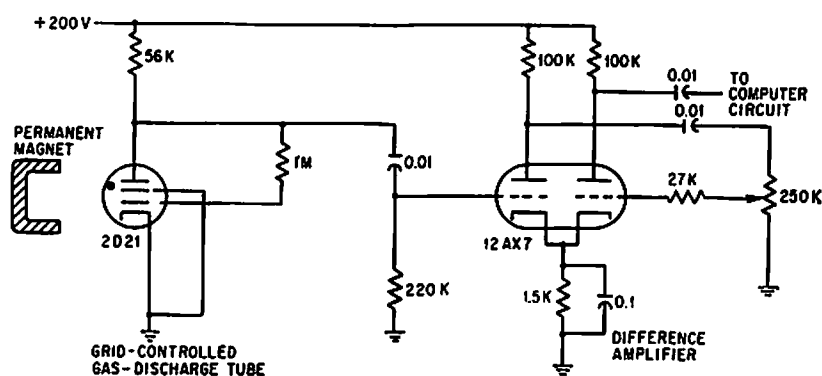
20-40 CPS VARIABLE SWEEP—Used to test servos and related equipment. Sawtooth

waveform developed by unijunction transistor circuit is used to key blocking oscilla-

tor.—M. Rosen, Subaudio Swept Signal Generator, *Electronics*, 33:17, p 67-68.

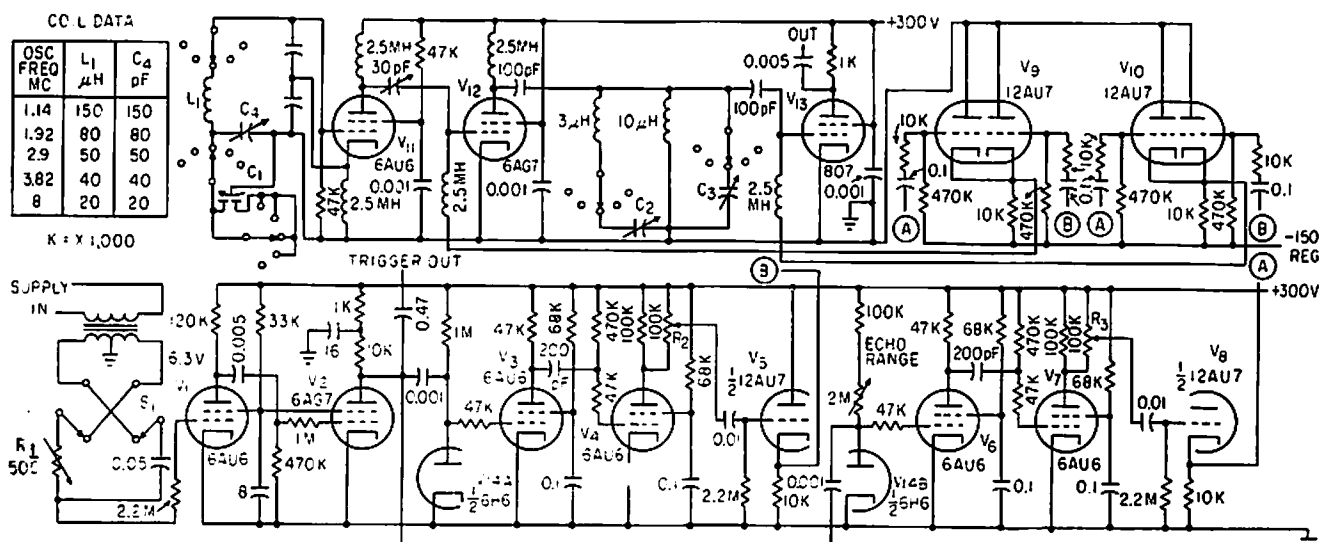
CHAPTER 80

Simulator Circuits



RANDOM-SIGNAL GENERATOR—Signals arise from fluctuations in dense layer of positive ions near cathode of 2021 grid-controlled gas-discharge tube. Used with computers to simulate random action, such as effect of

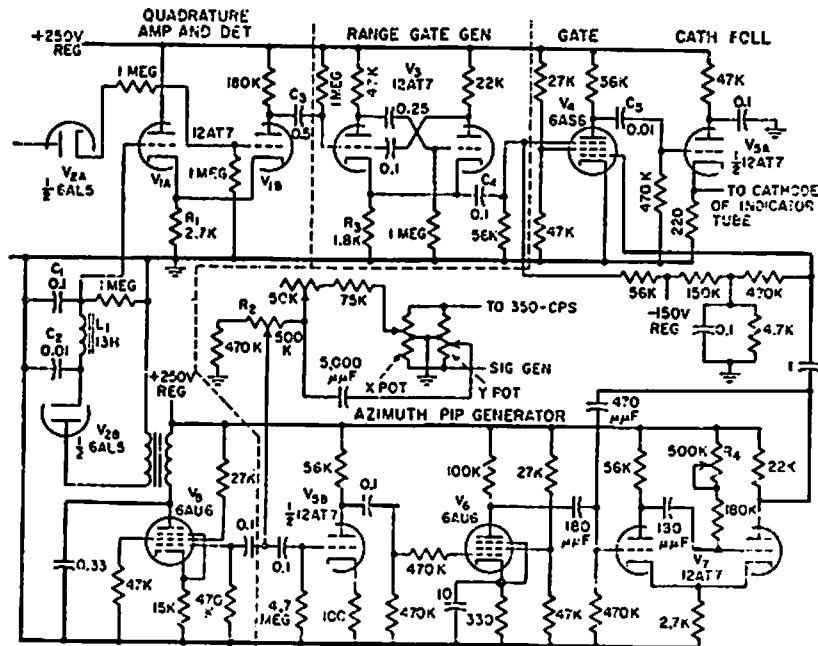
wind gusts on controls of airplane.—N. D. Diamantides and C. E. McCray, *Generating Random Forcing Functions for Control-System Simulation*, *Electronics*, 34:33, p 60-63.



PULSE RECEIVER TESTER—Simulates field conditions encountered by pulse receivers use in ionospheric soundings, by supplying powerful pulse followed by weak pulse, with

variable time separation. Weak pulse can be moved through fixed strong pulse without addition of pulse amplitudes. Uses cathode-follower mixer pulser and c-w oscillator puls-

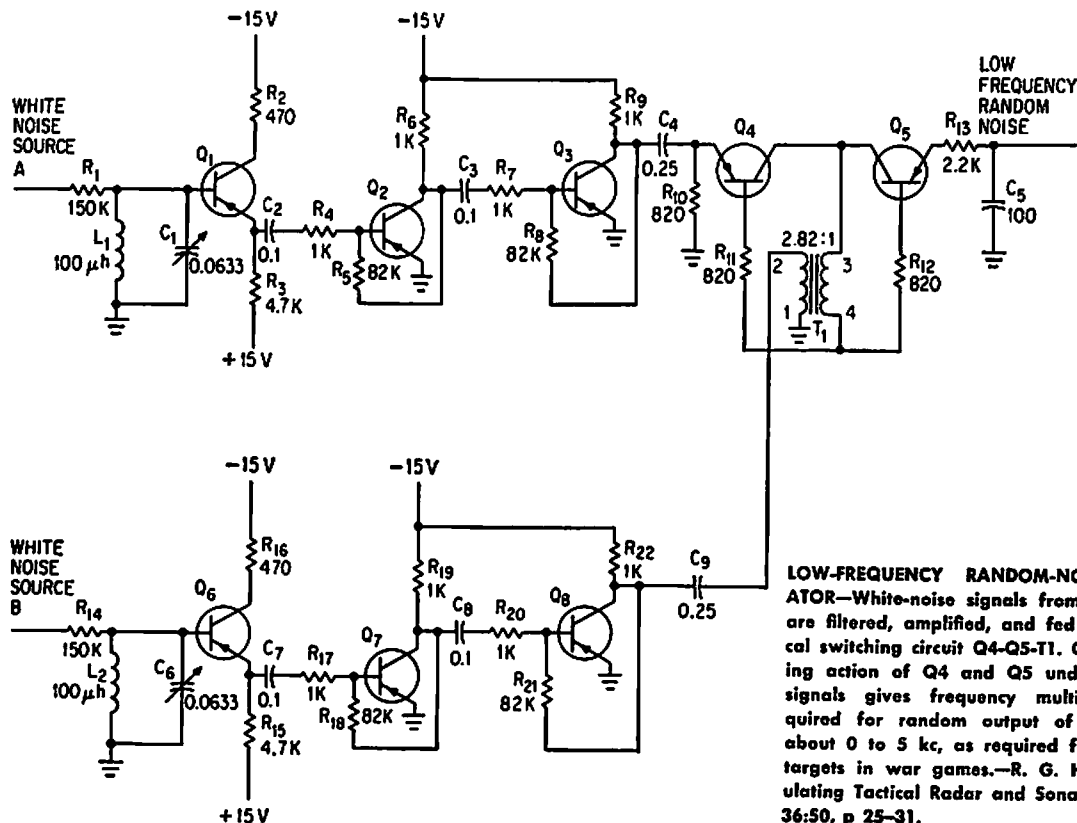
ing a buffer.—K. Perry, *Transmitter Simulator Tests Pulse and Phase-Path Receivers*, *Electronics*, 33:41, p 67.



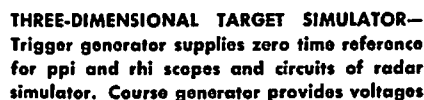
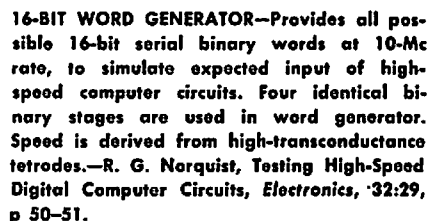
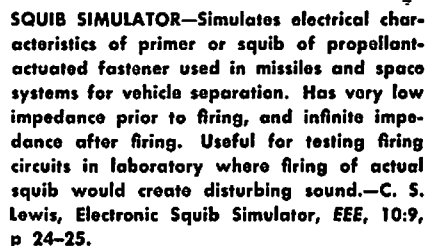
SPIRAL SWEEP SIMULATOR—Does not require operational radar equipment. Antenna signal is obtained from phase shifter and sweep amplitude potentiometer that provides spiral sweep for target on oscilloscope. Range is indicated by gating target to cor-

rect radius of spiral sweep. Azimuth is indicated by another gate that limits target appearance to correct angle on spiral sweep.

—J. I. Leskinen, Four Ways to Simulate Radar Targets, *Electronics*, 31:23, p 82-86.

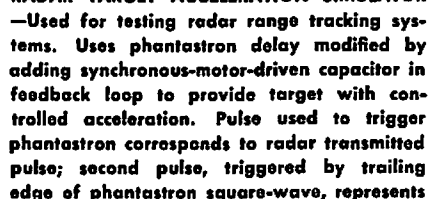
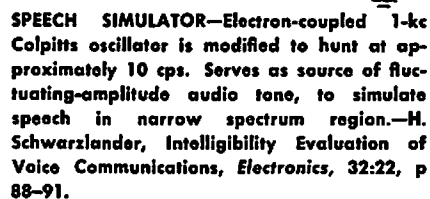


LOW-FREQUENCY RANDOM-NOISE GENERATOR—White-noise signals from two sources are filtered, amplified, and fed to symmetrical switching circuit Q4-Q5-T1. On-off switching action of Q4 and Q5 under control of signals gives frequency multiplication required for random output of pulses from about 0 to 5 kc, as required for simulating targets in war games.—R. G. Hundley, *Simulating Tactical Radar and Sonar*, *Electronics*, 36:50, p 25-31.

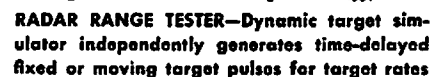
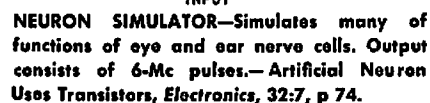


proportional to X, Y, and Z target coordinates. Transformation computer with function generators converts these into polar coordinates. Range voltage from computer is compared

with linear sawtooth to obtain time delay proportional to target range.—J. I. Leskinen, *Four Ways to Simulate Radar Targets*, *Electronics*, 31:23, p 82-86.



radar echo. Time between pulses, representing range, can be set to desired values by changing resistance between positive supply and grid and changing capacitance between plate and grid of phantastron tube.—Radar Target Acceleration Simulator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 171.

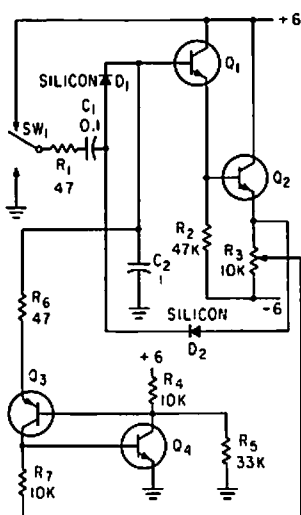
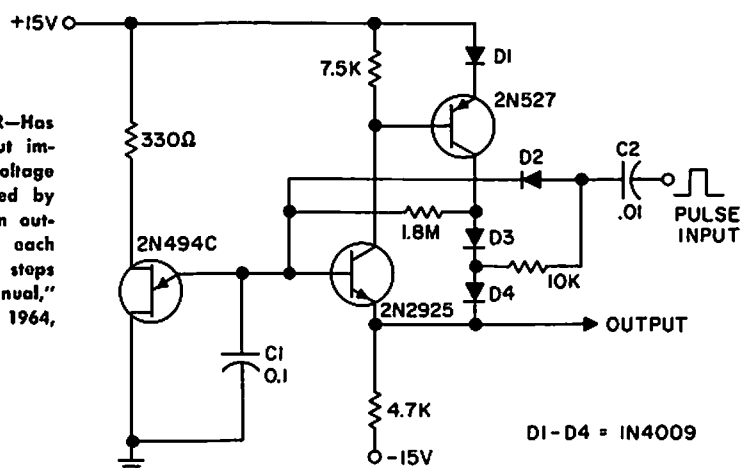


above mach 3, for testing range accuracy of airborne automatic-tracking radar.—O. B. Mitchell, Simulator Tests Radar Tracking

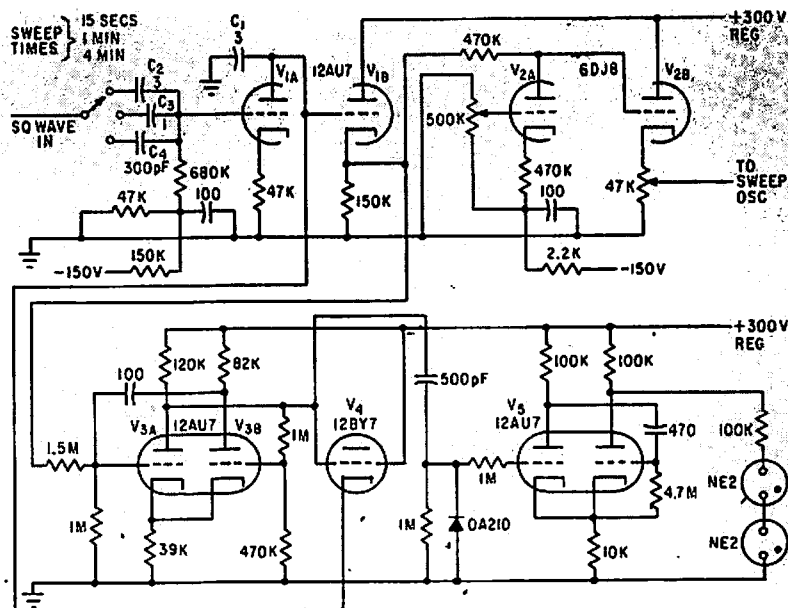
Systems, Electronics, 34:8, p 51-53.

Staircase Generator Circuits

WIDE-RANGE STAIRCASE GENERATOR—Has high input impedance and low output impedance, to reduce droop in output voltage between pulses. Staircase is generated by pump D2-C2, which is bootstrapped on output to maintain equal amplitude on each step. Circuit values shown give 10 steps with 12-v input pulse.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 345.



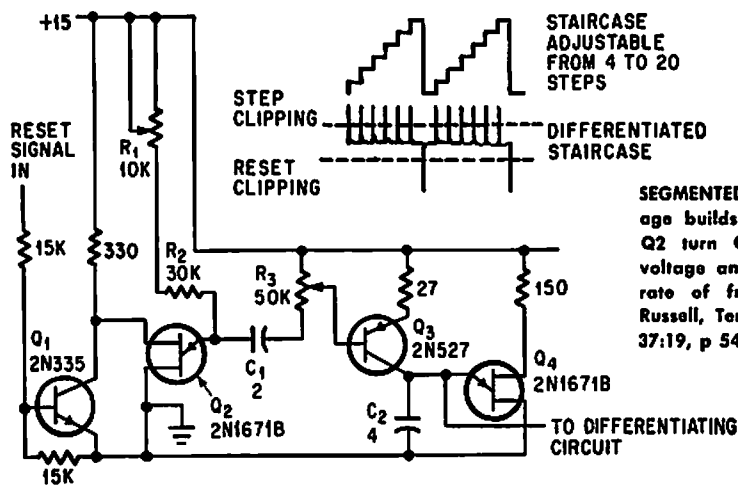
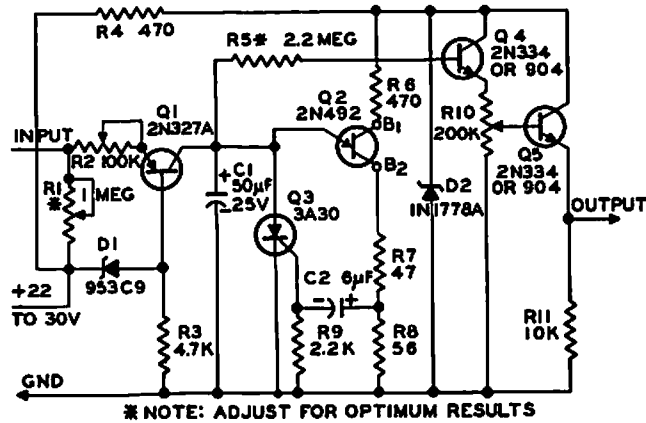
STAIRCASE COUNTER—Q1-Q2 serve as bootstrap amplifier for voltage on storage capacitor C2. Each incoming pulse transfers charge increment from C1 to C2. Reliable counts as large as ten are easily obtained.—N. C. Hakimian, PNP-NPN CIRCUITS: New Look at a Familiar Connection, *Electronics*, 35:47, p 42-46.



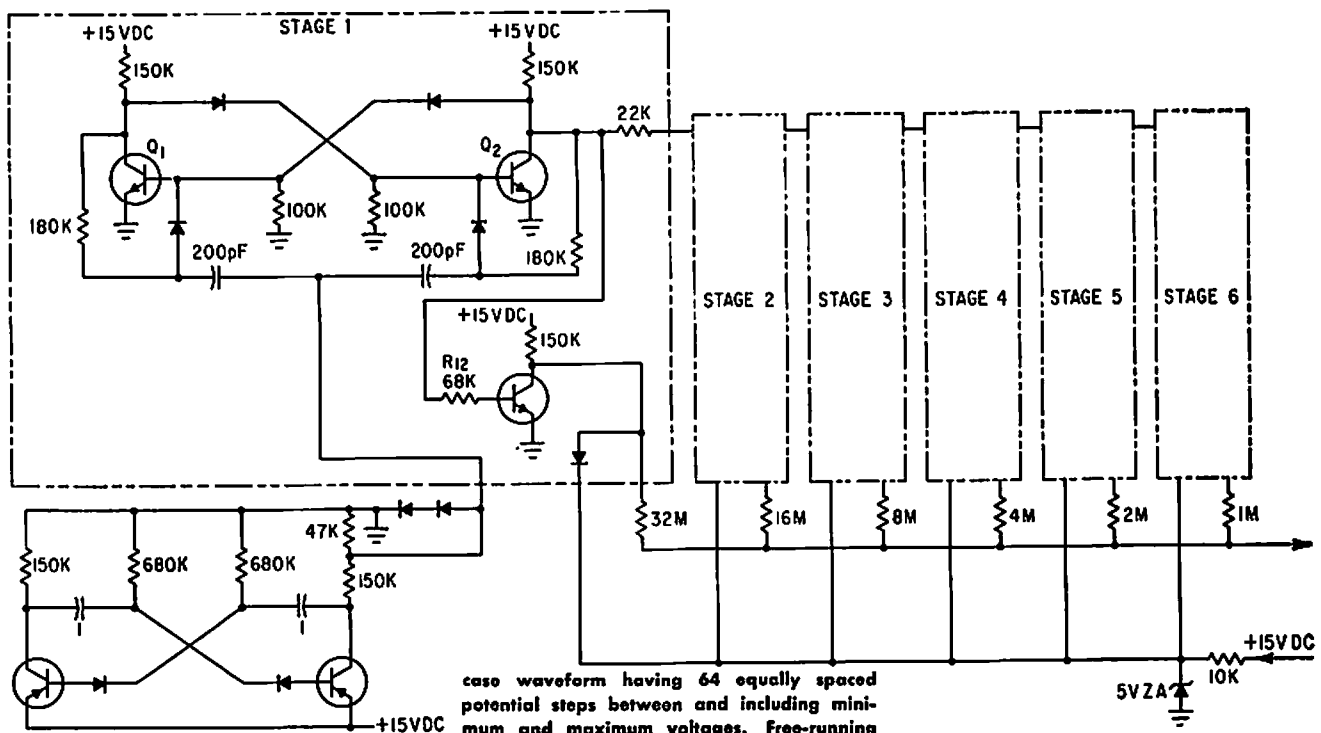
10-MINUTE STEPPED SWEEP—Provides long stepped sweeps required for swept-frequency ionosondes, with 100-v amplitude. Schmitt

trigger V3 detects end of rundown and initiates recharging of C1.—K. Perry, Long Staircase Generator, *Electronics*, 35:35, p 54.

LOW-FREQUENCY STAIRSTEP—Accepts pulse input, either random or evenly spaced, and produces output after fixed number of inputs. Useful in measuring and recording low-frequency data. Pulse widths may be 1 millisecc to several hundred millisecc. Output may have anywhere from 2 to 1,000 steps. By making R1 smaller and eliminating R2, output becomes sawtooth with 1% linearity, variable from 10 millisecc to 15 minutes depending on values of C1 and R1.—Low-Frequency Stairstep Generator and Timing Circuit, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 144.



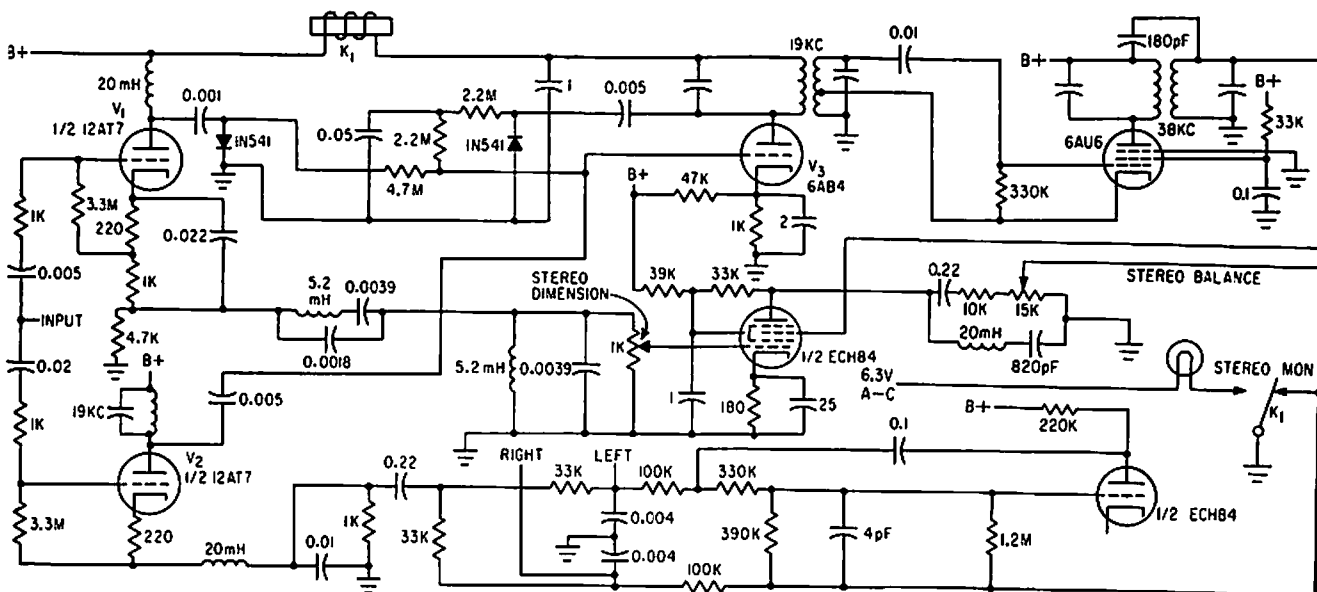
SEGMENTED-SWEEP DISPLAY—Staircase voltage builds up across C2 when pulses from Q2 turn Q3 on momentarily. R3 controls voltage amplitude of each step. R1 controls rate of free-running oscillator tube.—J. E. Russell, Ten Signals at a Glance, *Electronics*, 37:19, p 54-57.



64 STEPS—R-C clock, six binary counters, and summing network give repetitive stair-

E. E. Eberhard, Latest Thin-Film Circuit Techniques, *Electronics*, 35:24, p 37-39.

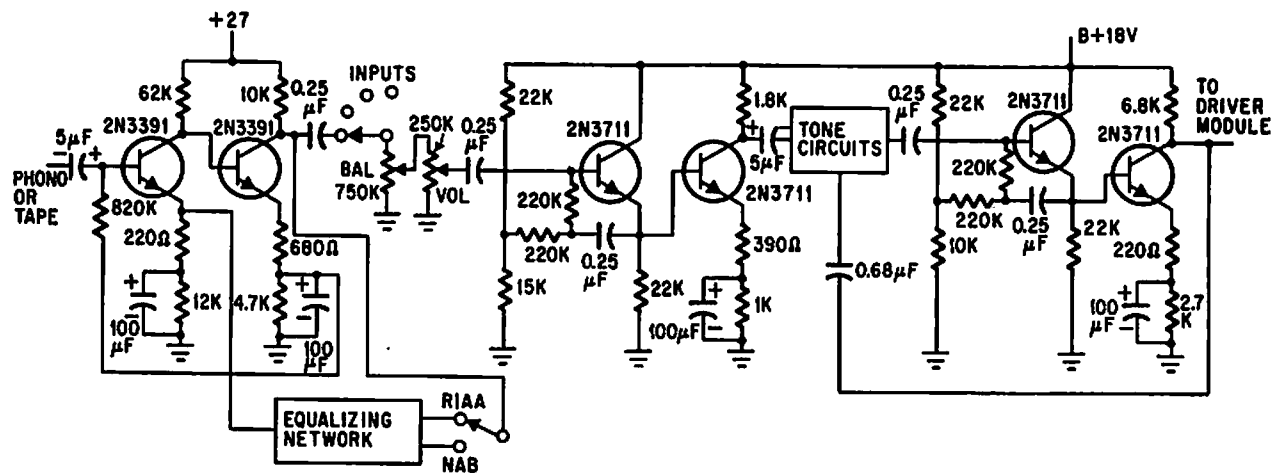
Stereo Circuits



AUTOMATIC STEREO SWITCH—Switches f-m tuner into stereo mode and turns on indicator lamp when f-m tuner is tuned to station

broadcasting stereo subcarrier or when subcarrier comes on during normal f-m broadcast.—L. Solomon, Audio Show Features Auto-

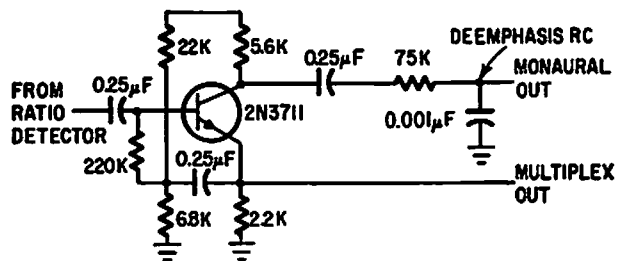
matic Stereo, Hi-Fi Earphones, Electronics,
34:39, p 34-35.



PREAMP MODULE—Bootstrapped emitter-followers accept high-impedance input signals

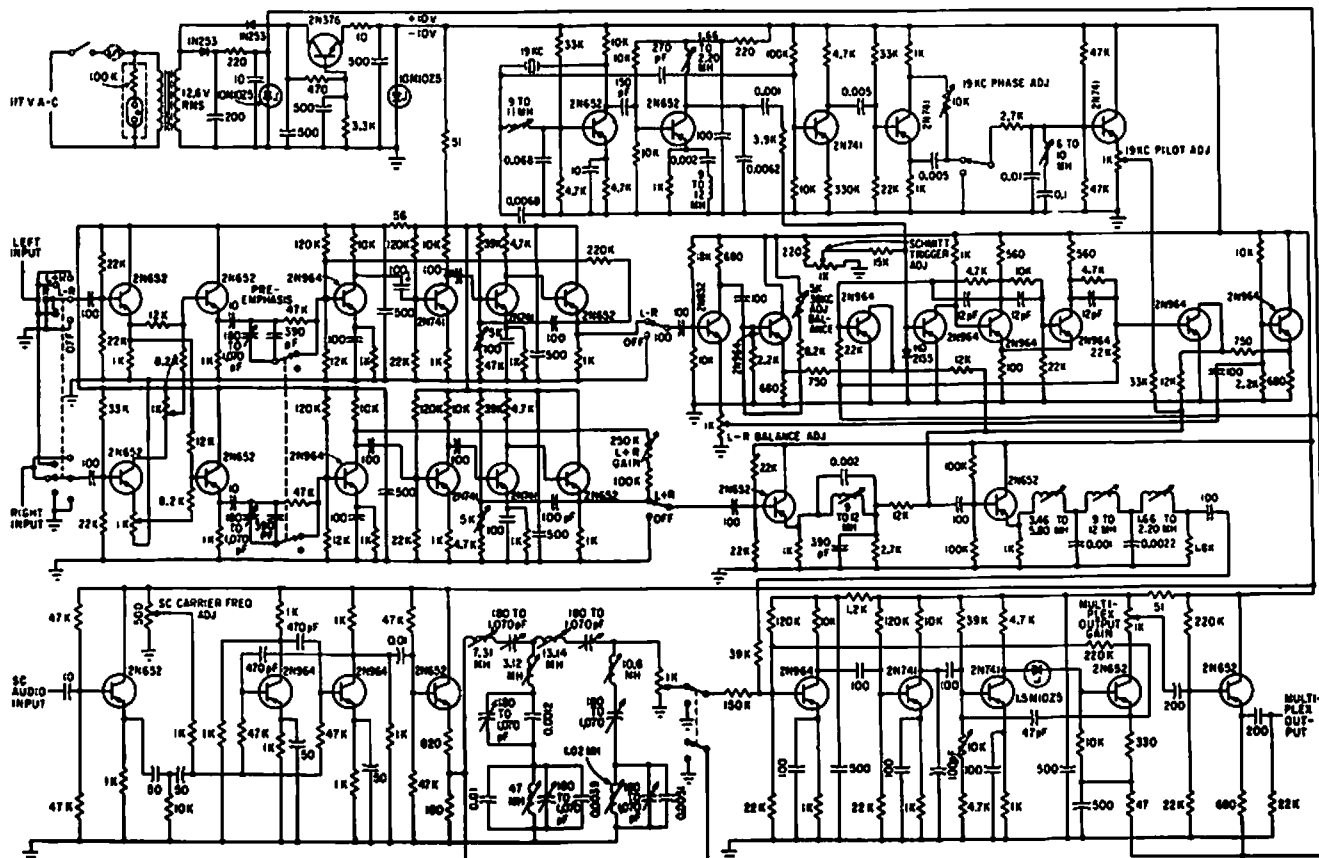
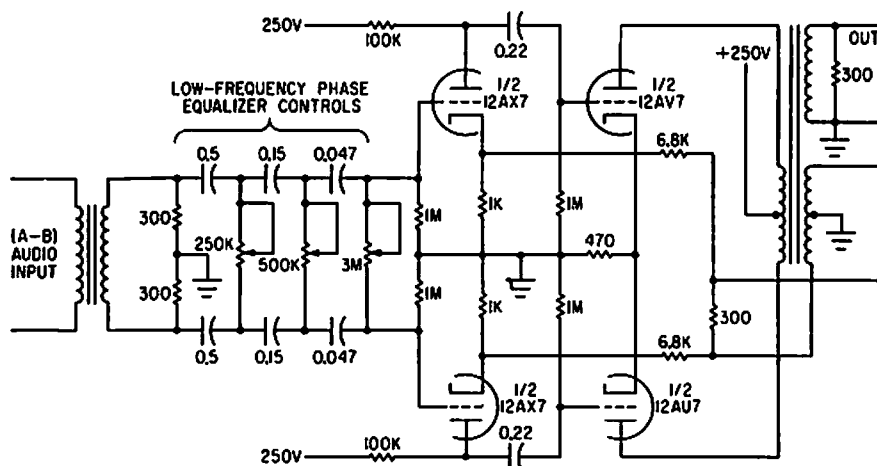
and minimize loading of equalizing and tone circuits.—S. Messin and T. E. Nawolinski, A

Solid State Stereo Set Built in Modules, Electronics, 38:16, p 88-92.



DUAL-OUTPUT I-F—Final i-f stage of stereo f-m set has two outputs, to permit independent feed of monaural f-m.—S. Messin and T. E. Nawalinski, A Solid State Stereo Set Built in Modules, *Electronics*, 38:16, p 88-92.

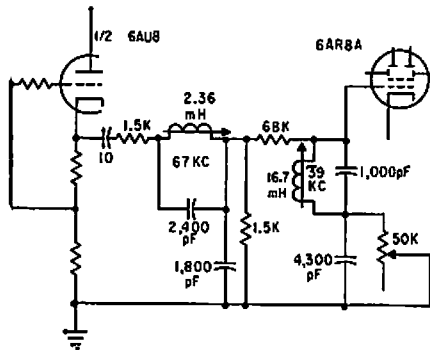
STEREO MULTIPLEX A-F AMPLIFIER—Provides low-frequency phase equalization for the A-B channel, using variable R-C high-pass filter sections that can be adjusted for cutoff between 5 and 25 cps.—Modifying an F-M Transmitter for Compatible Stereo Multiplex, *Electronics*, 34:28, p 60-62.



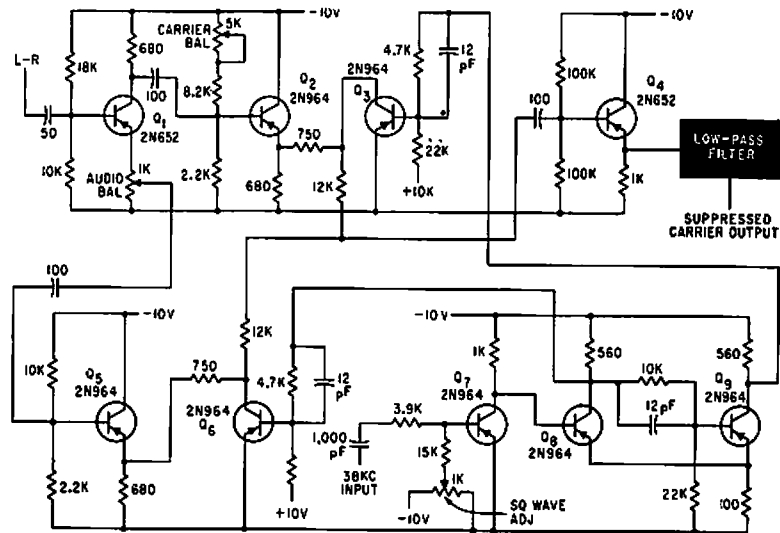
STEREO MULTIPLEX F-M SIGNAL GENERATOR—Used for testing and aligning multiplex receivers and adapters. Switches per-

mit generating L+R or L-R separately with or without preemphasis and inserting or removing SCA 67-kc signal.—S. Foldman, Stereo

F-M Multiplex Alignment Signal Generator, *Electronics*, 35:3, p 37-39.

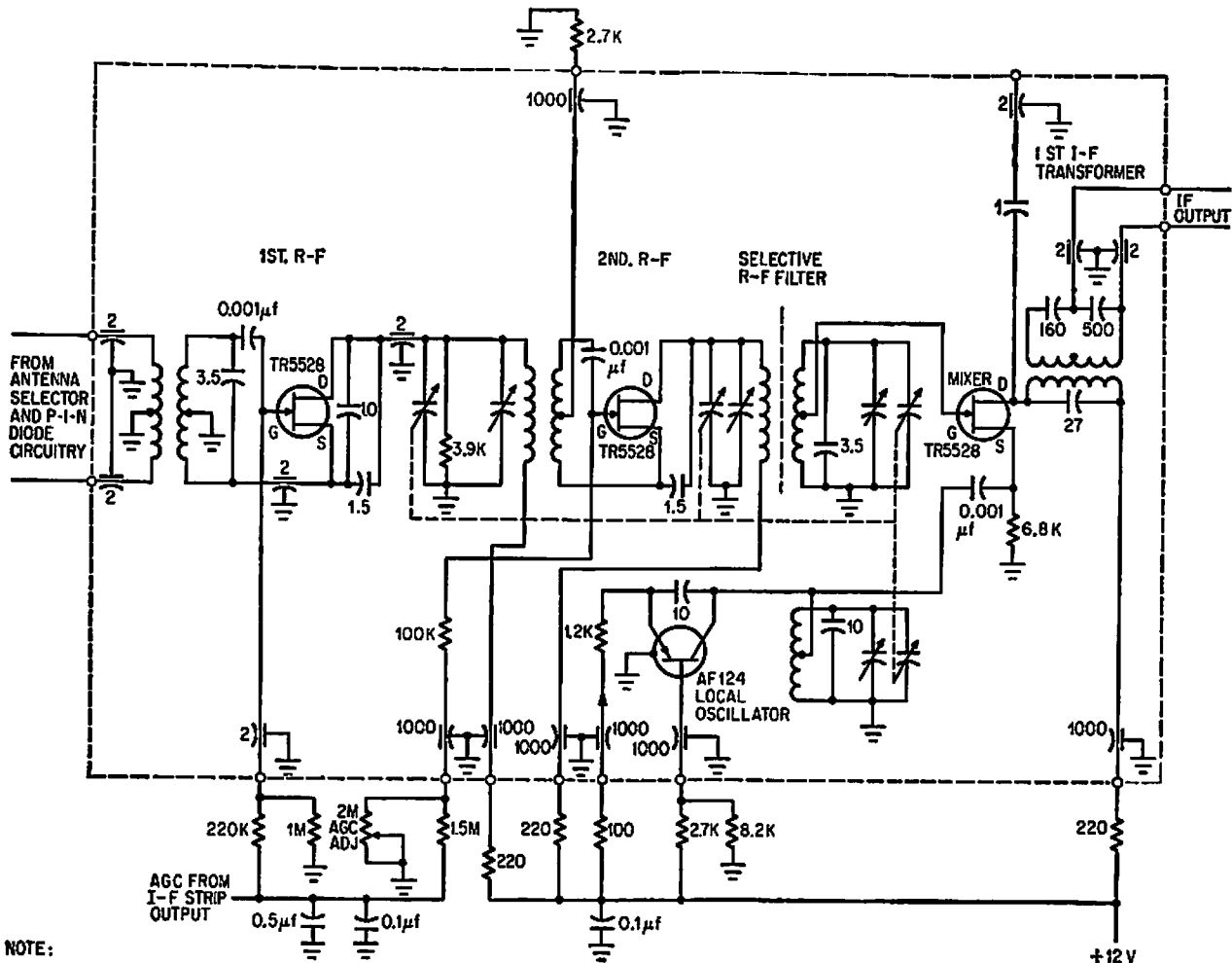


F-M STEREO MATRIXING—Matrixing is completely accomplished before detection. Can be substituted for 67-kc rejection filter of stereo demodulator. Also provides deemphasis.—L. Solomon, *Multiplex Adaptors for Compatible F-M Stereo Reception*, *Electronics*, 34:33, p 45-47.



SUPPRESSED-CARRIER SIGNAL GENERATOR—Output of 38 kc, modulated by L-R signal, is obtained by bias-modulating symmetrical stable multivibrator. Carrier will remain sup-

pressed 46 db below maximum signal level for days.—S. Feldman, *Stereo F-M Multiplex Alignment Signal Generator*, *Electronics*, 35:3, p 37-39.



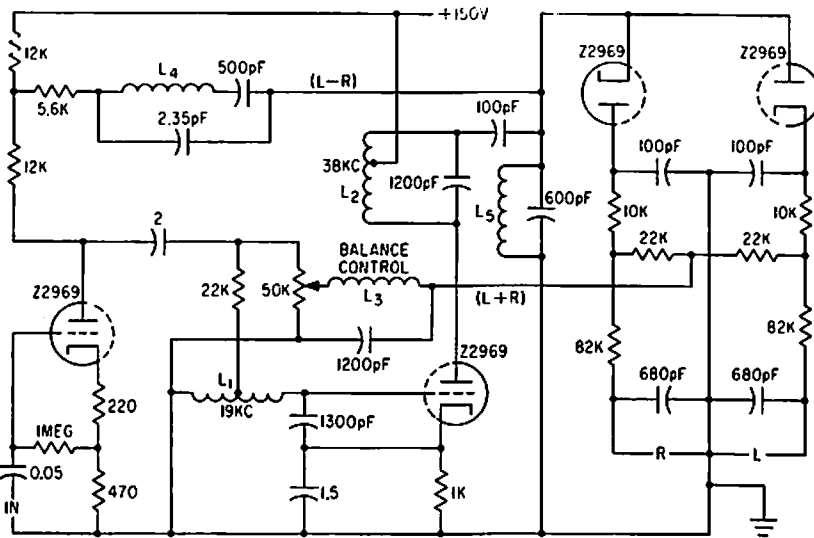
NOTE:

ALL CAPACITORS ARE pf UNLESS DESIGNATED ON DRAWING

FET STEREO-FM TUNER—Uses four tuned r-f circuits, two in high-Q highly selective band-pass filter, plus two fet r-f stages in addition

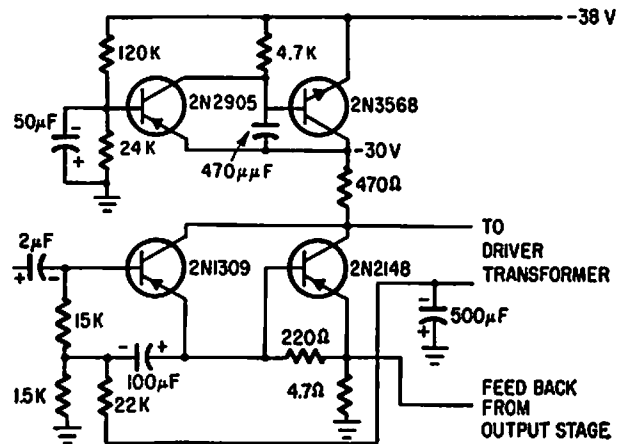
to fet mixer and local oscillator. Agc voltage for the two gain-controlled fet r-f stages is derived from fourth i-f stage, which also

drives narrow-band meter amplifier.—F. L. Mergner, *P-i-n Diode and FET's Improve F-M Reception*, *Electronics*, 39:17, p 114-118.

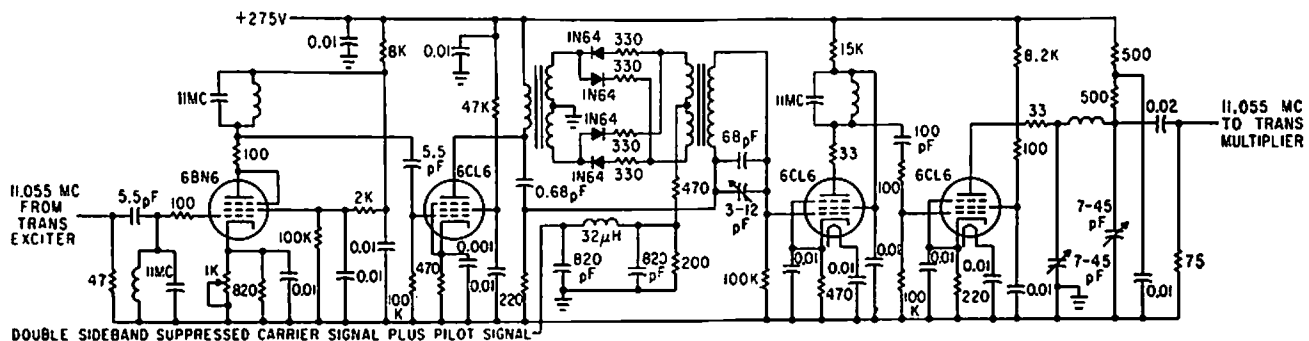


COMPACTRON FOR STEREO—Z2969 compactron performs functions of two triodes and two diodes in circuit for adapting f-m re-

ceivers to stereo.—L. Dillon, Single Compac-
tron Adapts Receiver for Stereo, *Electronics,*
34:43, p 62-64.



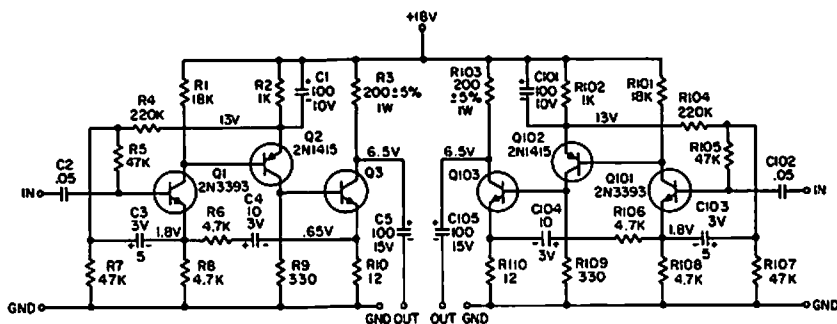
DRIVER—Upper pair of transistors provides voltage regulation, filtering, turn-on time delay, and decoupling for audio driver transistors below.—S. Messin and T. E. Nawalinski, *A Solid State Stereo Set Built in Modules*, *Electronics*, 38:16, p 88-92.



STEREO MULTIPLEX PHASE MODULATOR—Circuit serves for frequency-modulating main carrier signal by stereophonic subcarrier in

four-diode ring modulator to which 19-kc pilot subcarrier is also applied. Resultant signal is phase-modulated at same 11.055-Mc

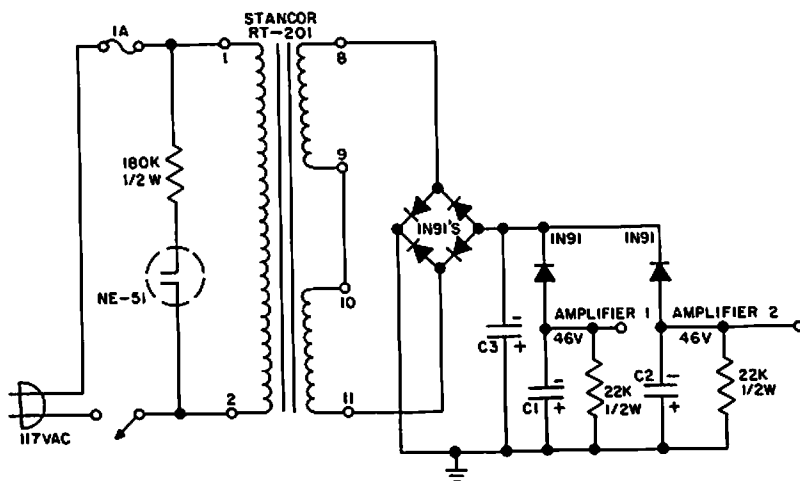
carrier frequency as input signal.—Modifying and F-M Transmitter for Compatible Stereo Multiplex, *Electronics*, 34:28, p 60-62.



NOTE: SELECT R4 AND R104 TO GIVE 13 VOLTS AT EMITTER OF Q2 AND Q102.
Q3 AND Q103-2N2868 OR 11C1536(G.E.)

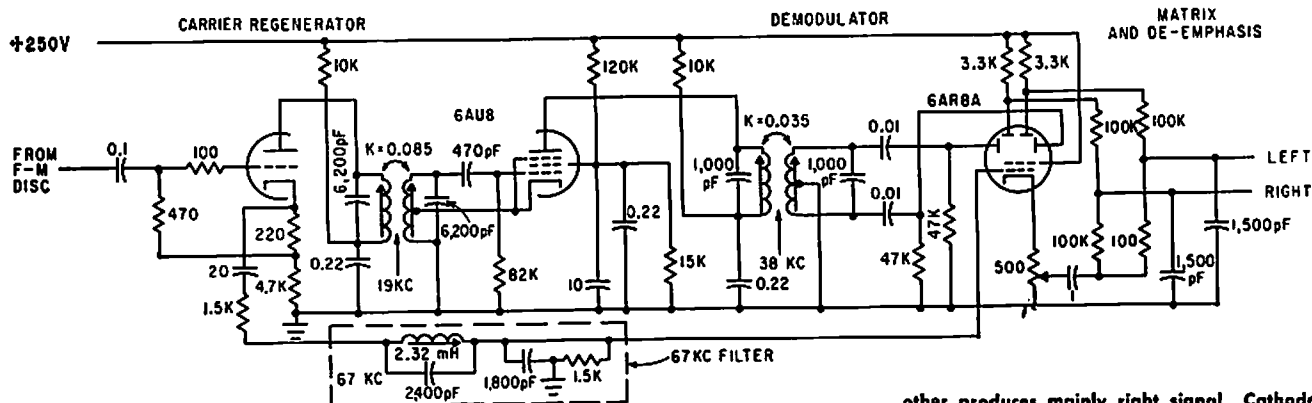
STEREO HEADPHONE AMPLIFIER—Will drive dynamic headphones of 75 to 400 ohms impedance to power level of 60 mw. Program source may be tuner or ceramic cartridge. Frequency response is flat within 0.33 db from 20 cps to 20 kc. High input

impedance, 1 meg up to 2.5 kc and decreasing to 400K at 15 kc, is obtained by using bootstrapped bias network for Q1 along with negative feedback.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 272.



C1, C2, & C3—1500 μ F, 50V.

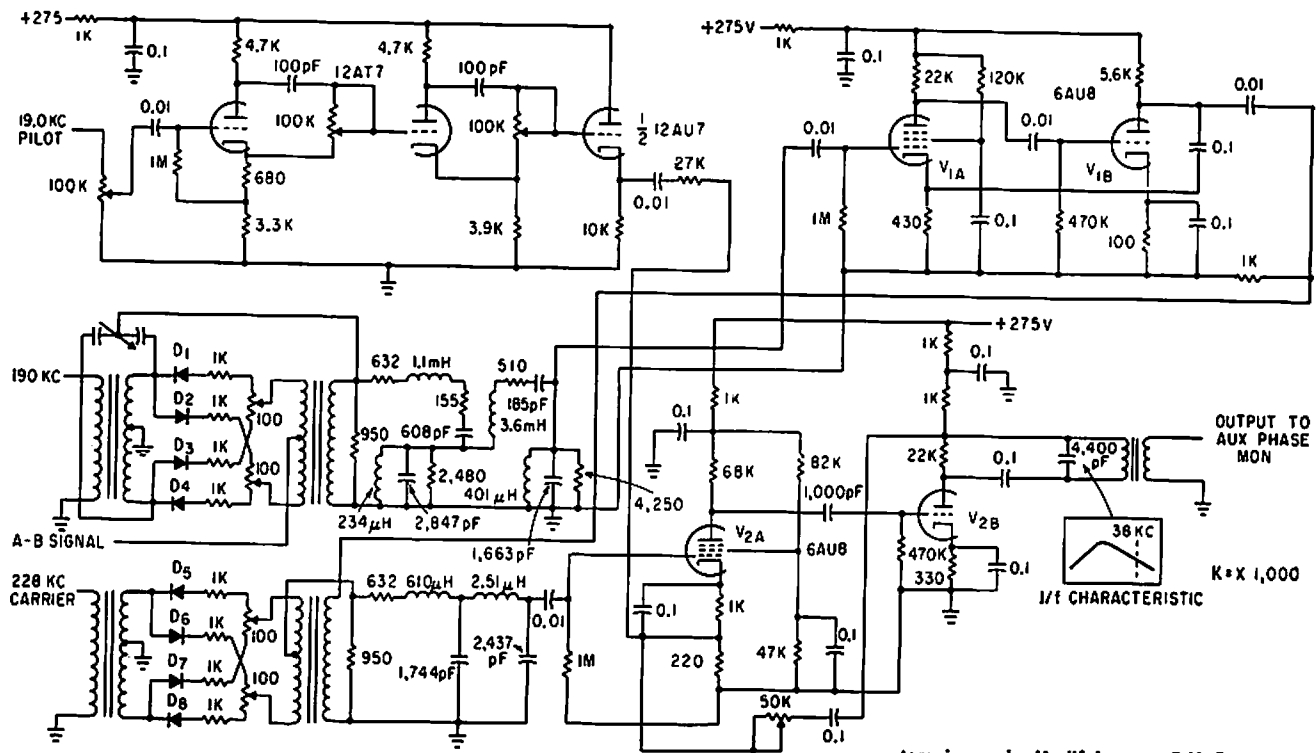
STEREO AMPLIFIER POWER SUPPLY—Diode decoupling provides 80 db of separation between two stereo amplifier channels. Designed for use with 10-w power amplifiers.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 261.



F-M STEREO DEMODULATOR—Signal from f-m discriminator is passed through 67-kc rejection filter to control grid of 6AR8A as elec-

tronic switch, while high-amplitude 38-kc sine wave is applied to its deflection plates. One plate produces mainly left signal, and

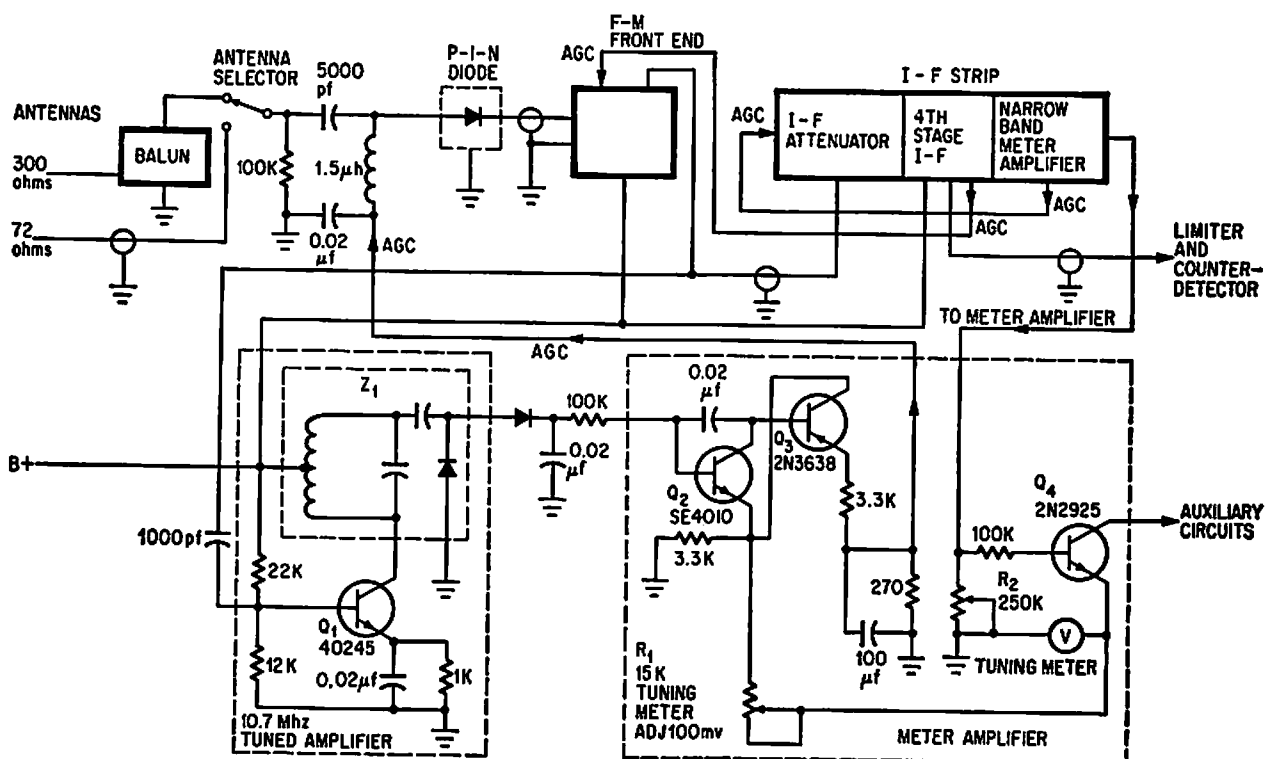
other produces mainly right signal. Cathode gives balanced L+R signal.—L. Solomon, Multiplex Adapters for Compatible F-M Stereo Reception, *Electronics*, 34:33, p 45-47.



STEREO MULTIPLEX SUBCARRIER GENERATOR—Double-sideband suppressed-carrier a-m

subcarrier generator uses inverse feedback for low distortion. Double-modulation sys-

tem is used.—Modifying an F-M Transmitter for Compatible Stereo Multiplex, *Electronics*, 34:28, p 60-62.



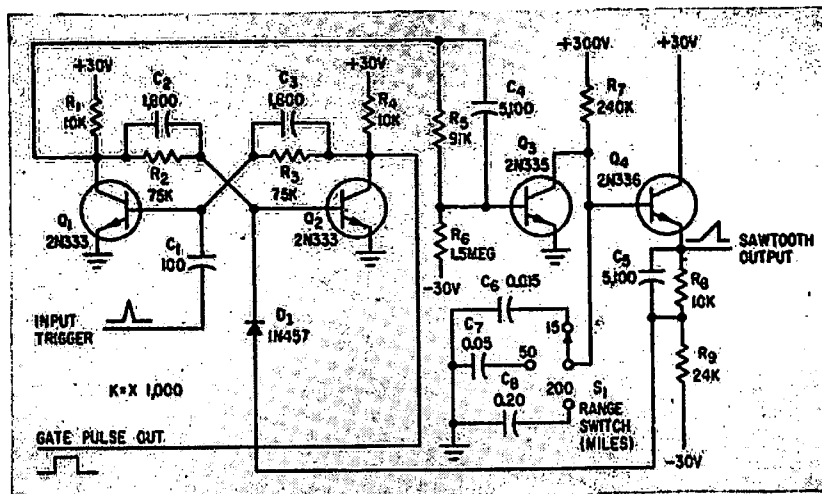
PIN DIODE PROVIDES 120 DB AGC RANGE—Dynamic signal range exceeds 120 db in fully transistorized Fisher TFM-1000 stereo set. Will receive signals as low as 1.5 microvolts without distortion, yet 0.5-v signals can

be handled without overload or spurious response. Solid-state pin diode serves as gain-controlled attenuator. Separate 10.7-Mc tuned amplifier delivers agc voltage that, along with d-c amplifier, controls pin diode.

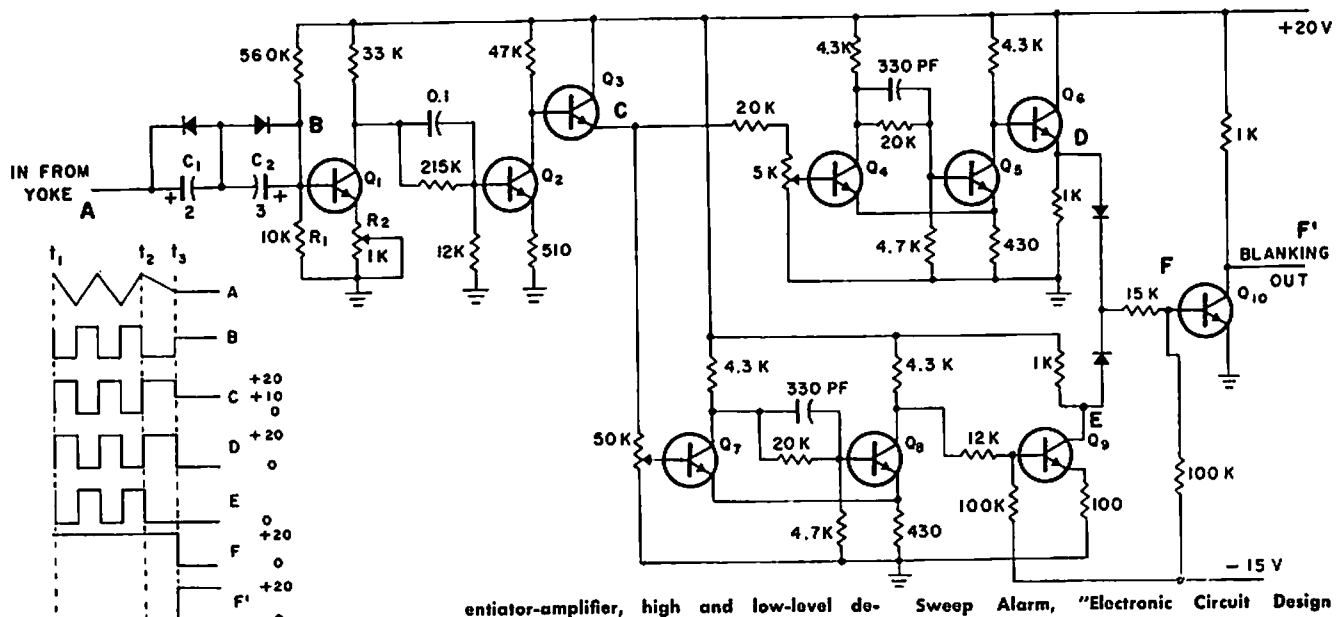
Action of tuned amplifier is delayed until antenna signal is 1 mv.—F. L. Mergner, P-i-n Diode and FET's Improve F-M Reception, *Electronics*, 39:17, p 114-118.

CHAPTER 83

Sweep Circuits



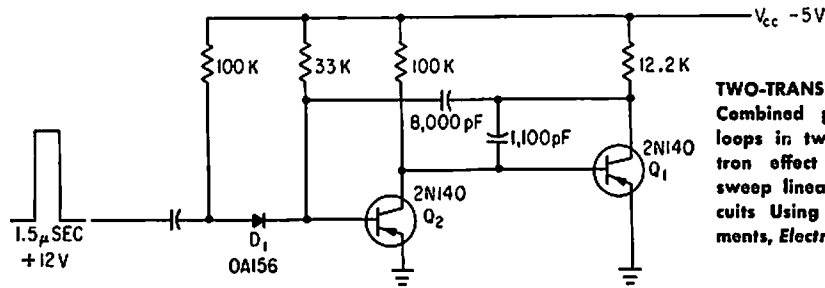
CONSTANT AMPLITUDE FOR THREE RANGES
—Multiple-range sweep generator for airborne radar provides constant-amplitude output sawtooth, along with fast-rising gate pulse or pedestal having duration of sawtooth. Linearity is kept within 1% without using bootstrap.—H. P. Brockman, Sweep Generator Design: How to Keep It Simple, *Electronics*, 33:3, p 92.



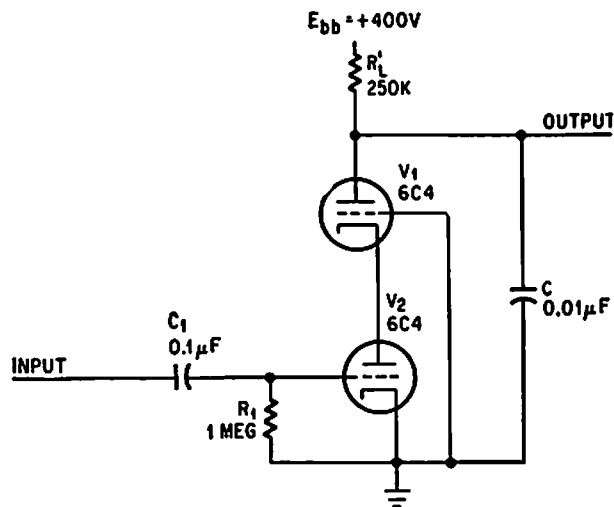
SCANNER SWEEP FAILURE ALARM—Prevents burning of phosphor on face of flying-spot scanner if sweep is lost. Consists of differ-

entiator-amplifier, high and low-level detector, inverter, and summing and blanking generator. All transistors are 2N1302, and all diodes are 1N497.—Flying Spot Scanner

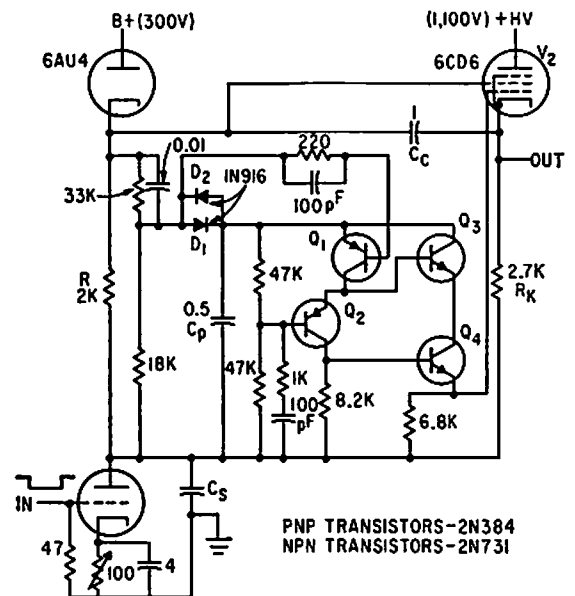
Sweep Alarm, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 125.



TWO-TRANSISTOR PHANTASTRON SWEEP—Combined positive and negative feedback loops in two-transistor circuit give phantatron effect of single pentode with good sweep linearity.—A. S. Kislovsky, *Sweep Circuits Using Two Three-Terminal Active Elements*, *Electronics*, 35:12, p 54-55.

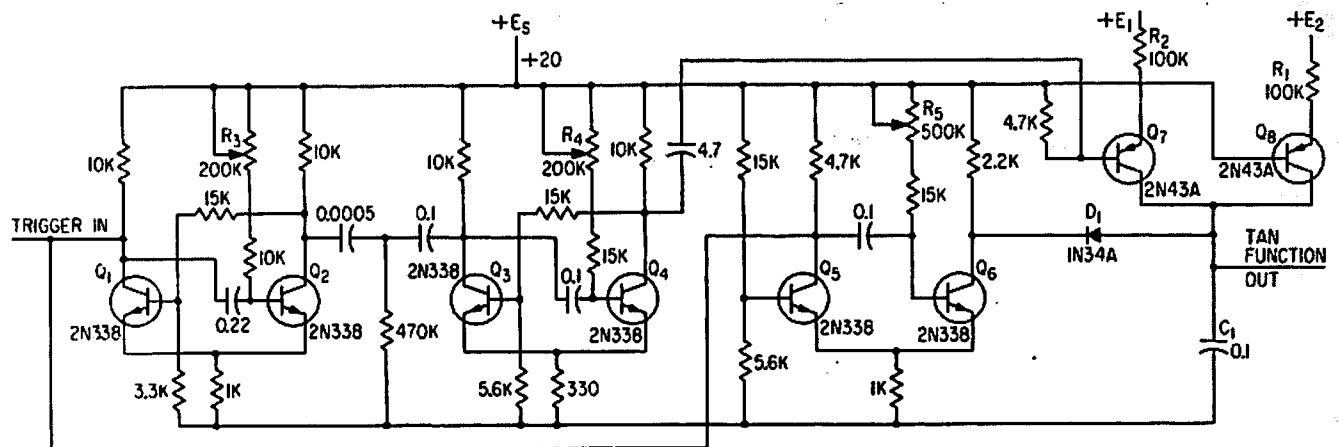


TRIGGERED GROUNDED GRID—Triggered linear sawtooth generator uses grounded-grid amplifier, eliminating initial step voltage that usually occurs in Miller sweep.—C. Sing, *Grounded-Grid Circuit Sweeps Better Than Miller or Bootstrap*, *Electronics*, 38:6, p 83-84.



PNP TRANSISTORS—2N384
NPN TRANSISTORS—2N731

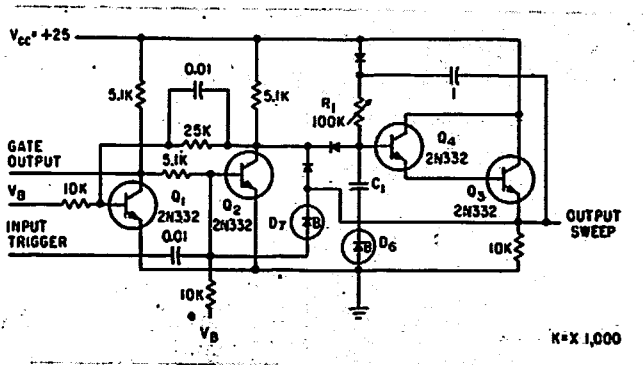
FAST 800-V SWEEP—Hybrid bootstrap arrangement with transistors in cascade generates 800-v sweeps in either polarity with 0.15 microsec duration.—F. C. Creed, *Hybrid Bootstrap Circuits Increase Sweep Linearity*, *Electronics*, 34:31, p 46-48.



TANGENTIAL WAVEFORM GENERATOR—Generates approximation of tangent function for slant-range correction of video signal from

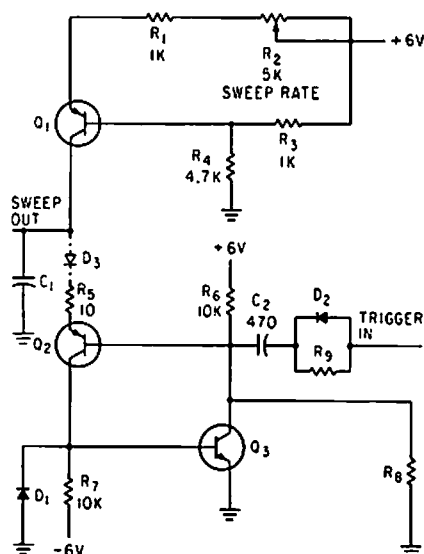
airborne infrared scanner. Ramp mvbr Q5-Q6 and timing mvbr Q1-Q2 are triggered simultaneously.—J. L. Woika, *Generating Tan-*

gential Sweeps for Infrared Mapping, *Electronics*, 34:41, p 64-66.

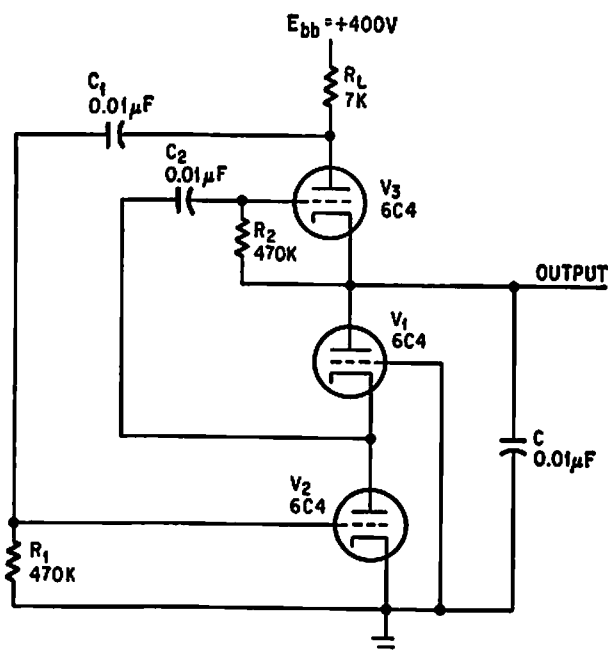


DARLINGTON WITH BOOTSTRAP FLIP-FLOP SWEEP—Transistor Q3 in Darlington connection improves linearity of controllable sweep comparable to vacuum-tube phantas-

tron.—J. B. Payne III, Voltage-Controlled Bootstrap Generator, *Electronics*, 33:11, p 177-178.

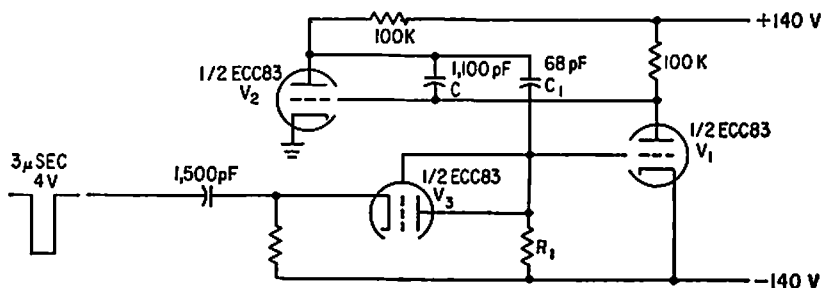


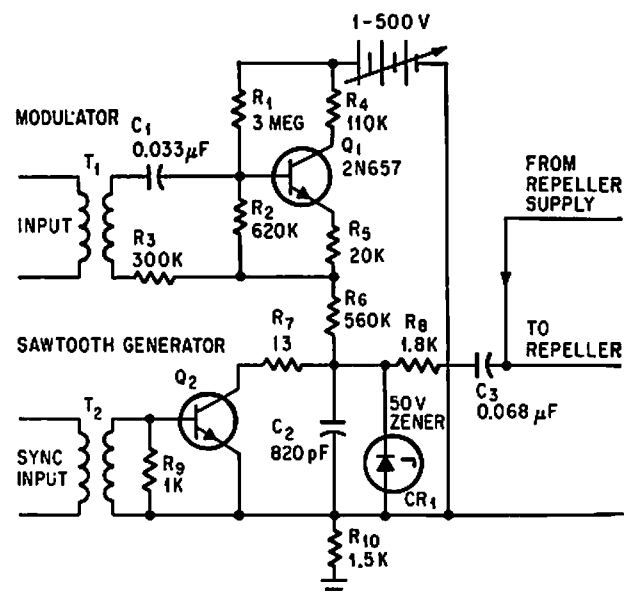
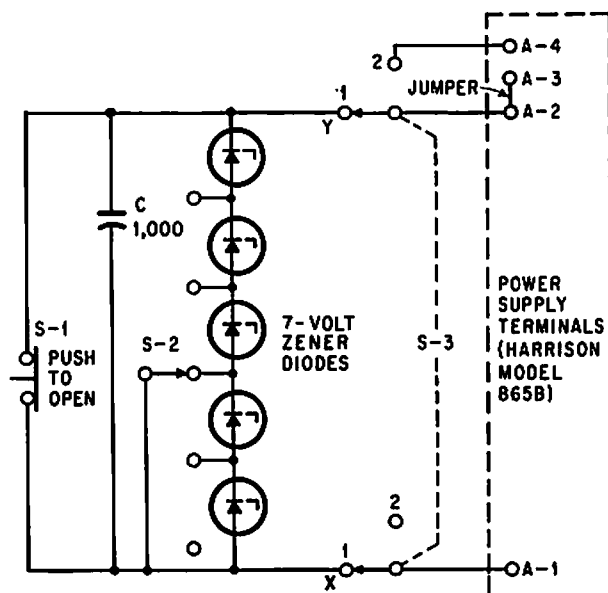
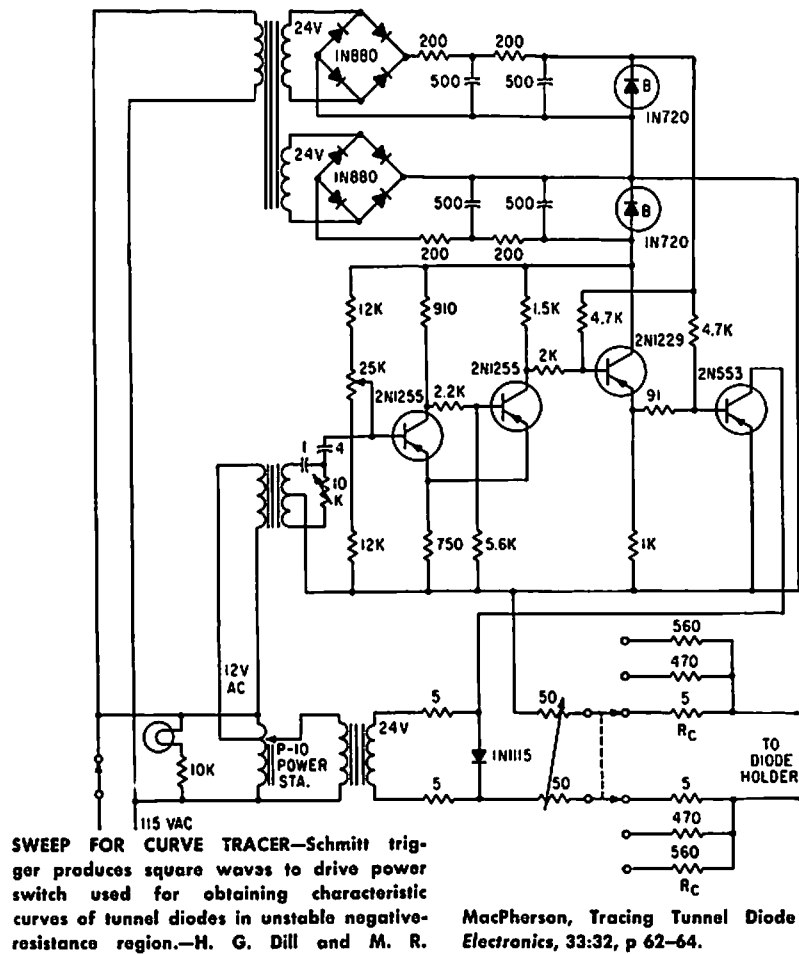
FAST-RESET SAWTOOTH—Regenerative pnp-npn pair in positive-feedback circuit Q1 is constant-current charging source for C1, with R2 varying charging rate and free-running frequency, which can range from 60 cps to 1 Mc.—N. C. Hekimian, PNP-NPN CIRCUITS: New Look at a Familiar Connection, *Electronics*, 35:47, p 42-46.

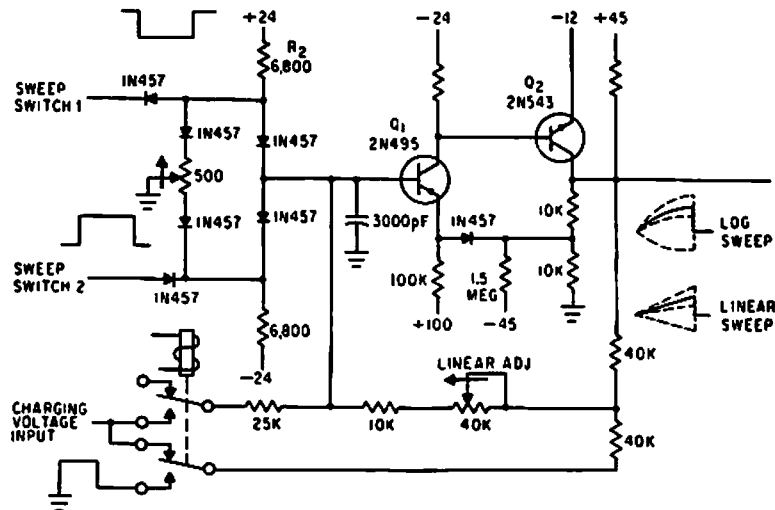


FREE-RUNNING GROUNDED GRID—Has higher output impedance than other time-base sweep circuits. Reducing RL increases period.—C. Sing, Grounded-Grid Circuit Sweeps Better Than Miller or Bootstrap, *Electronics*, 38:6, p 83-84.

TRIODE PHANTASTRON SWEEP—Active elements V1-V2 serve with isolation diode V3 to give action of single pentode with good sweep linearity.—A. S. Kislovsky, Sweep Circuits Using Two Three-Terminal Active Elements, *Electronics*, 35:12, p 54-55.

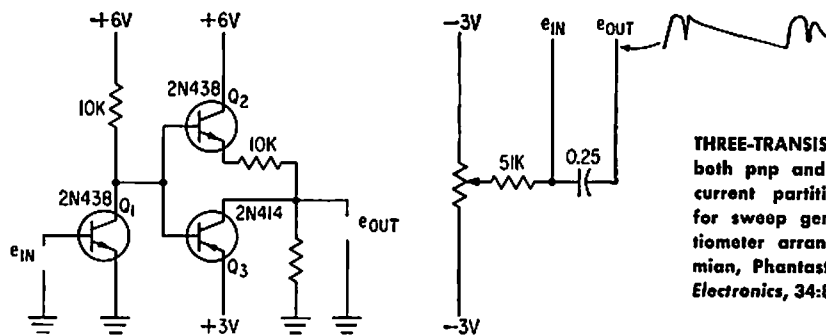




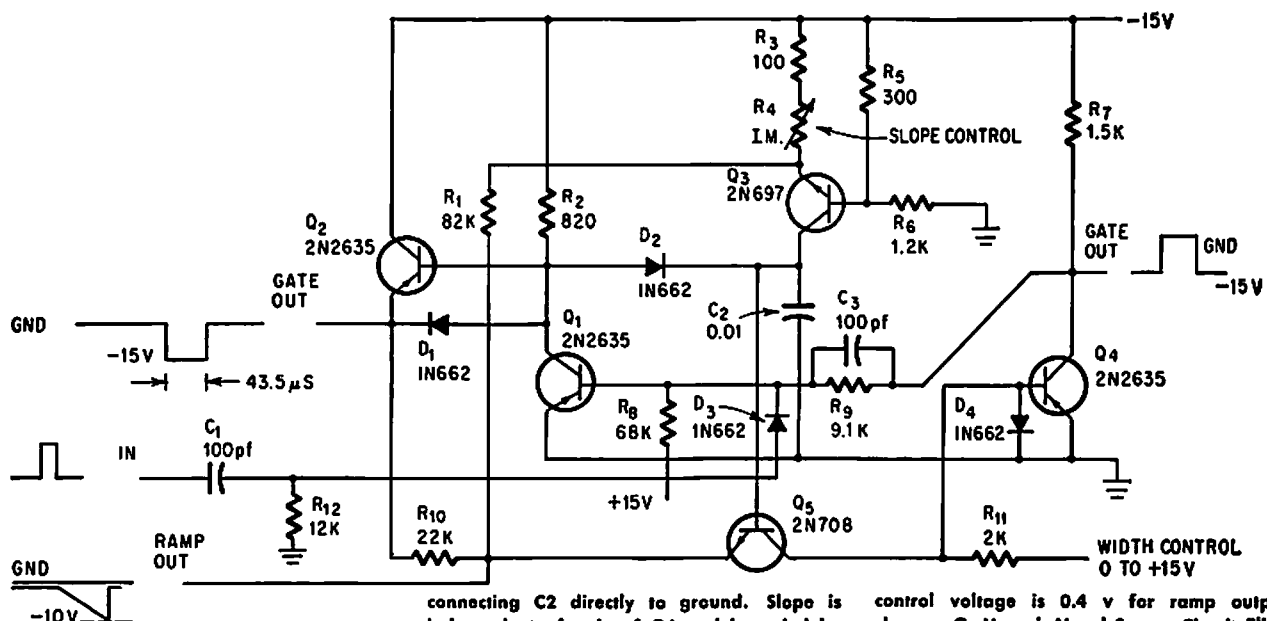


LOG SWEEP—Resistance coupling in feedback loop permits positive-going as well as negative-going waveforms. Circuit gives choice of logarithmic, exponential, or linear

sweep output. Relay switches between linear and long sweep.—J. Curry and W. Sander, Bootstrap Generates Logarithmic Sweeps, *Electronics*, 33:52, p 60.



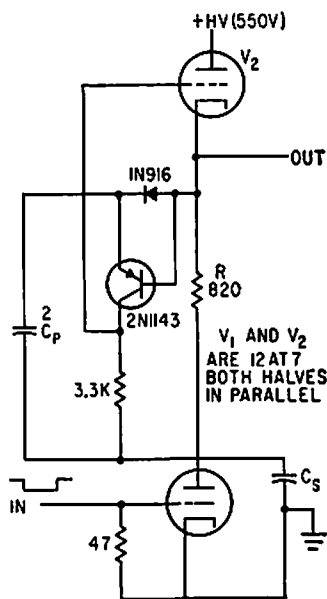
THREE-TRANSISTOR PHANTASTRON—Use of both pnp and npn transistors gives desired current partition, while feedback required for sweep generator is provided by potentiometer arrangement at right.—N. C. Hakimian, Phantastron Circuits Using Transistors, *Electronics*, 34:8, p 46-47.



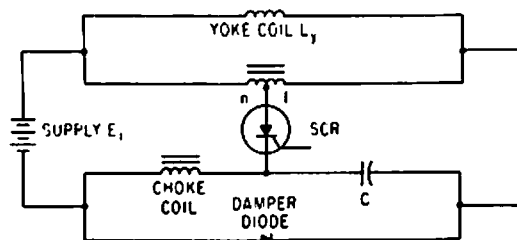
PHANTASTRAN—Transistorized version of phantastron eliminates voltage pedestal by

connecting C2 directly to ground. Slope is independent of gain of Q4 and is varied by R4 ove. range of 100 to 1. Duty cycle can be up to 98%. R4 is 1,000 ohms and width

control voltage is 0.4 v for ramp output shown.—G. Marosi, Novel Sweep Circuit Eliminates Ramp Pedestal, *Electronics*, 38:26, p 68.

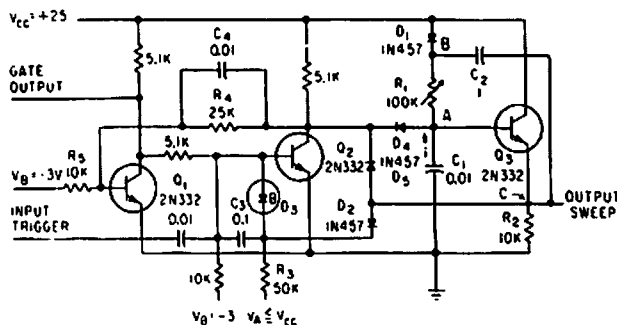


BOOTSTRAP SWEEP FOR CRO—Hybrid circuit has high linearity and moderate sweep speed.—F. C. Creed, Hybrid Bootstrap Circuits Increase Sweep Linearity, *Electronics*, 34:31, p 46-48.



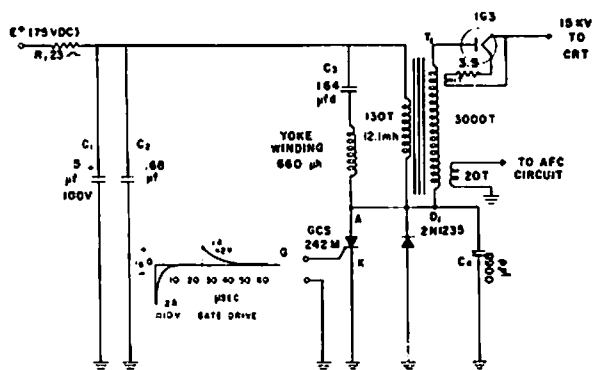
SCR HORIZONTAL SWEEP—Scr, fired by trigger pulse at start of retrace, transfers to yoke coil the energy stored in 1.27-mfd capacitor C. At end of retrace, energy transfer is completed, damper diode turns on, and energy in yoke returns to supply source E1,

giving linear sweep current through yoke. Scr can be General Electric C35, C36, or C40 series. Yoke is 200 microhenrys.—T. Tarui, New Deflection Circuit Uses SCR, Saves Power, *Electronics*, 36:32, p 56-57.



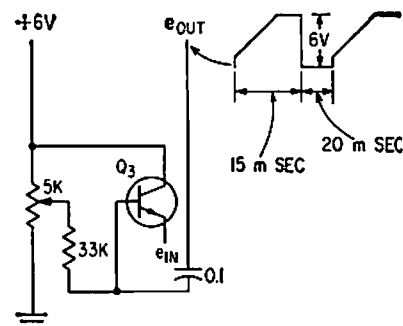
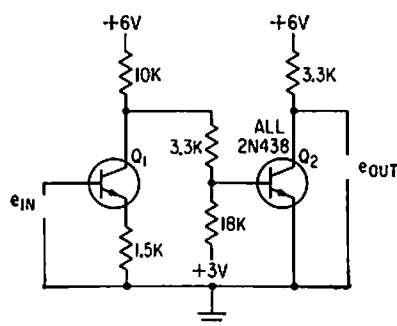
VARIABLE BOOTSTRAP FLIP-FLOP SWEEP—Gives same type of waveform as phantatron. Output pulse length can be varied by

d-c bias or by control voltage.—J. B. Payne III, Voltage-Controlled Bootstrap Generator, *Electronics*, 33:11, p 177-178.



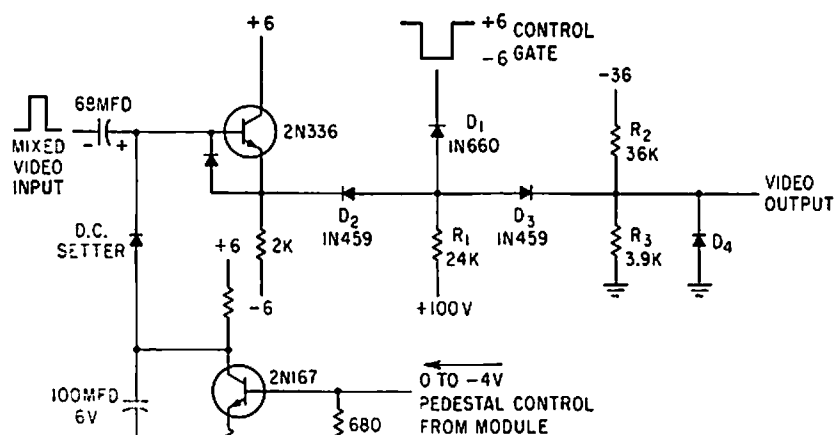
GCS HORIZONTAL SWEEP—Uses gate-controlled switch GCS to replace horizontal output tube in television receiver, and semiconductor diode D1 to replace damper. GCS can cut off 2.5-amp peak current in 500 nsec.—J. W. Motto, Jr. GCS Sweep Circuit, *EEE*, 12:5, p 89-90.

TWO-TRANSISTOR PHANTASTRON—Q1 and Q2 simulate vacuum-tube phantatron sweep generator. Since input impedance is low, linearity can be improved by using emitter-follower Q3.—N. C. Hekimian, Phantatron Circuits Using Transistors, *Electronics*, 34:8, p 46-47.



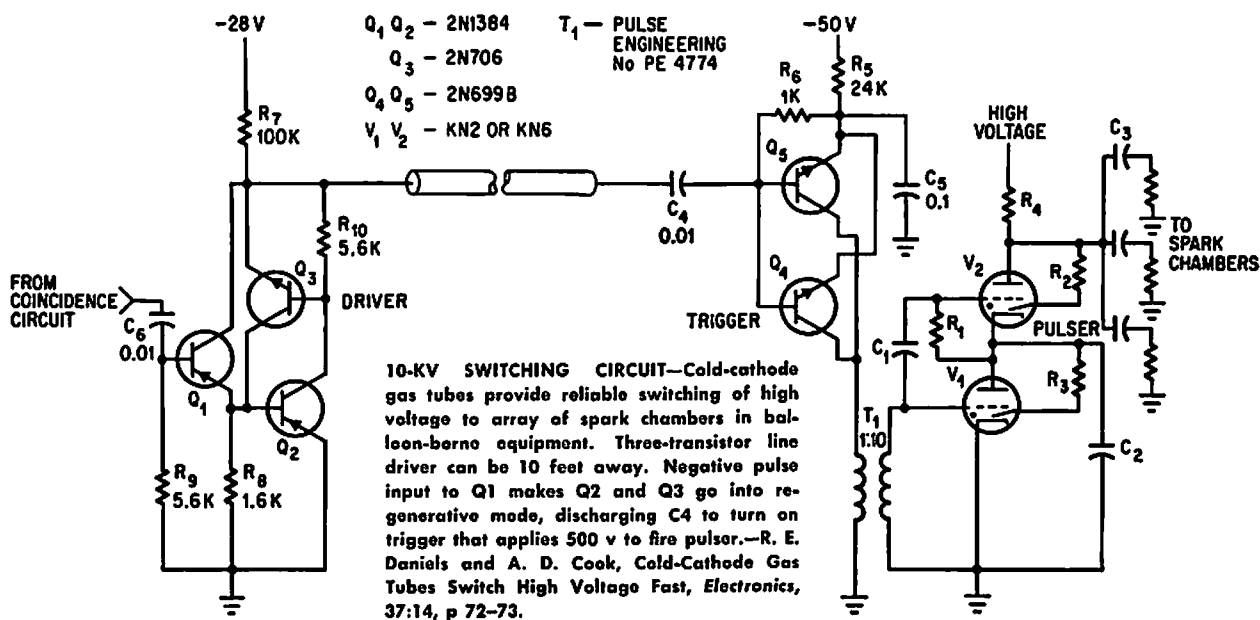
CHAPTER 84

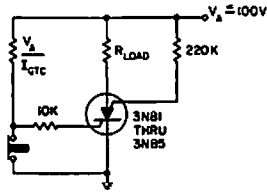
Switching Circuits



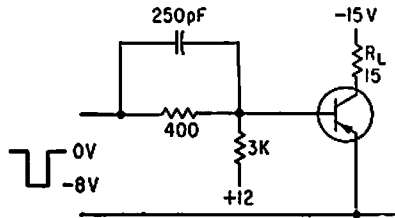
THREE-DIODE SWITCH FOR VIDEO TIME SHARING—Gate pulse applied to diode D1 draws current from R1 through D1 (switch open) or through D2 and D3 (switch closed

so video passes from input to output). Clamp diode D4 keeps base line of output always positive.—T. Vagt, *New Light on Air Traffic: Bright Plan Display with Alphanumerics*, *Electronics*, 36:30, p 42-46.

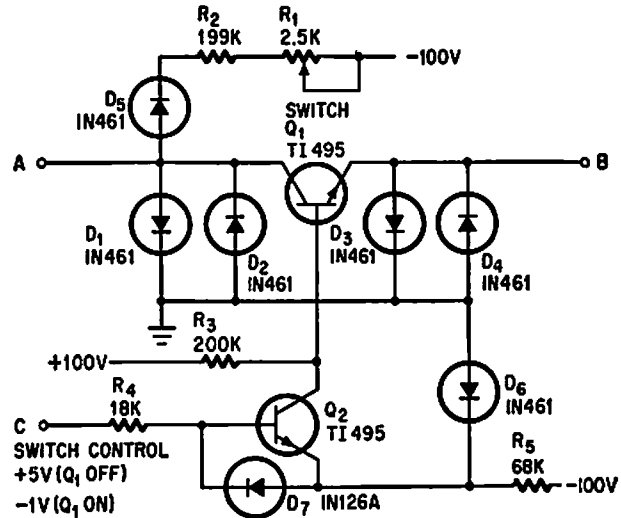




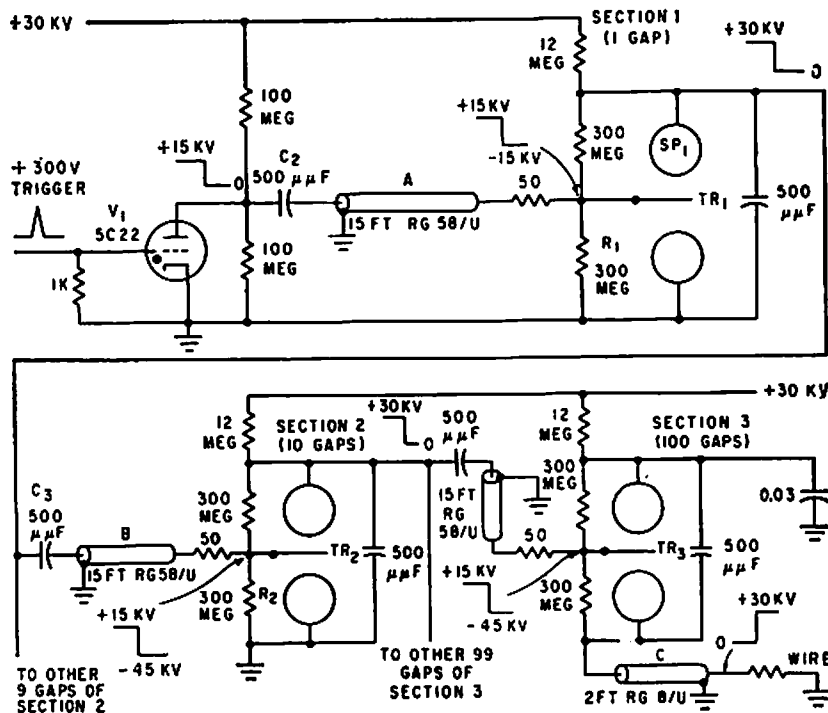
SCS CONTACT ISOLATOR—Eliminates contact bounce in grounded-switch arrangement where switch is opened to trigger scs.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 433.



HIGH-CURRENT HIGH-SPEED SWITCH—Alloy junction transistor (2N2648 germanium) has useful frequency range up to 10 Mc for switching up to 1 amp with gain of 50 in 0.8 microsec.—Medium Power Transistor Has Multiple Uses, *Electronics*, 36:1, p 132.

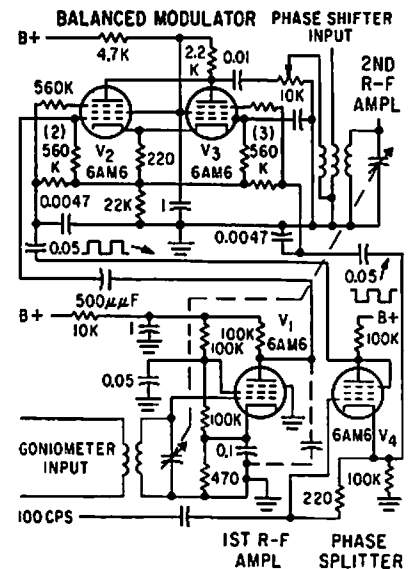


OPERATIONAL AMPLIFIER CONTROL—Switch must pass 1 ma in either direction (A to B or B to A). Diode pairs D1-D2 and D3-D4 are voltage limiters, while Q1 is basic switching element. Switching time is about 2 microsec. Used as sample-and-hold circuit, with 500-microsec sampling interval.—R. W. Maloy, Transistor Switch Passes Current Both Ways, *Electronics*, 38:1, p 79.

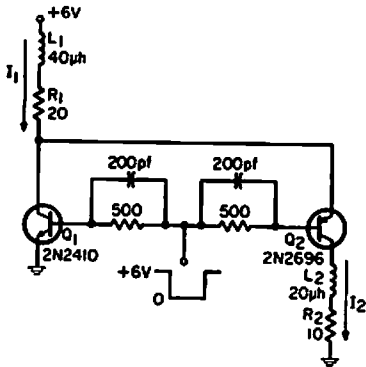


SPARK-GAP SWITCH—Bank of 111 spark gaps switches 30 kv in less than 10 millimicrosec, to explode wire for generating plasma in shock tube or generating hypersonic waves in wind tunnel. Jitter problem is solved by using one spark gap to trigger another.

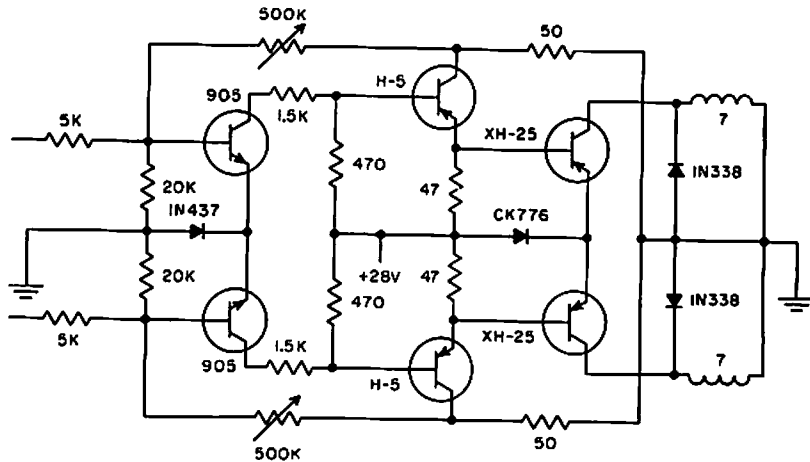
Thyratron V1 fires section 1 having single gap, and this triggers 10 gaps in section 2, each of which in turn triggers 10 gaps in section 3.—H. B. McFarlane, Spark Gaps for Fast High-Voltage Switching, *Electronics*, 32:31, p 72-73.



ELECTRONIC R-F SWITCH—V1 isolates gonimeter from balanced modulator tubes V2 and V3, which provide 90° phase shift of gonimeter signal. V2 and V3 are made to conduct alternately by 100-cps-drive applied to their suppressor grids by phase-splitter V4.—J. F. Hatch and D. W. G. Byatt, Direction Finder with Automatic Roadout, *Electronics*, 32:16, p 52-54.

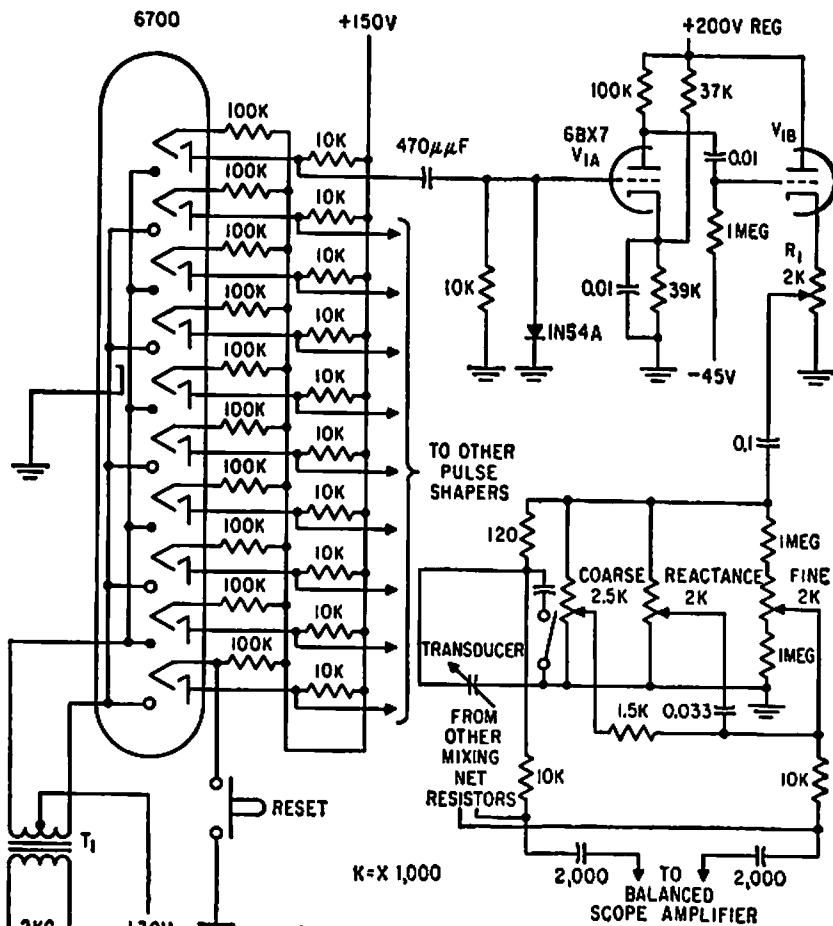


FAST INDUCTIVE SWITCHING—Fast-rise current switching circuit provides current as fast as it can be switched into highly inductive load. Used as current driver for digital computer memory arrays as well as for speeding up ordinary relays.—T. W. Collins, Fast-Rise Current Switch, *EEE*, 13:1, p 65-66.



HIGH-SPEED TRANSISTOR RELAY—Push-pull switch handles up to 10 amp with rise time of 50 microsec. Rising d-c voltage at input produces no output until predetermined level is reached, when power supply voltage is suddenly switched across load.

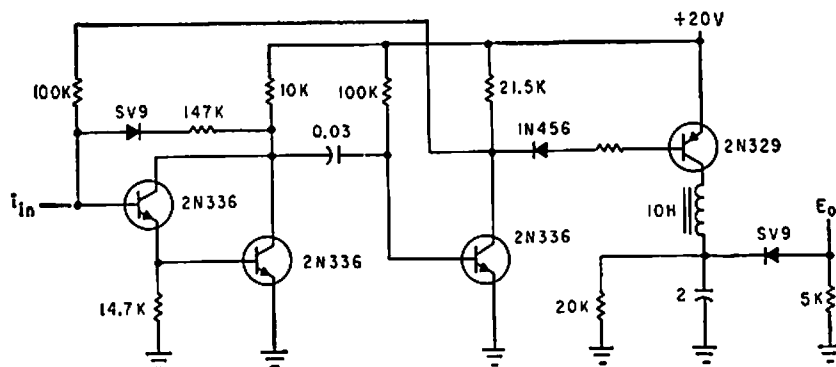
Circuit remains locked in until input voltage drops below trip level. Trip voltage is determined largely by breakdown voltage of 1N437 zener diode.—D. L. Anderson, Fast Transistor Relay, *Electronics*, 31:11, p 145.



SWITCHING CAPACITANCE TRANSDUCERS—Beam-switching tube pulses bridges sequentially. Each bridge has pressure-sensitive

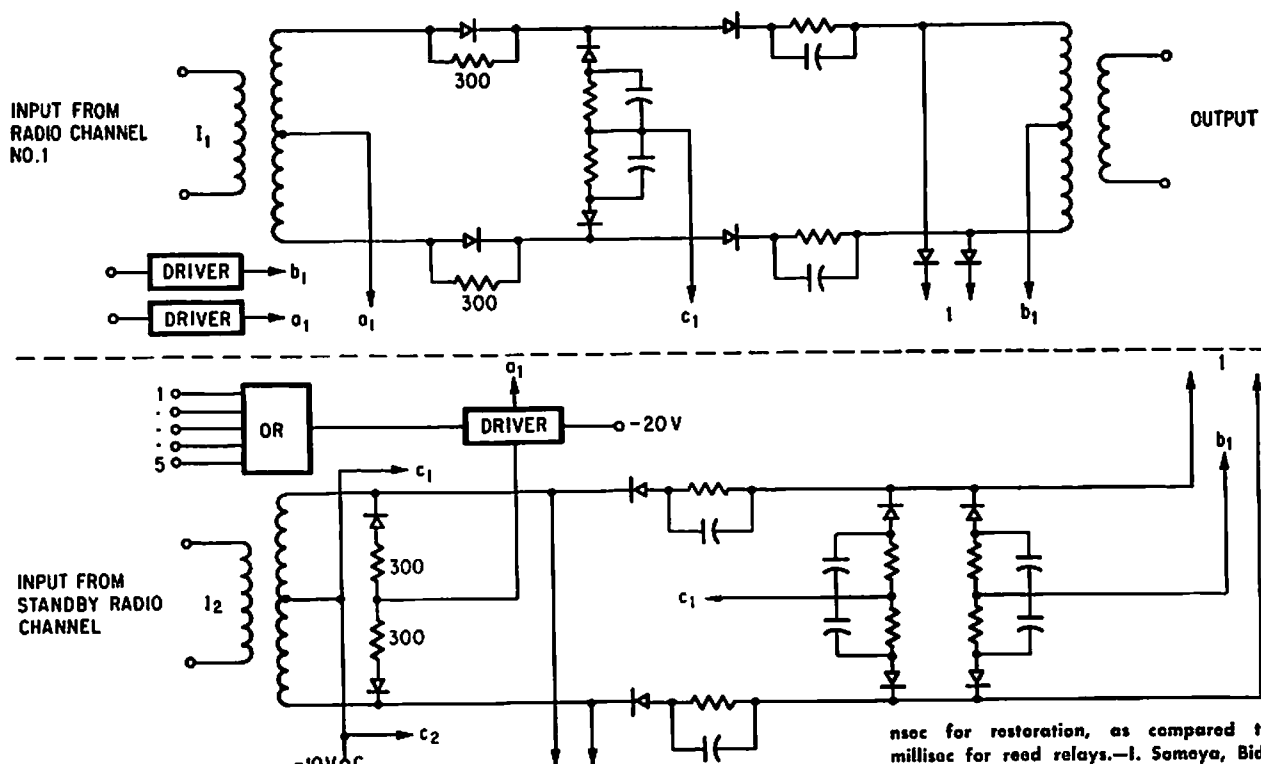
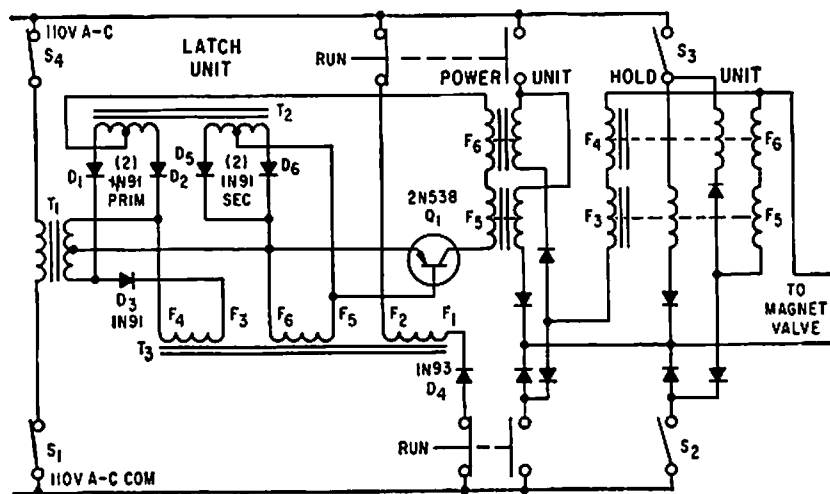
capacitance transducer that unbalances bridge in proportion to changes in pressure. Outputs of bridges are displayed as pulse pattern corresponding to pattern of pres-

sure imposed on auto seat.—A. Stiebel, High-Speed Switching of Low Level Signals, *Electronics*, 32:12, p 54-55.



MISSILE COUNT-DOWN SWITCH—Level-sensitive switch uses nonlinear negative feedback to provide stable operation (within 1.5%) over 100°C temperature range. Monostable mvbr is followed by rectifying transistor and filter. For signals above trigger level, circuit is periodically switched into its transient state.—D. W. Boensel, *Switching Circuits for Missile Count-Downs*, *Electronics*, 32:31, p 76-78.

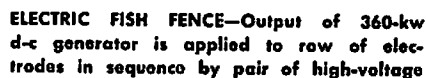
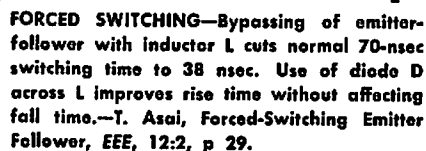
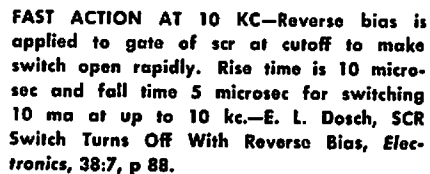
PUNCH PRESS SAFETY SWITCH—Static switching control for dangerous presses requires that both hands of operator be on run push-buttons, out of danger area, before ram can descend. To prevent operators from jamming or taping one or more buttons closed, control circuit stops press at end of cycle. Both buttons must then be released and depressed again to start new cycle. Self-excited magnetic amplifier operates much like snap switch.—S. A. Zarling, *Static Switching Techniques for Machine-Tool Safety*, *Electronics*, 32:24, p 57-59.



SOLID-STATE SWITCHING—Used in high-speed data transmission system having 15-

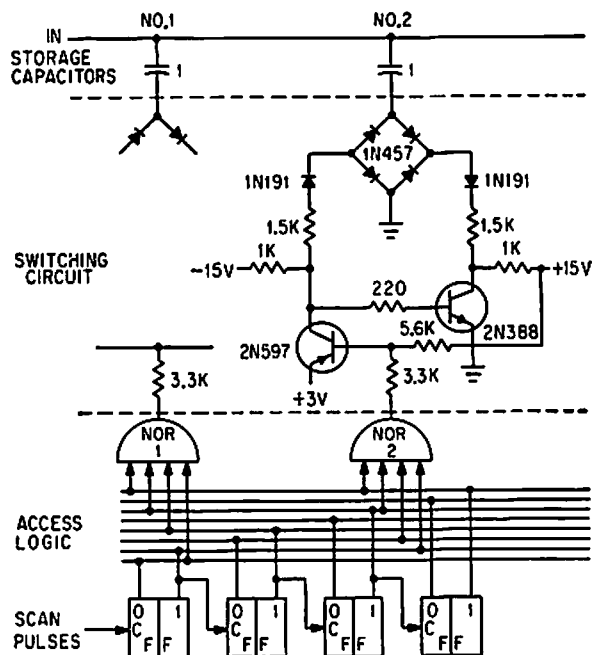
gc bandwidth. Use of Kita diodes gives transition time of 20 nsec for break and 40

nsec for restoration, as compared to 1 millisecond for reed relays.—I. Somaya, *Bidding For World Leadership in Solid State Microwave Gear*, *Electronics*, 38:25, p 99-105.



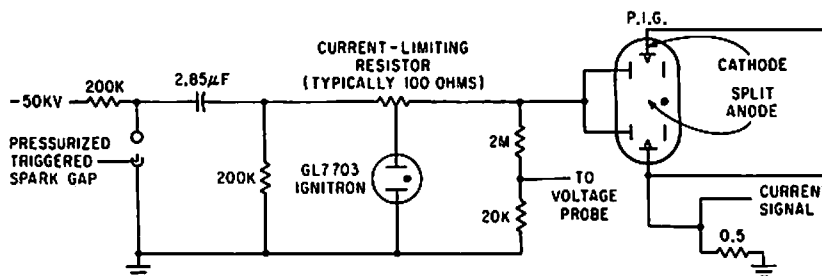
ignitron tubes that turn pulse on and off for each electrode in turn. Single turnoff ignitron terminates pulse period of whichever

loaded ignitrons are conducting.—C. D. Volz, Ignitron-Pulsed Electric Fence Guides Migrating Salmon, *Electronics*, 35:16, p 50-52.



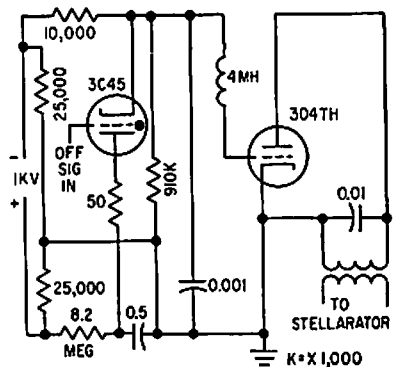
ROW STORAGE UNIT FOR MAGNETIC CONTOUR DISPLAY—Scan pulses activate *nor* gates in sequence. For 11 by 11 display matrix, there are 11 *nor* gates each with its switching circuit. On read-in, *nor* gate output of -10 v activates switching circuit, grounding

its capacitor and making capacitor charge up to value of that data point.—W. W. Anderson, Latest Antisubmarine Aid—Magnetic Contour Display System, *Electronics*, 36:32, p 58-61.

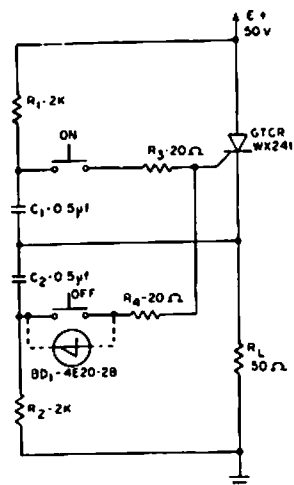


IGNITRON SHORTS SPARK GAP—Used for continuous production of plasma in mirror-geometry magnetic field of Philips ionization gage. Ignitron shorts spark gap about 100 microsec after discharge begins. Ionization

gage (PIG) receives positive potential at peak of externally applied magnetic field by closing of triggered spark gap.—M. F. Wolff, Plasma Engineering—Part 1: Generating and Heating Plasma, *Electronics*, 34:24, p 47-53.

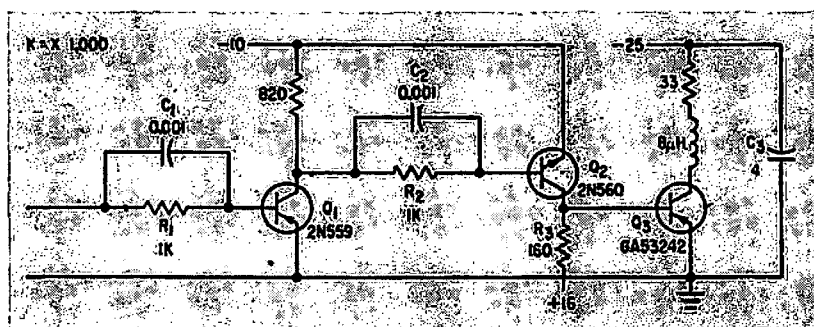


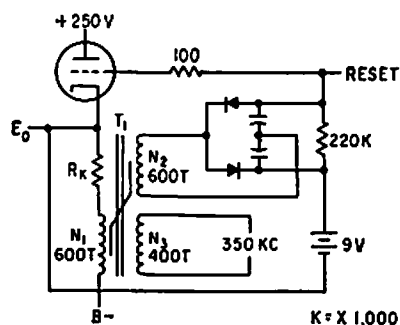
CROWBAR—Used to cut off oscillator sharply in tank circuit of stellarator. Consists of tube with plate holdoff rating comparable to peak instantaneous output tank voltage. At end of pulse, grid is driven to +500 v and tube becomes low impedance across tank, to damp out oscillation within a cycle or two.—R. L. Gamblin, Radio-Frequency Circuits for Plasma Physics, *Electronics*, 32:27, p 50-52.



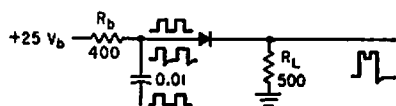
GATE-TURNOFF D-C CIRCUIT BREAKER—Closing on switch discharges C1 into gate to initiate turn-on. Closing off switch discharges C2 out of gate, opening power circuit.—J. W. Motto, Jr., Switching Circuits Using the Gate Turnoff Controlled Rectifier, *EEE*, 13:3, p 52-55.

MEMORY DRIVER AMPLIFIER—Proves 750-ma current pulse for 8-microhenry load, at repetition rate of 0.25 Mc. Positive turnoff voltage is automatically applied, with no extra loss in gain or power, by driving pnp transistor Q3 with npn transistor Q2.—J. S. Ronne, Computer Switching With High-Power Transistors, *Electronics*, 33:10, p 44-47.

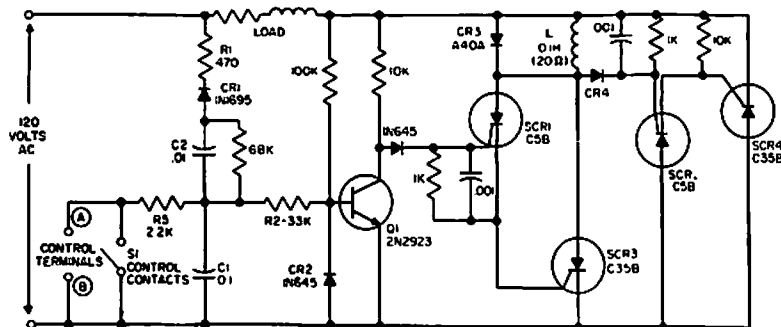




SATURABLE REACTOR LATCH—Offers operating simplicity, high speed, and low cost. Transistor model can switch in less than 0.5 microsec. Current through N1, when V1 conducts, saturates core of T1.—W. J. Roop, *Simple Latch Circuit Uses Saturable Reactor*, *Electronics*, 33:2, p 66.

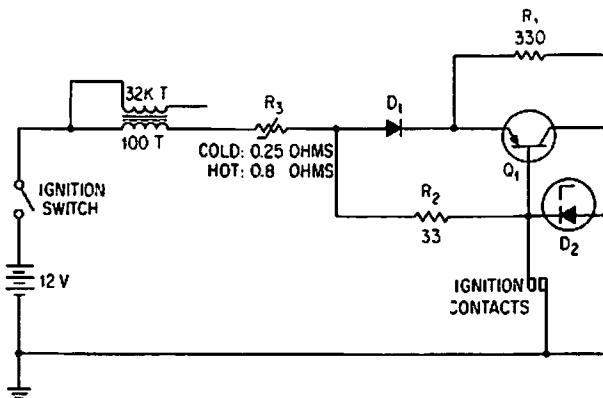
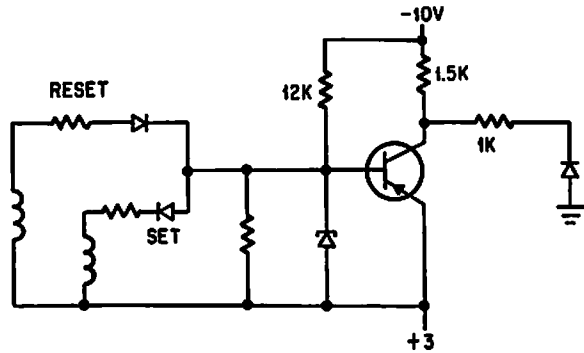


SENSITIVE A-C POWER SWITCH—Used to switch load in response to gradually changing signal, as from photocell or thermistor. Provides snap-action switching from full on to full off, with differential between



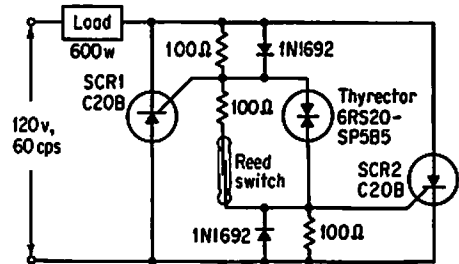
LIGHT-BEAM COUPLING—Gallium arsenide light source and silicon photodetector pre-

TEMPORARY MEMORY—Arrangement using Esaki or tunnel diode is equivalent to bistable flip-flop. Used in 24-channel pulse code modulation system.—T. Kojima and M. Watanabe, *When You're Second, You Try Harder*, *Electronics*, 38:25, p 81-89.

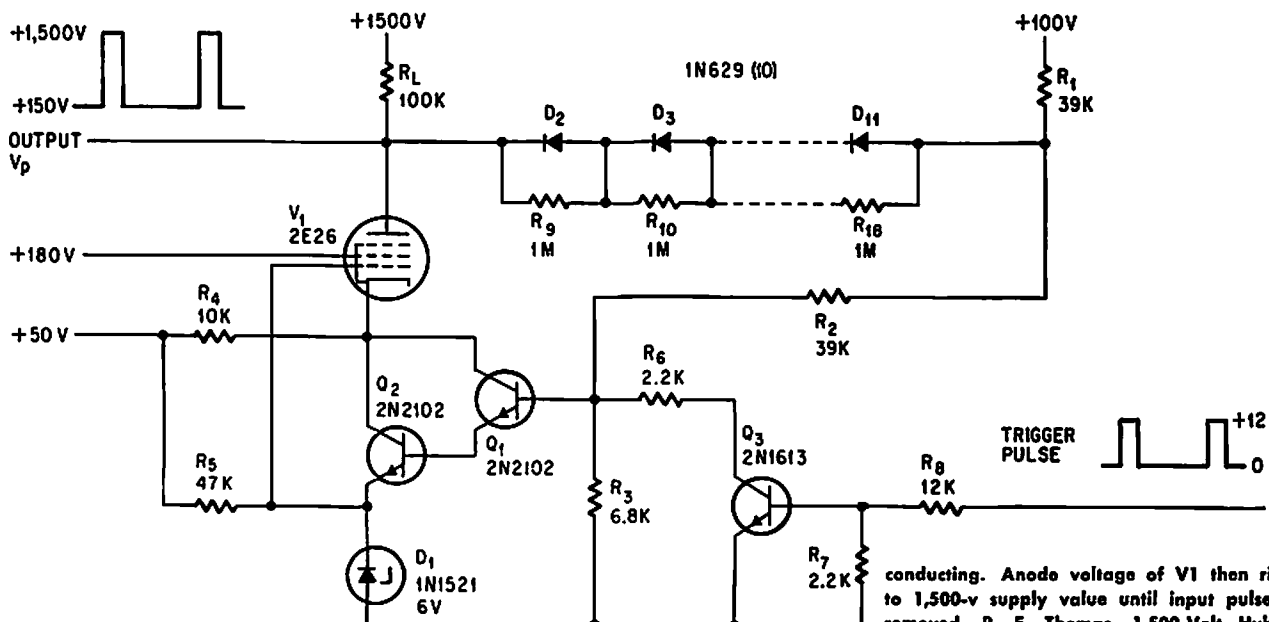


TRANSISTOR-SWITCHED IGNITION—Ignition breaker points handle only current of about 0.25 amp for switching transistor Q1, increasing contact life, while transistor handles 9-amp peak ignition coil current. Diode D1 reverse-biases emitter-base junction when

distributor contacts are open, to ensure transistor cutoff at high temperatures. Zener D2 clips peaks of transients that might damage transistor.—S. B. Gray, *Home and Auto Controls*, *Electronics*, 36:19, p 52-56.



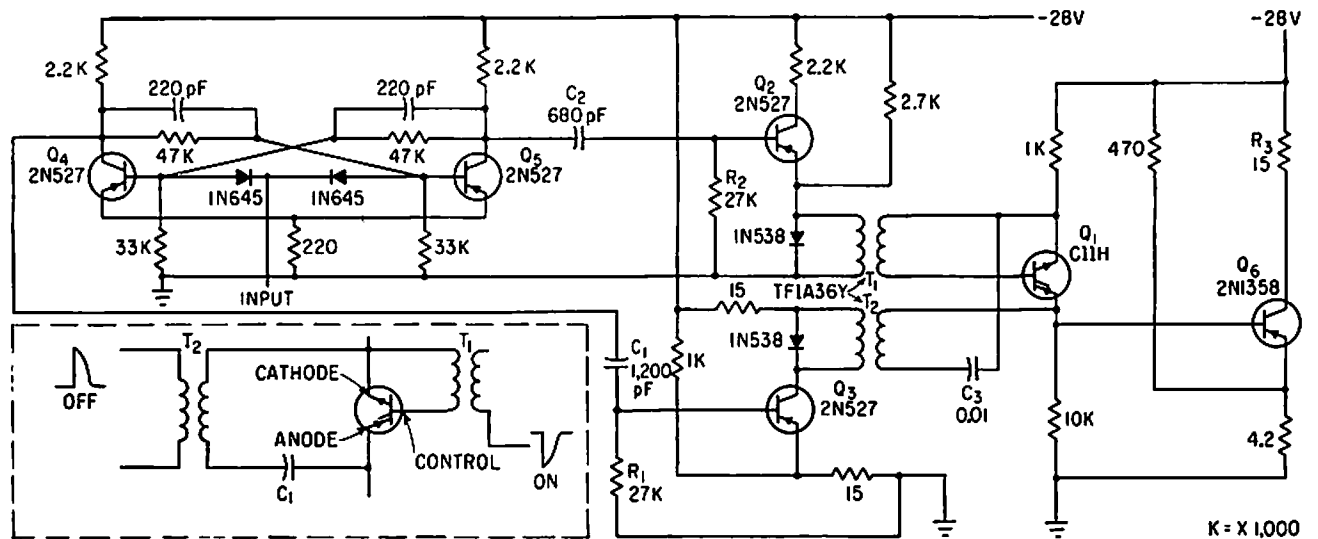
SCR-REED A-C SWITCH—Magnetic reed switch makes ideal trigger for silicon controlled rectifiers, even though nine components are required for switching 600-w load.—M. P. Southworth, *Bidirectional Static Switch Simplifies Ac Control*, *Control Engineering*, March 1964, p 75-76.



HYBRID SWITCH PROVIDES 1,500 V PULSE—Trigger pulse at 12 v saturates Q3, voltage

across R3 drops below level of zener D1, and Q1 and Q2 turn off, making V1 stop

conducting. Anode voltage of V1 then rises to 1,500-v supply value until input pulse is removed.—R. E. Thomas, *1,500-Volt Hybrid Switch has Low "On" Impedance*, *Electronics*, 37:22, p 74-75.

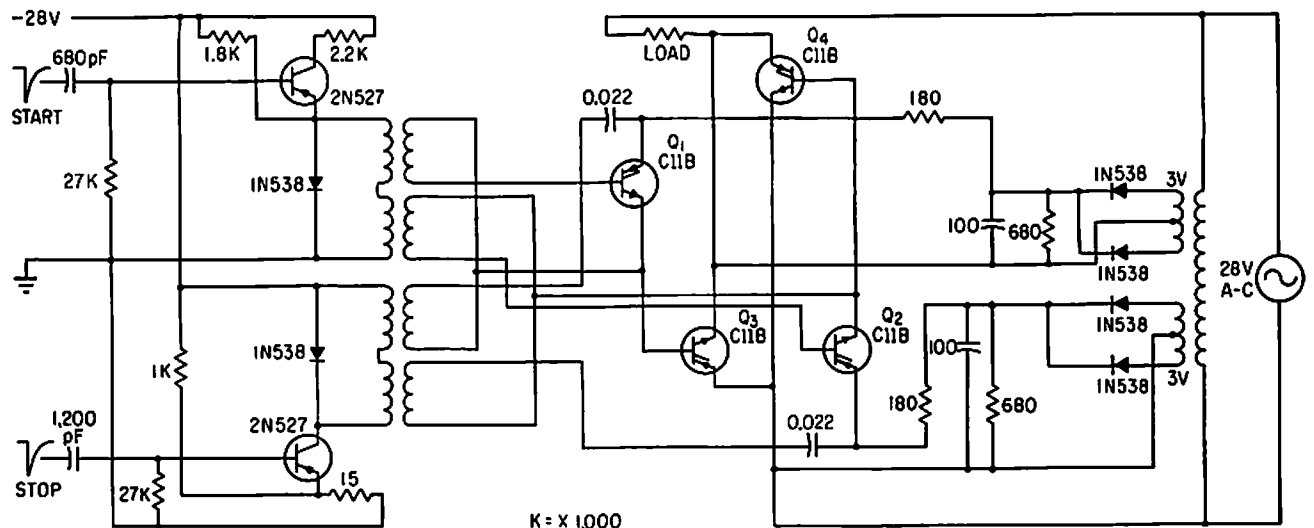
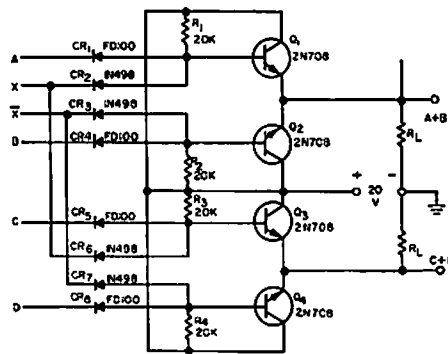


FAST SWITCHING OF D-C POWER—Power transistor Q6 switches 20 w d-c through 15-ohm load R3 under control of scr Q1, which

in turn is controlled by start-stop pulse amplifiers Q2 and Q3, and multivibrator Q4-Q5. Switching rate can be up to 700 cps.

—J. E. Roberts, *Controlled Rectifiers for Fast Power Switching*, *Electronics*, 35:17, p 58-59.

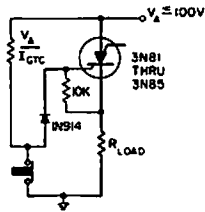
SOLID-STATE DPDT SWITCH—Eight diodes and four transistors connected as shown give same action as double-pole double-throw relay.—R. C. Going, *Solid-State DPDT Relay*, *EEE*, 11:10, p 26-27.



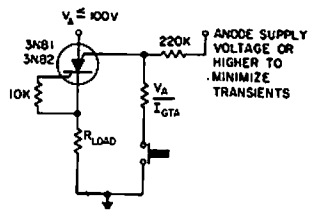
FAST SWITCHING OF A-C POWER—Pulsed scr's Q1 and Q2 turn on load-current-carrying scr's Q3 and Q4 under control of start-stop pulses from an external vibrator

that feeds the 2N527 start-stop pulse amplifiers. Each pulsed scr has its own 3-v source. Load being switched requires 350 ma at 400 cps.—J. E. Roberts, *Controlled Rectifiers for*

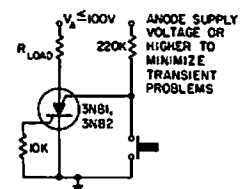
Fast Power Switching, *Electronics*, 35:17, p 58-59.



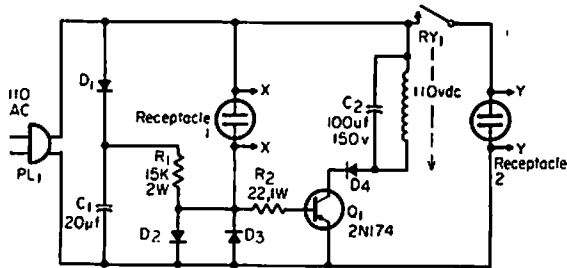
SCS CONTACT ISOLATOR—Eliminates contact bounce when both switch and load are grounded and opening of switch triggers scs.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 433.



SCS CONTACT ISOLATOR—Eliminates contact bounce when switch is closed to trigger scs, with both switch and load grounded.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 433.

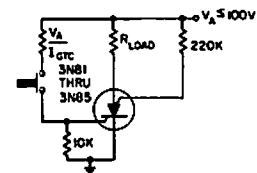


GROUND-SWITCH SCS CONTACT ISOLATOR—Eliminates contact bounce when switch is closed to trigger scs, because load current increases rapidly and latches on.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 433.

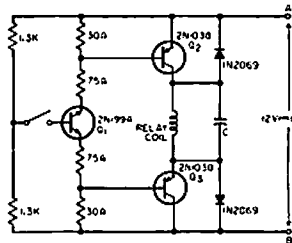


A-C POWER INTERLOCK—Any device drawing over 5 w (up to amount allowed by D2-D3) will produce 60-cps square wave at base of Q1 when device is turned on, to energize

relay through Q1 and apply power to receptacle 2.—C. J. Ulrick, AC Power Interlock, EEE, 13:6, p 65.

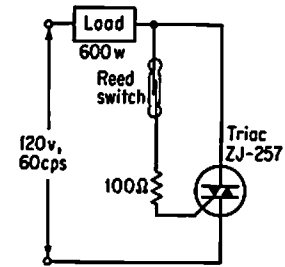


UNGROUND-SWITCH SCS CONTACT ISOLATOR—Eliminates contact bounce when switch is closed to trigger scs, because load current increases rapidly and latches on.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 433.



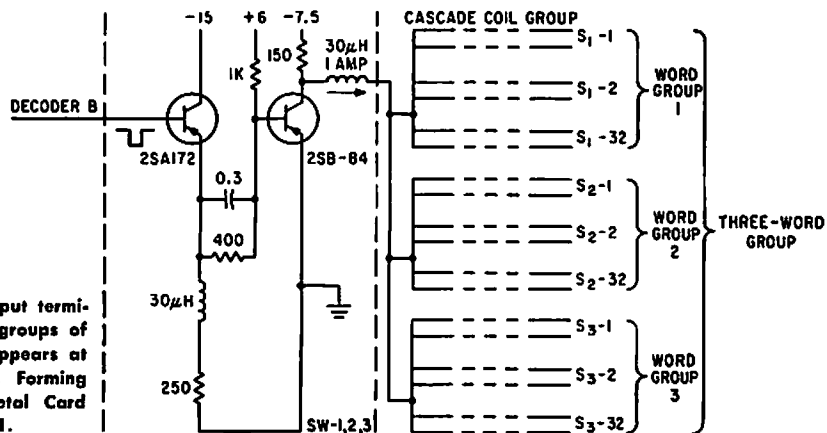
A-C RELAY DRIVE—Drives 12-v a-c relay with transistors triggered by low direct current of pair of switch contacts. Can drive any a-c

relay rated in voltage up to breakdown rating of transistors.—R. K. Walters, Transistor Driven AC Relay, EEE, 11:2, p 25-26.

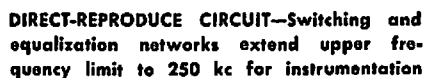
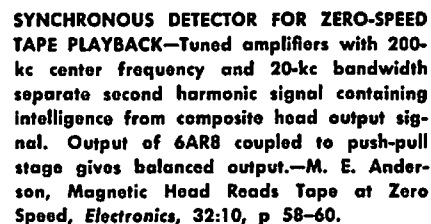


TRIAC-REED A-C SWITCH—Gate-controlled semiconductor switch (G-E Triac) and magnetic reed switch provide on-off a-c switching with minimum components. Gate signal of 3 v at 50 ma, either polarity, triggers Triac for handling 600-w load.—M. P. Southworth, Bidirectional Static Switch Simplifies Ac Control, Control Engineering, March 1964, p 75-76.

MEMORY MATRIX SWITCHING—Output terminal, connected to three word coil groups of matrix, is grounded when pulse appears at input.—J. Yamato and Y. Suzuki, Forming Semi-Permanent Memories with Metal Card Storage, Electronics, 34:46, p 136-141.

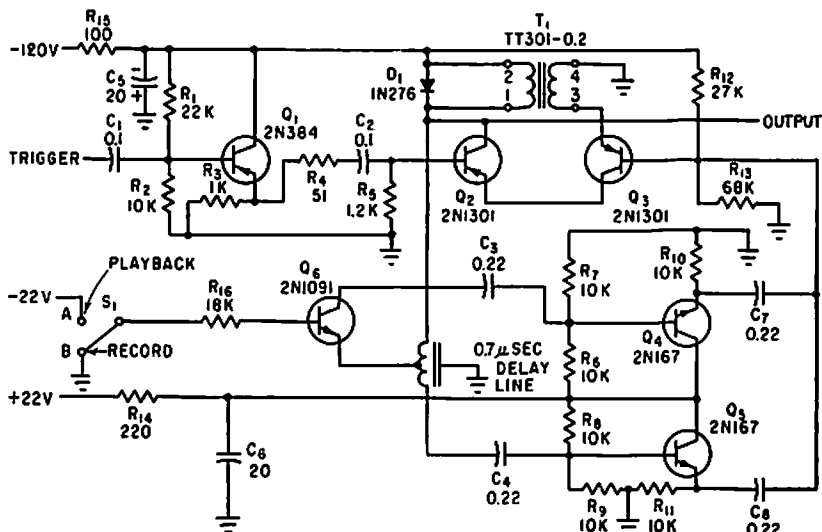
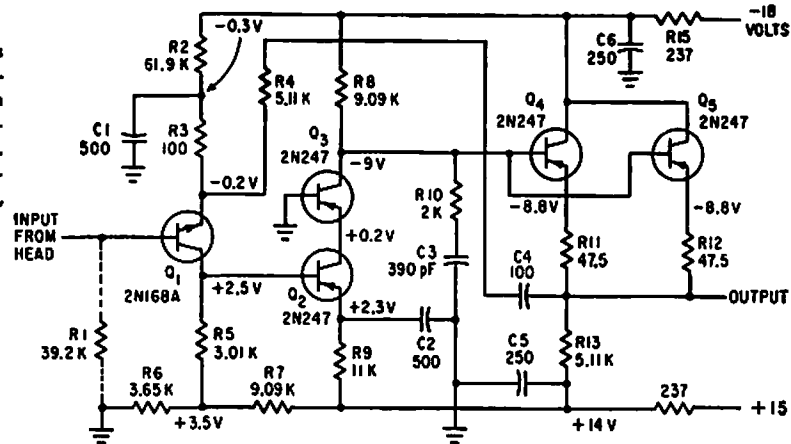


Tape Recorder Circuits

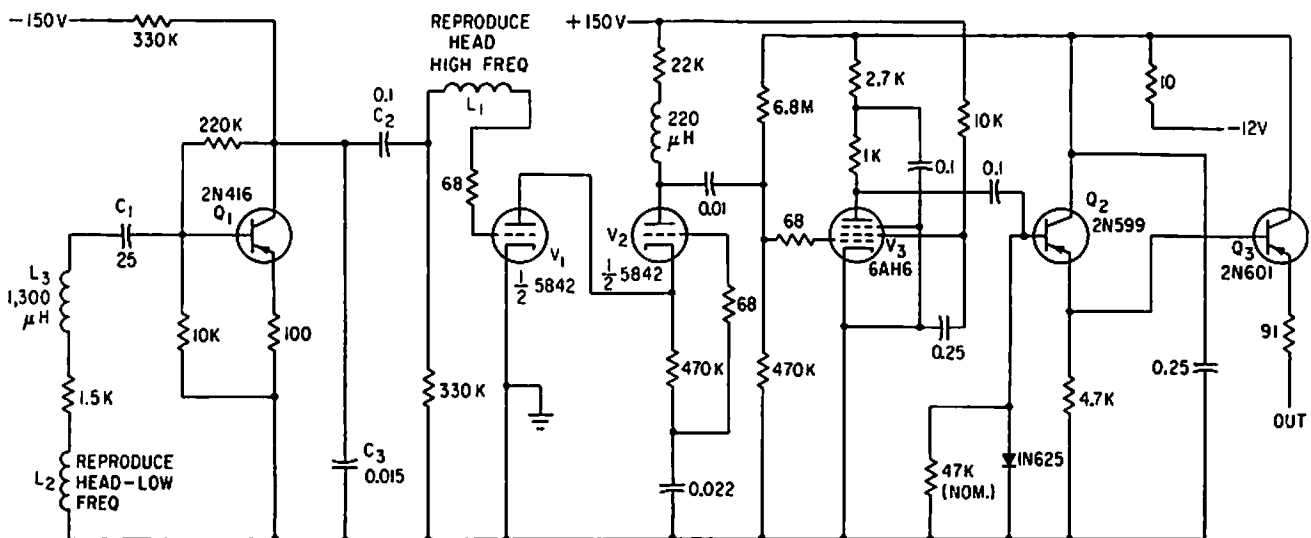


—D. R. Steele, More Bandwidth for Magnetic Recorders, *Electronics*, 33:2, p 44-47.

MAGNETIC-TRANSDUCER PREAMP—Gain is constant at 49 within 2% for a-c source impedances ranging from 0 to 5,000 ohms, such as magnetic read heads. Gain remains constant within 3 db from 10 cps to 1 Mc.—S. R. Parris, Wideband Transistor Preamplifier Handles Low-Resistance Transducers, *Electronics*, 34:11, p 57-59.



DELAY-LINE PULSES FOR VIDEO RECORDER—Blocking oscillator arrangement gives 0.3 microsec pulse length for recording by time-division multiplexing of 52 channels on two-track video recorder, and 0.8 microsec pulses when S1 is set for playback.—M. H. Damon and F. J. Messina, High-Density Storage of Wideband Analog Data, *Electronics*, 35:13, p 45-49.

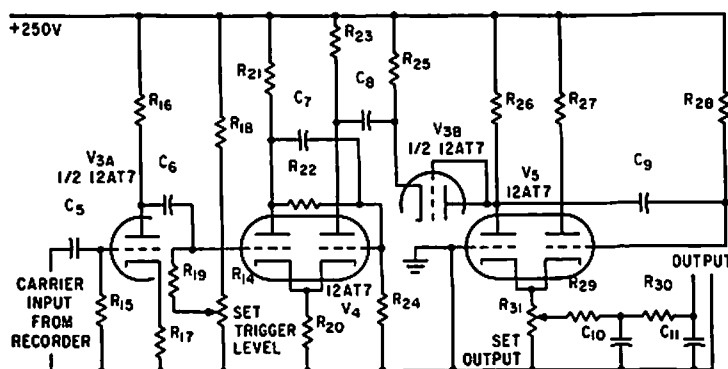
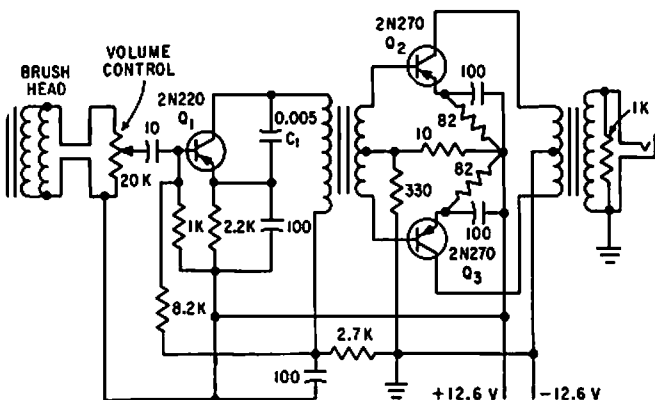


VIDEO TAPE PREAMP—Two windings on reproduce head extend frequency response to 1 Mc. Winding L2 is connected conventionally to input of Q1; when transistor gain drops

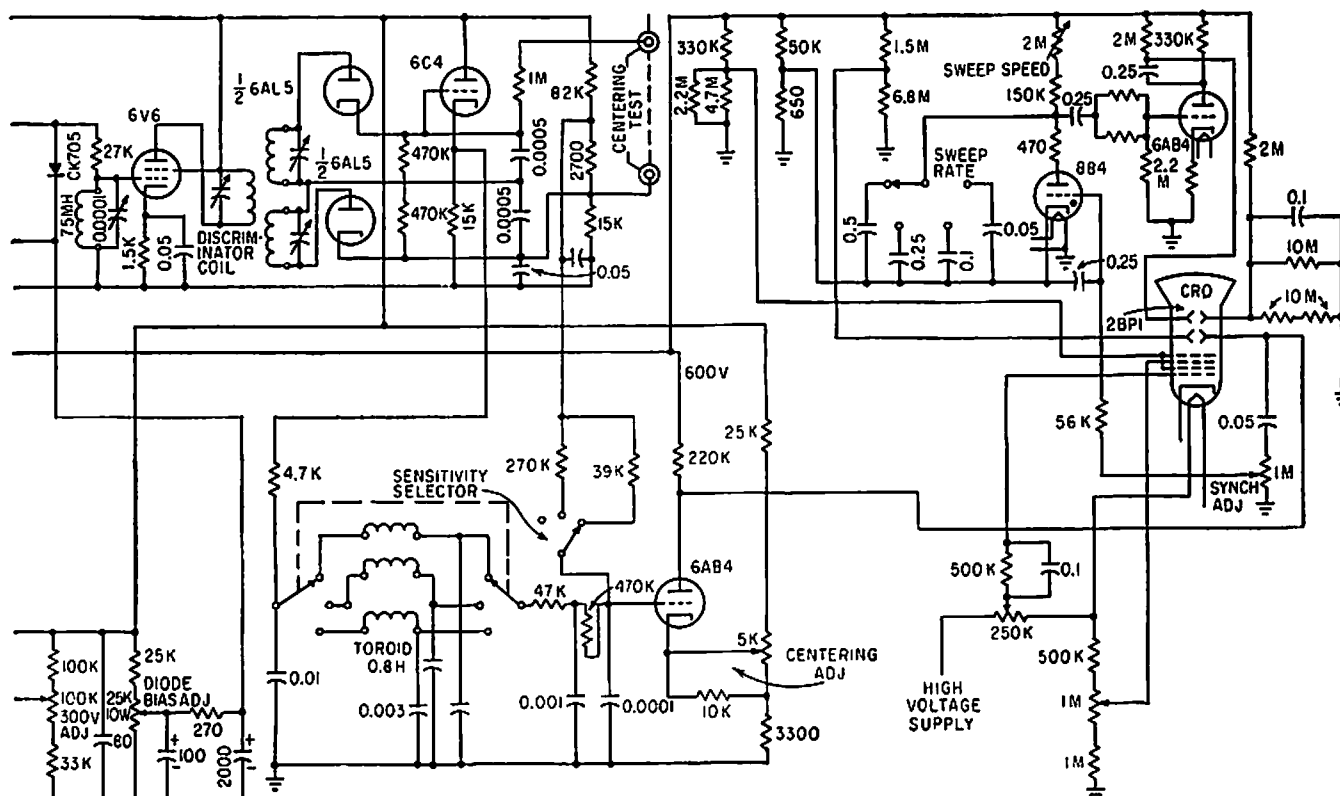
off at higher frequencies, L1 at input of first tube takes over. Preamp output to 91-ohm line is 1.5 v peak to peak.—G. N. Johnson, W. R. Johnson, and J. T. Mullin, Magnetic

Recorder Response, *Electronics*, 34:10, p 186-188.

HIGH-FREQUENCY COMPENSATION—Compensates for 23 db/decade loss above 500 cps in high-frequency response caused by spacing pickup head 1 mil from magnetic tape of vlf induction radio link.—E. A. Hanyasz, J. E. Stevens, and A. Medovsky, *Communication System for Highway Traffic Control, Electronics*, 33:42, p 81-83.

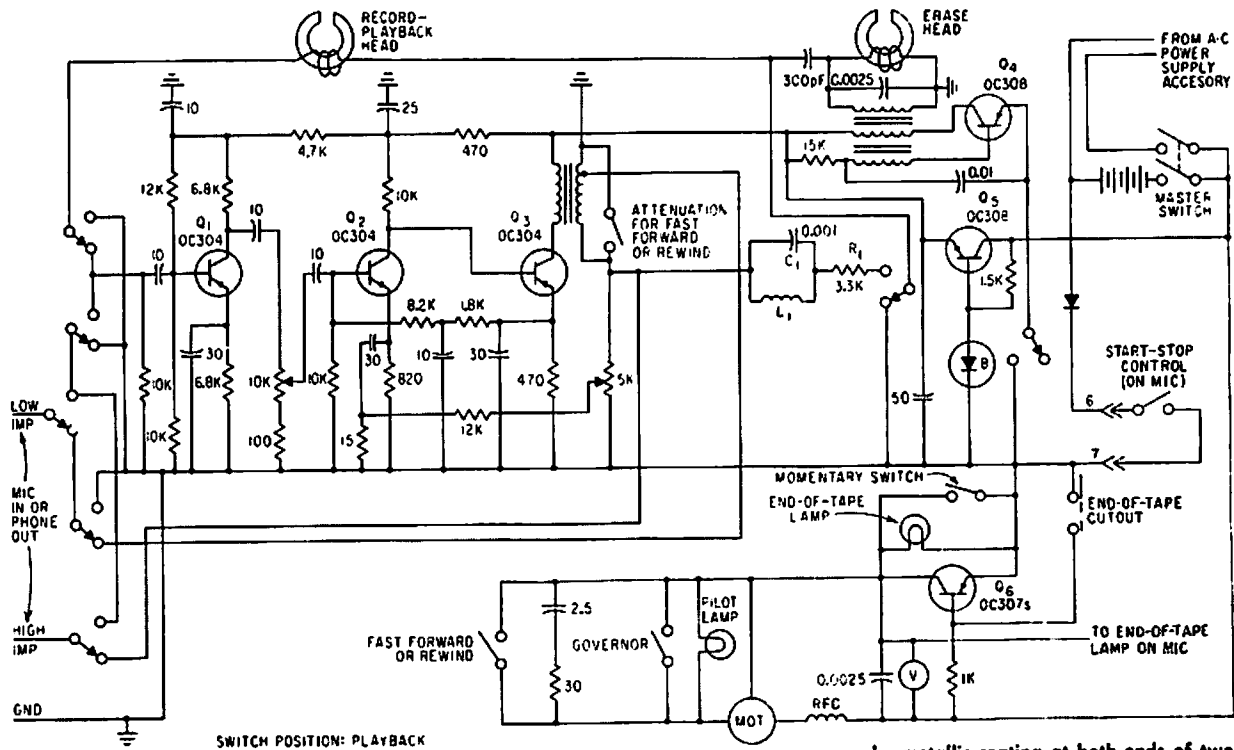


F-M DEMODULATOR FOR TAPE RECORDER—Removes low-frequency frequency-modulated nerve-potential signal from 7.5-kc carrier recorded on magnetic tape. Amplifier V3a feeds squarer V4 that is connected as Schmitt trigger to give square-wave output for differentiation by C8-R25. Negative-going edge of resulting square wave triggers monostable mybr V5 which serves as demodulator.—K. D. Broadfoot, F-M Magnetic Tape System Records Low-Frequency Nerve-Fiber Potentials, *Electronics*, 34:28, p 66-67.



various spectrum cutout filters to show all drift, wow, and flutter components from d-c to 4,000 cps for magnetic tape recorder.—J. T. Mullin, *Precise Measurement of Wow*

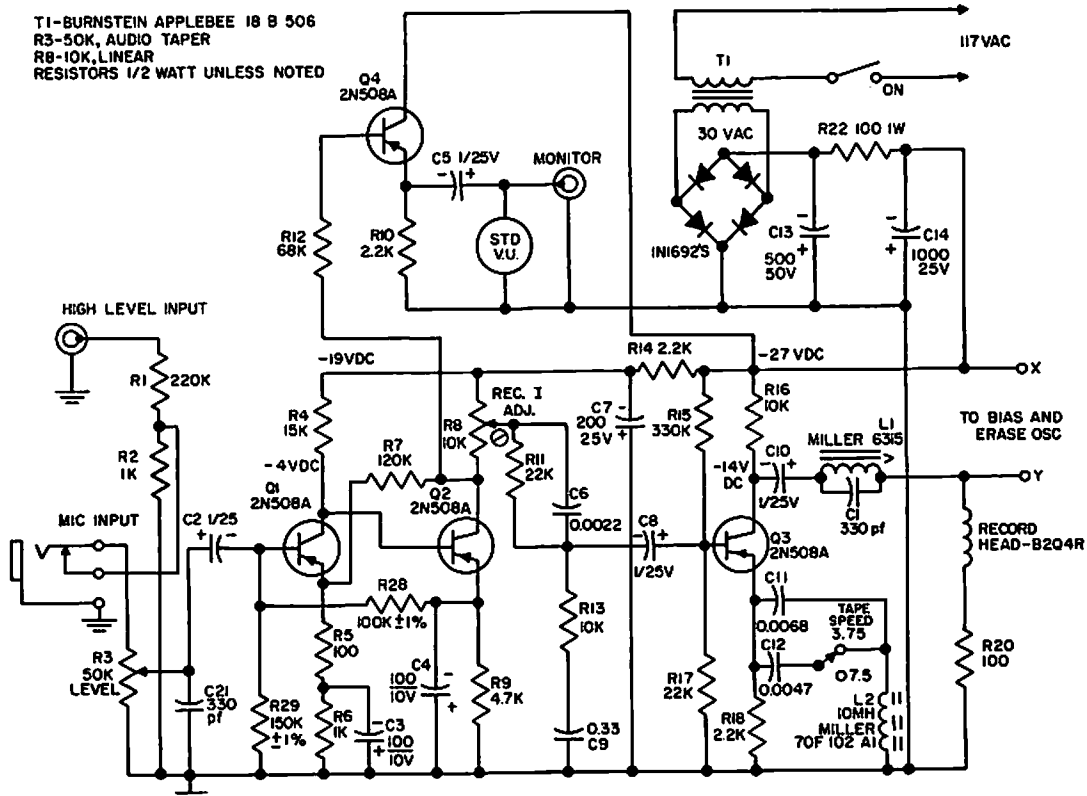
and Flutter, *Electronics*, 33:26, p 100-102.



BATTERY-OPERATED DICTATING MACHINE—
Amplifier voltage is regulated by Q5 and

zener diode. Q6 functions as on-off switch controlled either by microphone switch or

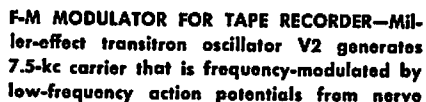
by metallic coating at both ends of two-track tape.—L. Hannemann, Pocket-size Dictating Machine, *Electronics*, 33:44, p 73.



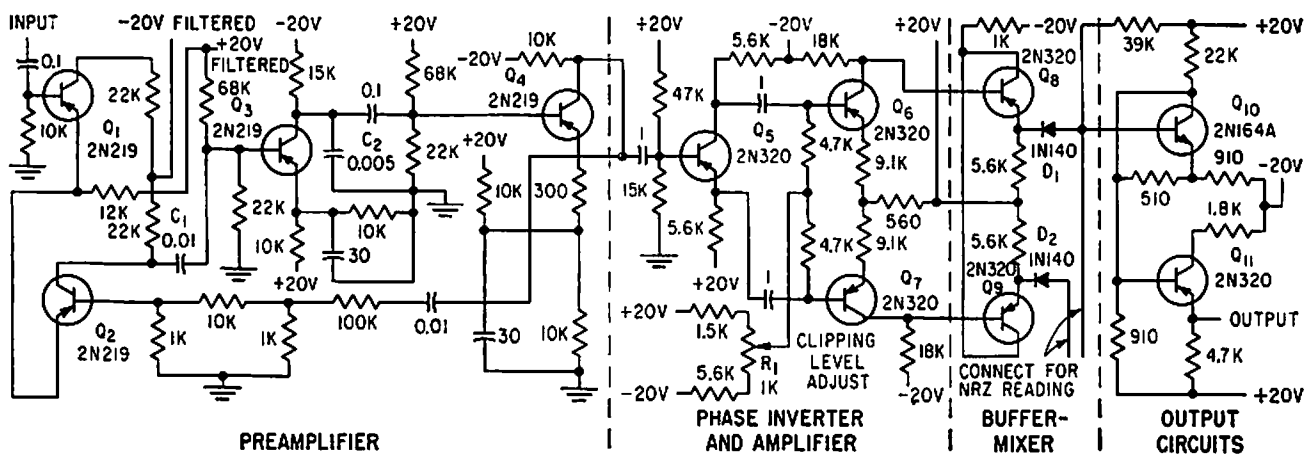
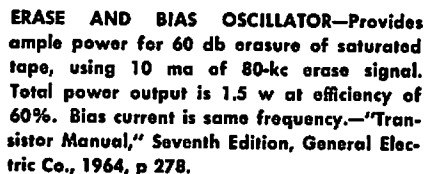
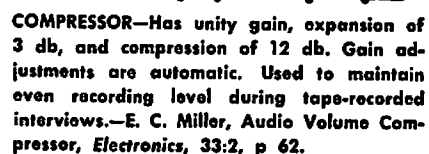
RECORDING AMPLIFIER—Has sufficient input impedance for medium-high-impedance magnetic microphone. Includes equalization to

produce flat response with Nortronic low-impedance recording head when playback preamp is adjusted for NAB equalization at

tape speed of 7.5 inches per second.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 272.



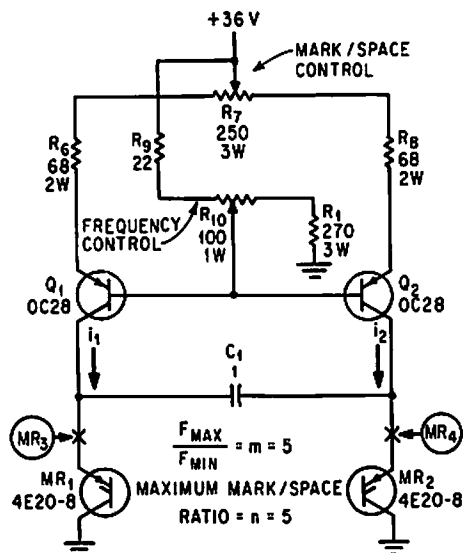
fibers, to permit recording on ordinary tape recorder.—K. D. Braadfoot, F-M Magnetic Tape System Records Low-Frequency Nerve-Fiber Potentials, *Electronics*, 34:28, p 66-67.



DIGITAL DATA READ AMPLIFIER—Presents 10,000-ohm input impedance to read head. No-signal input produces -4 v output; peak

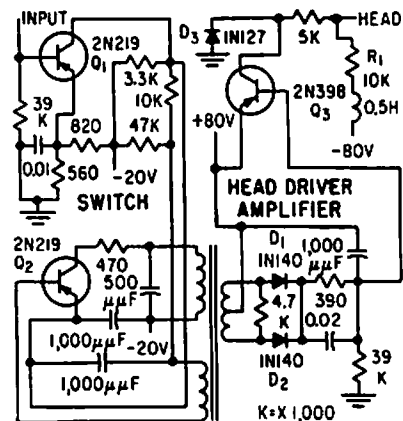
input as low as 1.35 mv zero-to-peak produces +4 v output. Gives satisfactory reading at pulse repetition rates up to 22 kc.—R. F.

Shaw, Universal Tape Amplifiers for Digital Data Systems, *Electronics*, 31:41, p 91-93.

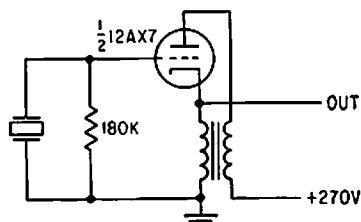
**SHOCKLEY-DIODE MVBR TESTS TRANSPORTS**

—For testing tape transports, frequency can be varied over 15:1 range and mark/space ratio from 1:15 to 15:1. Shockley diodes MR1 and MR2 serve as changeover switch.

Two additional diodes, at MR3 and MR4, are needed if reverse voltage rating of diodes is less than their striking voltage.—C. J. Dakin, Novel Multivibrators Test Tape Transports, *Electronics*, 37:7, p 40–43.



OSCILLATOR RECTIFIER-FILTER
DIGITAL DATA WRITE AMPLIFIER—Delivers 8-ma swing to record head. Rise time is 15 millise, sufficient for nonreturn-to-zero recording at 20 kc, using head with 50-mh inductance. Carrier-type amplifier overcomes usual stability and level problems associated with d-c amplifiers.—R. F. Shaw, Universal Tape Amplifiers for Digital Data Systems, *Electronics*, 31:41, p 91–93.

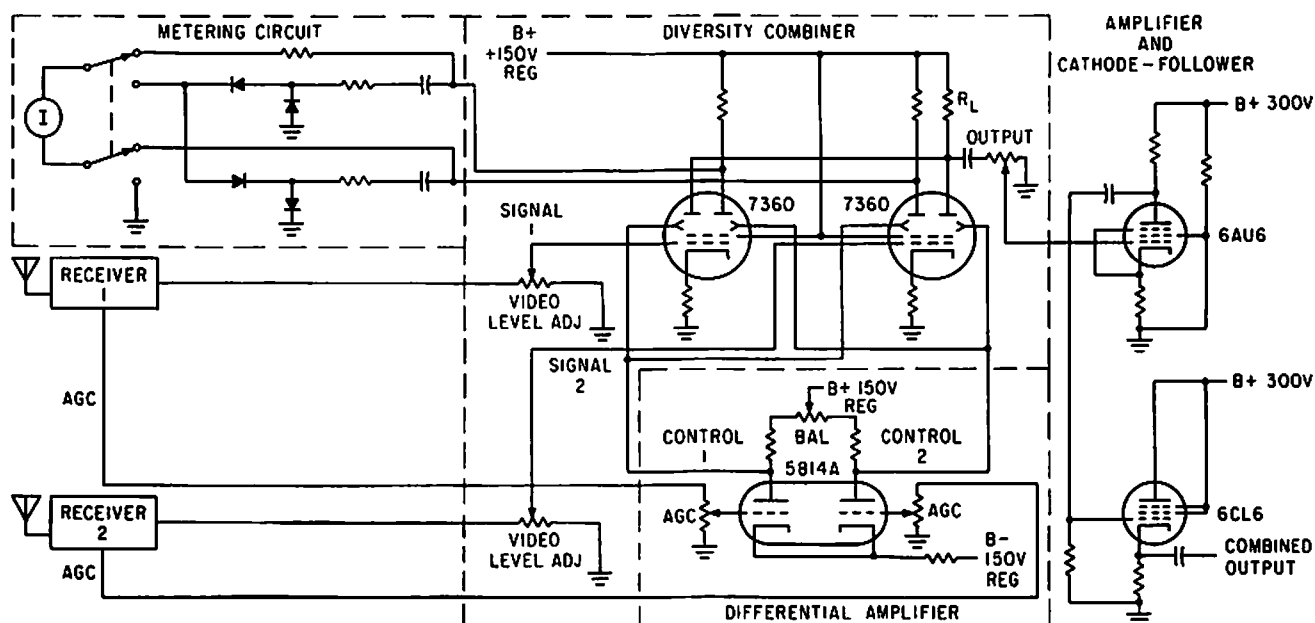
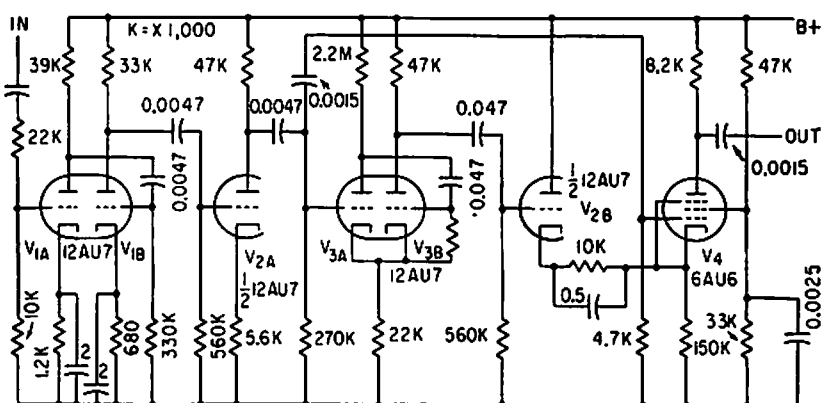


CRYSTAL-CONTROLLED BLOCKING OSCILLATOR—Used for recording 50-kc reference base on magnetic tape in 10-channel instrumentation system. Circuit is ordinary plate-to-cathode coupled blocking oscillator with crystal substituted for capacitor. If free-running frequency (without crystal) is lower than crystal frequency by no more than 40%, oscillator locks to crystal frequency.—P. S. Gengston, Blocking Oscillator is Crystal Controlled, *Electronics*, 31:25, p 88–90.

CHAPTER 86

Telemetry Circuits

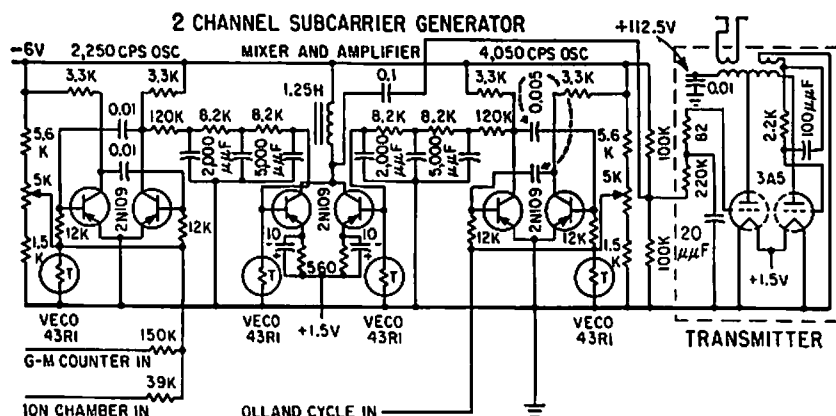
GATED AMPLIFIER FOR RECEIVER—Input from f-m discriminator of ground receiver for neutron-detecting radiosonde contains two sub-carrier oscillator frequencies plus steep unwanted pulses at audio blocking rate of 10 to 200 cps. Three-stage amplifier feeds amplified input signals to one-shot mvbr V3A-V3B for blocking of unwanted pulses. Output of gated amplifier V4 then contains only bursts of the two desired subcarrier frequencies.—L. Hillman and R. C. Haymes, *Modifying a Telemetry System for Balloon-Borne Neutron Detection*, *Electronics*, 34:11, p 60-63.



TWO-CHANNEL DIVERSITY COMBINER—Beam-deflection tubes provide ratio-squared combining of two telemetry receiving channels, to counteract fading signals from tumbling

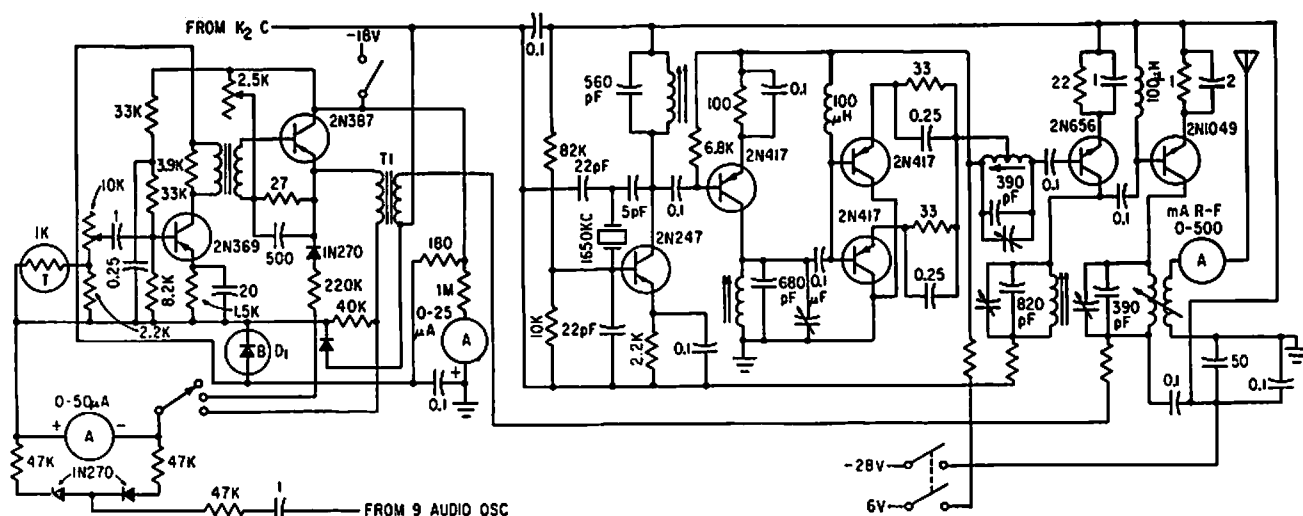
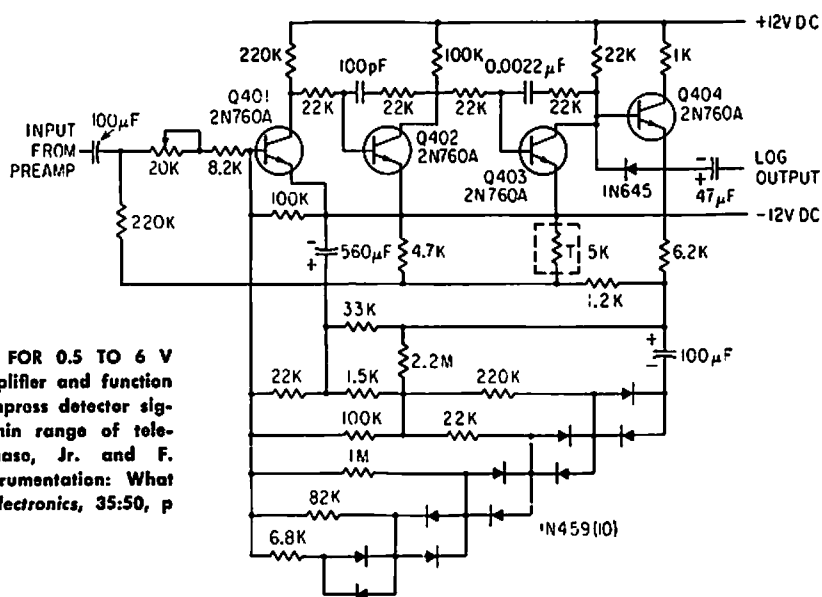
or spinning spacecraft missile. Video signals go directly to control grids of type 7360 deflection tubes, while control voltages from receivers are applied to the respective de-

flection electrodes through differential amplifier.—V. A. Ratner, *Telemetry Diversity Combiner Uses Beam Deflection Technique*, *Electronics*, 35:4, p 42-43.



F-M/F-M TRANSMITTER—Output power is 0.5 w at 95 Mc, and range is 400 miles. Provides two channels.—L. E. Peterson, R. L. Howard, and J. R. Winckler, Balloon Gas Monitors Cosmic Radiation, *Electronics*, 31:45, p 76-79.

LOGARITHMIC AMPLIFIER FOR 0.5 TO 6 V D-C—Uses operational amplifier and function generator principle to compress detector signal levels to values within range of telemetering system.—S. Chaso, Jr. and F. Schwarz, Mariner II Instrumentation: What Will It See on Venus?, *Electronics*, 35:50, p 42-45.

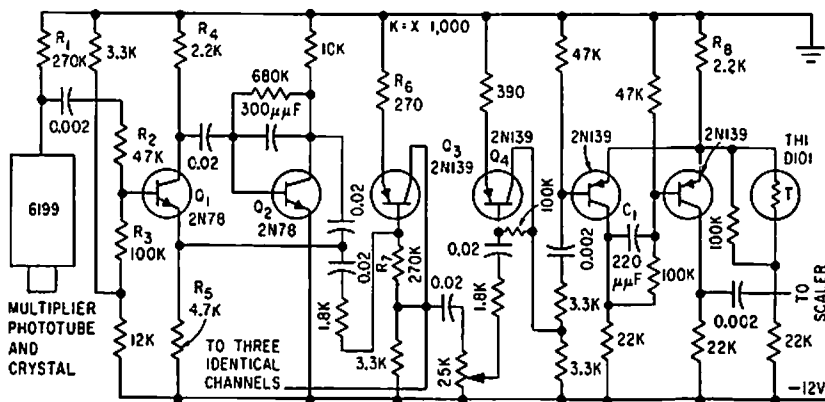
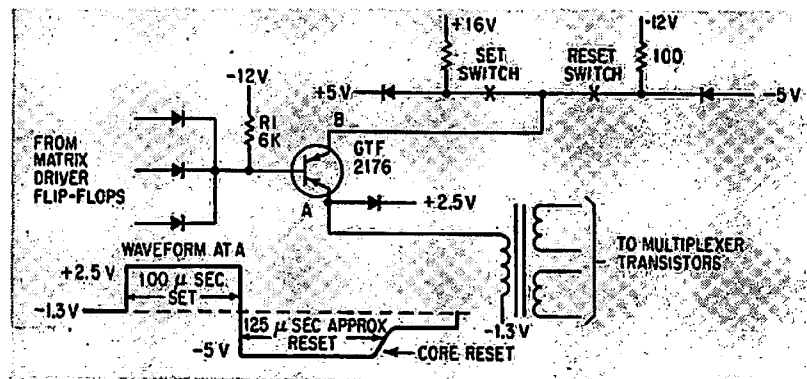


WIND VELOCITY TRANSMITTER—Battery-operated transmitter at remote mountain site uses transmitters exclusively, for power econ-

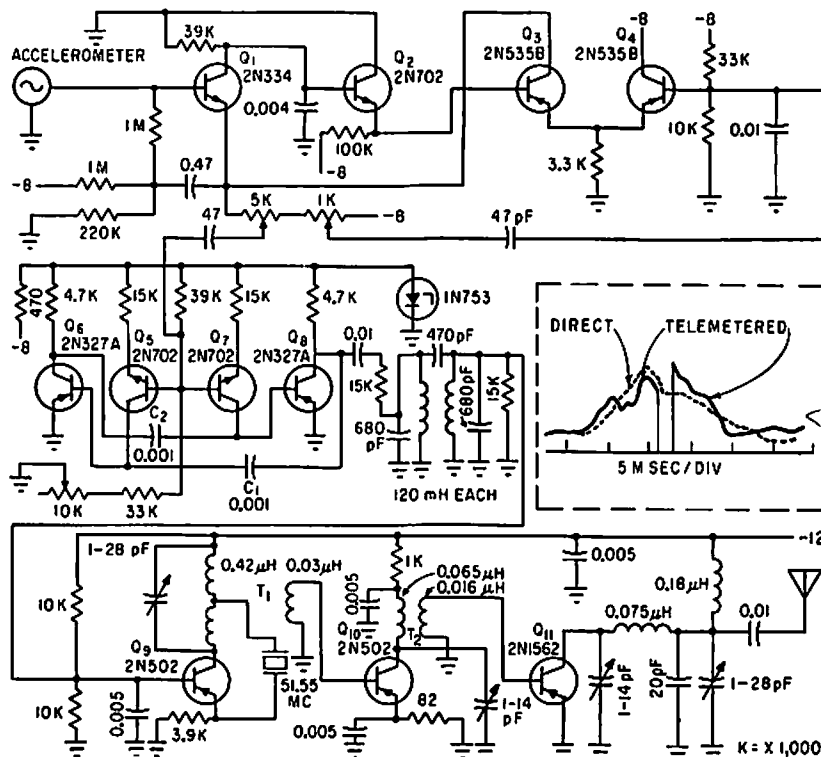
omy, to telemeter wind direction and velocity for predicting avalanches. Modulator uses nine separate 2N366 audio oscillators (not

shown) that feed 2N369 class-A buffer.—R. Beaulieu and G. Neal, Wind Velocity Telemetering System, *Electronics*, 33:29, p 68-70.

MULTIPLEX DRIVER—Diode matrix drives bilateral transistors similar to core memory drivers. Drive circuit is regulated to within 10%.—J. V. Dirocco and J. W. Peghiny, Low-Level Encoding Approach: Latest Details of Titan II Telemetry, *Electronics*, 35:47, p 36-39.



FOUR-CHANNEL DISCRIMINATOR—Common amplifier and four individual amplifiers drive triggers for four channels of scalars. Common amplifier supplies 7 v on common bus from Q3 to four potentiometers, settings of which determine discrimination point for each channel.—D. Enemark, Balloon-Borne Circuits Sort High-Altitude Cosmic Rays, *Electronics*, 32:35, p 52-55.

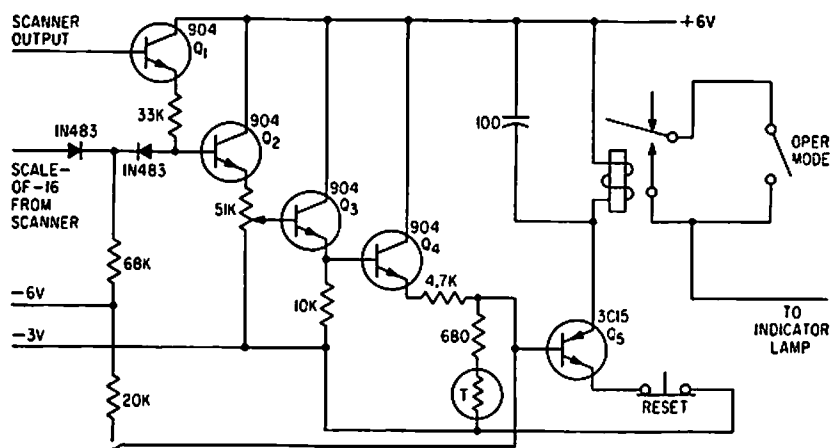


FOOTBALL-HELMET TRANSMITTER—Impact data sensed by accelerometer in helmet is transmitted to sideline receiver by f-m/f-m trans-

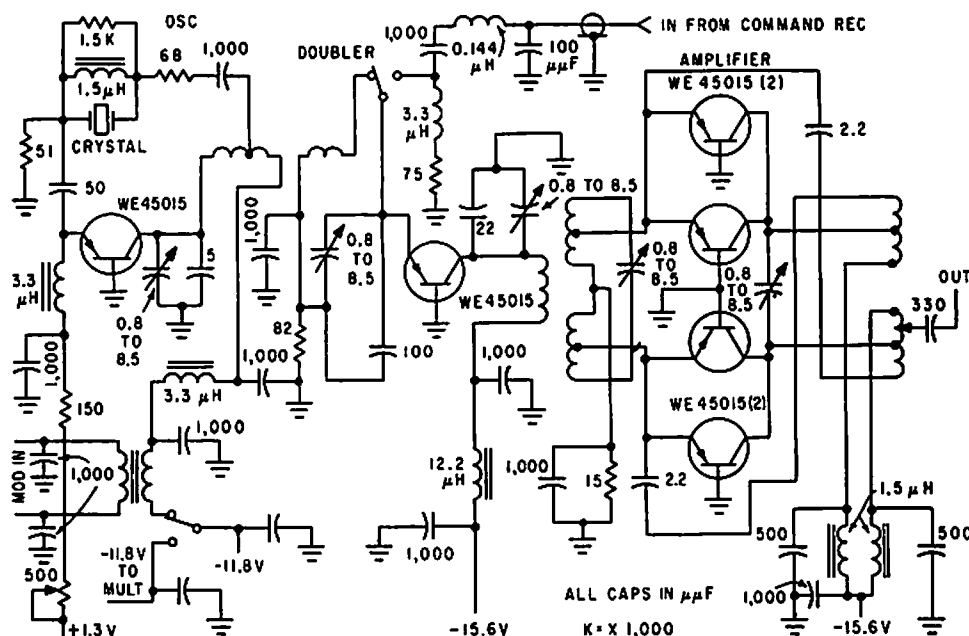
mitter. Use of subcarrier oscillator makes transmitter more immune to shock and vibration than with conventional main-channel

oscillator.—J. S. Aagaard and J. L. DuBois, Telemetering Impact Data from the Football Field, *Electronics*, 35:14, p 46-47.

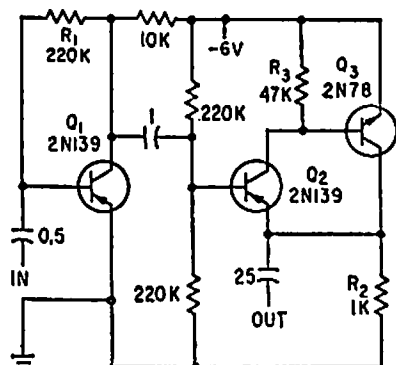
The schematic diagram shows the transmitter and modulator sections of the motor speed control circuit. The transmitter section consists of a 2N417 transistor (Q1) with a 10K resistor (T) in the base circuit, a 56K resistor (W) in the emitter circuit, and a 1M resistor in the collector circuit. It is powered by a +6V supply and includes capacitors C1 and C2. The modulator section includes a diode and is powered by -28V and -18V supplies. The output of the modulator is connected to a motor-operated switch (S2, S3) and a speed oscillator (OSC). The transmitter output is connected to a motor-operated switch (S1).



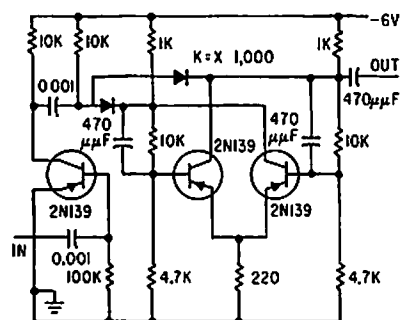
MALFUNCTION MONITOR—Used for monitoring missile in flight while maintaining radio silence of telemetry transmitter unless abnormal condition is detected. After arming of missile, monitor can be used to transmit missile kill data.—R. C. Wright, Collecting Data from Live Missiles in Flight, *Electronics*, 34:12, p 46-49.



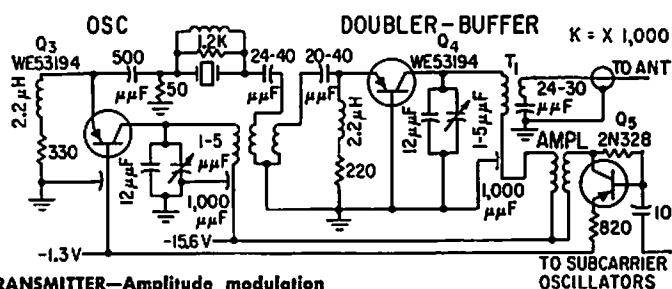
pull parallel to give 400 mw output.—R. R. Bennett et al., *Circuits for Space Probes, Electronics*, 32:25, p 55-57.



F-M MODULATOR—Provides at least 200 kc deviation when applied to base of oscillator transistor, before severe distortion sets in. Feedback keeps output impedance low.—D. Enemark, *Transistors Improve Telemetry Transmitter*, *Electronics*, 32:11, p 136-137.

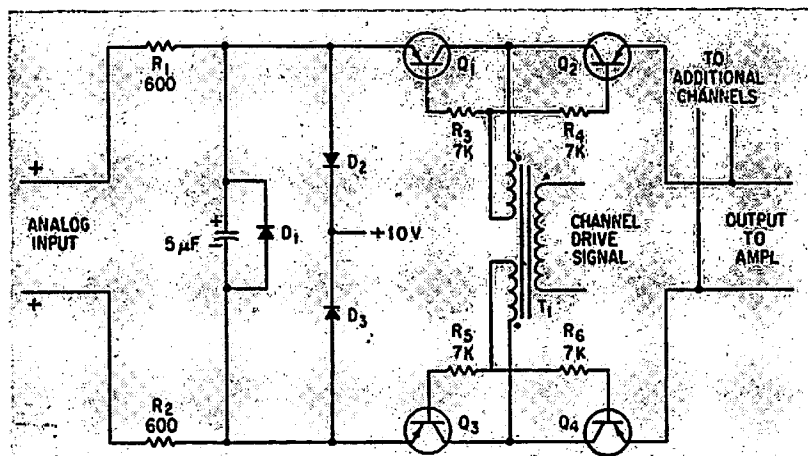


BASIC SCALER—Each scaler stage is bistable circuit with trigger amplifiers between each pair of stages. Used to make 512 counts from ion chamber give one output to telemetry system.—D. Enemark, *Balloon-Borne Circuits Sort High-Altitude Cosmic Rays*, *Electronics*, 32:35, p 52-55.

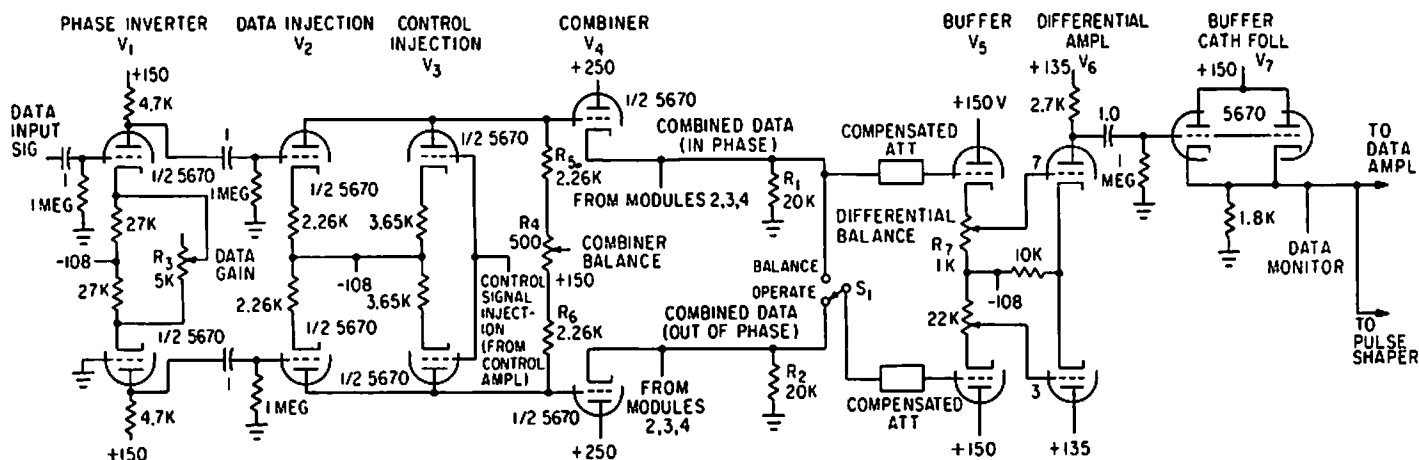


60-MW TRANSMITTER—Amplitude modulation is applied at collector of amplifier-doubler Q4.—H. L. Richter et al., *Instrumenting the*

Explorer I Satellite, *Electronics*, 32:6, p 39-43.



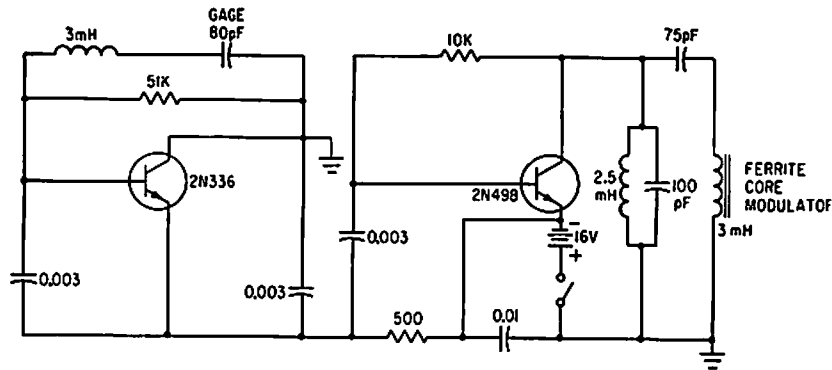
MICRO-MULTIPLEXER—Solid-state high speed time-division commutator connects several low-level inputs, one at a time, to common differential bus. Each multiplexer channel consists of two transistor switch pairs.—J. V. Direcco and J. W. Peghiny, *Low-Level Encoding Approach: Latest Details of Titan II Telemetry*, *Electronics*, 35:47, p 36-39.



POST-DETECTION DIVERSITY COMBINER—Can handle any IRIG modulating signal and feed any telemetry receiver having external agc

output. Will combine two, three, or four channels.—W. Casson and R. C. Robinson, *Versatile Diversity Combiner Handles Most*

Missile-Range Signals, *Electronics*, 35:44, p 40-43.

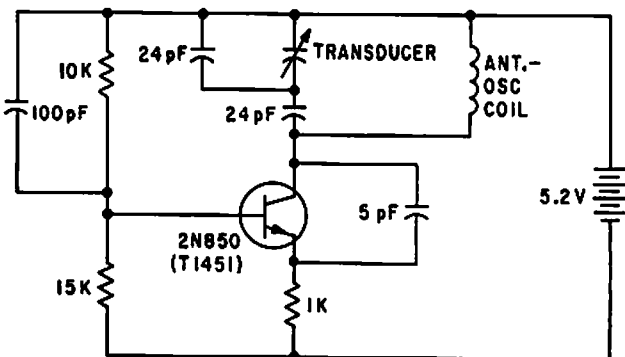
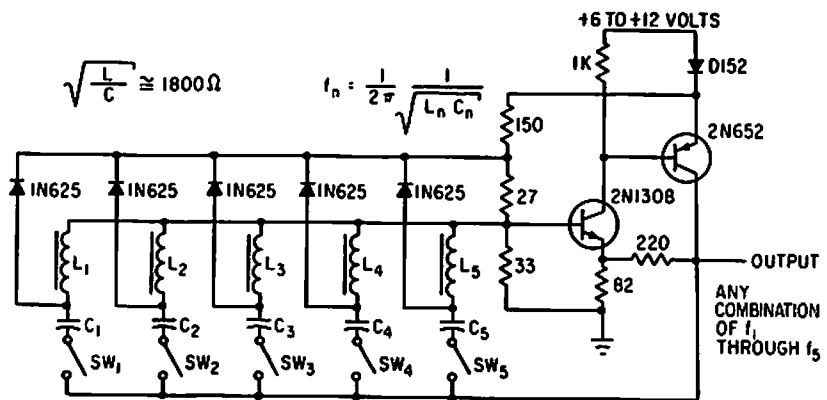


PROJECTILE ACCELERATION TELEMETER—Microwave signal at 24,000 Mc is aimed down barrel of howitzer by sheet aluminum reflector that is replaced after each firing. Variations in reflection coefficient of ferrite device

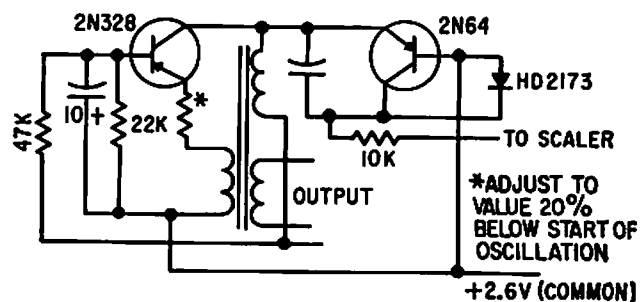
on projectile nose modulate reflected microwave signal from moving projectile, at frequency dependent on acceleration. Circuit shows encapsulated transducer mounted on nose. Capacitance gage produces frequency

shift of 70-kc subcarrier that is proportional to acceleration, for driving ferrite-core modulator through amplifier stage.—W. M. Kendrick and L. A. Peters, *Projectile Telemetry with Microwaves*, Electronics, 33:38, p 68-71.

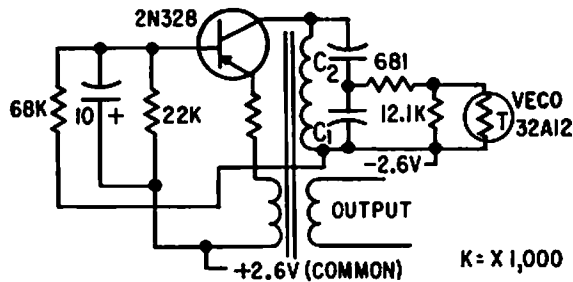
FIVE-FREQUENCY OSCILLATOR—Two-transistor circuit generates up to five different tones simultaneously for five-bit parallel encoder for telemetry. Starting transients are built up in individual series-tank circuits. Amplitude of oscillation stabilizes at value where energy from negative-resistance source equals energy lost in tanks.—R. Stapelfeldt, *Multitone Oscillators—New Source of Simultaneous Frequencies*, Electronics, 36:1, p 86-87.



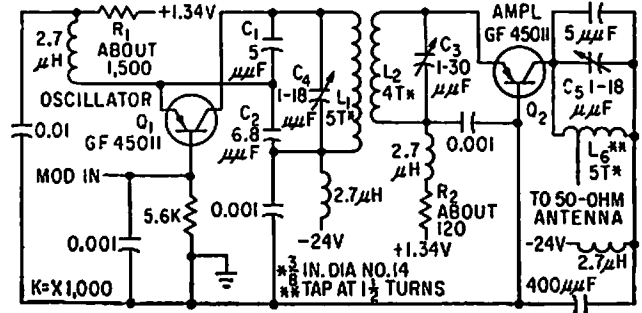
PROJECTILE NOSE PRESSURE TELEMETER—Variable-capacitance pressure transducer modulates 150-Mc carrier for telemetering stagnation pressure at nose of projectile during flight. Antenna-oscillator coil has four turns of No. 24 AWG wire, 0.16 inch inside diameter.—O. H. Bock and P. L. Clemens, *Aerodynamic Measurements in a Hypervelocity Gun Range*, Electronics, 34:44, p 33-37.



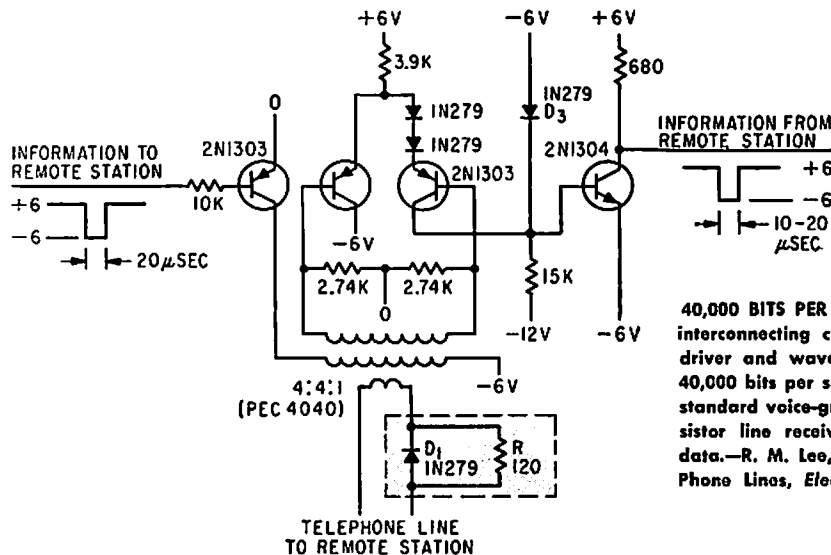
CURRENT-CONTROLLED SUBCARRIER OSCILLATOR—Uses time-controlled reactance modulation. Operating frequency is altered by introducing alternating current having same frequency but 90° out of phase with oscillator voltage. Frequency shift thus produced is proportional to amount of additional current fed into tuned circuit.—H. L. Richter et al., *Instrumenting the Explorer I Satellite*, Electronics, 32:6, p 39-43.



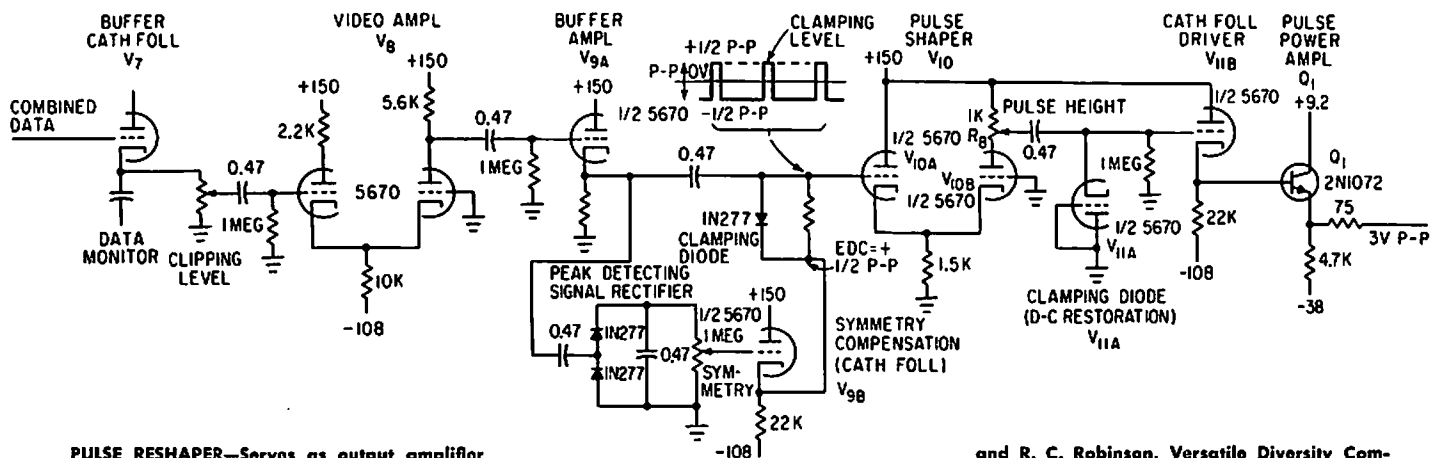
RESISTANCE-CONTROLLED SUBCARRIER OSCILLATOR—Required 7.5% frequency deviation is obtained with ratio of 1.5 for C_1/C_2 .—H. L. Richter et al., Instrumenting the Explorer 1 Satellite, *Electronics*, 32:6, p 39-43.



F-M TRANSMITTER—Provides 250 mw at 92 Mc, for use with balloon-borne ionizing radiation detectors. Variable-frequency oscillator can be used because only moderate stability is required.—D. Enemark, Transistors Improve Telemeter Transmitter, *Electronics*, 32:11, p 136-137.



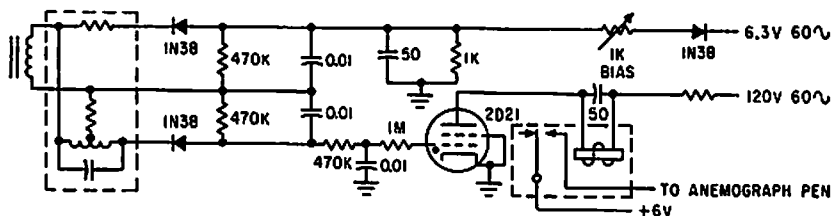
40,000 BITS PER SEC OVER PHONE LINE—For interconnecting computers, one-transistor line driver and wave shaper permit transmitting 40,000 bits per second up to half a mile over standard voice-grade phone lines. Three-transistor line receiver and pulse slicer receive data.—R. M. Leo, Speeding Digital Data Over Phone Lines, *Electronics*, 36:39, p 30-31.



PULSE RESHAPER—Serves as output amplifier for pcm and pdm signals from diversity combiner circuit. Over-amplification and clip-

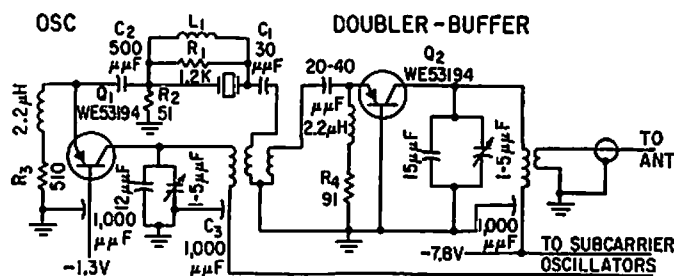
ping stages give fast rise and decay time without risk of false triggering.—W. Casson

and R. C. Robinson, Versatile Diversity Combiner Handles Most Missile-Range Signals, *Electronics*, 35:44, p 40-43.



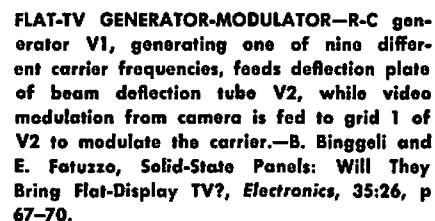
WIND VELOCITY DECODER—Each of nine decoders in receiver of wind direction and velocity telemetering system has notch filter of different frequency. At resonant frequency of filter, desired audio tone is blocked, causing thyratron to fire for part of every supply

voltage cycle. Resulting pulsating d-c pulls in sensitive plate relay, operating pen of anemograph.—R. Baulieu and G. Naal, Wind Velocity Telemetering System, *Electronics*, 33:29, p 68-70.



LOW-POWER 54-MC TRANSMITTER—Draws only 5 ma at 8 v d-c. Phase modulation is produced by varying voltage applied to col-

lector of Q1.—H. L. Richter et al., Instrumenting the Explorer I Satellite, *Electronics*, 32:6, p 39-43.



video bandwidth about 6 Mc. Used with uvicon camera tube for ultraviolet tele-

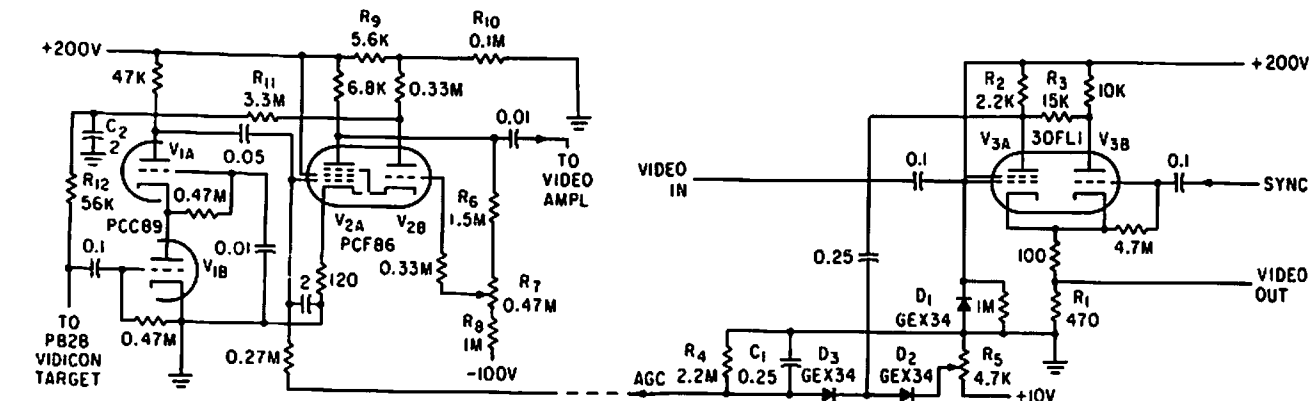
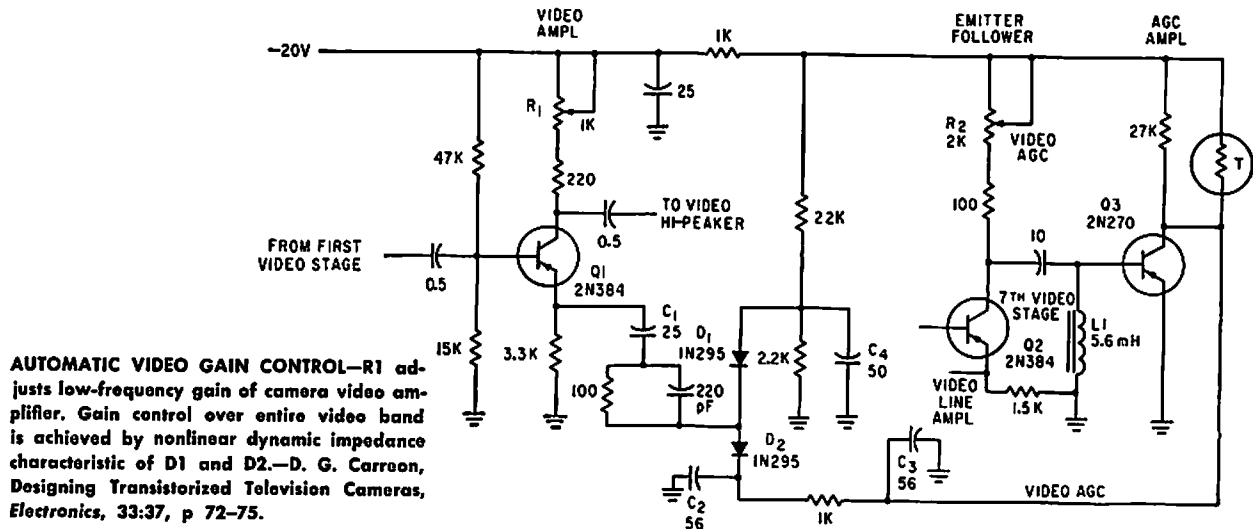
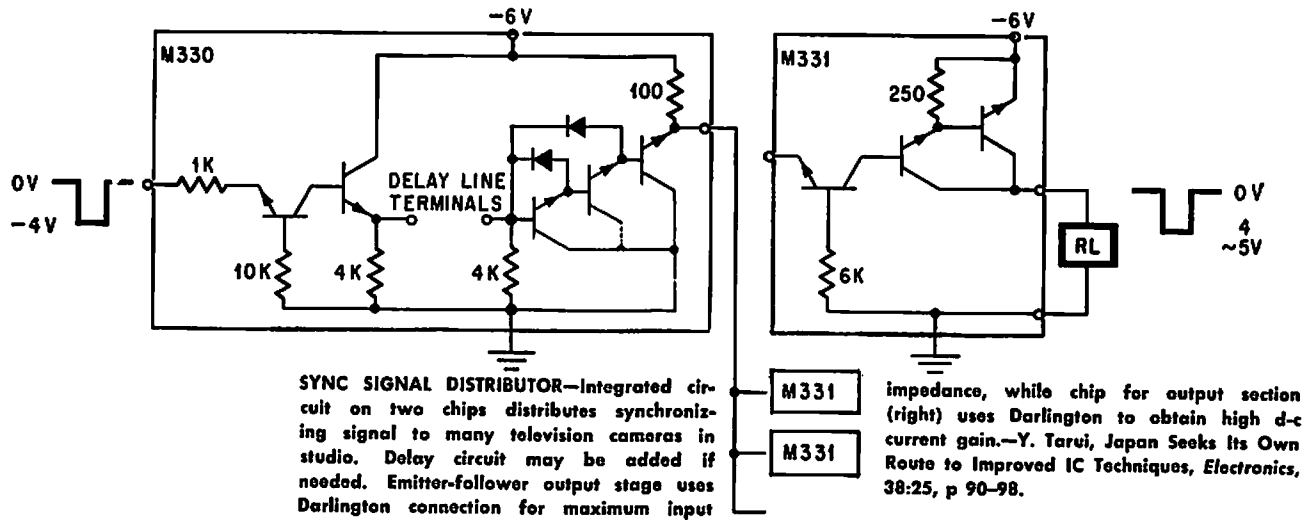
scope.—R. N. Riggs, Ultraviolet Space Telescope Will Scan the Stars, *Electronics*, 35:46, p 37-43.

The diagram illustrates a video amplifier circuit divided into three functional sections:

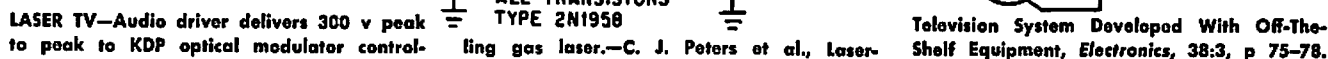
- APERTURE CORRECTION:** This section takes input from a 5TH VIDEO STAGE and a D2 peak unit. It features a 2N384 vacuum tube (Q₁) with a 15T tuning circuit. The output is coupled to the next stage via a 0.05 microfarad capacitor (C₃).
- KEYED CLAM:** This section uses two 2N384 vacuum tubes (Q₁ and Q₂) and two 1N295 diodes (D₁ and D₂). It is designed to process KEYING PULSES. The circuit includes a 3.3K resistor, a 1.5K resistor, and a 33K resistor (R₃). A 50 microfarad capacitor (C₄) is connected to the output.
- EMITTER FOLLOWER:** This section provides a buffer stage for the video signal. It uses a 2N384 vacuum tube (Q₂) and a 1.5K resistor. The output is connected to the VIDEO AGC (Automatic Gain Control) and the VIDEO LINE AMPLIFIER. A 20V supply is connected to the grid, and a 2K resistor (R₂) is used for biasing. A 100 ohm resistor is connected to the AGC AMPLifier.

Other components and connections include a -15V supply for the first stage, a -6V supply for the emitter follower, and a 1K resistor (R₁) connected to a SYNC LEVEL of 100.

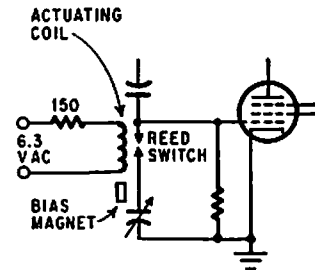
et al., Television System for Stratoscope I, *Electronics*, 33:25, p 49-53.



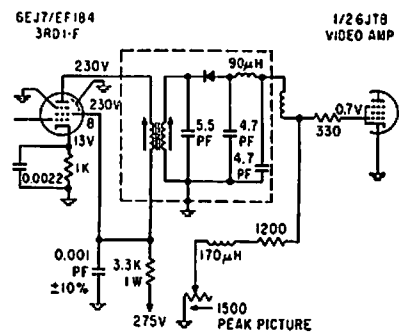
Television Circuits—Black-and-White

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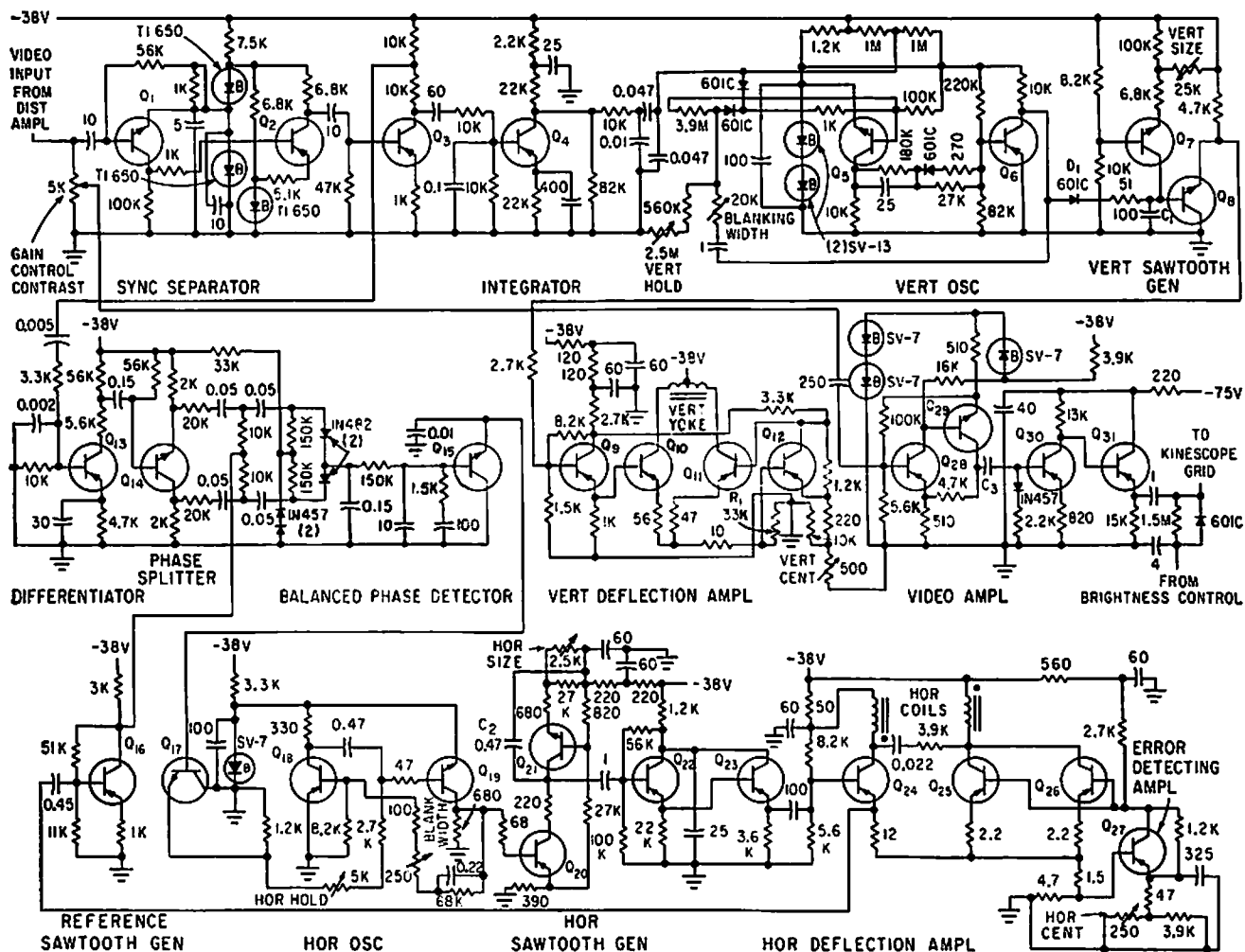
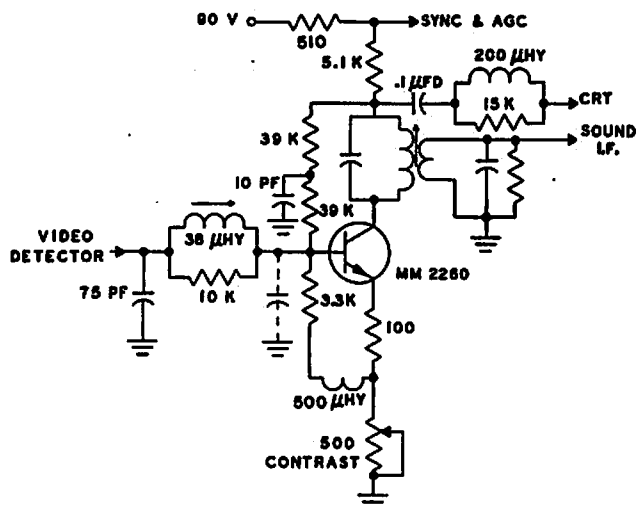
HORIZONTAL SYNC TRANSIENT DISPLAY— Reed switch operated at field frequency from a-c heater voltage, with permanent magnet providing magnetic bias to get 60 cps, permits observing single transient continuously while making tv receiver circuit adjustment.— M. B. Knight, Reed Switches for Breadboarding, *Electronics*, 37:16, p 93.



PEAK PICTURE CONTROL—Variable resistor in video detector load circuit can be adjusted to improve snowy pictures in fringe areas.—*Tv Set Size Shrinks, Electronics, 36:23, p 22.*



ONE-STAGE TRANSISTOR VIDEO AMPLIFIER—Overcomes Miller capacitance effect that normally causes excessive high-frequency rolloff. Intended for 12-inch and smaller b-w receivers, and provides direct cathode-ray drive. Bandwidth is 2 Mc. Uses MM2260 npn high-voltage silicon epitaxial transistor.—D. L. Wallesen, "A Single Stage Video Amplifier," Motorola Application Note AN-186, Feb. 1966.



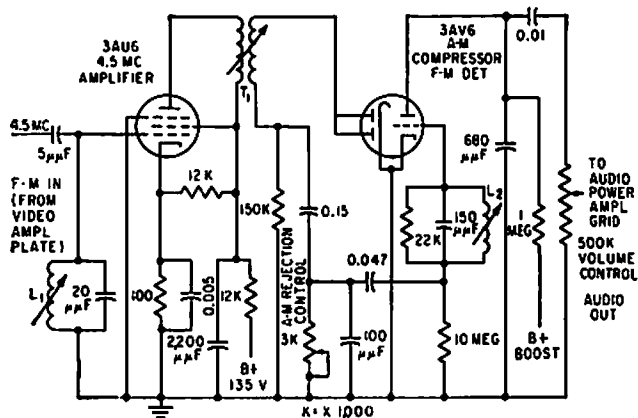
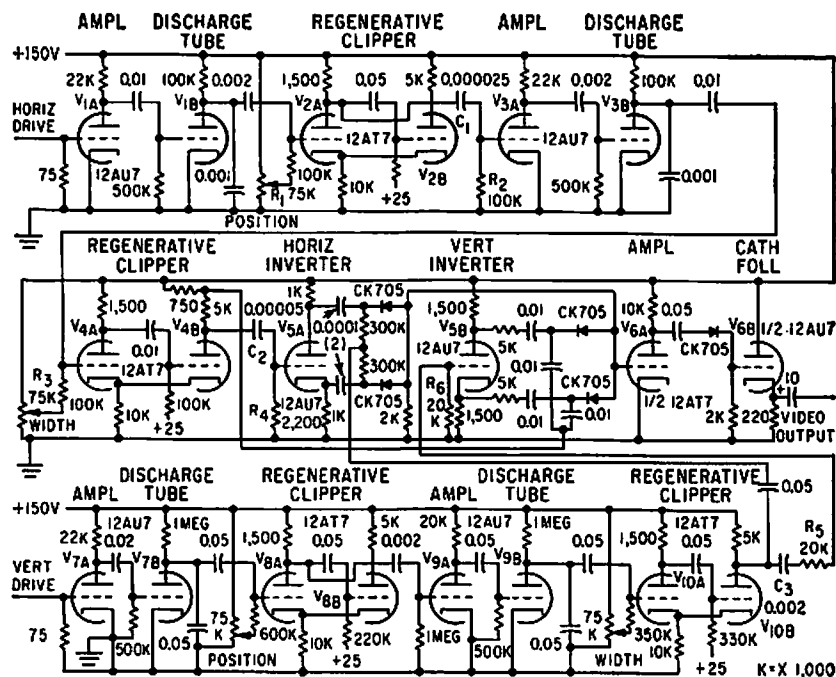
TRANSISTOR LEGEND

Q₁, Q₇, Q₈, Q₁₇, Q₁₉, Q₂₁, Q₂₉—2N585 Q₂, Q₃, Q₄, Q₆, Q₉, Q₁₂, Q₁₃—2N404 Q₁₄—2N388 Q₁₀, Q₁₁, Q₂₄, Q₂₅, Q₂₆—2N158
Q₅, Q₁₅—2N335 Q₁₈, Q₂₀, Q₂₂, Q₂₃, Q₂₇, Q₂₈—2N404 Q₁₆—2N586 Q₃₀, Q₃₁—2N247 (SELECTED FOR E_c = 75V)

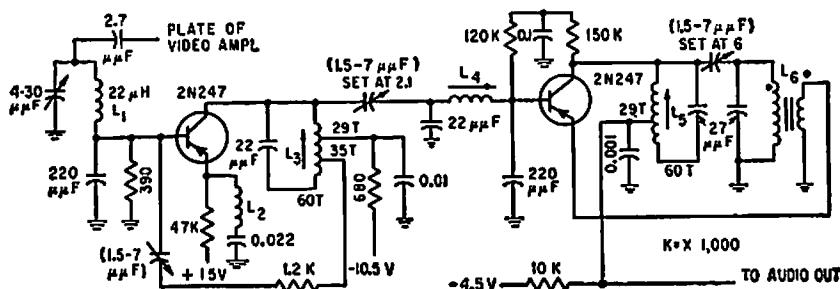
SLOW-SCAN TV RECEIVER—Signals from f-m telemetry system in balloon are picked up by commercial receiver and fed to distribu-

tion amplifier serving three monitors, having identical circuits as shown. Video bandwidth is 200 kc.—L. E. Flory et al., Television

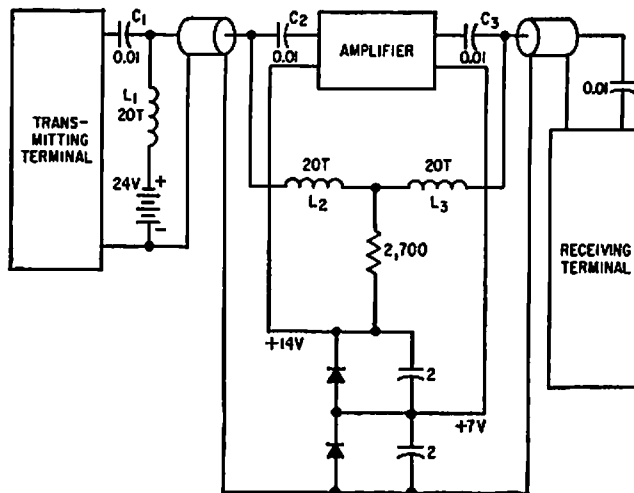
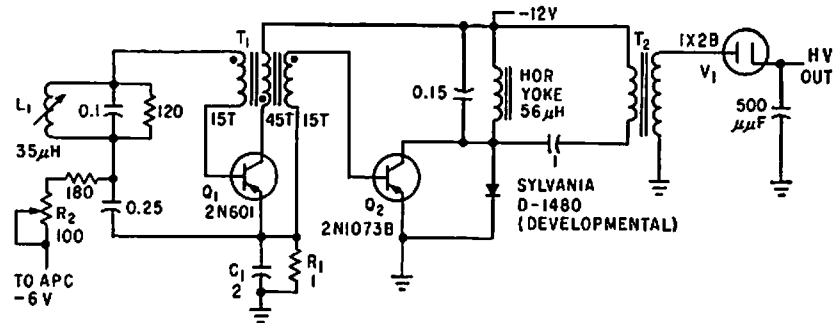
System for Stratoscope I, *Electronics*, 33:25, p 49-53.



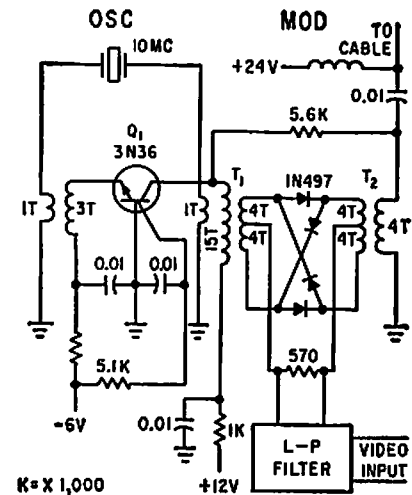
TV SOUND SLOPE DETECTOR—Uses drift transistor as efficient, highly sensitive oscillating linear-slope detector, injection-locked by one-stage sound driver. A-m rejection is uniformly high over full detector bandwidth. Audio output is constant, independent of carrier strength.—M. Meth, *Tv Sound Detector Uses Drift Transistor*, *Electronics*, 32:8, p 62-64.



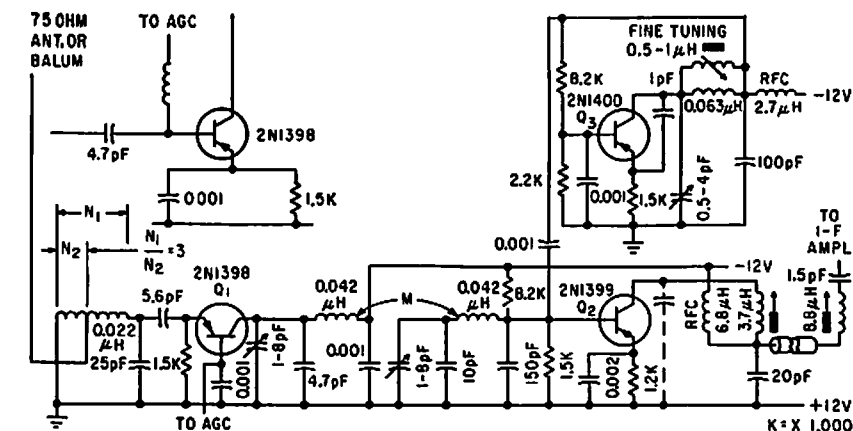
HORIZONTAL DEFLECTION—Two-transistor circuit provides high ratio of reverse to forward base drive. Oscillator current is 0.12 amp, output-stage current 0.72 amp, and push-pull yoke current is 11 amp.—M. Fischman, *Transistorized Horizontal Deflection for Television*, *Electronics*, 32:33, p 60-63.



CCTV REPEATER POWER SUPPLY—Operates from 24-v battery at transmitting terminal. C1 isolates battery voltage from terminal equipment, and L1 prevents shorting of signal. Two 7-v zener diodes in series serve as voltage regulators.—L. G. Schimpf, *Carrier Transmission for Closed-Circuit Television*, *Electronics*, 32:24, p 66-68.

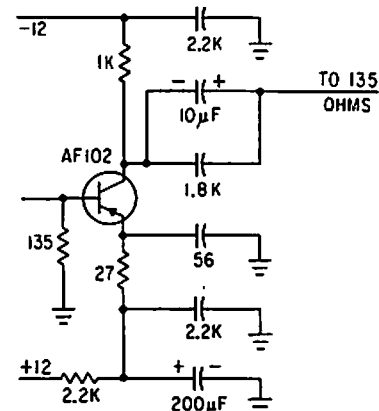


CCTV 10-MC CARRIER TRANSMITTER TERMINAL—Uses single tetrode transistor in oscillator to feed four-diode balanced modulator. Peak level of modulator output is 4 db below 1 mw.—L. G. Schimpf, *Carrier Transmission for Closed-Circuit Television*, *Electronics*, 32:24, p 66-68.

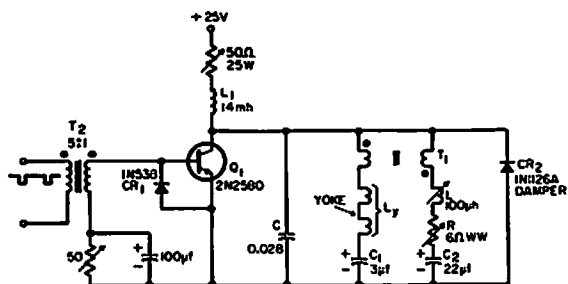


FOUR-TRANSISTOR TUNER—Diffused-base mesa transistors permit design of tv tuners with noise performance equal to that of tube tuners. Article gives complete design pro-

cedure for r-f amplifier, mixer, and oscillator stages.—H. F. Cooke, *Designing Tv Tuners with Mesa Transistors*, *Electronics*, 33:15, p 64-69.

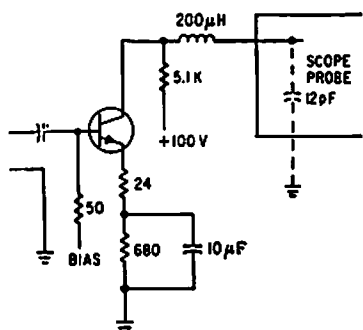


EMITTER FEEDBACK PEAKING GIVES 100-MC BANDWIDTH—Four identical AF102 stages amplify pulse output of delta modulator in tv waveguide link.—C. Kramer and J. C. Balder, *Delta-Modulated Television Waveguide Link*, *Electronics*, 36:31, p 50-52.



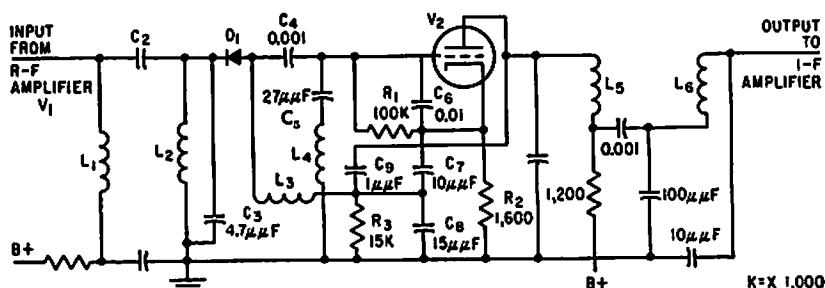
LINEAR TRANSISTORIZED HORIZONTAL SWEEP—Placing transformer in yoke circuit provides better than 0.5% linearity for deflecting 16-inch crt having 52° deflection angle and 15-kv acceleration voltage. Line rate is 28.35 kc with 945 lines. Supply volt-

age determines width or amplitude of sweep. C can be increased to get 525-line rate.—J. W. Greninger, *Improving Linearity in Transistorized Horizontal Sweeps*, EEE, 12:9, p 61-62.



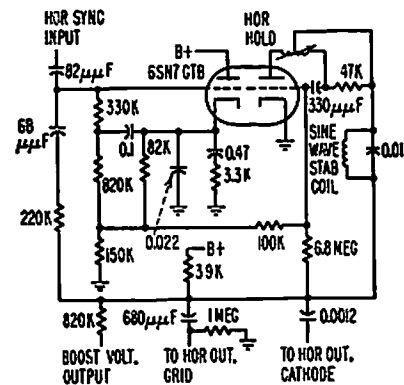
VIDEO OUTPUT AMP—With 2N834 circuit, output is sufficient to drive crt.—W. D. Roehr,

Epitaxial Process Improves Transistor Characteristics, Electronics, 34:9, p 52-53.

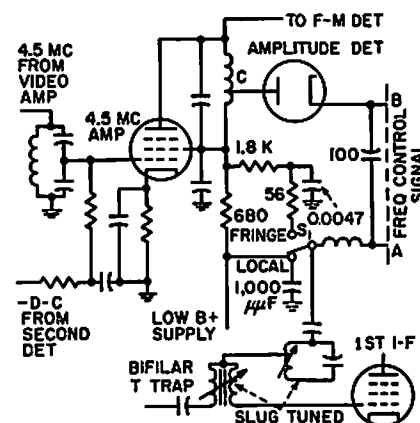


DIODE MIXER FOR TUNER—1N87 semiconductor diode mixer D1 improves isolation of r-f amplifier from 6ER5 tube V2, connected

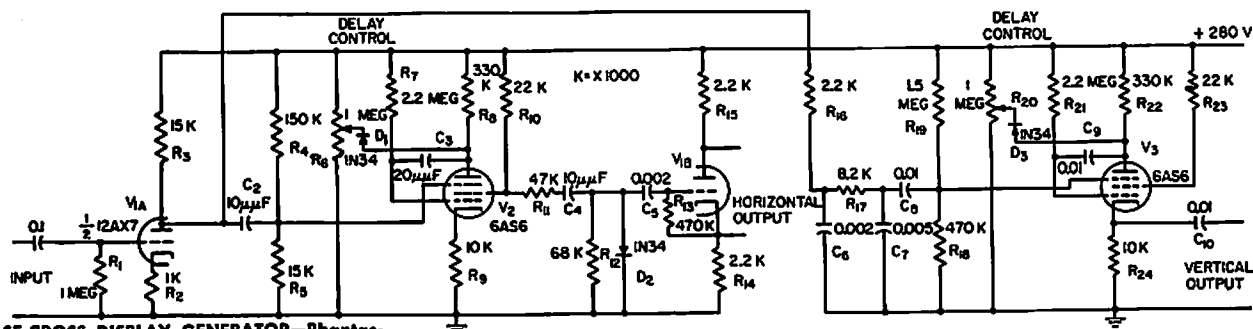
as Colpitts oscillator.—E. H. Hugenoltz, *One-Tube Oscillator Mixers for Tv and F-M Tuners*, Electronics, 33:3, p 76-79.



STABILIZED HORIZONTAL OSCILLATOR—Sine-wave stabilization or ringing coil pulls Synchroguide horizontal oscillator frequency back to correct value when tube or other components drift in value.—W. E. Babcock, *Unusual Tube Effects Cause Circuit Troubles*, Electronics, 31:37, p 90-93.



AUTOMATIC FRINGE TUNING—Circuit serves to disable sound track, to increase amplitude of sound carrier, so weak picture carrier will be tuned higher on i-f pass band, at point of desired fringe tuning.—C. W. Baugh, Jr. and L. J. Sienkiewicz, *Sound Signal Tunes Tv Automatically*, Electronics, 31:17, p 54-58.



PULSE-CROSS DISPLAY GENERATOR—Phantasticon circuits delay horizontal and vertical sync pulses, when added to monitor or tv

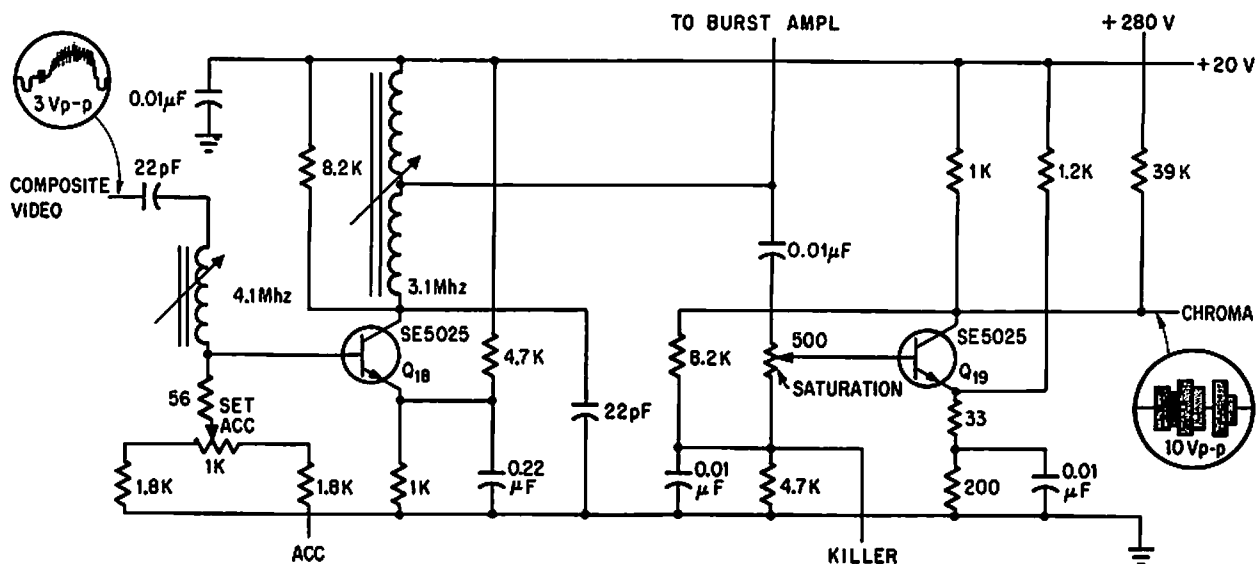
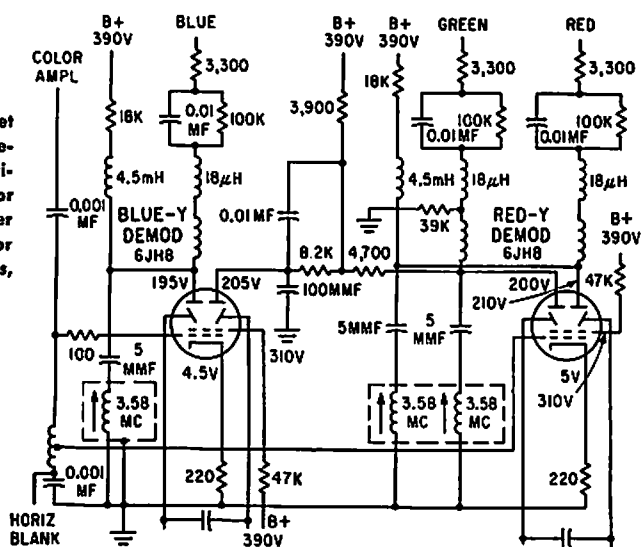
receiver, to provide display for checking operation of tv station sync generator.—H. E.

O'Kelley, *Pulse-Cross Modification of Tv Receivers*, Electronics, 31:9, p 54-55.

CHAPTER 89

Television Circuits—Color

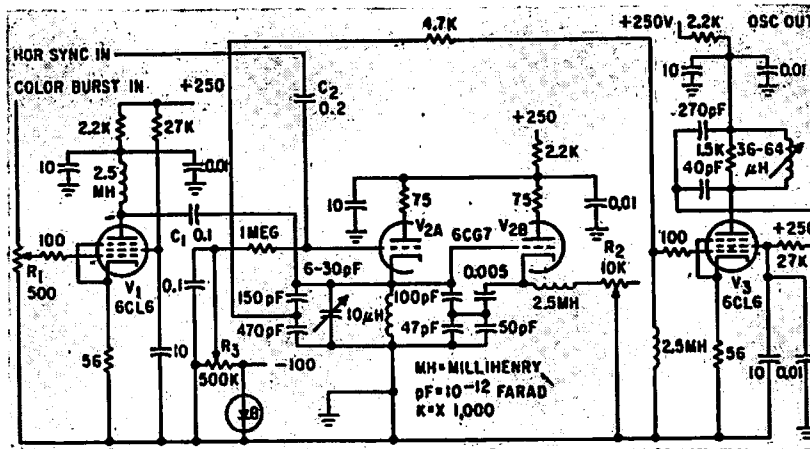
COLOR DEMODULATOR—Uses two 6JH8 sheet beam tubes as red and blue luminance demodulators. Balanced outputs of both polarities on plates of tubes eliminate need for additional phase inverter stages to recover green luminance signal.—Color Demodulator Uses Beam Switching Tubes, *Electronics*, 34:36, p 30–31.



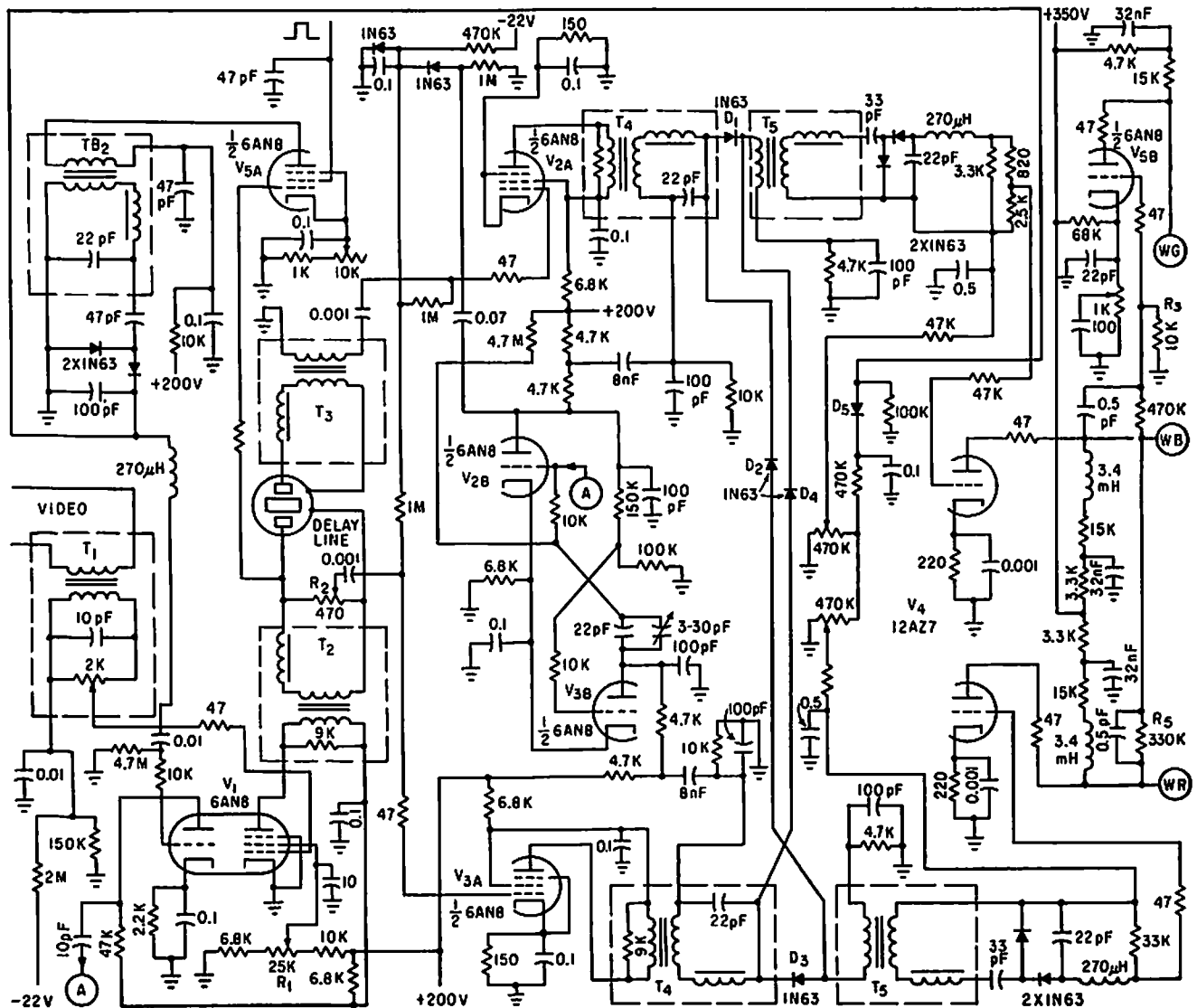
CHROMA AMPLIFIER—Used in transistorized color tv to provide response slope opposite that of i-f amplifier. Automatic color control

signal reduces voltage gain of first stage Q18. Color killer signal cuts off Q19 during monochrome operation.—D. Bray, Solid State

Makes Debut in Big-Screen Color Tv, *Electronics*, 39:8, p 99–105.



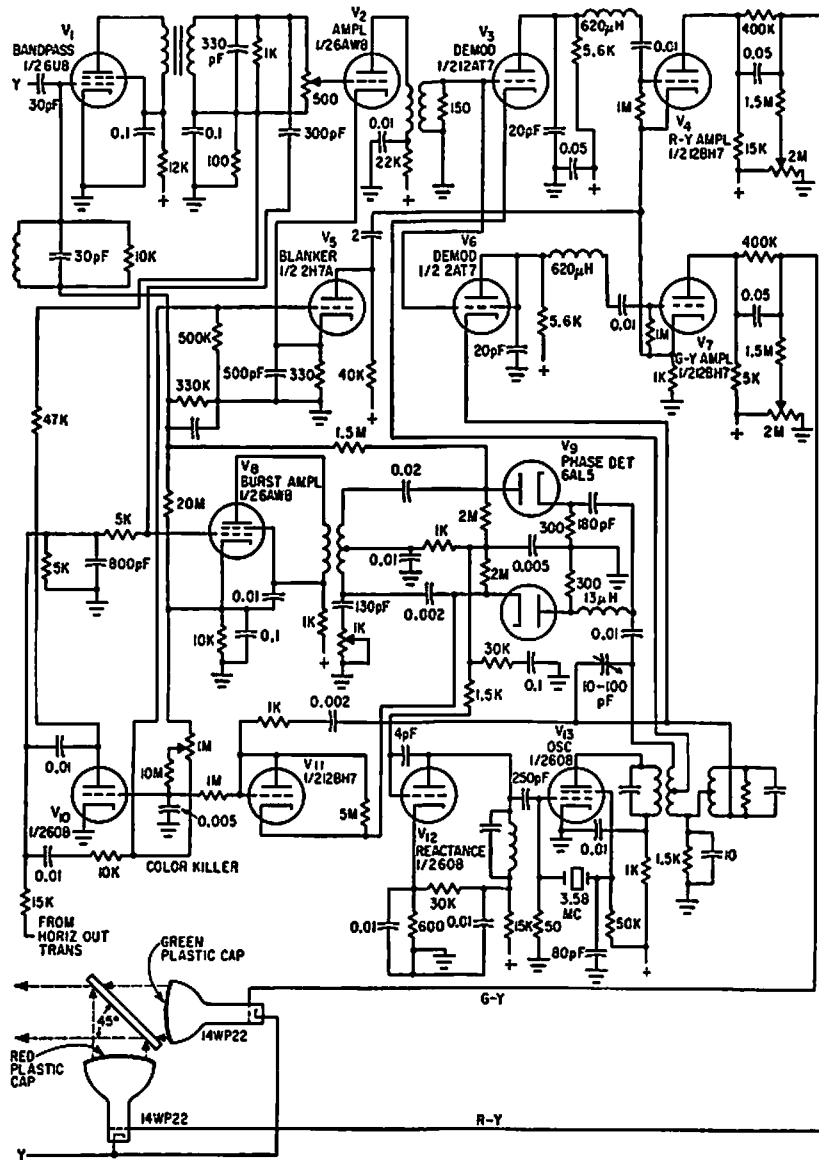
COLOR-BURST-GATED OSCILLATOR—For playback of color tv recordings on magnetic tape, color burst is removed from composite video signal on tape, amplified by V1, and used to gate 3.58-Mc start-stop oscillator V2 to make this oscillator ring at burst frequency. Regenerated 3.58-Mc signal is amplified by V3 and fed to decoder for demodulating chroma information.—J. Roizen, *Magnetic Recording of Color Television*, *Electronics*, 33:1, p 76-79.



FRENCH COLOR TV CHROMINANCE-SECAM system uses time multiplexing of two chrominance signals, transmitted sequentially, with

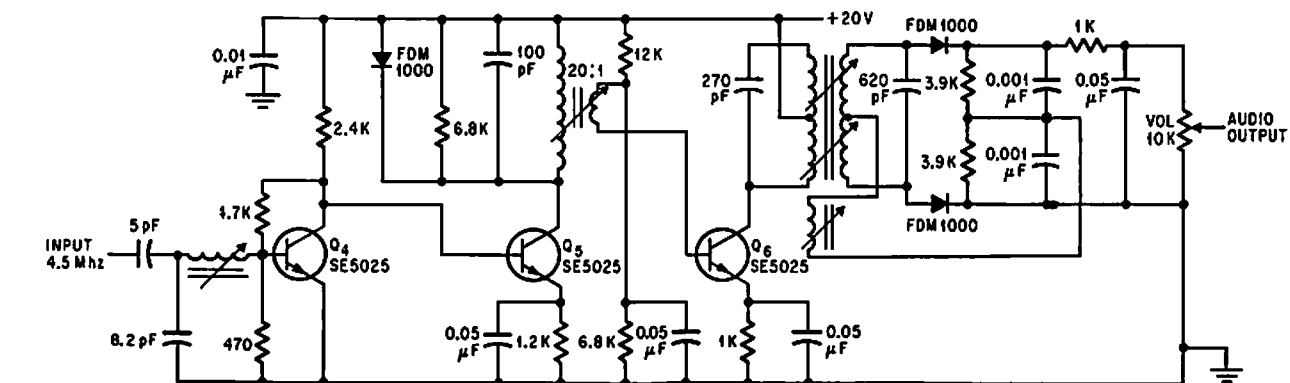
one-line memory in receiver circuit.—R. Chaste, P. Cassagno, and M. Colas, *Sequential Receivers for French Color Tv Sys-*

tom, *Electronics*, 33:19, p 57-60.



TWO-COLOR TV—Picture is viewed on half-silvered mirror that combines images of red and green 14-inch picture tubes. Receiver circuits accept standard NTSC color

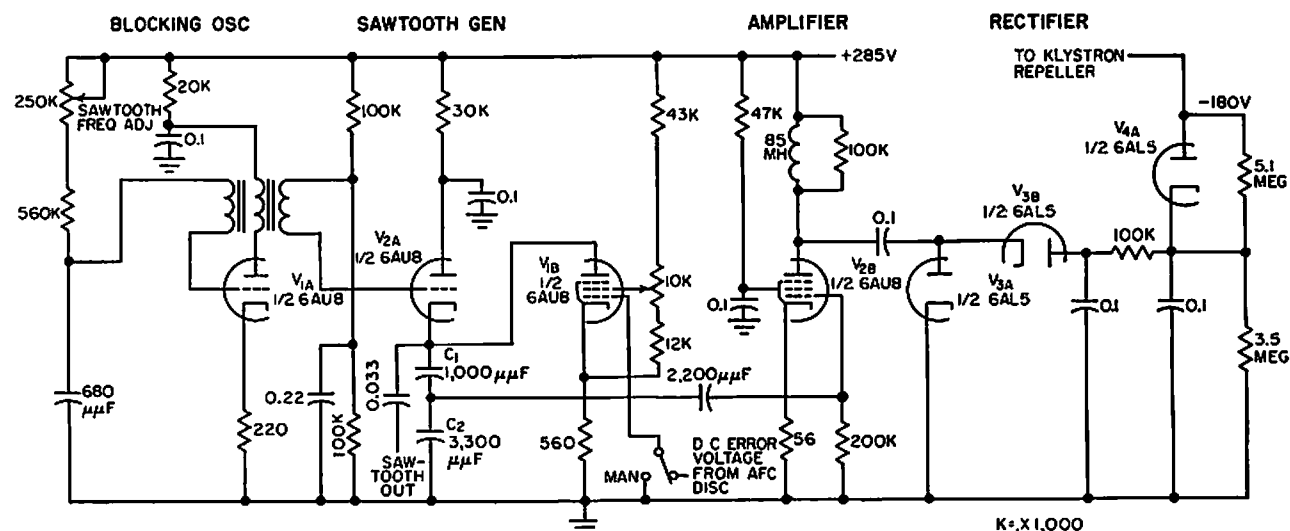
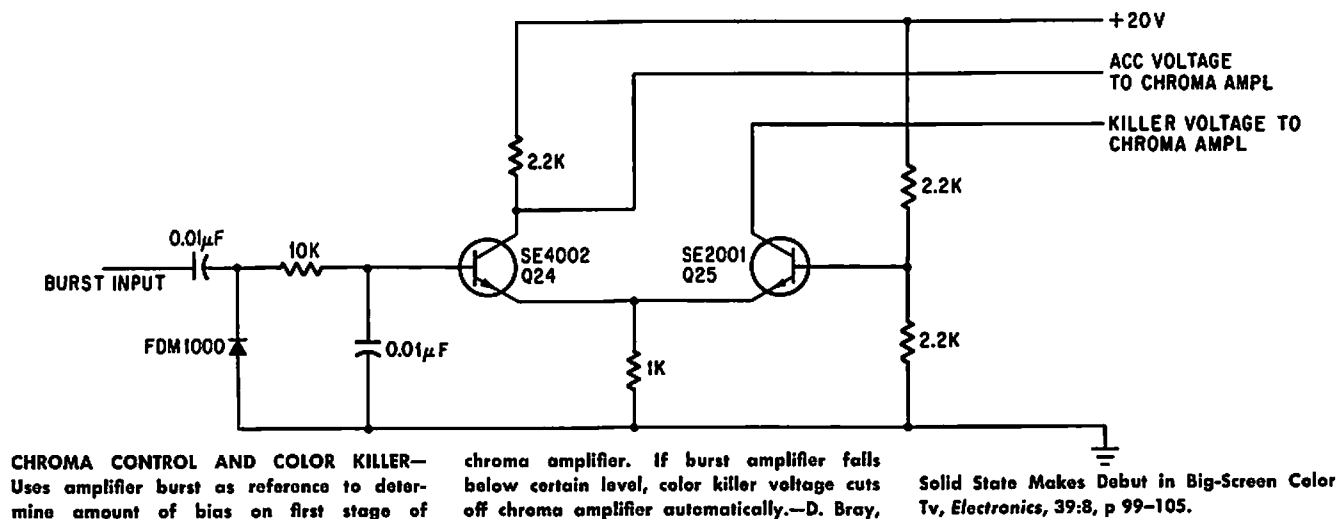
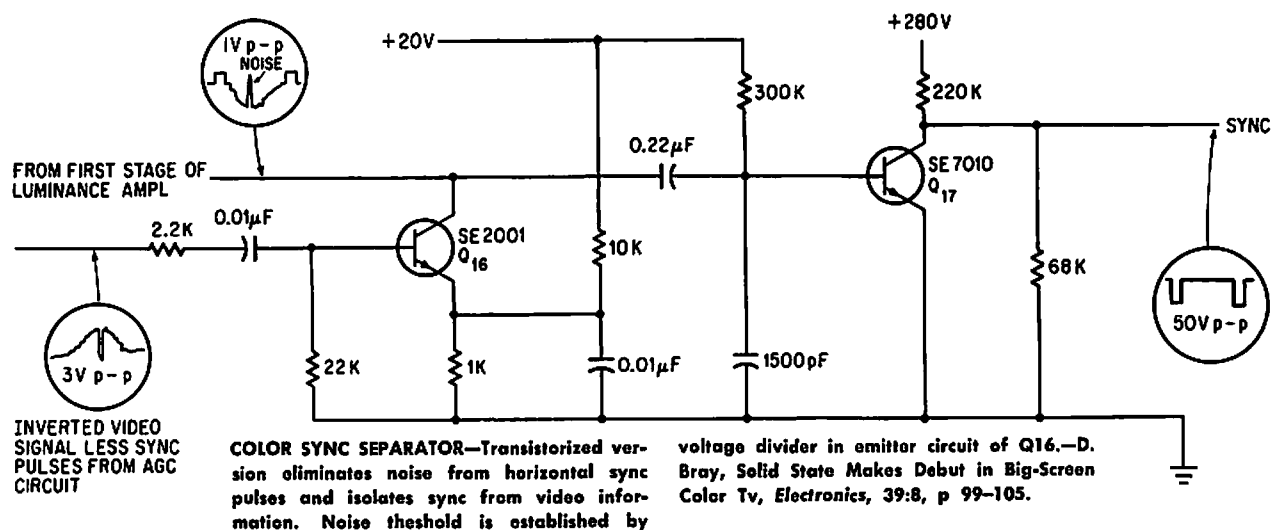
signal. Chief drawback is narrow angle of vision.—K. Hashimoto, *Color TV Based on Land Theory* uses Two Single-Gun Tubes, *Electronics*, 35:38, p 54-55.

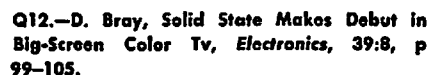
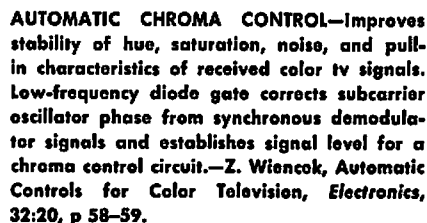
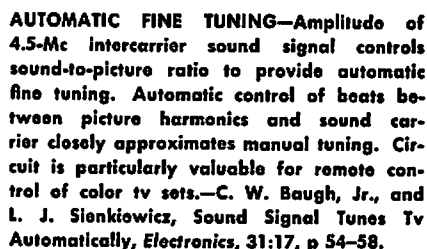


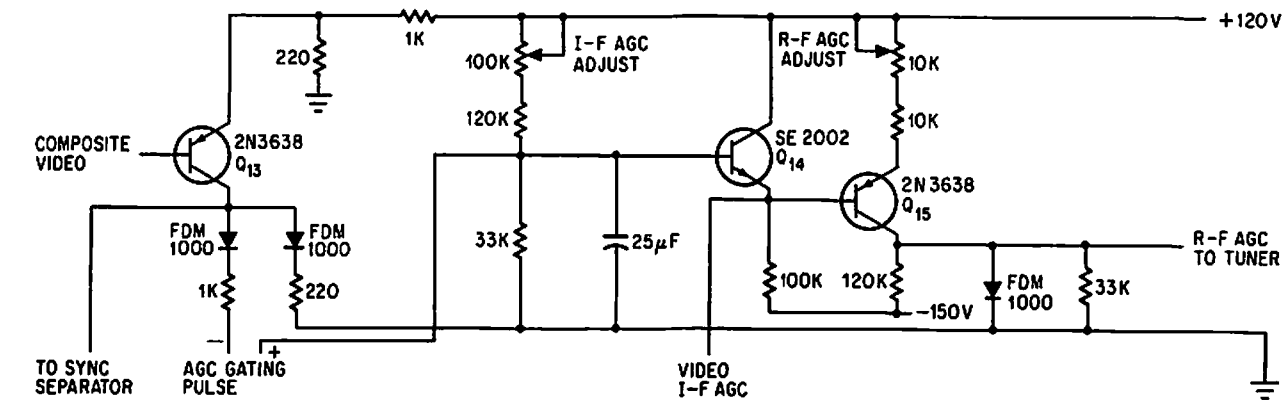
SOUND I-F FOR COLOR TV—Uses three transistor stages and Foster-Seely discriminator

to give audio output of 1 v peak to peak. —D. Bray, *Solid State Makes Debut in Big-*

Screen Color Tv, *Electronics*, 39:8, p 99-105.



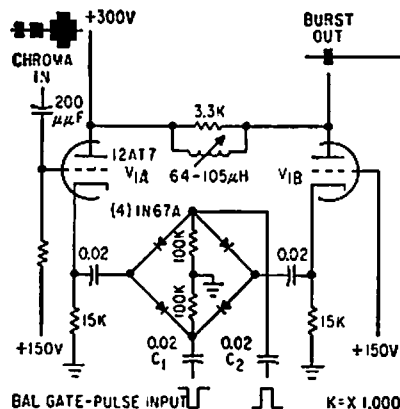




COLOR AGC—Supplies bias voltage to r-f and video i-f stages of color set using transistors in all but deflection and rectifier

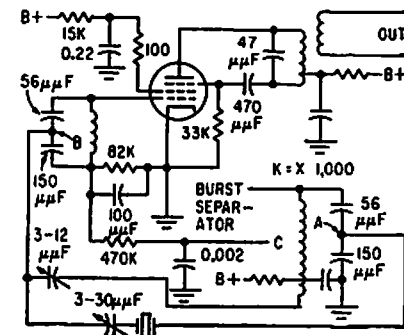
stages, to maintain video output amplitude at about 3 v.—D. Bray, Solid State Makes Debut in Big-Screen Color Tv, *Electronics*,

39:8, p 99–105.

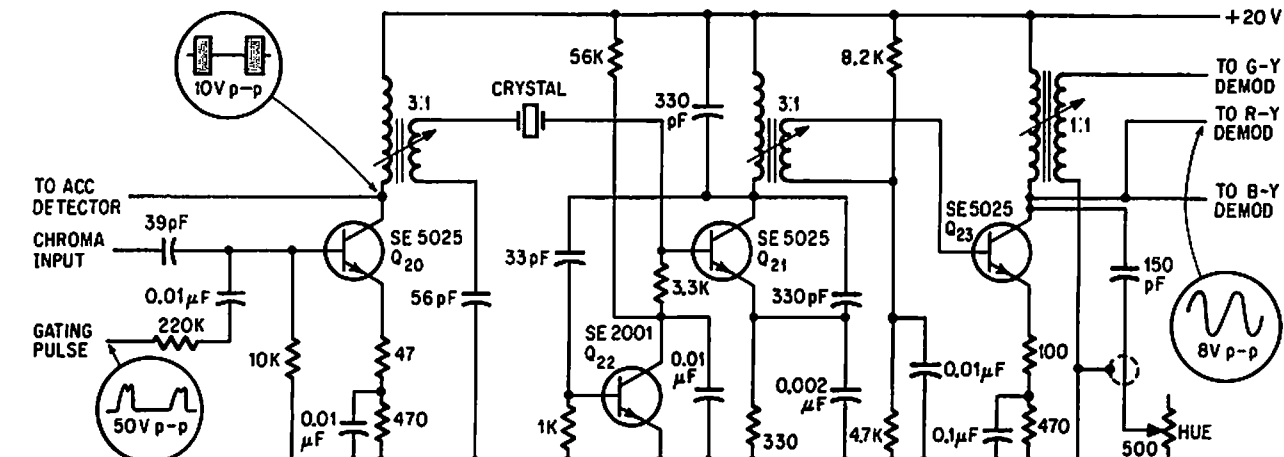


BALANCED-DIODE COLOR-BURST GATE—Used in automatic video-processing amplifier that instantly compensates for wide variations in color or monochrome input signal levels,

to maintain output signal components at correct levels.—J. O. Schroeder, Holding Video Levels While Switching Studios, *Electronics*, 32:22, p 96–98.



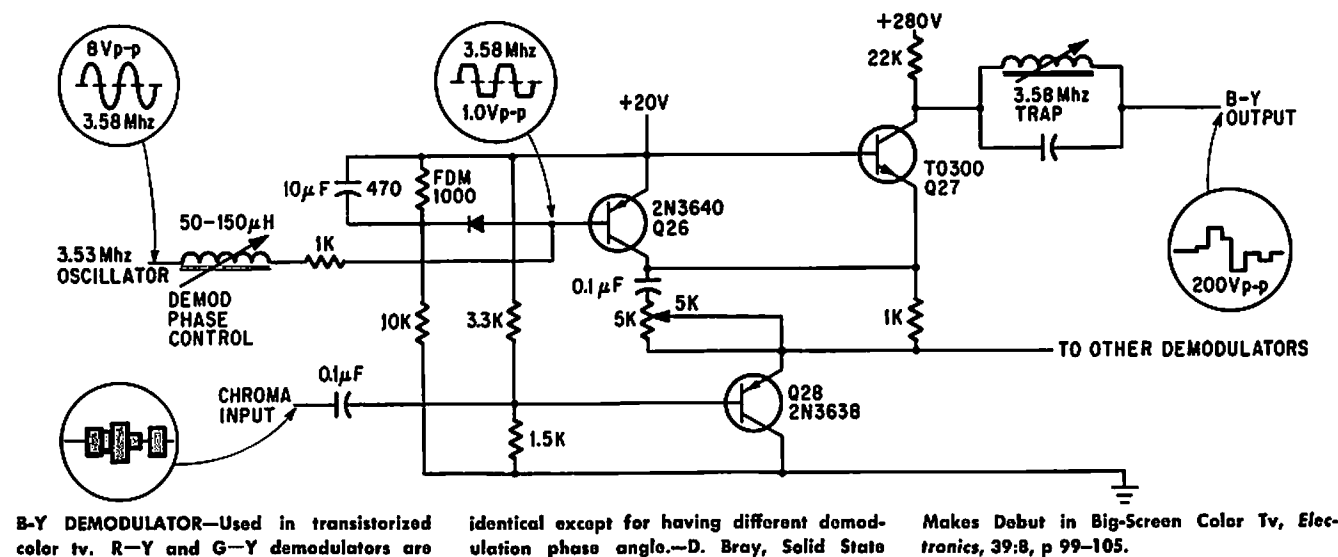
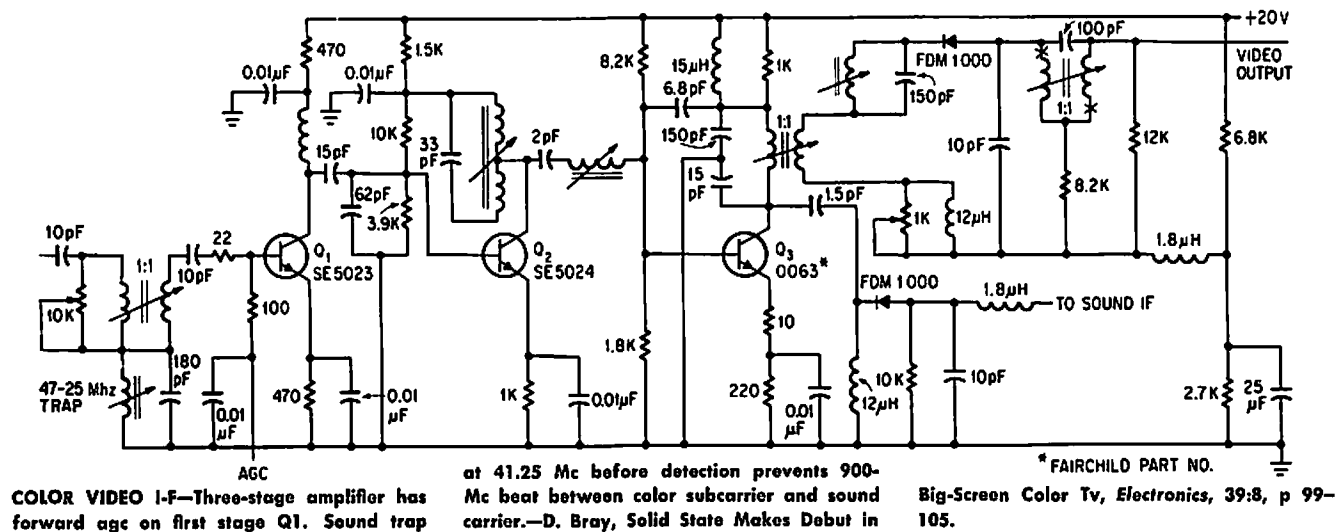
COLOR HOLD—Uses passive filter to separate color subcarrier frequency from sync burst, along with injection-locked oscillator that combines amplitude limiting and power amplification for direct drive of color demodulators.—I. N. Moth, Locked Oscillator for Color Tv, *Electronics*, 32:39, p 91–92.



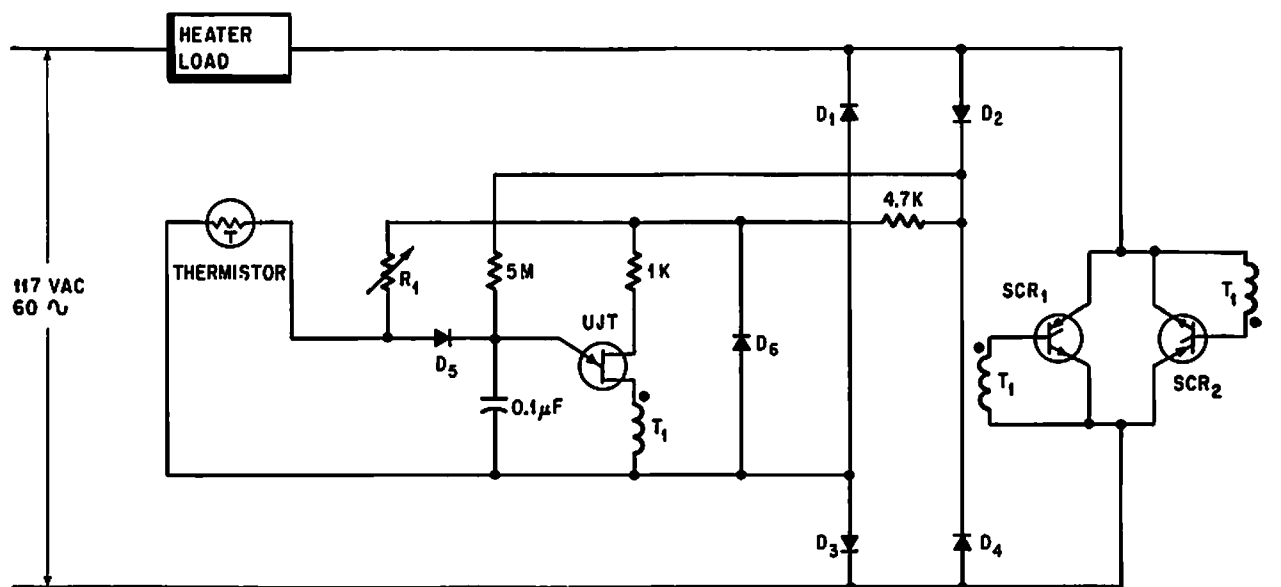
COLOR BURST AMPLIFIER—Burst amplifier Q20 and 3.58-Mc crystal oscillator Q21 are driven by output of first stage of chroma amplifier of transistorized color tv. Amplifier burst

locks oscillator frequency to subcarrier frequency required by color demodulators, and provides reference burst for automatic color control circuit.—D. Bray, Solid State Makes

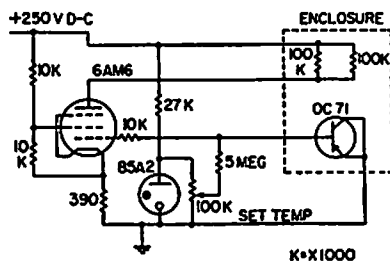
Debut in Big-Screen Color Tv, *Electronics*, 39:8, p 99–105.



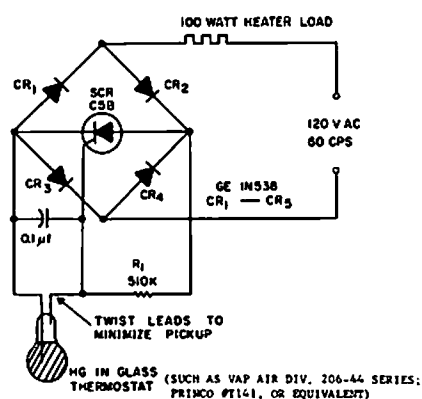
Temperature Control Circuits



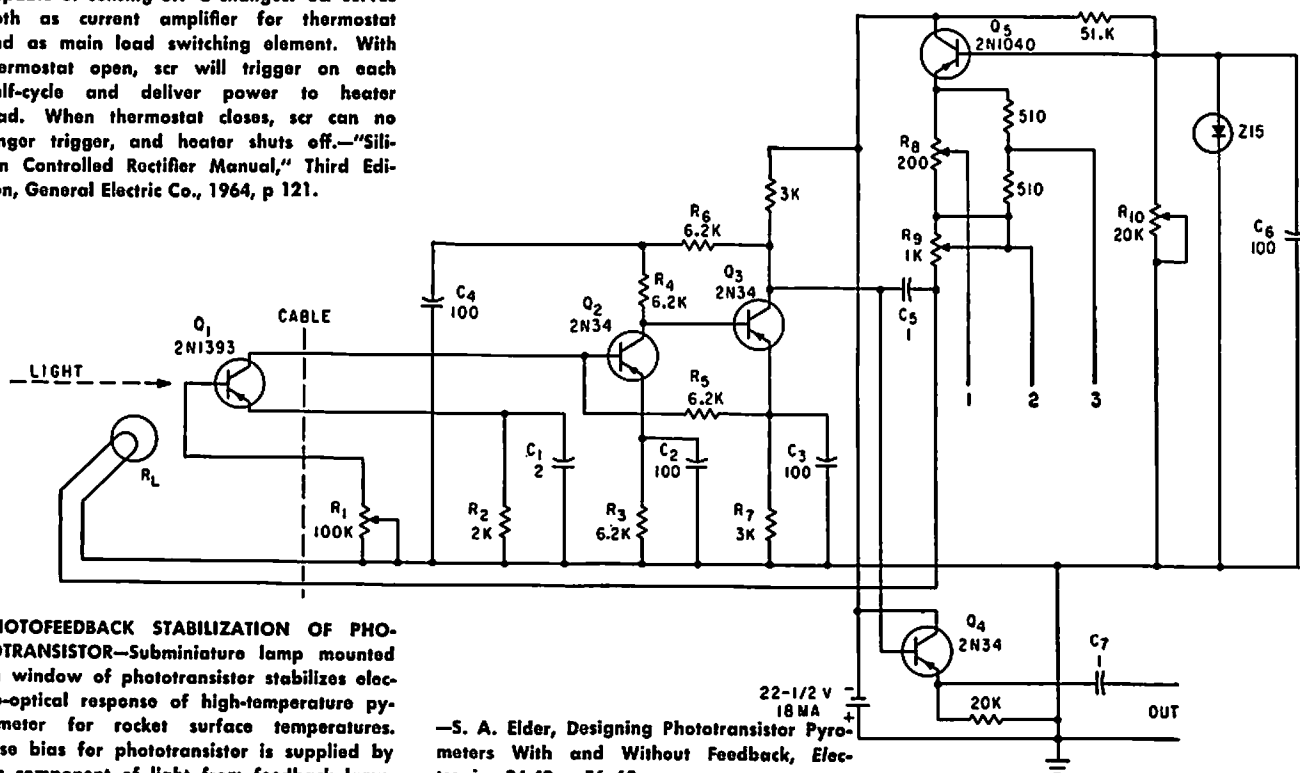
tion transistor trigger earlier in line voltage cycle so scr's deliver more power to oven.



HYBRID THERMOSTAT—Utilizes reverse characteristics of pnp junctions for temperature control. Provides continuous control with higher sensitivity than thermistors, along with quiet operation, remote resetting of temperature, and small thermal time constant. Chief disadvantage is high impedance.—H. Sutcliffe, Transistor Temperature Controller, *Electronics*, 31:13, p 81-84.

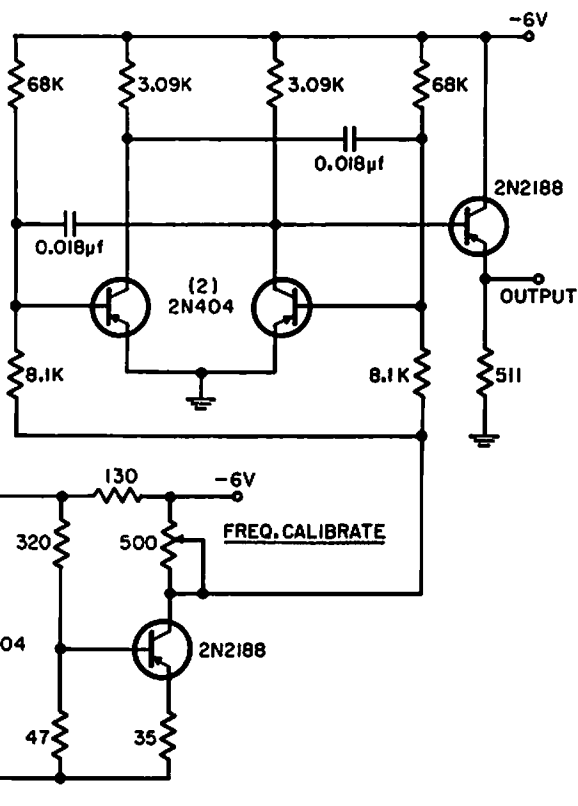


MERCURY THERMOSTAT AND SCR CONTROL HEATER—Uses mercury-in-glass thermostat capable of sensing 0.1°C changes. Scr serves both as current amplifier for thermostat and as main load switching element. With thermostat open, scr will trigger on each half-cycle and deliver power to heater load. When thermostat closes, scr can no longer trigger, and heater shuts off.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 121.



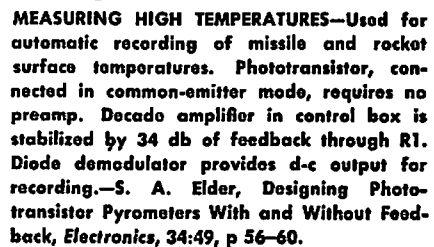
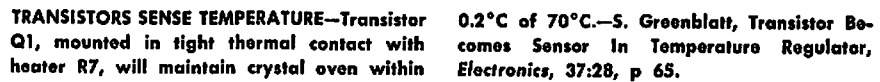
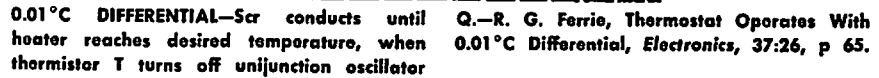
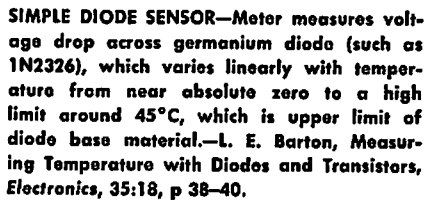
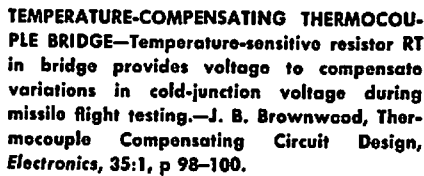
PHOTOFEEDBACK STABILIZATION OF PHOTOTRANSISTOR—Subminiature lamp mounted on window of phototransistor stabilizes electro-optical response of high-temperature pyrometer for rocket surface temperatures. Base bias for phototransistor is supplied by d-c component of light from feedback lamp.

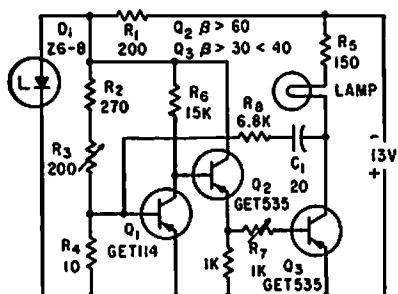
—S. A. Elder, Designing Phototransistor Pyrometers With and Without Feedback, *Electronics*, 34:49, p 56-60.



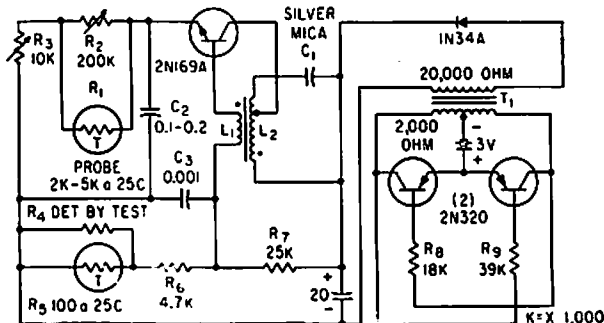
TEMPERATURE TELEMETRY FOR BALLOON—Designed for range of -70 to $+70^\circ\text{C}$, for which circuit produces frequency change of 1.5 kc. Uses temperature-sensitive base-to-emitter voltage of transistor, which varies

linearly with temperature, as transducer for voltage-controlled oscillator based on astable mvbr.—G. F. Ingle, Using Transistors for Temperature Measurement, *EEE*, 11:8, p 53-55.



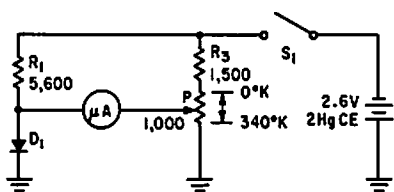


ROAD ICING ALARM—Sensing transmitter mounted on auto about 2 feet above road, with junction of transistor connected to case, is connected to low-frequency oscillator having lamp load. R7 is adjusted so lamp is out but on verge of flashing of 2°C. When temperature drops, lamp flashes. Duration of each flash increases down to 0°C after which lamp remains on.—J. A. Irvine, Reducing Winter Skids with a Transistor Warning Circuit, *Electronics*, 36:4, p 56-58.

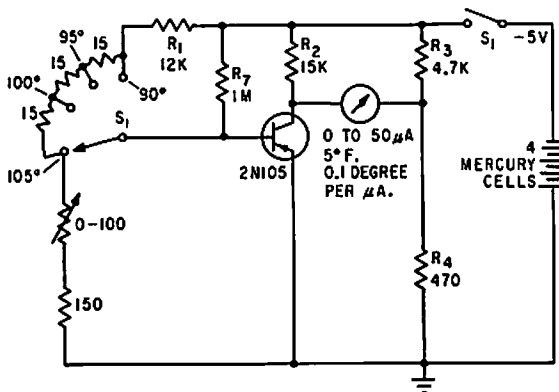


TEMPERATURE TRANSMITTER—2N169A transistor is used in tuned-collector oscillator, with large R-C time constant in emitter circuit to give self-modulator for quenching action. Variation in quench break is ac-

complished with temperature-sensing element R1, consisting of glass-enclosed bead thermistor.—R. H. Elksen, Temperature Telemetry Aids Frozen Food Study, *Electronics*, 33:33, p 129-131.

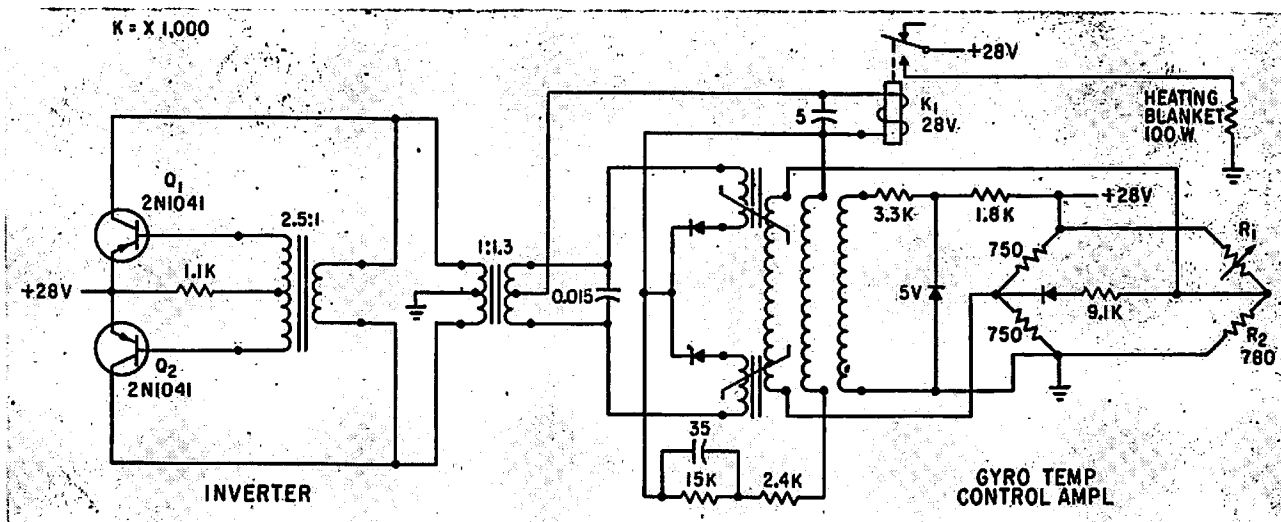


WIDE-RANGE DIODE THERMOMETER—Temperature-sensing germanium-diode bucking-voltage microammeter has null indicator, covers full usable range of from near absolute zero to about 45°C with resistance values shown.—L. E. Barton, Measuring Temperature with Diodes and Transistors, *Electronics*, 35:18, p 38-40.



BODY-TEMPERATURE TRANSISTOR THERMOMETER—Covers range of 90° to 105°F in three steps, with temperature indicated on meter that measures base bias of germanium

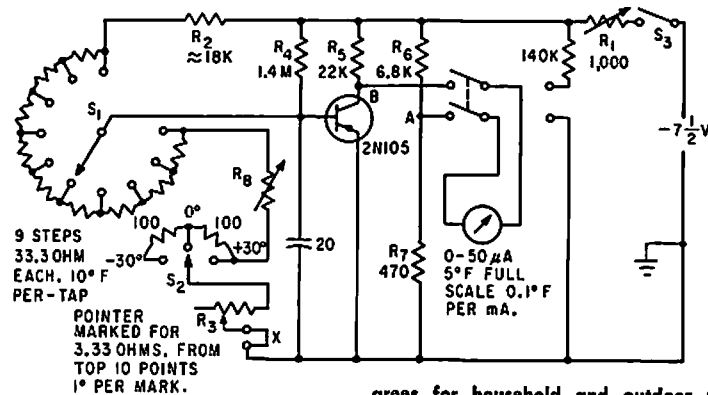
transistor, for which bias varies linearly with temperature.—L. E. Barton, Measuring Temperature with Diodes and Transistors, *Electronics*, 35:18, p 38-40.



GYRO TEMPERATURE CONTROL—Regulates temperature to 0.5°F by sensing differences between gyro-mounted temperature-sensitive

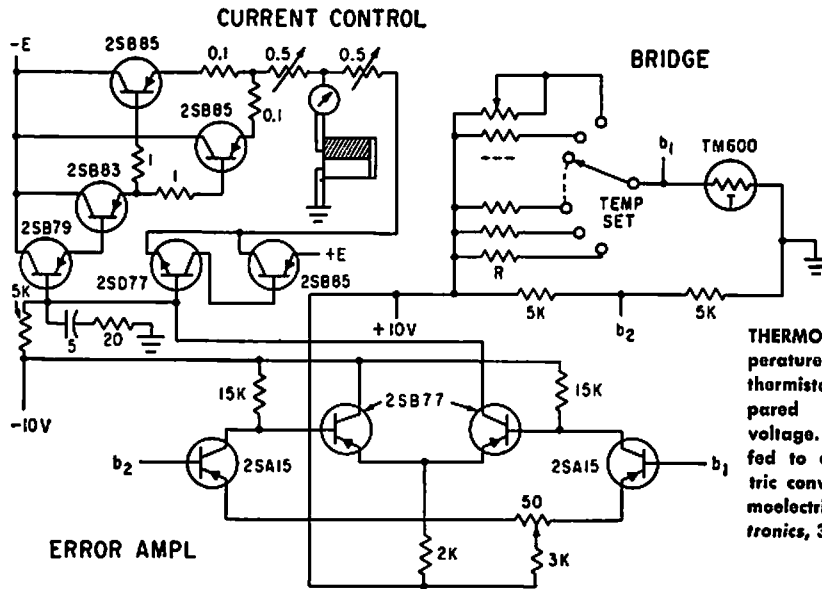
resistor R1 and fixed resistor R2 in bridge. Magnetic amplifier for bridge operates relay K1 to energize gyro heater when temperature

is low.—R. E. King and H. Low, Solid-State Guidance For Able-Series Rockets, *Electronics*, 33:5, p 60-63.

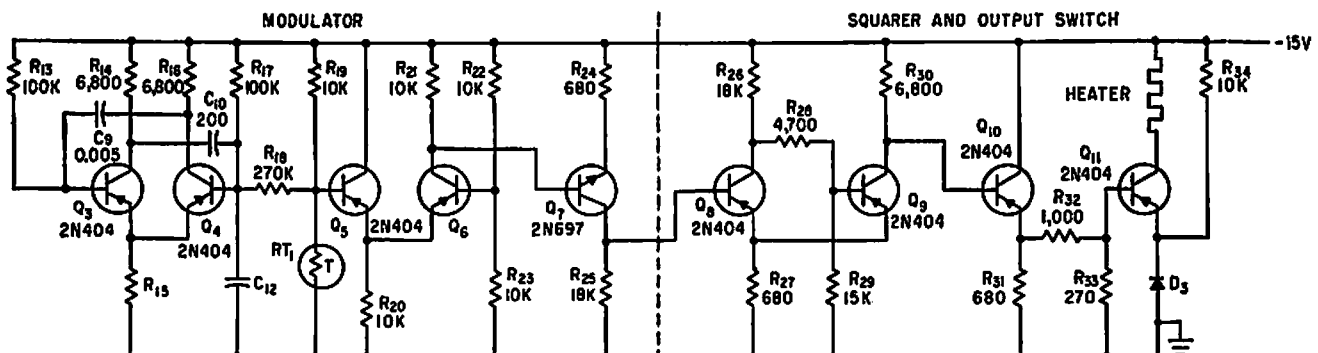


TRANSISTOR THERMOMETER—Base bias of germanium transistor varies linearly with temperature over wide range, at about 0.0014 v d-c per deg F. Meter is connected to measure base bias and calibrated in de-

grees for household and outdoor temperatures. Range switch S1 covers 0 to 100°F in ten steps, and S2 extends range 30° in both directions.—L. E. Barton, *Measuring Temperature with Diodes and Transistors*, *Electronics*, 35:18, p 38-40.



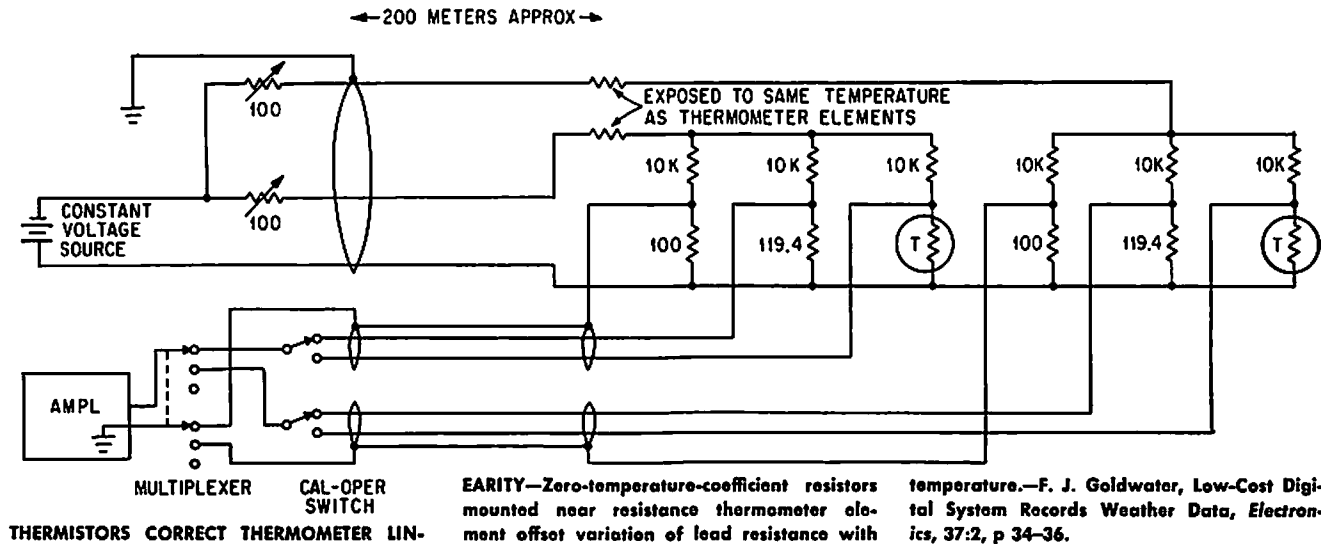
THERMOELECTRIC COOLING CONTROL—Temperature inside cooling chamber is sensed by thermistor bridge and bridge output is compared with temperature-setting reference voltage. Difference voltage is amplified and fed to current control circuit of thermoelectric converter.—M. Nagata and Z. Abe, *Thermoelectric Elements for Circuit Cooling*, *Electronics*, 34:41, p 54-55.



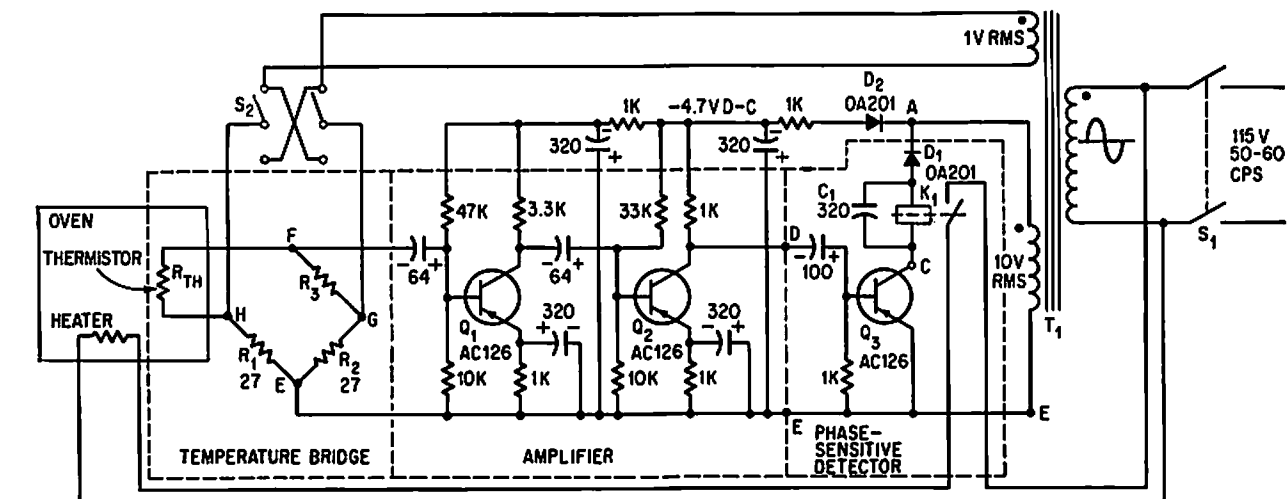
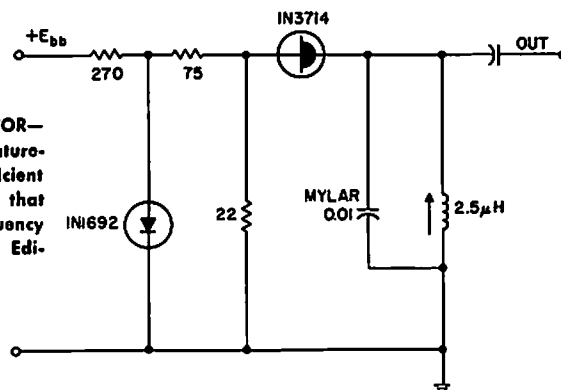
CRYSTAL OVEN—Thermistor RT1 senses temperature of crystal oscillator cavity and modifies output of asymmetric free-running mvbr Q3-Q4 whose output is integrated by C12.

Thermistor is followed by modulator, amplifier Q7, and four-transistor switch that applies power at fixed repetition frequency but with on time per cycle controlled by

thermistor.—M. Lyseboy, *Microminiature Crystal Oscillator Using Wafer Modules*, *Electronics*, 35:15, p 60-61.



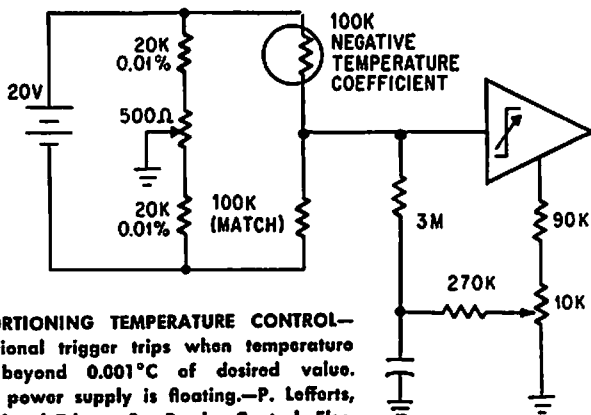
1.1-Mc TEMPERATURE-SENSING OSCILLATOR—Uses mylar capacitor as main temperature-sensing element, with temperature coefficient of 0.5 kc/°C, in tunnel-diode oscillator that translates temperature changes into frequency changes.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 350.



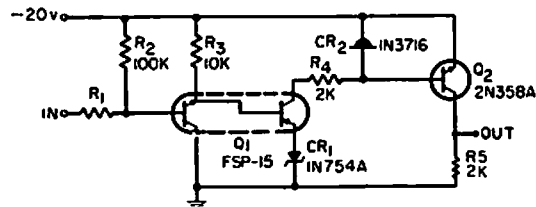
HIGH-ACCURACY CONTROL—Holds temperature constant to within 0.1°C for any value between -25 and 200°C. For controlling refrigerated unit instead of oven, switch S2

is placed in its other position and output relay is then used to energize solenoid valve that controls flow of refrigerant.—G. H. P. Kohnke, *Electronic Thermostat Controls Tem-*

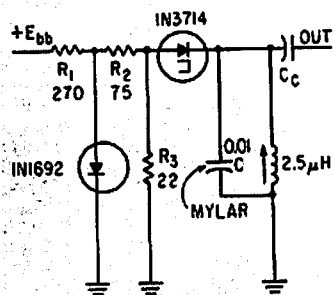
perature to Within 0.1°C, *Electronics*, 39:1, p 100-102.



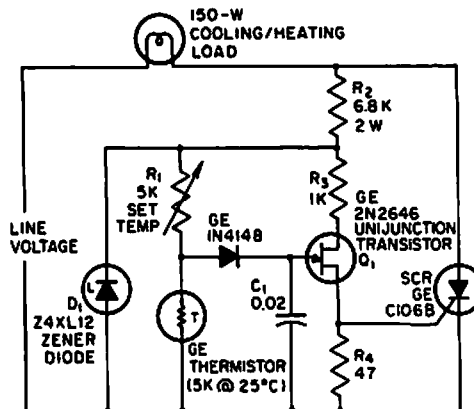
PROPORTIONING TEMPERATURE CONTROL—Operational trigger trips when temperature goes beyond 0.001°C of desired value. Bridge power supply is floating.—P. Lefferts, Operational Trigger For Precise Control, *Electronics*, 37:28, p 50-55.



LOW-HYSTERESIS D-C LEVEL DETECTOR—Serves as temperature control when thermistor or other temperature-sensing resistive device is connected to input. Dual complementary transistor Q1 is high-stability d-c amplifier, with zener diode CR1 providing threshold level. With sharp-breaking characteristic for zener, hysteresis can be less than 10 mv between turn-on and turn-off.—P. C. Murray, Accurate DC-Level Detector, *EEE*, 13:12, p 65.



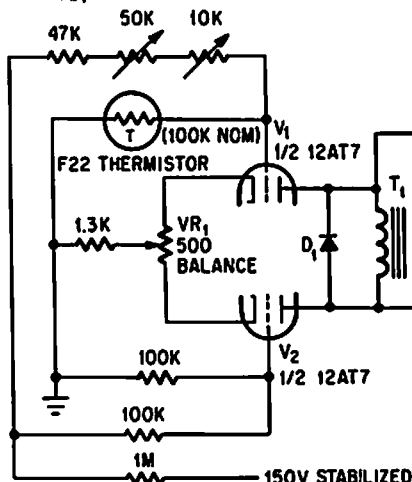
TEMPERATURE-SENSING TD OSCILLATOR—Mylar capacitor with known and reproducible temperature characteristics makes oscillator frequency vary with temperature. Diode bias regulator circuit is used.—E. Gottlieb and J. Giorgis, Tunnel Diodes—Using Them as Sinusoidal Generators, *Electronics*, 36:24, p 36-42.



REVERSE THERMISTOR AND R₁ FOR COOLING LOAD

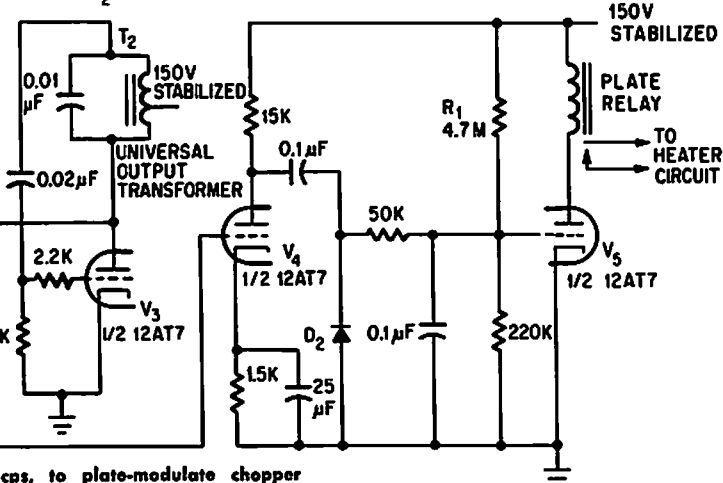
BATH TEMPERATURE CONTROL—Used to maintain temperature of photographic developer solution constant. When temperature drops, thermistor resistance increases and scr is turned on earlier in each cycle by ujt.—J. Embinder, SCRs in the Consumer Market, *EEE*, 14:8, p 100-103.

COARSE, FINE TEMPERATURE CONTROLS



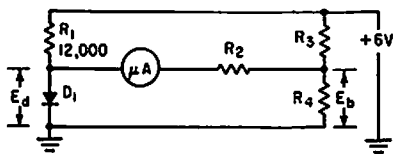
TEMPERATURE CONTROL—Fast thermal response is obtained with high-resistance thermistor in bridge circuit, feeding chopper V1-V2. V3 is Hartley oscillator operating at

D₁ LOW FORWARD RESISTANCE D₂ HIGH REVERSE RESISTANCE

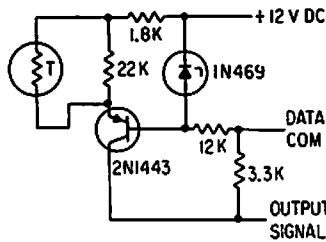


about 400 cps, to plate-modulate chopper tubes. When bridge is unbalanced by thermistor, pulses in secondary of T1 act through amplifiers V4 and V5 to operate relay.—

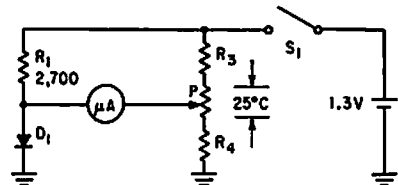
G. A. R. Trollope, Thermistor Regulator Provides Fast Response, *Electronics*, 39:5, p 106-107.



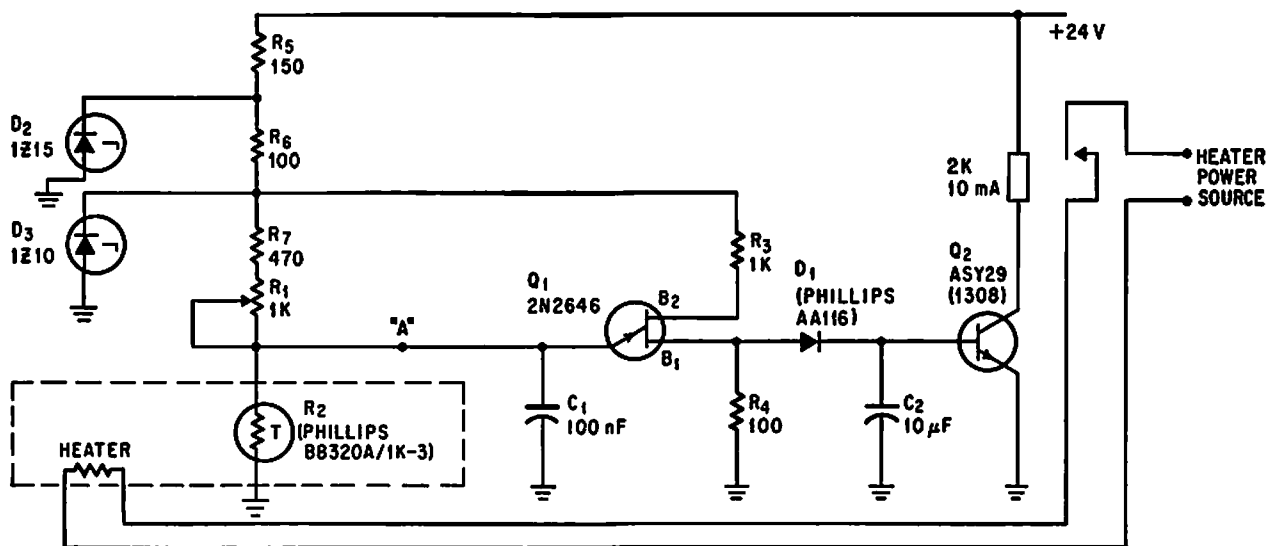
LIMITED-RANGE DIODE THERMOMETER—Values of R_3 and R_4 determine portion of temperature spectrum to be measured, while R_2 determines full-scale temperature value of meter, which may be as low as 25°C . Meter depends on fact that voltage drop across germanium diode is linear function of temperature.—L. E. Barton, *Measuring Temperature with Diodes and Transistors*, *Electronics*, 35:18, p 38-40.



TEMPERATURE MONITOR—Senses variations in ambient temperature near telemetry detector in space probe. Zener diode maintains constant voltage on transistor base.—S. Chase, Jr. and F. Schwarz, *Mariner II Instrumentation: What Will It See on Venus?*, *Electronics*, 35:50, p 42-45.



NULL-INDICATING DIODE THERMOMETER—Microammeter serves as null indicator. When potentiometer is adjusted for zero current, arm of potentiometer indicates temperature value directly. Values of R_3 and R_4 are chosen to place 25°C range anywhere from near absolute zero to about 40°C .—L. E. Barton, *Measuring Temperature with Diodes and Transistors*, *Electronics*, 35:18, p 38-40.



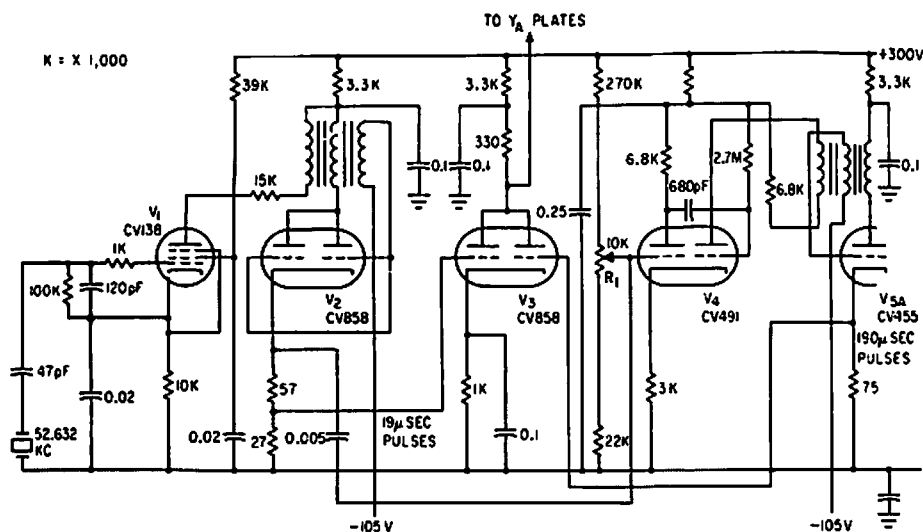
LIQUID BATH THERMOSTAT FOR 0.01° CONTROL—Based on thermistor R_2 , which has linear temperature coefficient of -6% per

degree C from 15 to 35 degree C. R_2 is one element in relaxation oscillator also consisting of Q_1 , C_1 , R_1 , R_3 , and R_4 .—K. van der

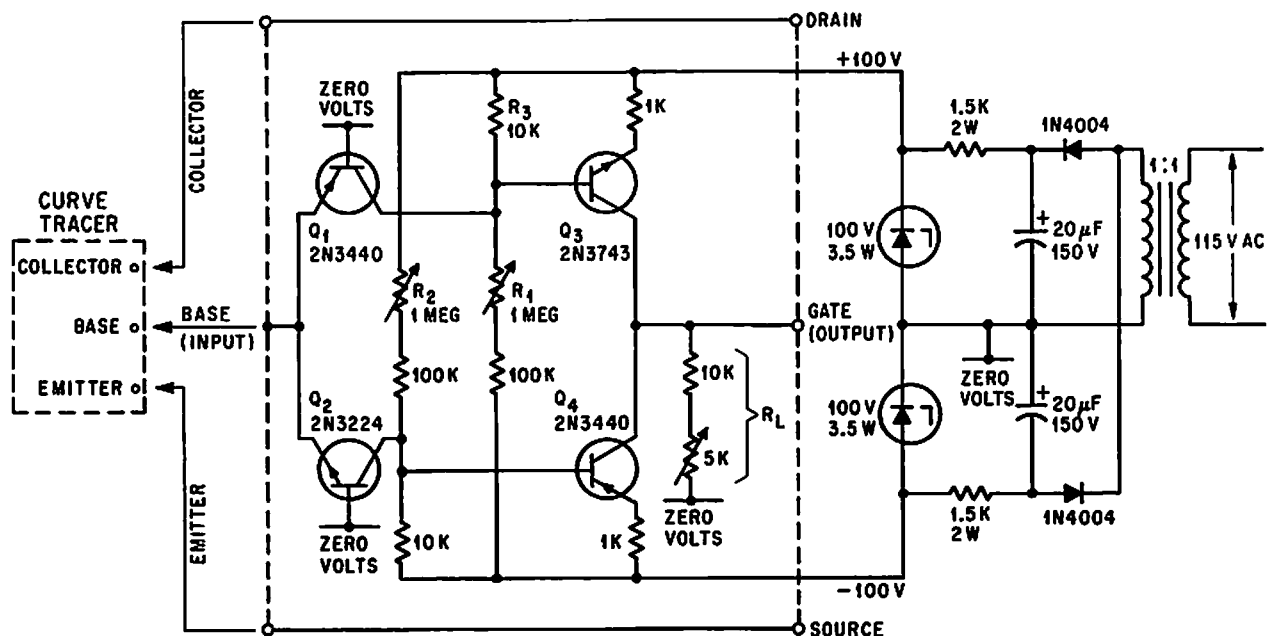
Geer, *Control is Accurate to 0.01°C* , *Electronics*, 39:12, p 111.

CHAPTER 91

Test Circuits



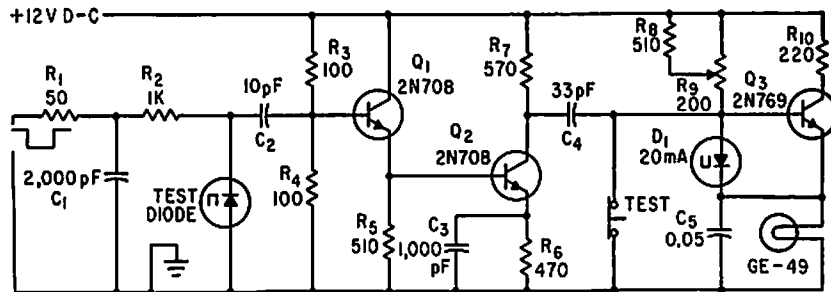
MARKER PULSE GENERATOR—Crystal oscillator V1 triggers blocking oscillator V2 to produce sharp pulses at 19-microsec intervals. These feed mono V4, whose output triggers blocking oscillator V5 to give larger pulse every 190 microsec for dual-beam scope of pulse-echo cable fault finder.—F. Jones and J. H. Reyner, Compact New Instrument Finds Undersea Cable Faults, *Electronics*, 35:37, p 48-50.



FET ADAPTER FOR CURVE TRACER—Used to convert input current steps from Tektronix

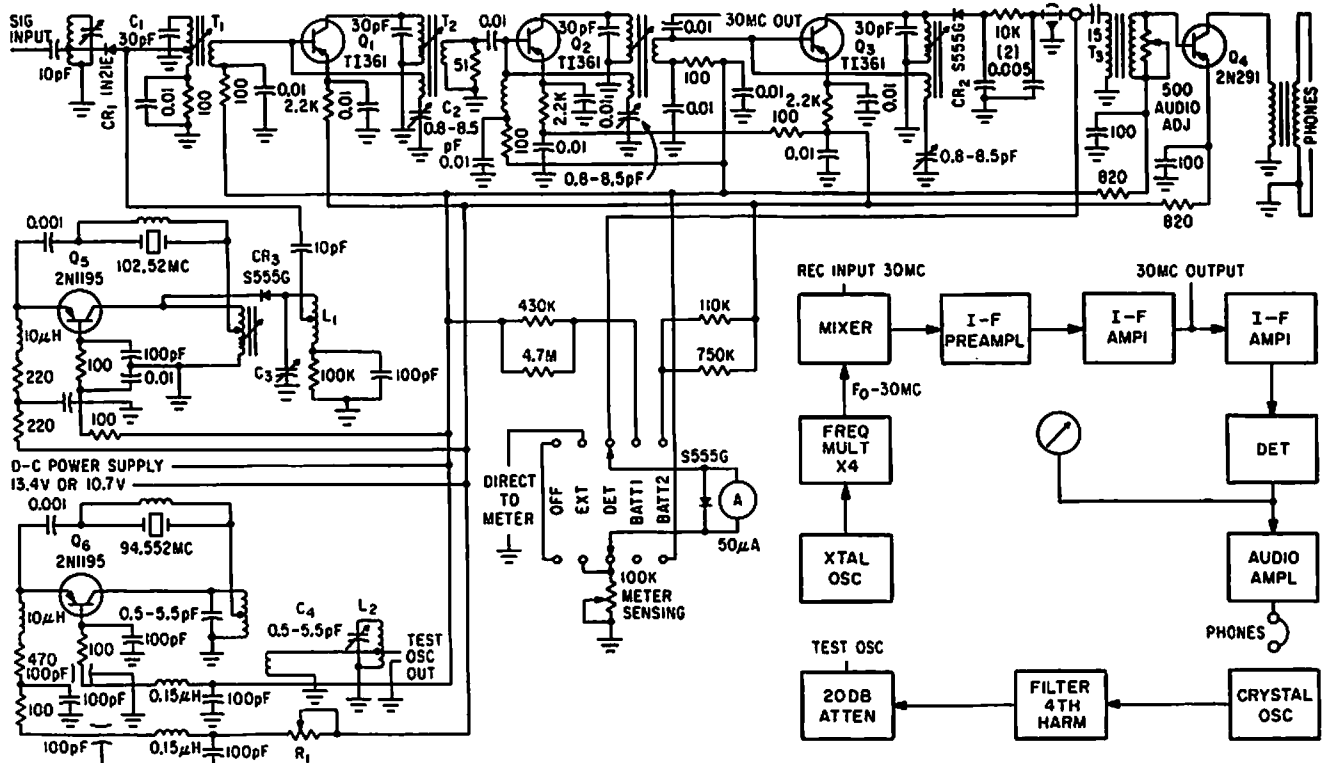
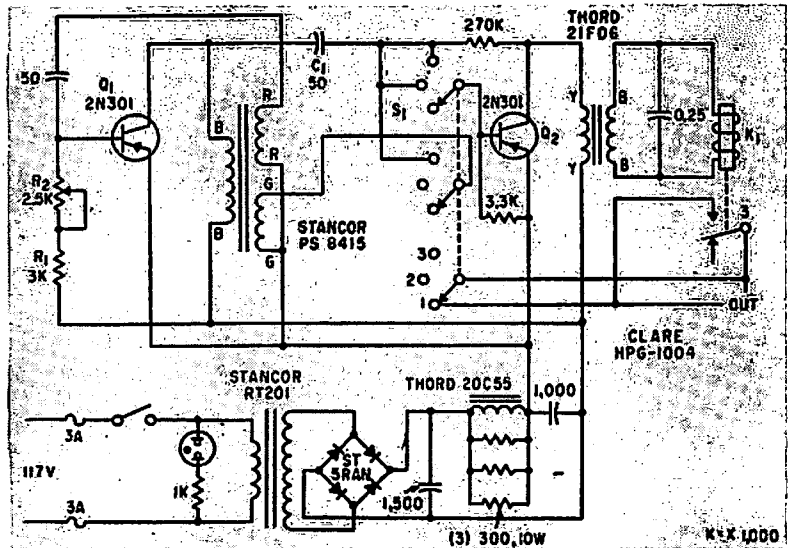
575 or other curve tracer to output voltage steps for fet gate.—R. Williams, Adaptor for

Curve Tracer Tests FET's at High Voltage, *Electronics*, 39:5, p 104-105.



TUNNEL-DIODE SWITCHING-TIME TESTER—With values shown, will light only if tunnel diode under test switches within 0.5-nsec.—J. E. Gersbach and I. Lieber, *Switching-Time Tester for Tunnel Diodes*, *Electronics*, 35:16, p 48-49.

TUNNEL-DIODE TEST ATTACHMENT FOR CURVE TRACER—Adapter switches sweep voltages of curve tracer on and off at reduced duty cycle to prevent overheating of tunnel diode while determining its series resistance. Increasing R1 gives lower duty cycle, because R1-R2 control frequency of inductively coupled series-resonance feedback oscillator Q1.—L. M. Zappulla, *Low Duty Cycle Tunnel-Diode Tester*, *Electronics*, 35:4, p 47.

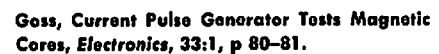


PARAMP TEST SET—Supplies c-w signal that can be injected into parametric amplifier under test, and indicates relative power out-

put of paramp on meter. Test set also has 30-Mc output for feeding automatic noise-figure meter.—C. F. Brett, *Parametric Am-*

plifier for Space Probe Tracking, *Electronics*, 34:4, p 41-45.

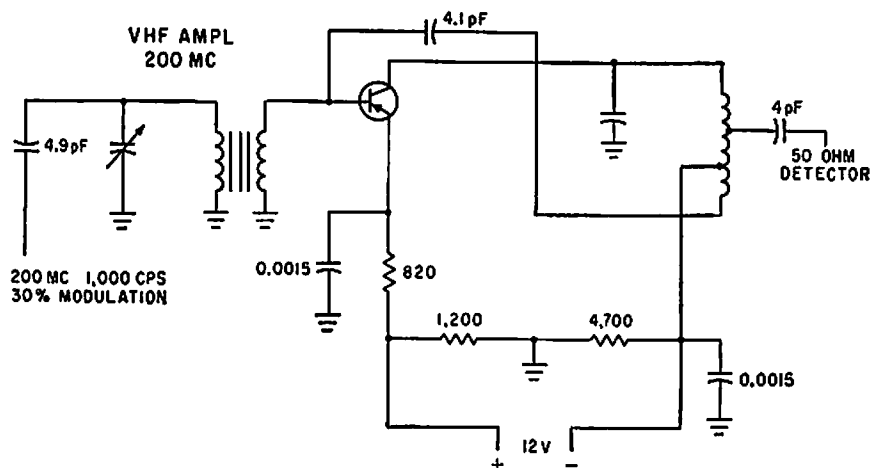
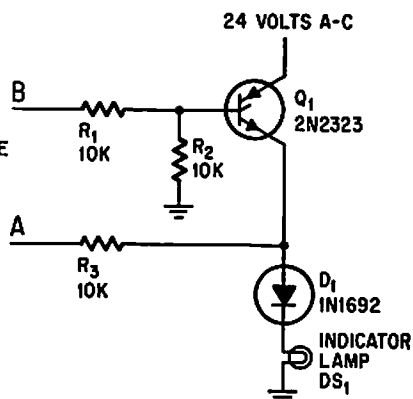
The schematic diagram illustrates a diode test circuit. It features a +250V supply connected to a network of resistors (510K, 2M, 100K, 10M, 75K, 390K, 0.1) and a 5691 tube. A ZENER CURRENT SUPPLY is connected to a ZENER diode, which is in turn connected to a TEST START switch. The DIODE UNDER TEST is connected to the circuit. A CLAMP SELECT switch is also present. The output of the circuit is connected to a MAG AMPLifier and a TO REJECT CIRCUIT. A list of voltages (145V, 6.2V, 80V, 4.7V, 8.4V) is shown on the right side of the diagram.



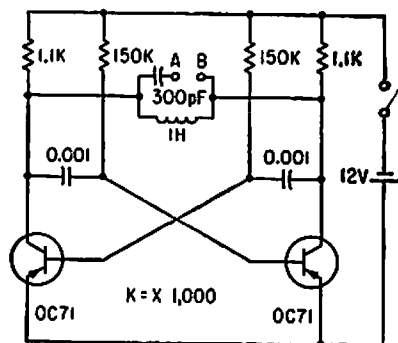
NEGATIVE-LOGIC LAMP DRIVER—Uses one scr and only five components per lamp, to switch lamps rapidly, without relays or excessive loading of control source. Positive test signal at B checks circuit and lamp. Control signal (−8 v) forward-biases gate electrode and scr fires during positive a-c half-cycles.—A. E. Popodi, *Reliable Repertoire Of Display Circuits, Electronics*, 38:2, p 60-66.

TEST INPUT
+8V CIRCUIT TEST
−8V CIRCUIT OPERATE

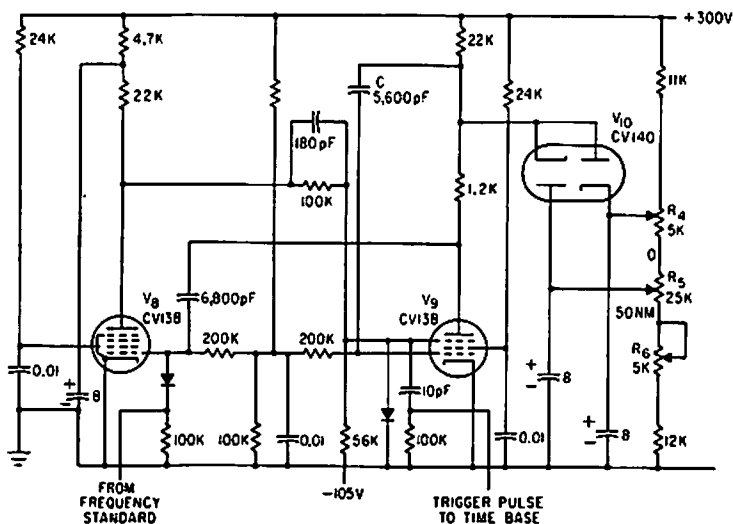
CONTROL INPUT
0V LAMP OFF
−8V LAMP ON



VHF TRANSISTOR AMPLIFIER TESTER—Has fixed match, neutralization, and bias for use as standard test circuit for transistors in tv or vhf r-f amplifier stage. With 2N1742 transistor, power gain is 19 db, bandwidth up to 16 Mc, and maximum noise 5.5 db.—G. J. Flynn, *Engineering Trends in Consumer Electronics, Electronics*, 34:1, p 115-117.

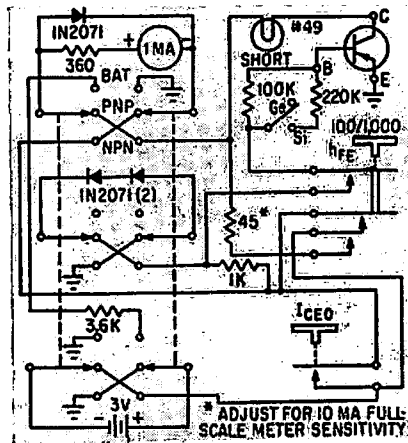


20-KC WIRE-TRACING MVBR—Used as signal source for identifying particular wire at midpoint in cable, for splicing. Ends of wire are connected between A and B, to become part of parallel-tuned circuit of astable mvbr. Tiny probe coil with amplifier is then used to locate wire carrying 20-kc signal.—J. S. Rushton, *Probe Identifies Cable Wiring, Electronics*, 34:9, p 51.

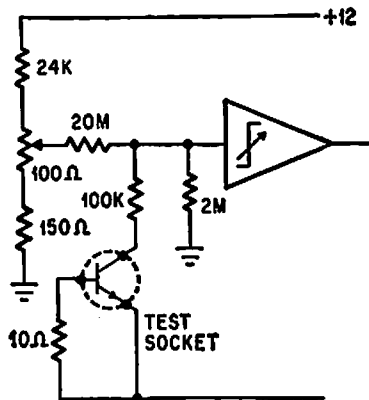


TIME-BASE GENERATOR—Variable time delay V8-V9 permits selecting portions of cro display in pulse-echo cable fault finder. Adjustment range is 190 to 1,140 microsec, or

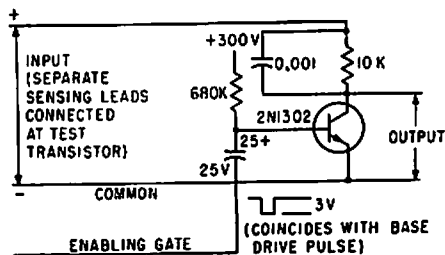
10 to 60 nautical miles.—F. Jones and J. H. Royner, *Compact New Instrument Finds Undersea Cable Faults, Electronics*, 35:37, p 48-50.



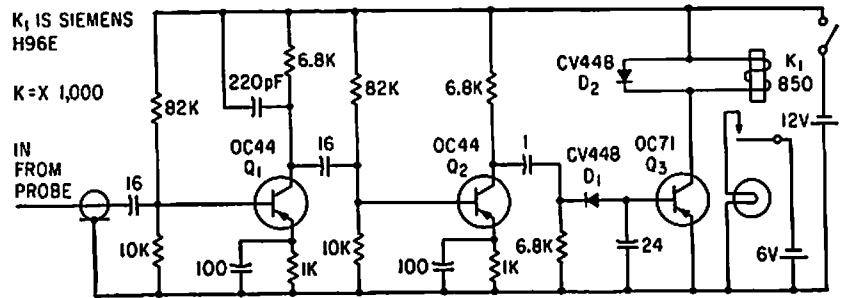
TRANSISTOR TESTER—Leakage current and common-emitter current amplification are checked by using forward voltage drop across two silicon rectifiers in series as reference voltage. Base current of transistor under test is held constant by switching series base resistance.—G. F. Montgomery, Building a Simple Transistor Tester, *Electronics*, 36:16, p 56.



LEAKAGE TESTER—Operational trigger trips when transistor leakage is above 5 ma. Response time is 40 millsec.—P. Lefferts, Operational Trigger For Precise Control, *Electronics*, 37:28, p 50-55.

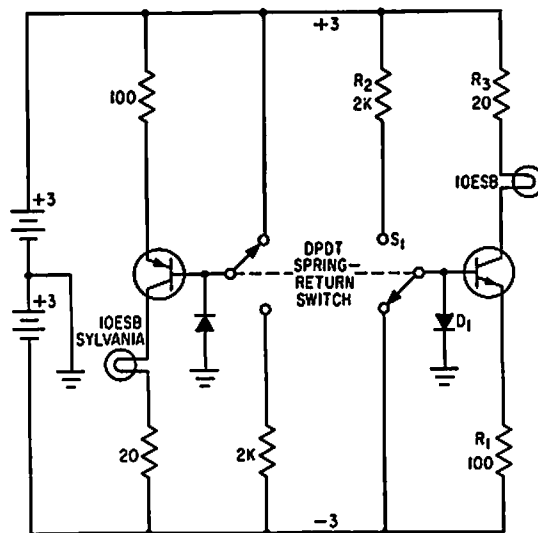


POWER TRANSISTOR TEST GATE—Rectangular pulse opens saturated amplifier; gate output is then a series of pulses whose amplitude is equal to voltage across power transistor under test when full load current is drawn.—D. H. Braslow, Measuring Parameters by Power Transistor Pulse Techniques, *Electronics*, 34:1, p 120-122.



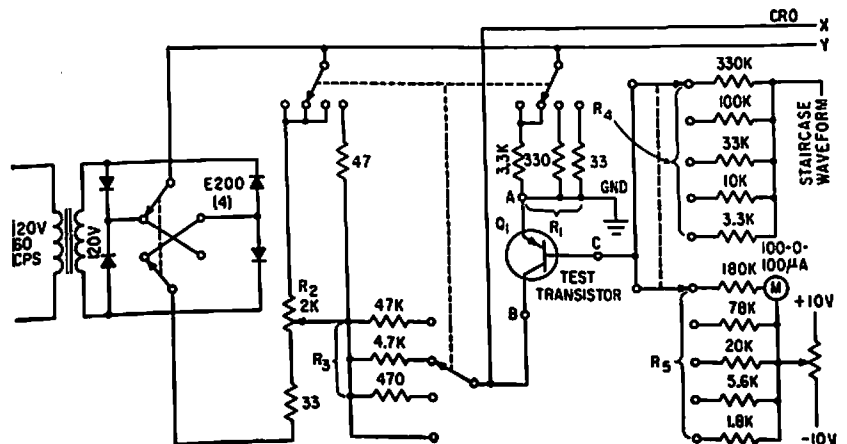
20-KC WIRE-TRACING PROBE—Used to identify wire in middle of long cable, carrying 20-kc mvbr signal. Pickup probe for amplifier has 600 turns wound on U-shaped trans-

former steel. Relay closes and energizes lamp when probe is held near correct wire.—J. S. Rushton, Probe Identifies Cable Wiring, *Electronics*, 34:9, p 51.



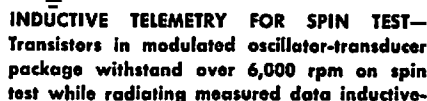
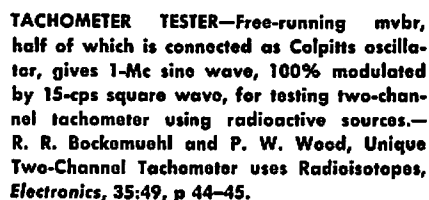
LOW-COST TRANSISTOR TESTER—Indicates, in one simple operation, whether transistor has had catastrophic failure and, if not, whether it can provide minimum data (gain) of 20 at 30 ma. Test circuit is inverter

with emitter degeneration resistor R1 providing control of collector current during warm-up of indicator lamp.—E. H. Sommerfeld, Simple Transistor Tester Uses Lamp for Indicator, *Electronics*, 34:36, p 80.

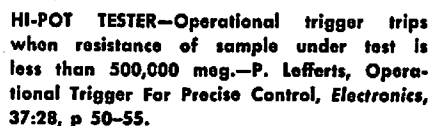


TRANSISTOR CURVE TRACER—Staircase waveform generator supplies test transistor with six values of base current during each cycle, to develop family of curves for cro. Range switches give wide choice of test voltages

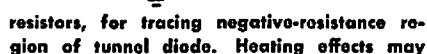
and currents. Four-layer and tunnel diodes can also be checked.—C. J. Candy, Simplified Curve Tracer for Transistors and Diodes, *Electronics*, 33:34, p 68-70.



ly from oscillator coils L1-L2 to stationary coil of readout equipment.—H. Baumann, Inductive Telemetry Improves Spin-System Measurements, *Electronics*, 36:46, p 41-42.

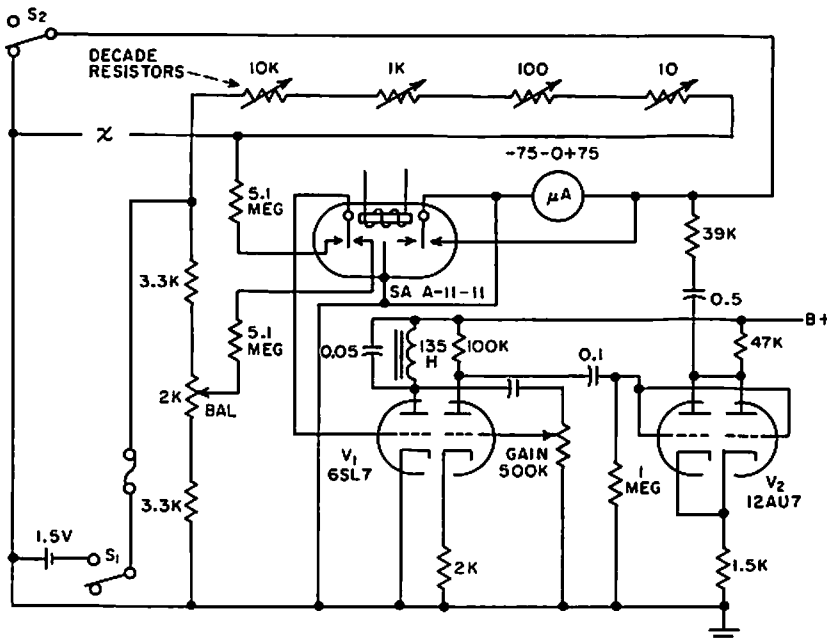
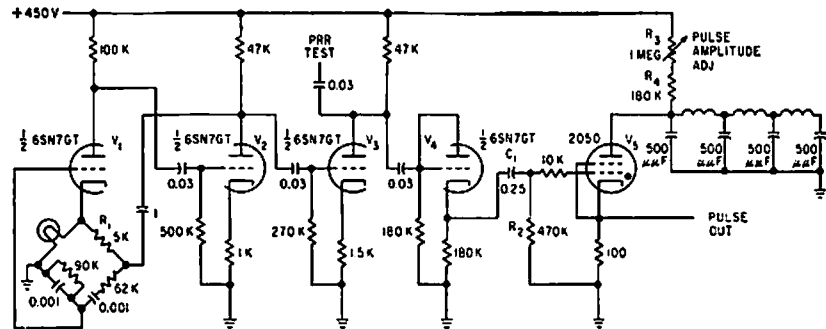


tors in tv and vhf receivers.—G. J. Flynn, Engineering Trends in Consumer Electronics, *Electronics*, 34:1, p 115-117.

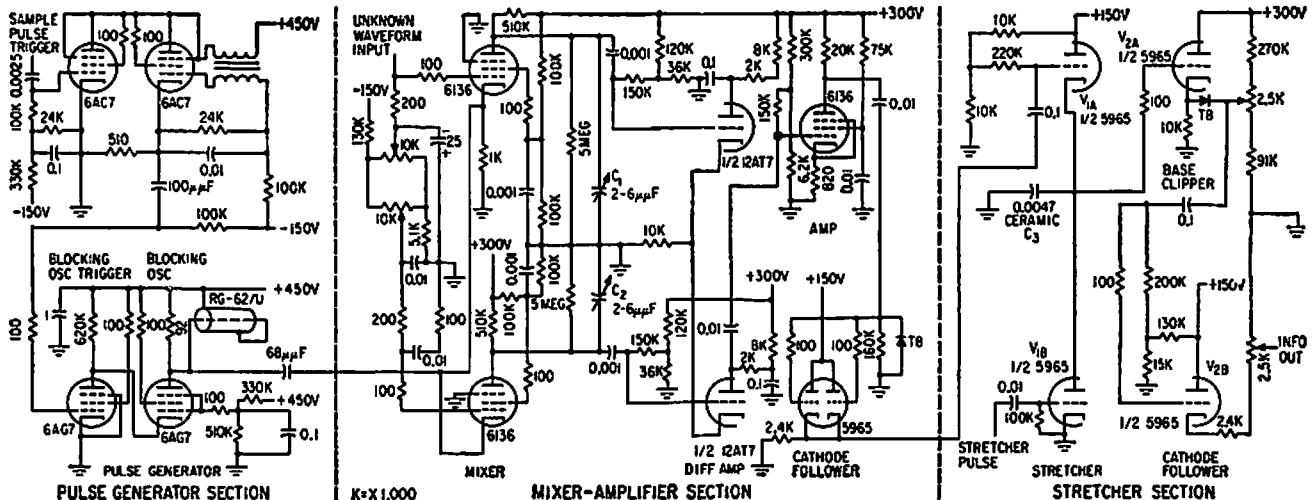


cause double trace.—H. G. Dill and M. R. MacPherson, Tracing Tunnel Diode Curves, *Electronics*, 33:32, p 62-64.

TUBE LIFE-TEST PULSER—Pulse generator produces 0.25-microsec pulses at 50 v for life-testing of 1 to 15 microwave triodes at a time. Amplitude and repetition rate are adjustable within limits.—R. S. Ringland, *Pulse Modulator Works into Variable Load*, *Electronics*, 31:37, p 102-103.



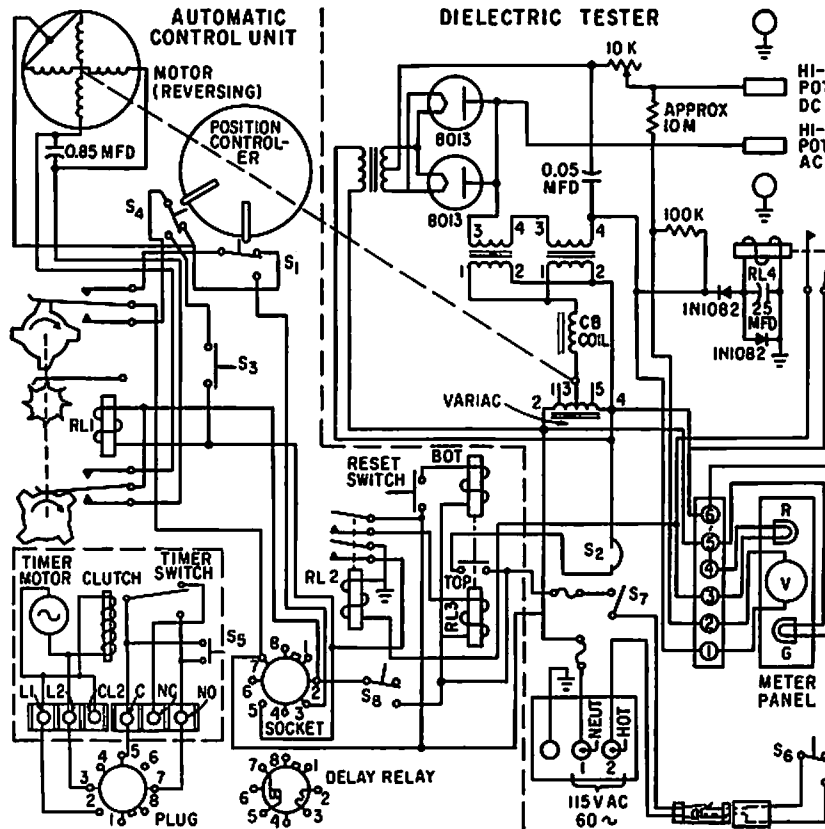
RESISTANCE CHECKER—Amplified error voltage from Wheatstone bridge feeds 75-0-75 microammeter to indicate whether resistance under test is higher or lower than desired value and within preset tolerance. Instrument range is 9,999 ohms in 1-ohm steps. Gives go-no-go indication, to speed production testing.—D. S. Randall, *Go No-Go Motor Speeds Resistance Check*, *Electronics*, 31:9, p 66-68.



WAVEFORM TESTER—Used in high-speed testing of ferrite cores, transistors, transformers, and other components requiring waveform

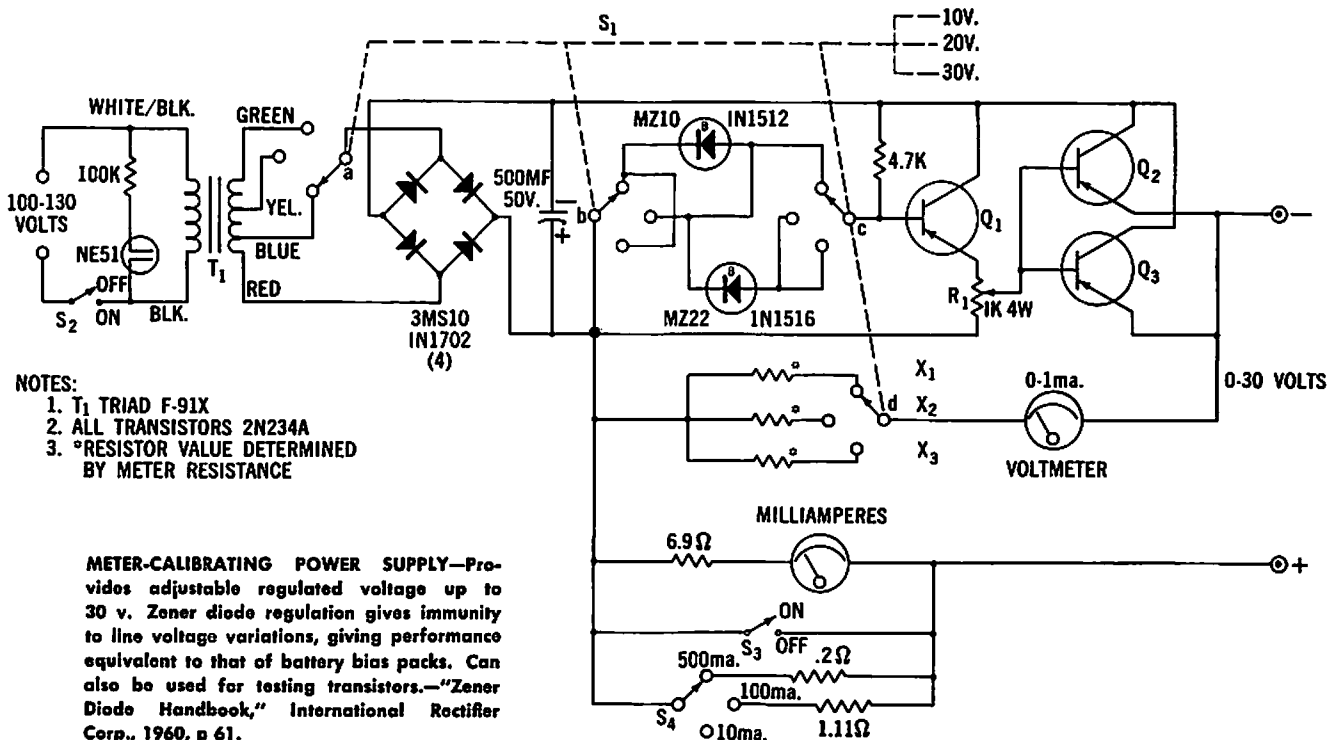
measurement. Based on sampling of unknown waveform at discrete intervals and comparing resulting digital output with that

provided by perfect component.—B. Agusta, *Sorting Components by Measuring Waveforms*, *Electronics*, 32:7, p 56-59.

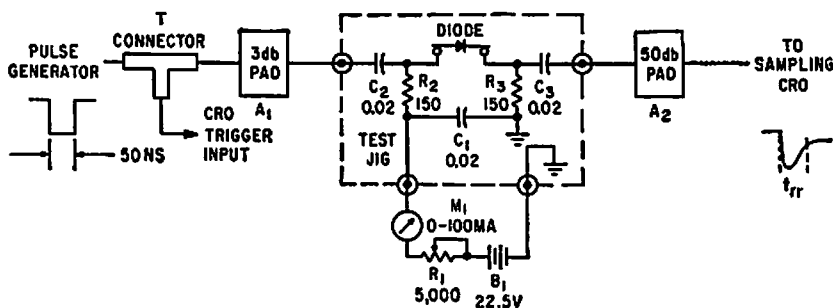
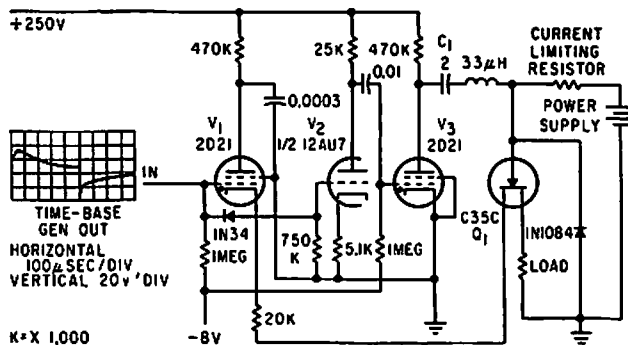


DIELECTRIC-STRENGTH TESTER—Automatic sequencing of test functions minimizes high-voltage danger to operator and improves accuracy of readings. Control system may

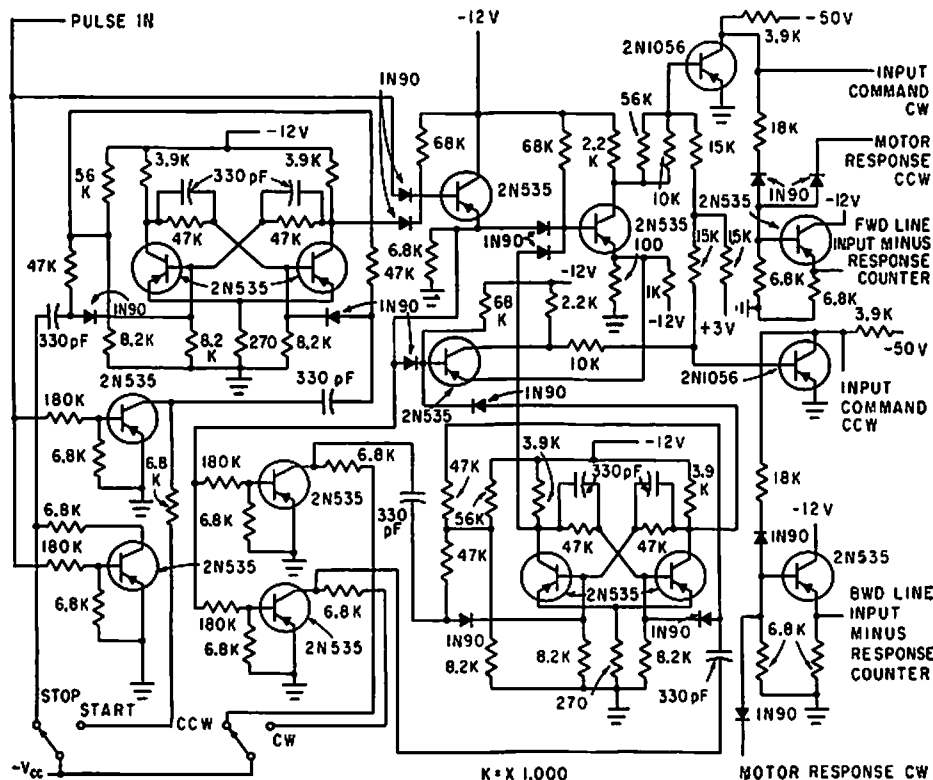
be inserted in any commercial high-pot tester. —F. J. Clounio, P. M. Degroat, and E. M. Szymanski, *Control Makes Test Safe, Accurate*, *Electronics*, 33:19, p 88-91.



ELECTRO-EXPLOSIVE DEVICE TESTER—Uses combination of tubes and solid-state thyatron to generate single pulse up to 100 amp with duration of several millisecond, for testing detonators, primers, squibs, and explosive switches.—V. W. Goldie, R. G. Amicone, and C. T. Davey, *Generating Pulses With Solid-State Thyatrons*, *Electronics*, 32:33, p 70.



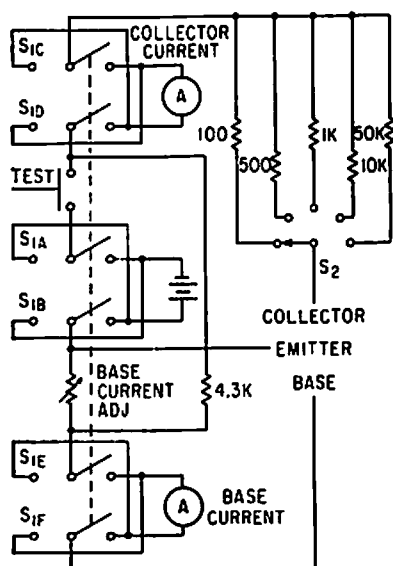
SWITCHING DIODE TESTER—Used in checking performance of computer diodes when handling steep-edged, short-duration pulses. Negative input pulse cuts off diode current, and sampling oscilloscope with 1,000-Mc bandwidth permits study of diode recovery times down to 500 picosec.—W. S. Eckess and P. G. Ducker, Measurement of Diode Switching Characteristics, *Electronics*, 33:15, p 59-61.



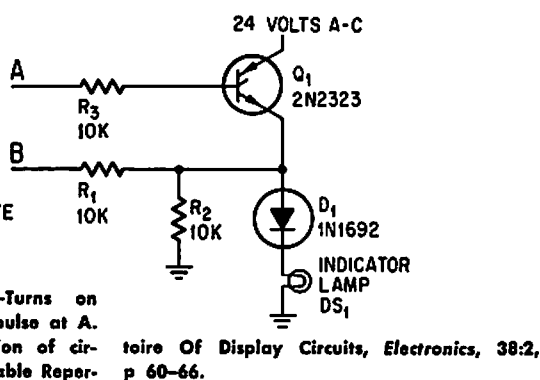
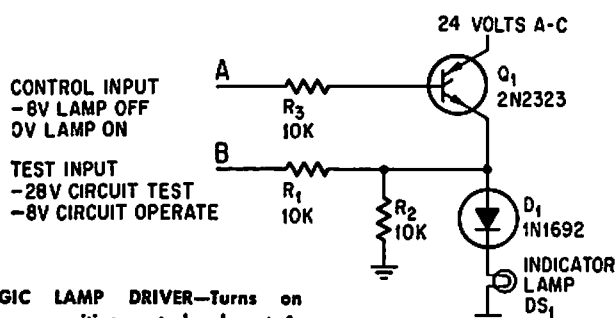
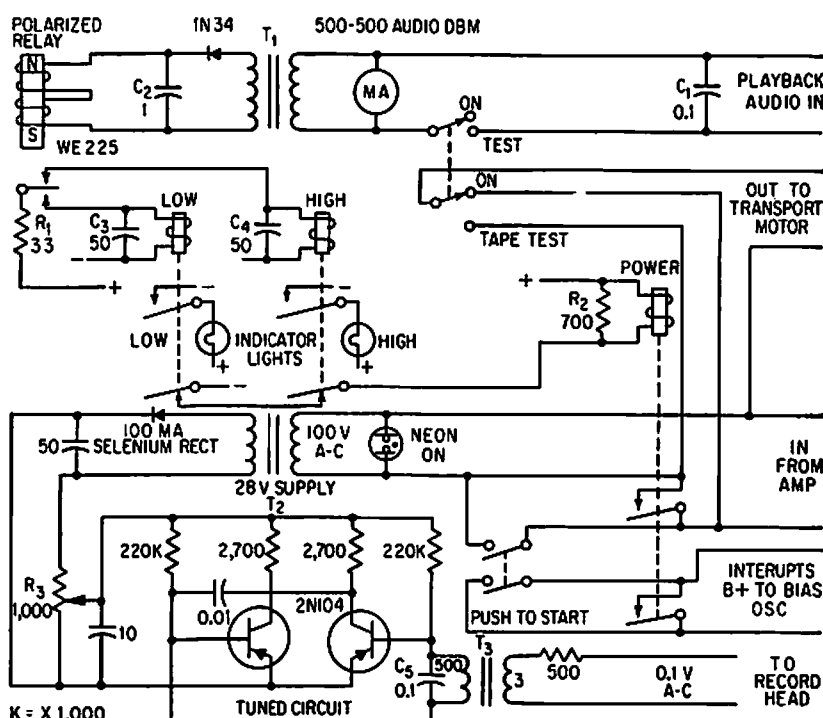
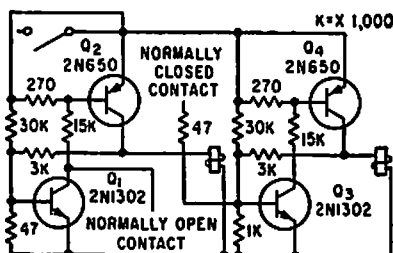
STEPPER MOTOR TESTER—Digital test equipment automatically evaluates performance of magnetically detented stepper motors in several modes, for wide variety of test conditions. Analyzer compares number of ap-

plied voltage steps with number of motor movements. Pulse train from power amplifier is gated through logic circuits that prevent switching from occurring in middle of pulse, and keep input pulse line closed even

when switching motor direction.—H. J. Weber and M. Weiss, Analyzing Magnetically-De-tented Stepper Servo Motors, *Electronics*, 33:39, p 71-74.



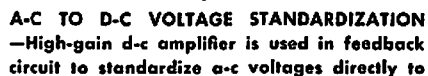
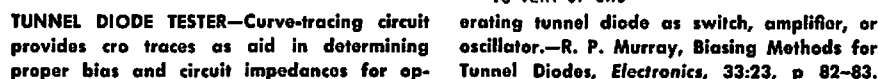
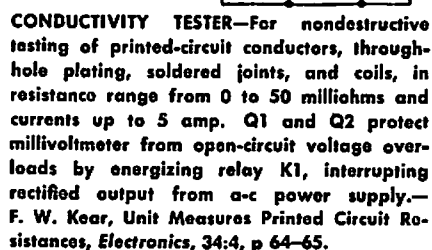
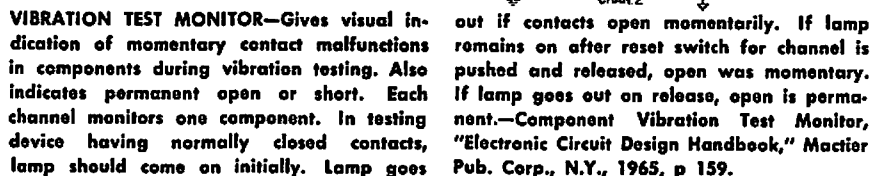
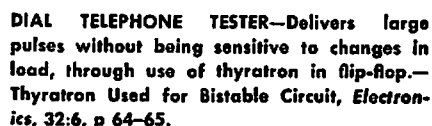
TRANSISTOR GAIN AND LEAKAGE TESTER—Designed for general testing of production units. Switch S1 changes over from npn to pnp transistors. After controls are set for a specific transistor type, checking involves only noting base current when test button is pressed.—F. W. Kear, Simple Test for Transistor Quality, *Electronics*, 35:39, p 80-81.



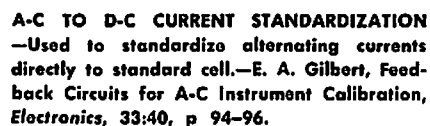
100-MC NOISE FIGURE TEST SET—Used in measuring upper noise-corner frequency of

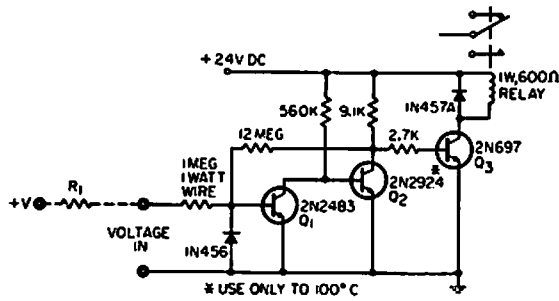
transistors, by measuring small-signal short-circuit forward current transfer ratio (common

emitter) h-fe or f-T.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 305.

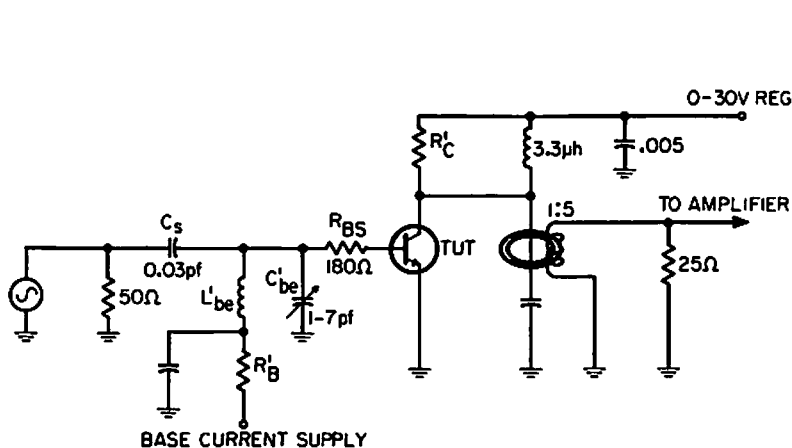


standard coll.—E. A. Gilbert, Feedback Circuits for A-C Instrument Calibration, *Electronics*, 33:40, p 94-96.



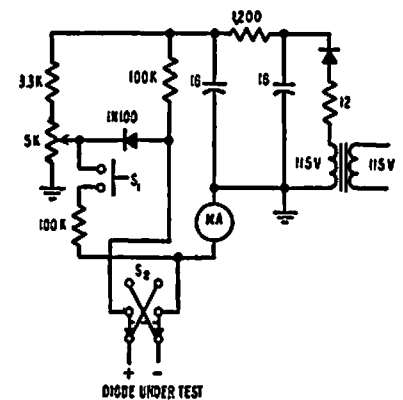


LEAKAGE TESTER—High-reliability current-detecting Schmitt trigger responds to nanoampere inputs for leakage testing of capacitors, diodes, and insulation, yet is not damaged or even affected by overloads of 1,000 v at input. Input of 300 na will trigger output relay.—P. Loefferts, Schmitt Triggers on Nano-amp Inputs, *EEE*, 14:6, p 91.

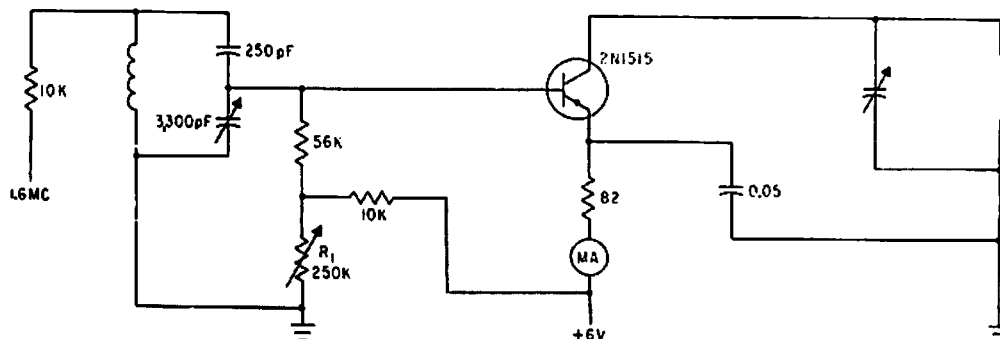


BASE CURRENT SUPPLY

TRANSISTOR MEASUREMENTS AT 100 MC—Used to measure h-fe at 100 Mc, where all circuit parameters become more significant and make accurate measurements difficult. Circuit has provisions for separating measured signal from noise, high-gain pre-meter amplifier, and accurate method of metering r-f level. Maximum error is under 5%.—W. H. Hamlin, How to Measure h-fe at 100 mHz, *EEE*, 13:6, p 70-72.



DIODE TESTER—Measures germanium diode reverse (leakage) current rapidly at 50 v back voltage, with no shock hazard and no danger to meter or diode even if diode is shorted or inserted incorrectly. S1 converts meter from ammeter to voltmeter for measuring test terminal voltage. R2 adjusts test voltage to desired half-scale meter reading value. S2 permits checking diode in both directions.—I. J. Levy, Reverse-Current Tester Speeds Diode Checks, *Electronics*, 31:1, p 88-90.

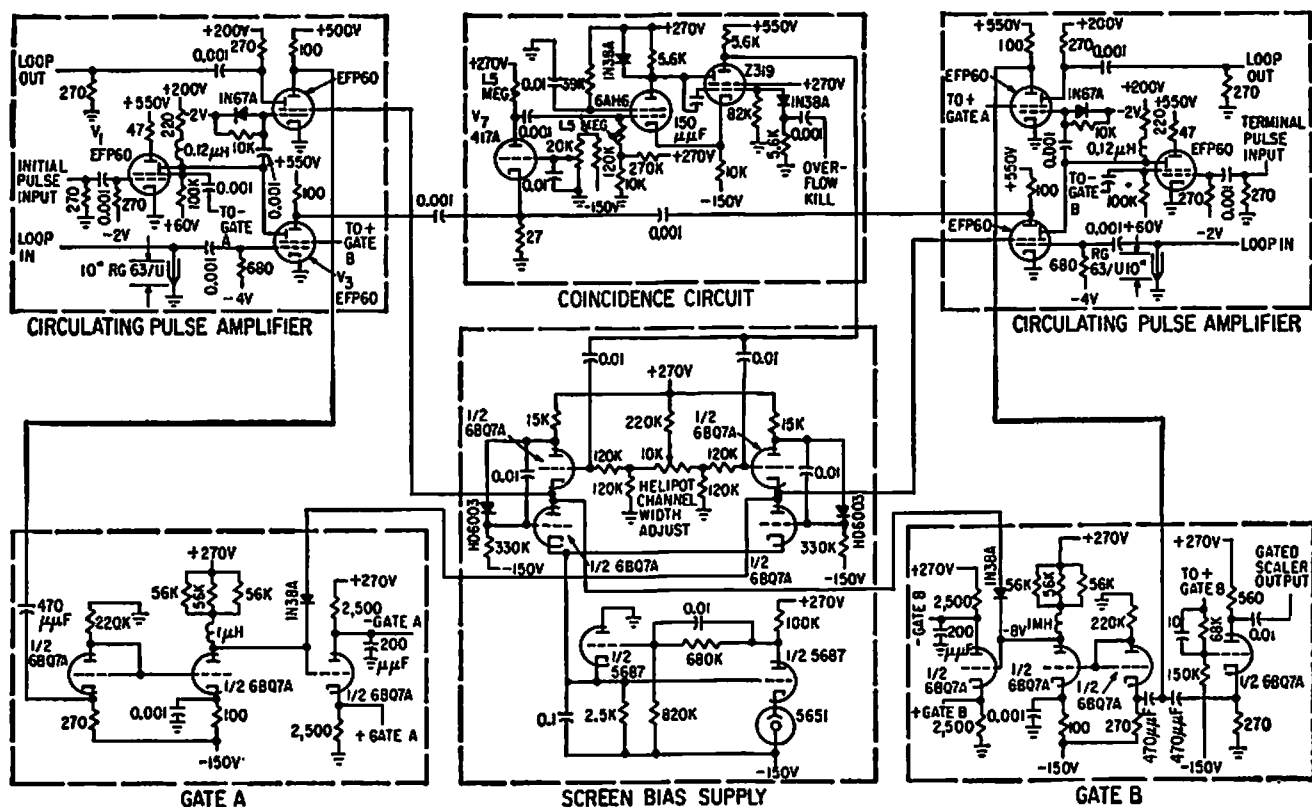
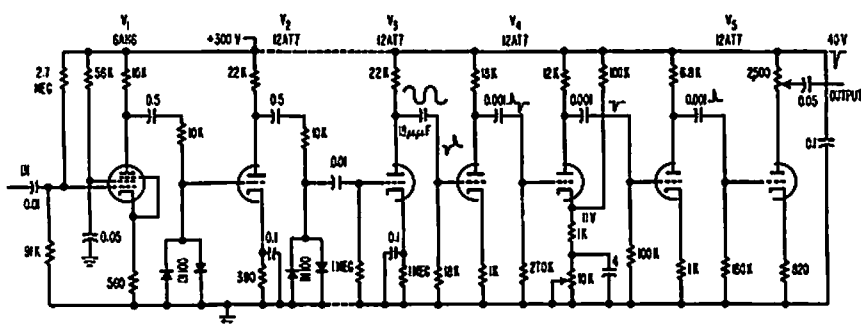


TRANSISTOR TEST CIRCUIT—Measures power gain, with emitter current varied manually by R1 in base circuit. Used to determine conditions for uniform emitter current, re-

quired for uniform gain in transistor circuits despite variations in d-c beta values of transistors.—K. Rodmond, Biasing Transistors for Uniform Gain, *Electronics*, 33:50, p 74-75.

Timer Circuits

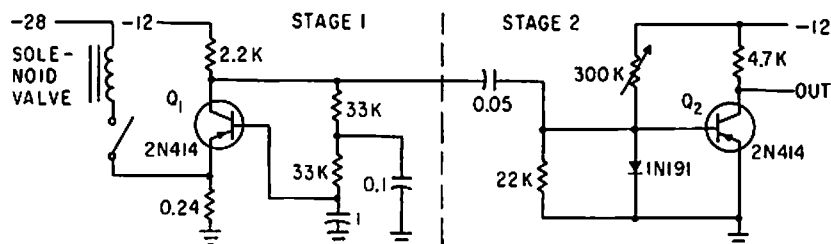
A-C ZERO LOCATOR—Locates zero of a-c voltage within 0.1 microsec for 50-kc input signal. Operation is independent of input signal amplitude between 0.15 and 30 v p-p. Used for accurate measurement of time interval between given number of cycles of exponentially decaying 50-kc signal.—L. Costrell, A-C Zero Locator, *Electronics*, 31:3, p 98-101.



TIME INTERVAL ANALYZER—Gives high resolution (better than 1 millimicrosec) for multi-channel measurements of short-life nuclear particles. Two pulses, defining time interval, are fed into the same loop-forming ends of

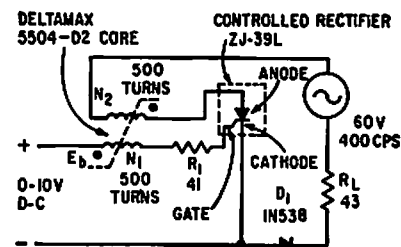
two transmission lines. By making coincidence circuit spacing slightly smaller on terminal pulse lines than on initial pulse lines, terminal pulse is made to overtake initial pulse. Pulses approach coincidence at 1 mil-

limicrosec per transit, so count of transits before coincidence gives time interval.—H. W. Lefevre and J. T. Russell, Vernier Chronotron Times Nuclear Particle Flight, *Electronics*, 32:10, p 44-47.

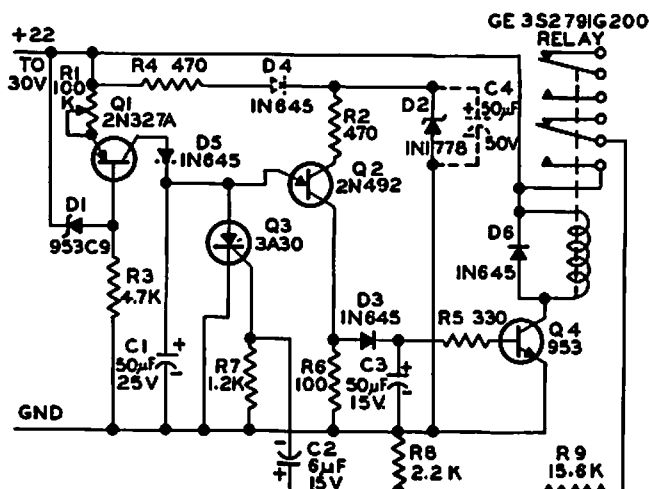


MEASURING VALVE-CLOSING TIME—Determines exact time of valve closure from wave-shape of current in solenoid. Energizing current is differentiated and shaped, to trigger circuits that measure interval between sole-

noid switch closing and final solenoid position.—R. L. Kissner, *Determining Closure Time in Missile Control Valves*, *Electronics*, 33:42, p 88-89.

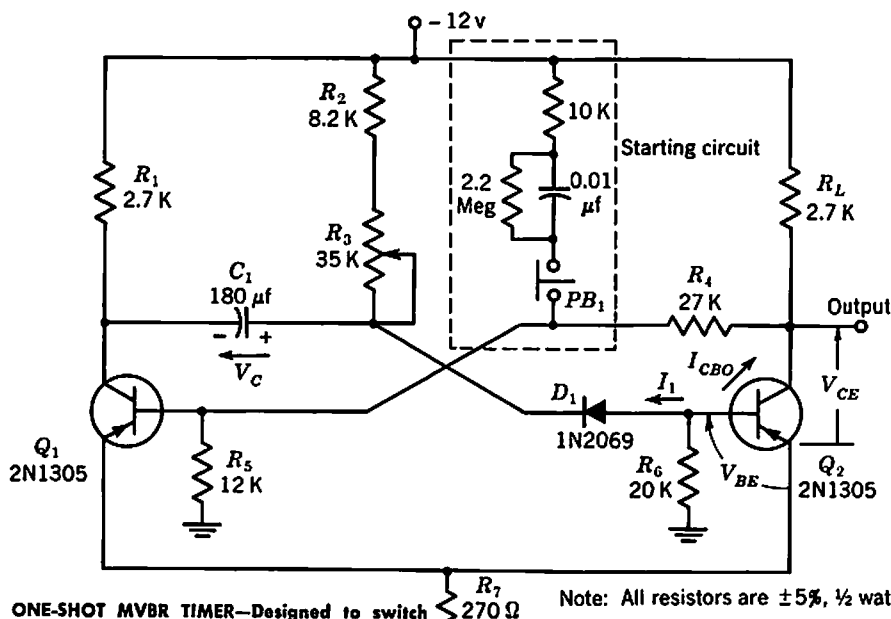
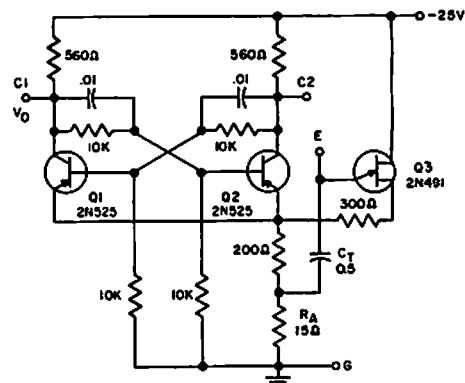


SATURABLE-REACTOR TIMER—Varying d-c bias voltage applied to control winding of saturable reactor changes time for magnetic flux to reach final value, permitting use of circuit as frequency divider for timing applications. In 400-cps circuit shown, division can be adjusted over range from 1 to 10. Output diode prevents thermal runaway in controlled rectifier when anode is going negative and gate is positive to cathode.—J. S. Sicks, *Counting and Timing Circuits Use Saturable Reactor*, *Electronics*, 33:19, p 61-63.



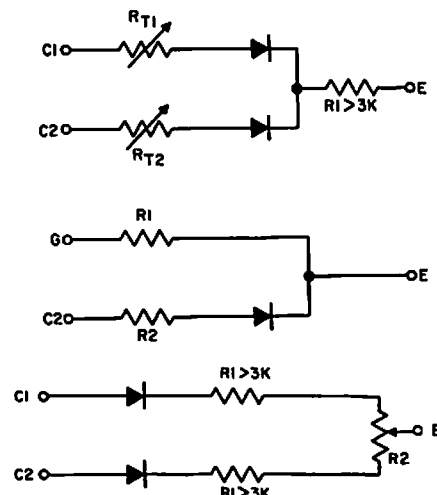
REPEAT-CYCLE TIMER—Provides output pulses over dynamic range of several thousand. Will tolerate large ripple from power source. If D4, D5, and C4 are added as shown in dotted lines, will tolerate transients up to

100% of supply voltage with several microsec duration.—Low-Frequency Stairstep Generator and Timing Circuit, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 144.

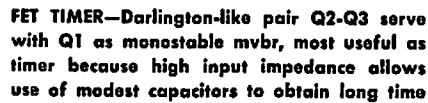
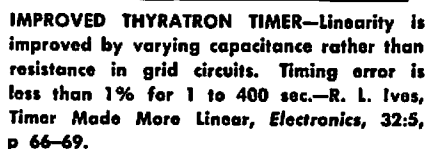
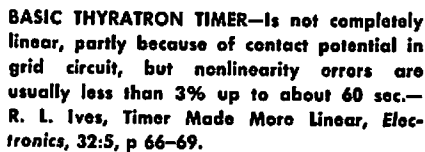
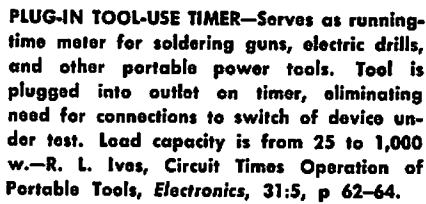


ONE-SHOT MVBR TIMER—Designed to switch 4-ma load having 12-v supply. Output is -12 v for period that can be adjusted from 1 to 5 sec. Timing is accurate within 10%

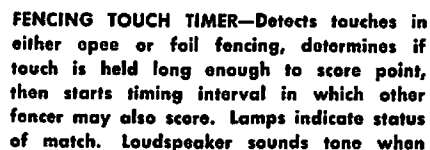
Note: All resistors are $\pm 5\%$, $\frac{1}{2}$ watt from -20 to $+60^\circ\text{C}$.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 413.



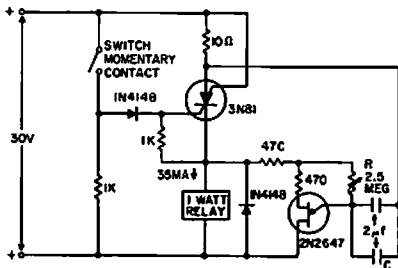
BASIC HYBRID UJT-PNP TIMER—Serves as symmetrical square-wave mvbr when fixed or variable resistor is connected between E and G. Serves as one-shot mvbr when fixed or variable resistor is connected between C1 and E. Other configurations shown for external connections give constant or variable-frequency nonsymmetrical multivibrators.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 338.



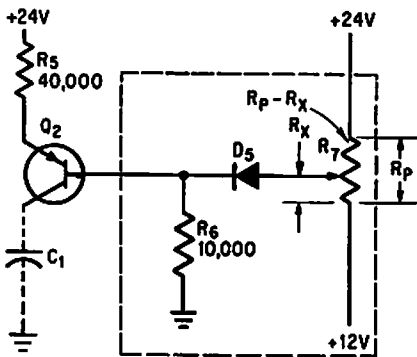
delays. In stable state, Q2 is on and Q3 is saturated, holding Q1 off.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 89.



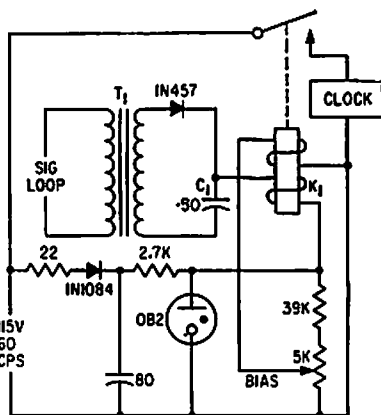
sequence of touches is correct, and switch S1 must then be reset for next scoring sequence.—W. R. Durrett, *Electronic Judging of Fast-Moving Sports Contests, Electronics*, 32:32, p 114-115.



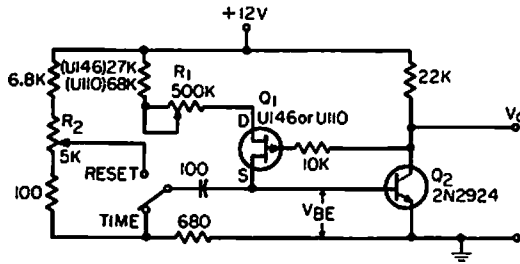
10-SEC SCS TIMER—Switch applies positive pulse to gate of scs, triggering it on and thereby supplying power to relay load and ujt timing circuit. At end of timing interval, determined by R-C, timer feeds negative pulse to anode to turn off scs.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 435.



CONSTANT-CURRENT GENERATOR—Insures that voltage across charging capacitor of unijunction-transistor timer increases linearly with time. Maximum charging current is about 0.3 ma with 2N2605 for Q2 and 1N643 for D5. R7 is 1,000-ohm potentiometer.—A. A. Lampell, Off-the-Shelf Integrated Circuits for Versatile and Accurate Timer, *Electronics*, 38:25, p 70-73.

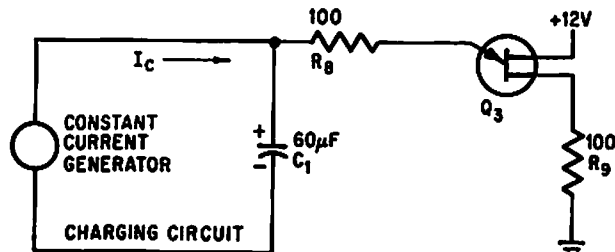


TELEPRINTER CHARACTER COUNTER—Simple circuit switches on timer clock only when teleprinter keying impulses are present, to give indication of traffic volume, flow, and routing.—R. E. Paffenberg, Character Counter Aids Teletypewriter Routing, *Electronics*, 34:17, p 120-121.



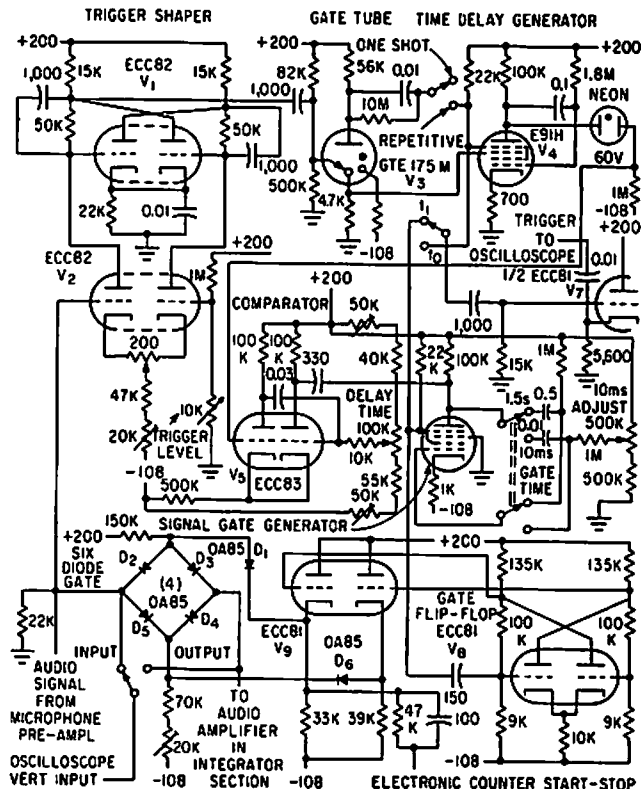
SOLID-STATE TIMER—Uses fet constant-current source to eliminate timing errors due to unregulated power supplies and line

transients. Range of 0.1 to 50 sec is controlled by R2.—J. Geokio, Simple Fet Timer, *EEE*, 14:3, p 62.



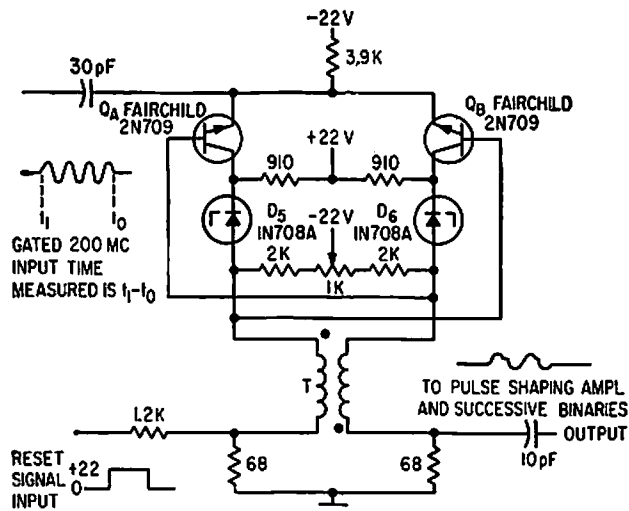
UJT TIMER—Charging current of C1 should be greater than 20-microamp peak-point current at which Q3 is triggered and less than 8-ma sustaining current of Q3, so ujt will

turn off after it is triggered. 2N2422A may be used for Q3.—A. A. Lampell, Off-the-Shelf Integrated Circuits for Versatile and Accurate Timer, *Electronics*, 38:25, p 70-73.



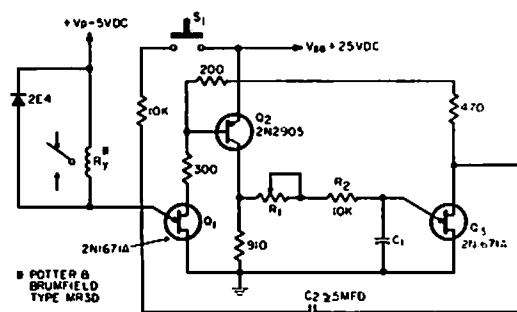
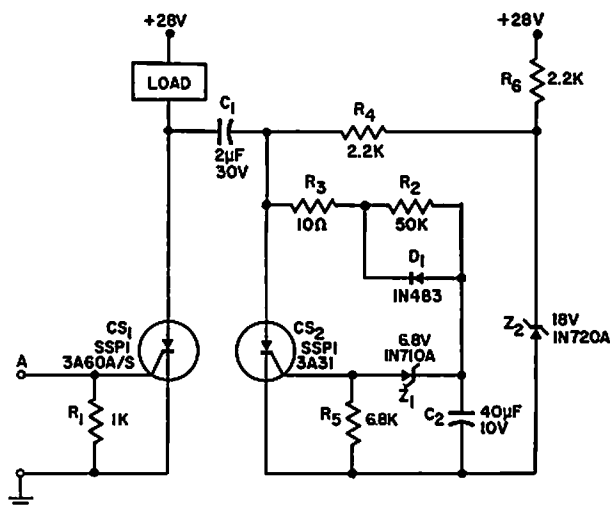
AUDITORIUM ACOUSTICS TIMER—Can be set to accept any portion of incoming sound signal for feeding to electrostatic squarer and digital counter. Microphone preamp feeds gate input and trigger shaper. Time base consists of phantastron sawtooth generator

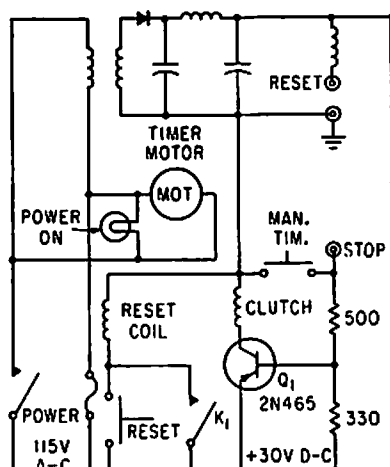
and comparator giving delay time linearly variable from 0 to 120 millisec.—J. P. A. Lechner and P. Meffert, Electrostatic Squarer for Acoustic Measurements, *Electronics*, 33:35, p 66-68.



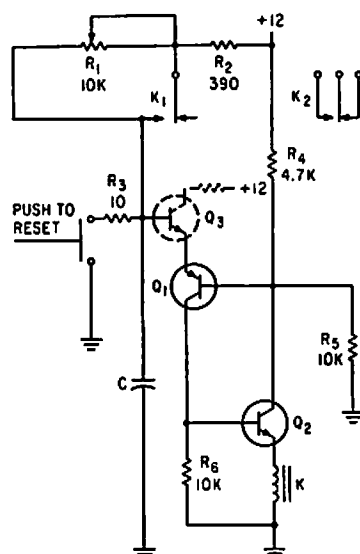
200-MC BINARY FOR INTERVAL TIMER—Gated 200-Mc input is fed to both emitters. Individual zener diodes provide collector bias. Base bias is obtained from collector resistor load of opposite transistor. Potentiometer permits perfect balancing. Time intervals are measured accurately to 5 nsec.—C. S. Coffey, VHF Counter Measures Time Intervals Precisely, *Electronics*, 36:34, p 27-29.

HIGH-CURRENT SCS INTERVAL TIMER—When triggered by low-level 5-microsec pulse, furnishes 1 amp to load for 1 sec. Advantages are simplicity and high reliability through use of silicon controlled switches.—Y. J. Lubkin, High Output Interval Timer, *EEE*, 10:9, p 92.

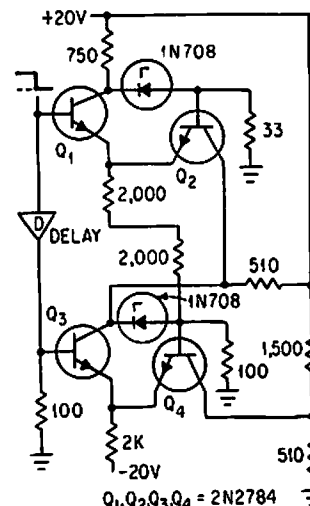




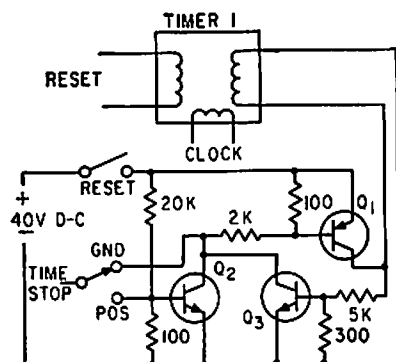
CLUTCH COIL FOR TIMER MOTOR—Start-and-stop signals are applied to stop jack, for automatic timing. Manual-timing pushbutton applies 0-v signal to stop jack, to make Q1 conduct. Fast switching time of transistor permits reading time to within 0.005 milli-sec.—F. W. Kear, *Electromechanical Timer for Lab Applications*, *Electronics*, 36:7, p 78-79.



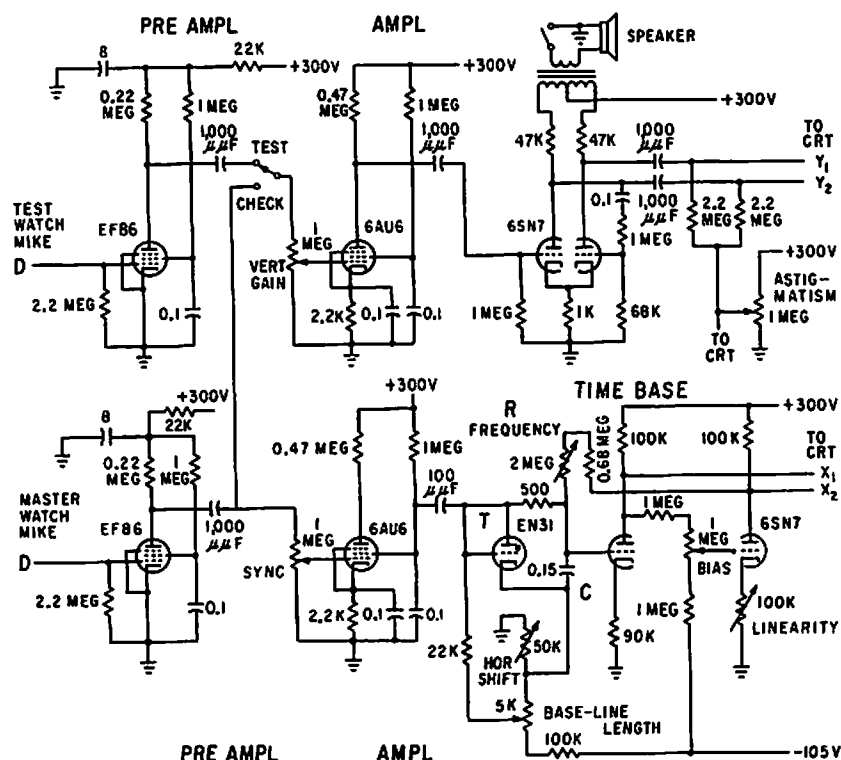
INTERVAL TIMER—Interval is determined by C, which can be paralleled capacitors to increase range.—N. C. Hekimian, PNP-NPN CIRCUITS: New Look at a Familiar Connection, *Electronics*, 35:47, p 42-46.



CURRENT-MODE SWITCH—With 1,000-Mc time base applied to base of Q2, two current-mode switches together serve to give accuracy of 0.5 nsec in measurement of time intervals with quinary scaler.—R. Englemann, *Quinary Scalers: Measure Time Intervals Digitally*, *Electronics*, 37:5, p 34-36.

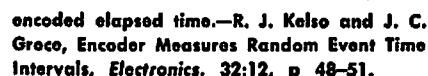
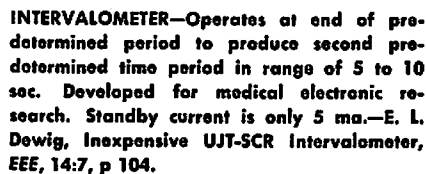
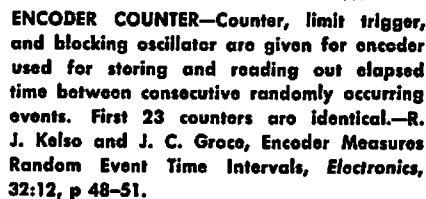
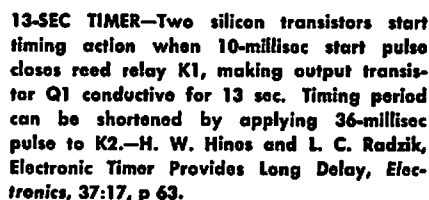


DUAL-POLARITY START-STOP CONTROL—Switches connect relay coils either to positive bus or to ground, so timer can be controlled by either positive or negative pulses. Additional transistor, used in place of relay, provides stop switching. Q3 forms clamping circuit for use where stop pulse duration is too short.—F. W. Kear, *Tests Show Control is Key to Timer Accuracy*, *Electronics*, 33:27, p 62.



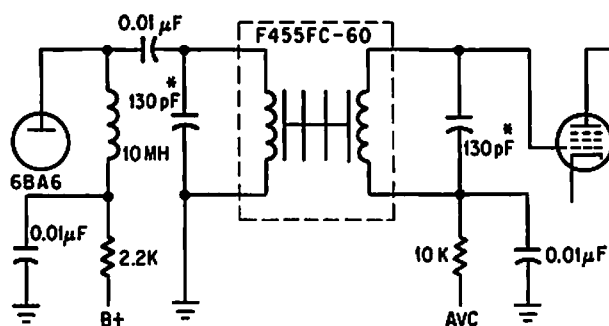
WATCH TIMER—Simple time base, with high linearity, is achieved by two-stage d-c amplifier having unity gain, back-coupled to R-C integrator. Time-base reference, synchronized

with master clock, can check accuracy of any timing device.—S. T. Kiewit, *Watch Timer with Precise Time Base*, *Electronics*, 31:51, p 84-85.



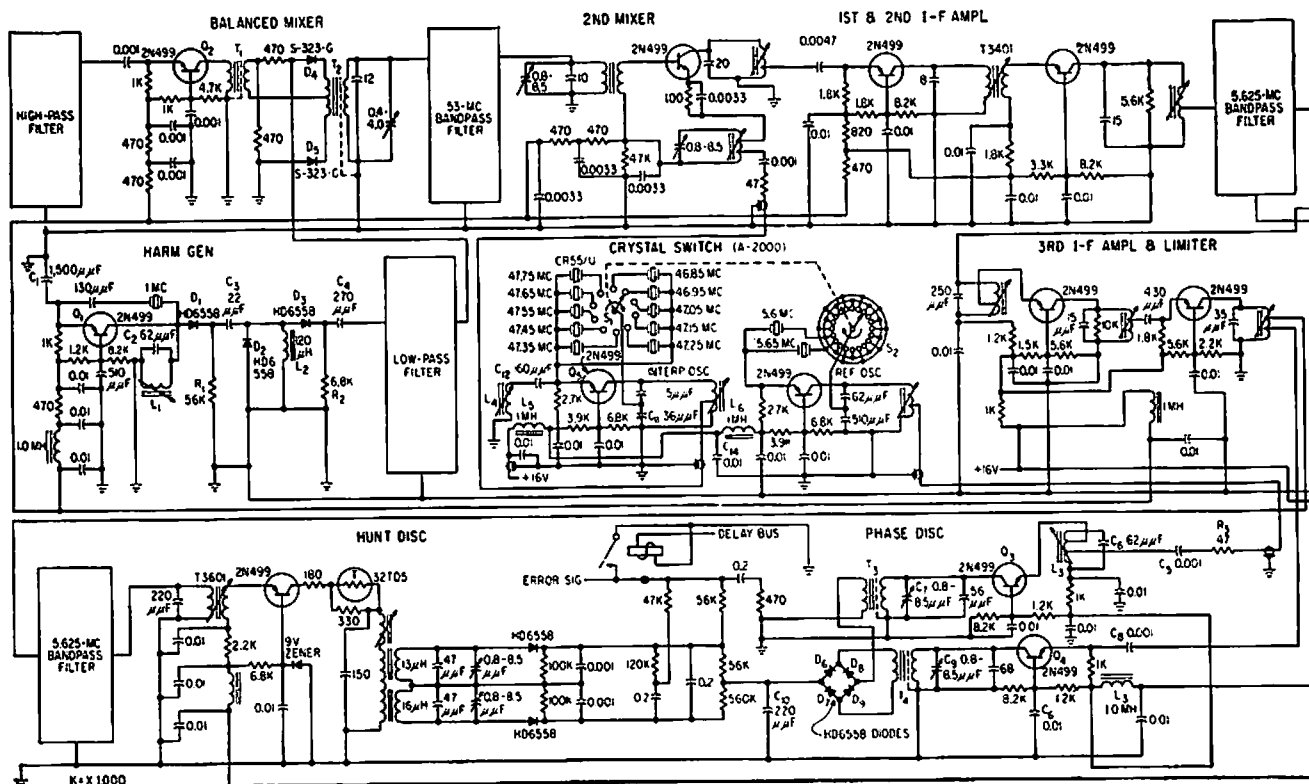
CHAPTER 93

Transceiver Circuits



* INCLUDES STRAY CAPACITANCE

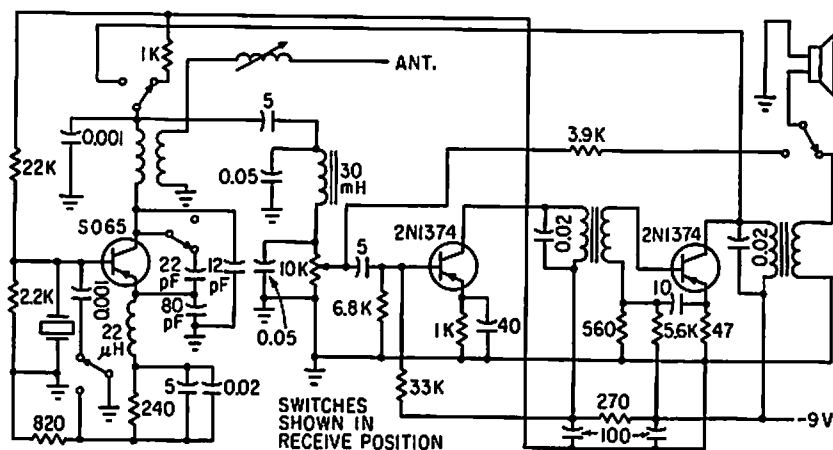
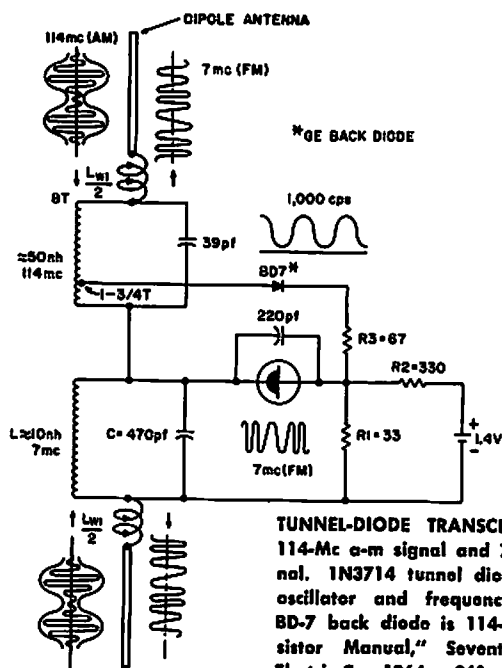
CB FILTER—Electromechanical filter with 6-kc bandwidth at 455 kc gives 8 db signal-to-noise improvement.—Filtering the Chatter on Citizens' Band, *Electronics*, 38:5, p 81.



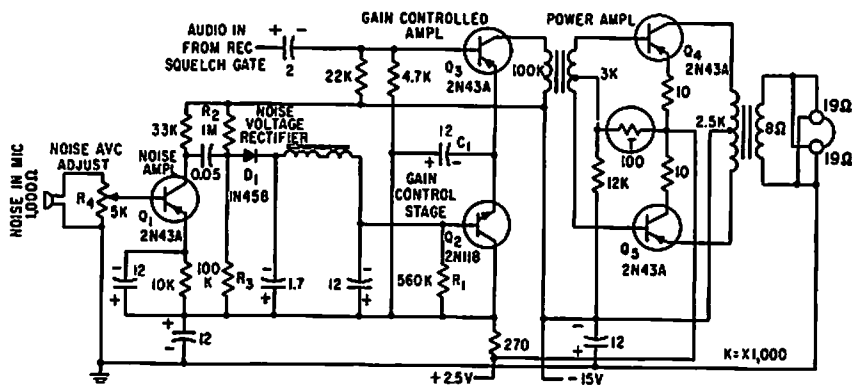
920-CHANNEL CRYSTAL REFERENCE—Controlled-frequency mobile radio transceiver, operating in two bands, uses improved band-pass filter techniques that double number of

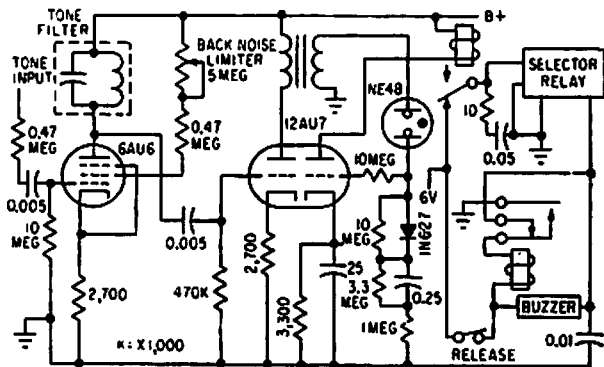
channels per megacycle of spectrum. Oscillator-stabilized system is designed for 50-kc channel spacing and selects any of 920 channels between 30 and 76 Mc.—F. Brauer

and D. Kammer, *Mobile Radio System Provides 920 Channels*, *Electronics*, 31:41, p 96-99.

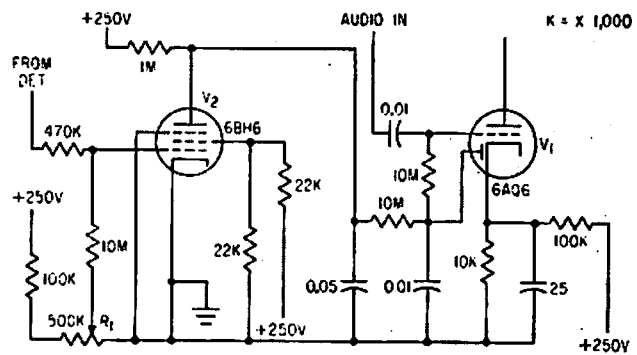


NOISE-ACTUATED AVC—Emitter current of 2N43A controlled low-level audio amplifier stage Q3 is regulated indirectly by sound pressure level of ambient noise. With no noise, gain of controlled amplifier is prevented from going to zero by applying quiescent conduction bias through R1 to gain-control stage Q2.—D. C. Gibson, *Holmet Transceiver for Flight Deck Communications*, *Electronics*, 33:39, p 56-60.

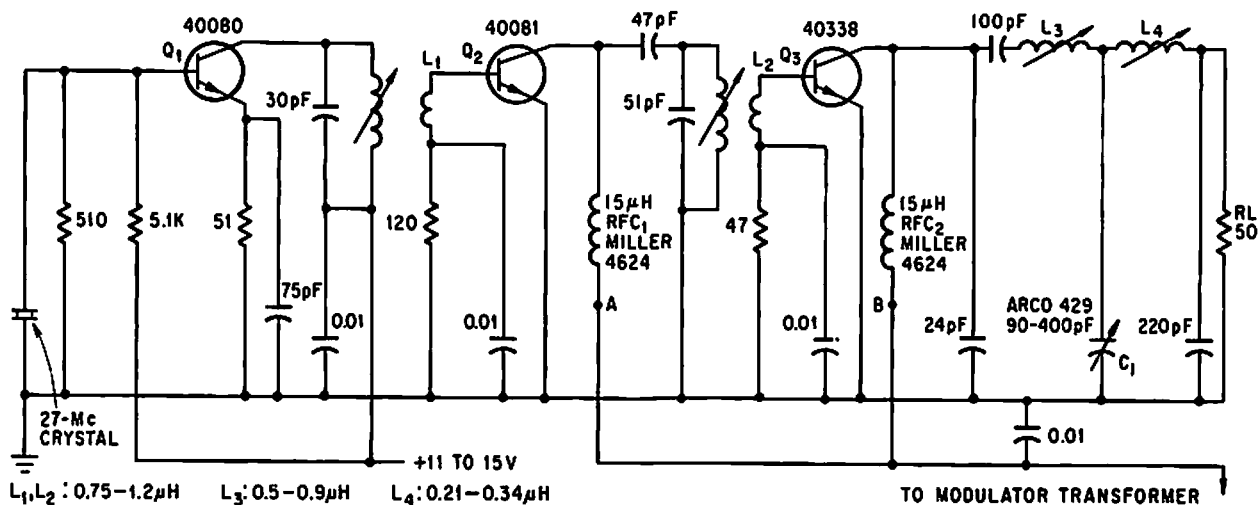




CB DECODER—Responds to telephone-dial digital tone pulses from receiver. Rejects noise pulses and functions even when noise is stronger than desired single-tone signal. Used in mobile dial telephones.—L. G. Sands, Citizens Radio Revision Spurs Equipment Design, *Electronics*, 32:15, p 55-57.

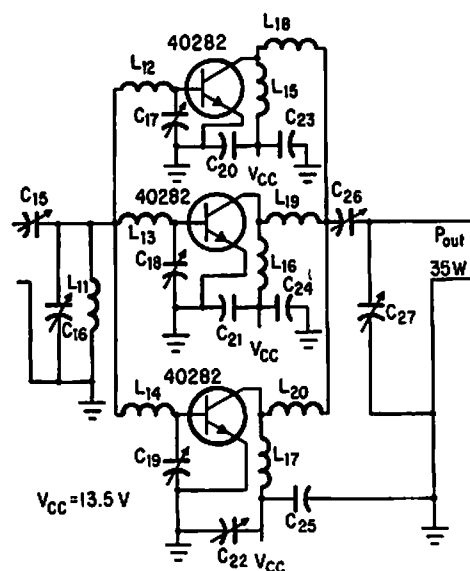
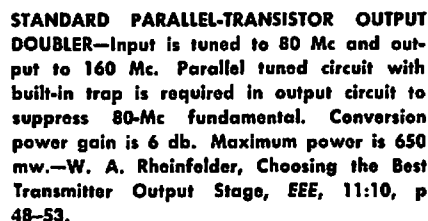


NOISE SQUELCH—When negative-going signal is received from detector, control grid of squelch tube V2 goes negative until positive bias set by squelch control R1 is overcome. Used in Vocaline CB transceiver.—L. Solomon, Citizens Band Equipment Design, *Electronics*, 33:45, p 70-72.



CB—Three overlay transistors provide 5 w for 27-Mc citizens-band a-m transmitter.—D. J.

Donahue and B. A. Jacoby, Putting the Overlay to Work at High Frequencies, Electronics,



L₂, L₃, L₈: ferrite choke, 450 ohms
L₁, L₄, L₉, L₁₁: 0.1 microhenry

Capacitors

C₂, C₆, C₁₀, C₁₂, C₁₈, C₁₉, C₂₇: 8-60 pF

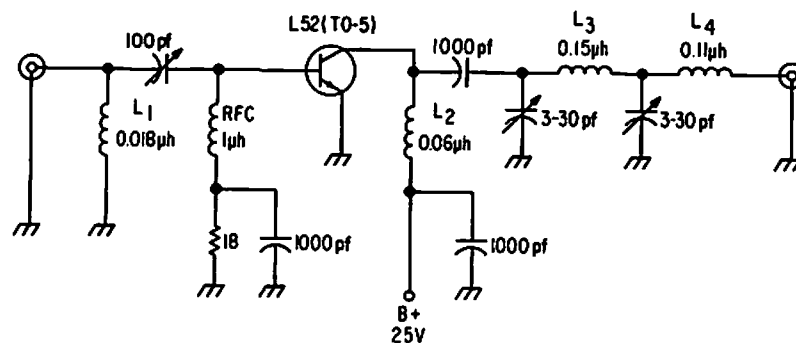
C₄, C₈, C₁₂, C₂₄, C₂₃, C₂₅: feed-through, 1500 pF

C_{1,5}: 1.5-20 pF

C₂₀, C₂₂.

C₂₁: ceramic disk, 0.2 μ F

1 W AT 170 MC—Single L52 feeds 1 w to 50-ohm antenna through pi-L network. Power gain is 4 db and efficiency is 30% for class C operation.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 323.



$P_o = 1$ watt power gain = 4 db average

$V_{cc} = 25$ v

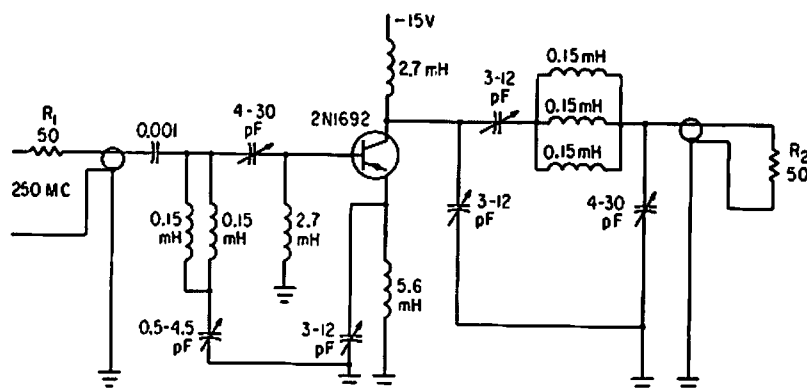
$I_c = 125$ ma

$L_1 = 1$ T No. 14 Buss, 0.5" diam

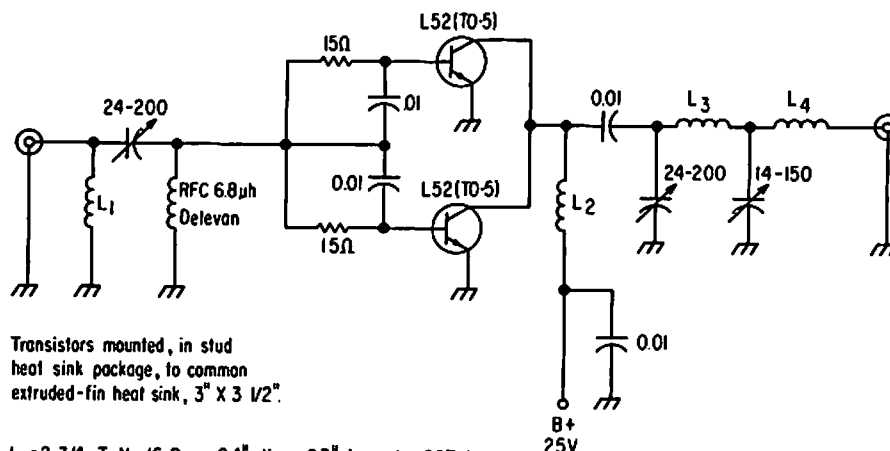
$L_2 = 3$ T No. 14 Buss, 0.4" diam, 0.3" long

$L_3 = 5$ T No. 14 Buss, 0.4" diam, 0.5" long

$L_4 = 4$ T No. 14 Buss, 0.4" diam, 0.4" long



NEUTRALIZED EMITTER BOOSTS H-F GAIN— New operating mode increases h-f gain more than 20 db, reduces interstage matching problems, improves selectivity and stability, and cuts cost. Based on neutralizing of emitter-circuit inductances with small variable capacitor from emitter to ground and r-f choke to provide d-c path from emitter to ground. Technique works best above 100 Mc. —Extend Transistor Frequency, *Electronics*, 34:44, p 25.



Transistors mounted, in stud heat sink package, to common extruded-fin heat sink, 3" X 3 1/2".

$L_1 = 2$ 3/4 T No. 16 Buss, 0.4" diam, 0.3" long, $L = 0.067$ μh

$L_2 = 5$ T No. 16 Buss, 0.4" diam, 0.4" long, $L = 0.14$ μh

$L_3 = 5$ T No. Solderize, 0.5" diam, 0.4" long, $L = 0.22$ μh

$L_4 = 6$ T No. Solderize, 0.5" diam, 0.5" long, $L = 0.34$ μh

Power out = 10w

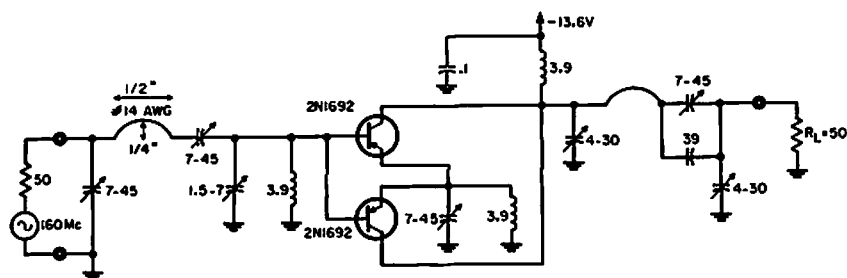
Power gain = 10db

DC Power in = 600ma at 25v = 15w

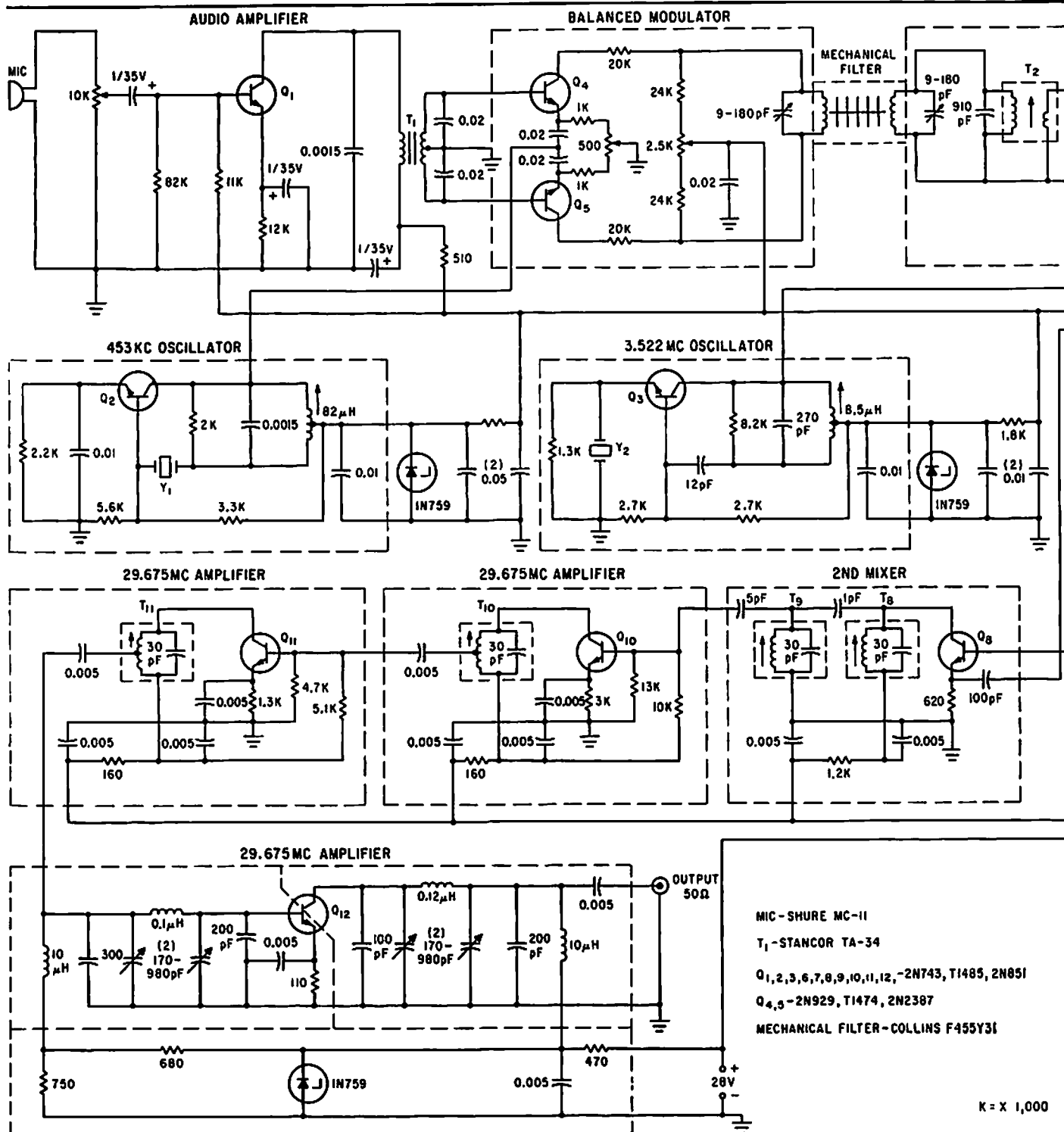
10 W AT 50 MC—Two L52's in parallel provide 10 w output power with 10 db gain. Separate biasing resistors are used in base

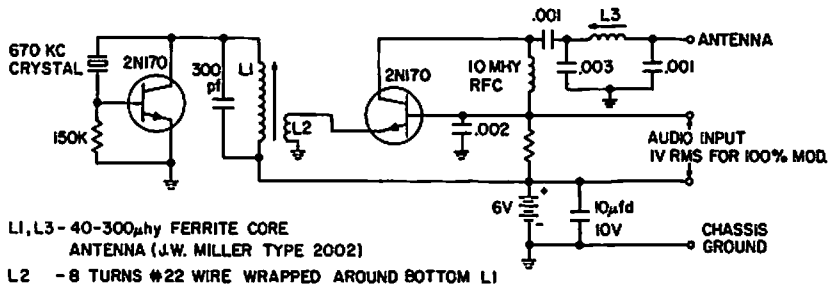
circuits to balance operating currents. Input and output impedances are both 50 ohms, and overall efficiency is 65%.—Texas Instru-

ments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 323.

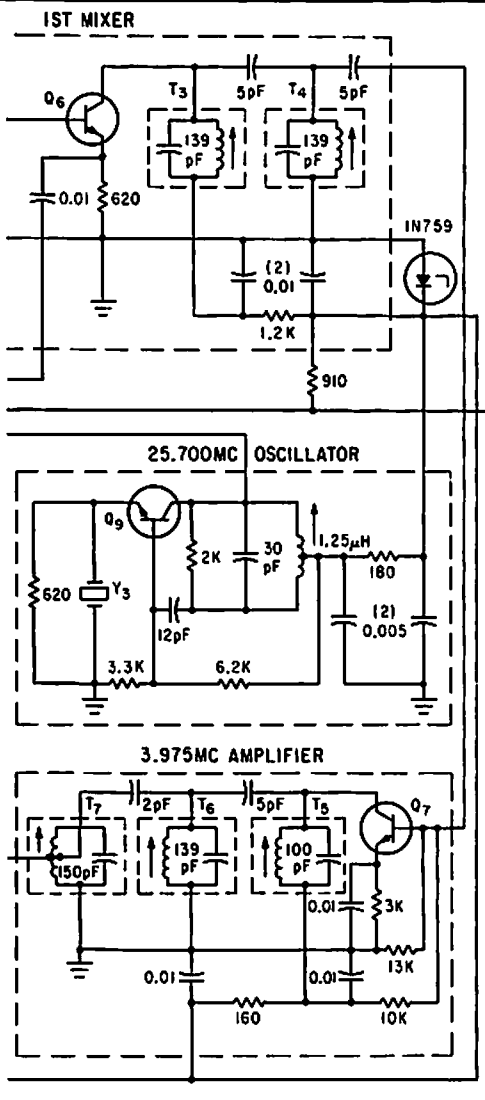


PARALLEL-TRANSISTOR OUTPUT STAGE WITH EMITTER TUNING—Variable capacitor common to both emitters, together with r-f choke for d-c path, provides efficient tuning and increased power gain at outputs near maximum of 2 w. Chief drawback is reduced power gain at low input levels.—W. A. Rheinfelder, *Choosing the Best Transmitter Output Stage*, *EEE*, 11:10, p 48-53.



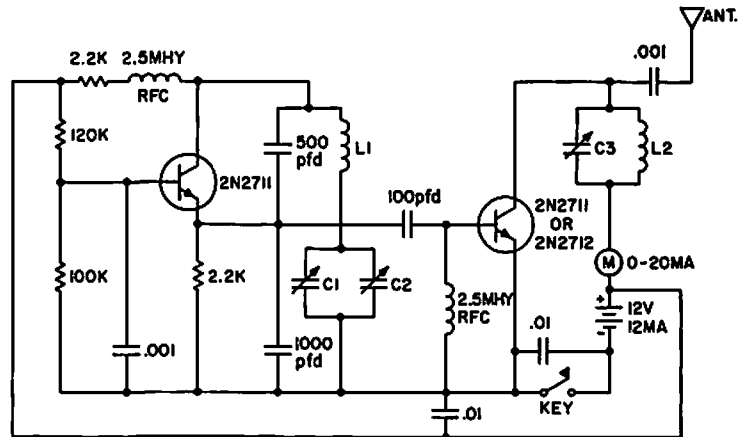


LOW-POWER BROADCAST-BAND TRANSMITTER—Crystal-controlled 670-kc oscillator and single-transistor amplifier stage provide 400 microwatts a-m for antenna.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 386.



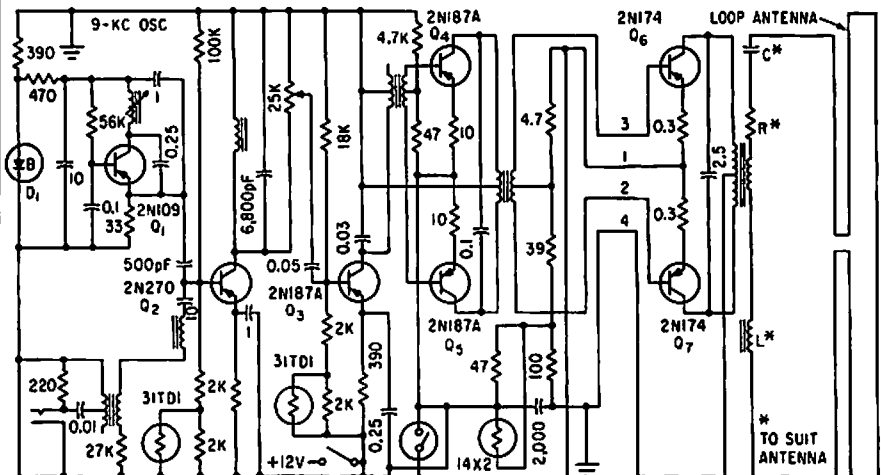
CRYSTAL COMPLEMENT			
SIDE BAND	Y ₁	Y ₂	Y ₃
UPPER	453KC	3.522MC	25.700MC
LOWER	457KC	3.518MC	25.700MC

SSB EXCITER—Silicon transistors reduce noise and increase reliability of single-sideband exciters. Circuit shown, for 10-meter band, gives choice of upper or lower sideband.—D. L. Wilcox, Single-Sideband Exciter uses Planar Silicon Transistors, *Electronics*, 35:32, p 65-67.



C1 5-15pf BANDSPREAD VARIABLE L1 57T { B & W NO. 3016
C2 5-55pf BANDSET VARIABLE L2 35T { MINI-DUCTOR COILS
C3 6-80pf FINAL TUNING VARIABLE } 1" DIA 32T/INCH

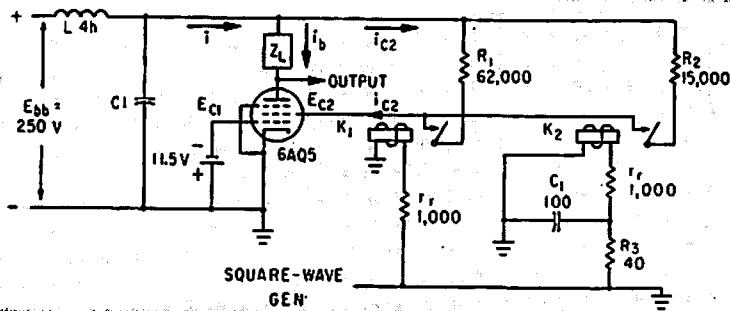
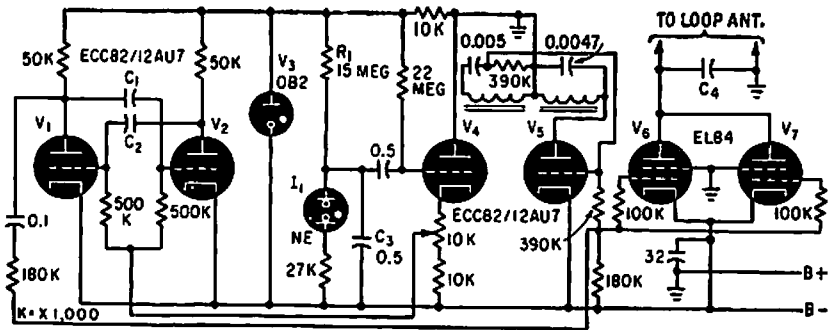
LOW-POWER VFO C-W 80-METER TRANSMITTER—Power output is 100 mw in amateur band.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 386.



9-KC OSCILLATOR STABILIZER—Insures 24 w output over temperature range of -20 to +120°F. Zener diode voltage-regulating circuit prevents changes in collector supply voltage of oscillator Q1. Base-driven modulator Q2 may be fed from tape repeater preamp,

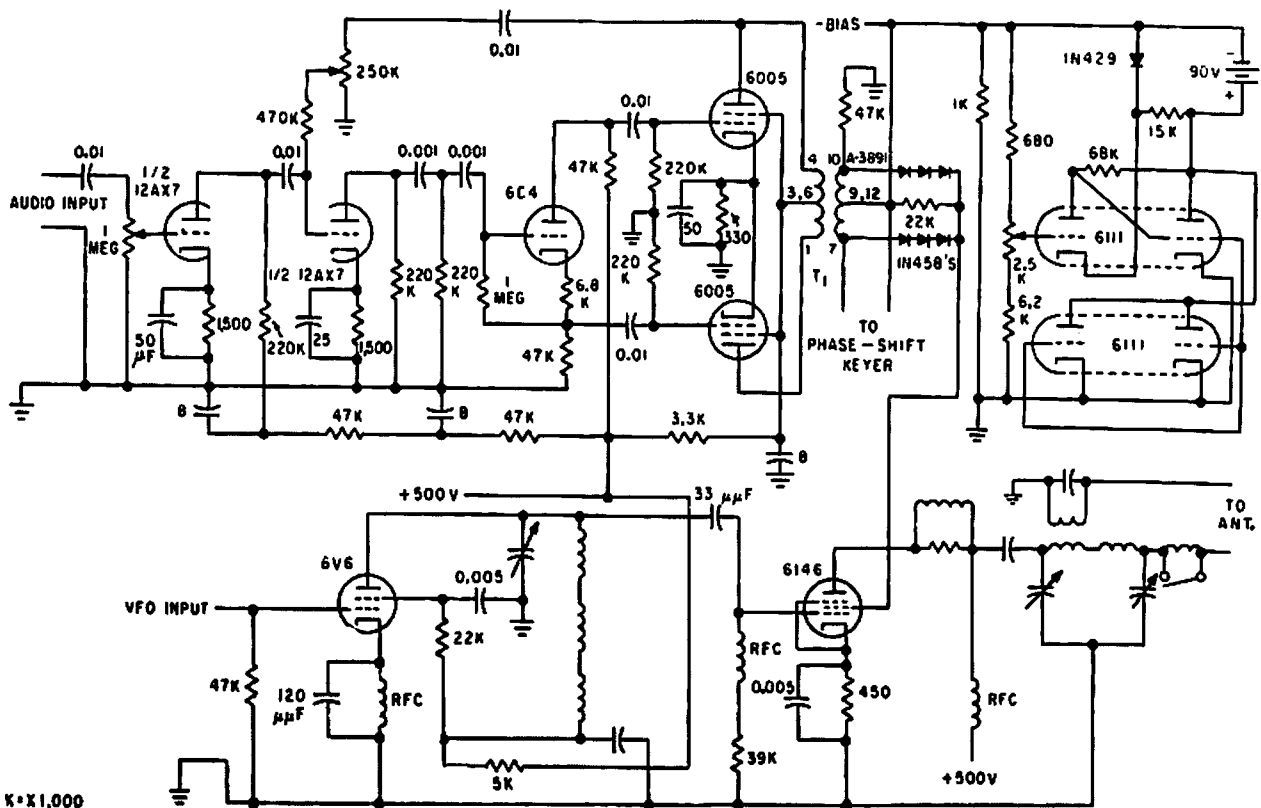
microphone, or master control center serving induction radio system using roadside telephone-lino loops.—E. A. Hanyasz, J. E. Stevens, and A. Medovsky, Communication System for Highway Traffic Control, *Electronics*, 33:42, p 81-83.

PAGING TRANSMITTER—Feeds single-wire loop surrounding area to be covered. Multi-vibrator V1-V2 produces carrier frequencies in range from 15 to 30 kc, keyed on and off at various repetition rates in range from 1/50th to 1/200th second, for selective paging of up to 45 different receivers.—J. G. De-Graaf, Selective Paging System Uses Coded Transmission, *Electronics*, 33:9, p 68-70.



VOICE-OPERATED CONTROL WITH DAMPING

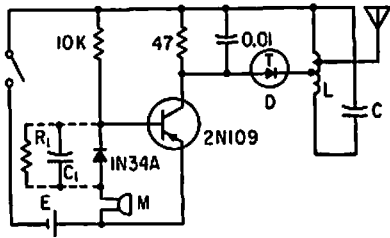
—Relays provide timed sequential switching of pentode load to LC-filtered power supply when operator speaks into microphone, thereby preventing overshoot.—E. L. Harris and O. J. M. Smith, Novel Circuit Damps Transients in Voice-operated Transmitters, *Electronics*, 35:39, p 66-67



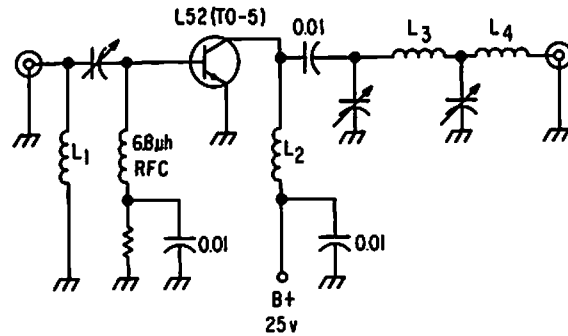
CARRIER SUPPRESSION—Suppressed-carrier modulation improves efficiency of medium-power transmitter and provides noise ad-

vantages of exalted-carrier detection in receiver.—J. Dysinger, W. Whyland, and R. Wood, New Suppressed-Carrier Modulation

Technique, *Electronics*, 33:6, p 47-49.



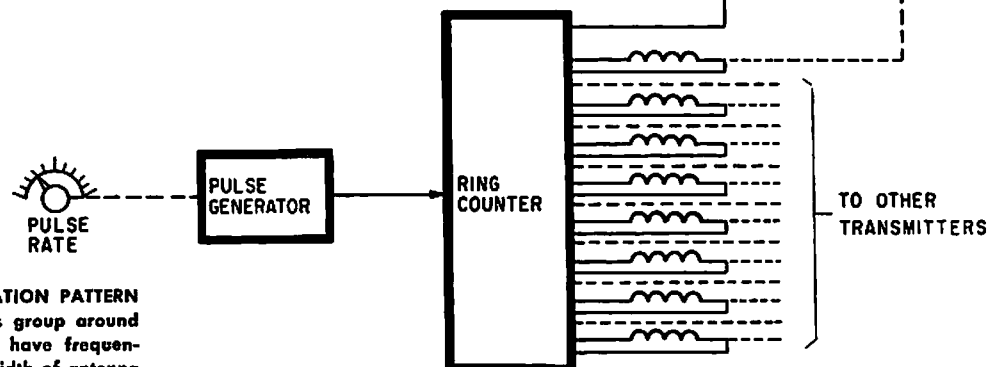
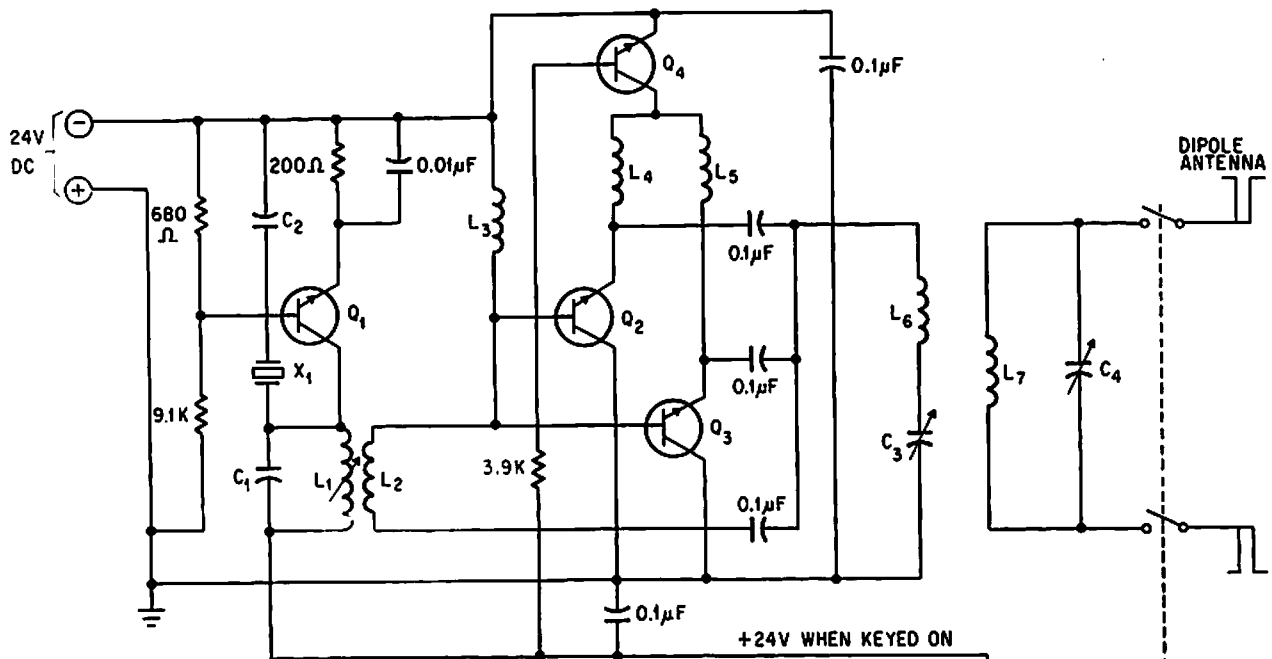
F-M WIRELESS MIKE—Oscillator-modulator using single tunnel diode produces 35-kc frequency deviation per mv of modulating signal at 90 Mc. When fed by dynamic mike, range is up to 100 feet. If pre-emphasis is desired, diode 1N34A may be replaced by R1-C1 circuit shown in dotted lines, having time constant of 75 microsec.—W. Ko, Tunnel Diode F-M Wireless Microphone, *Electronics*, 33:47, p 93-95.



L_1 = 3T NO. 16 Buss, 0.4 diam, 0.3" long, L = 0.085 μ h
 L_2 = 4T NO. 16 Buss, 0.4 diam, 0.4" long, L = 0.12 μ h
 L_3 = 8T NO. 16 Solderze, 0.5 diam, 0.5" long, L = 0.5 μ h
 L_4 = 10T NO. 16 Solderze, 0.4 diam, 0.6" long, L = 0.34 μ h

1 W AT 50 MC—Relatively high breakdown voltage of LS2 transistor permits amplitude modulation. Overall efficiency is 65%. Combination pi-L network matches common-

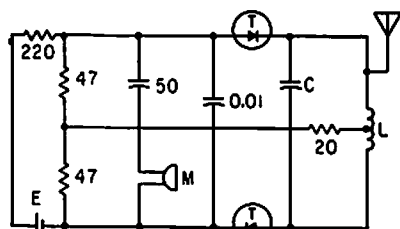
emitter class-C stage to 50-ohm antenna.—Texas Instruments Inc., "Solid-State Communications," McGraw-Hill, N.Y., 1966, p 322.



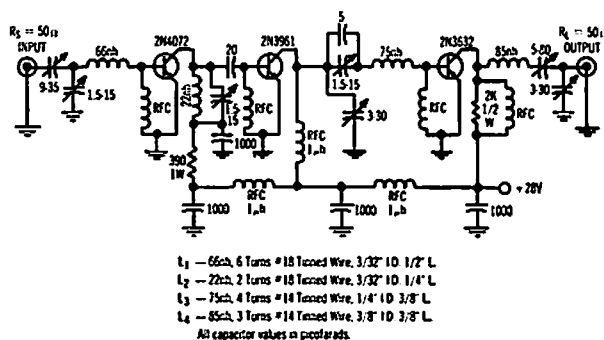
MEASURING ANTENNA RADIATION PATTERN—Eight transmitters, towed as group around ground antenna by airplane, have frequencies spaced throughout bandwidth of antenna under test, from 2 to 50 Mc. Pulse-controlled ring counter switches transmitters up to 40

times per second. All transmitters are type 2N3053.—C. Barnes, Transmitters Towed

Through Air Test Antenna's Radiation Pattern, *Electronics*, 38:21, p 96-101.

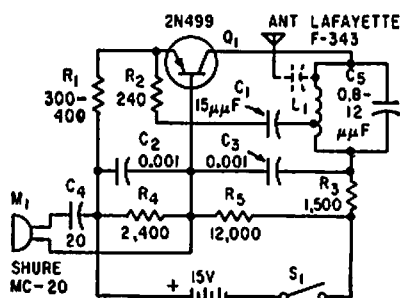


TUNNEL-DIODE WIRELESS MIKE—Two cascaded 2-ma germanium tunnel diodes serving as cascade oscillator and 90-Mc f-m modulator give range of over 100 feet. Coil L is about 5 microhenrys, with five turns a quarter-inch in diameter and half an inch long. C is 24 pf.—W. Ko, Tunnel Diode F-M Wireless Microphone, *Electronics*, 33:47, p 93-95.

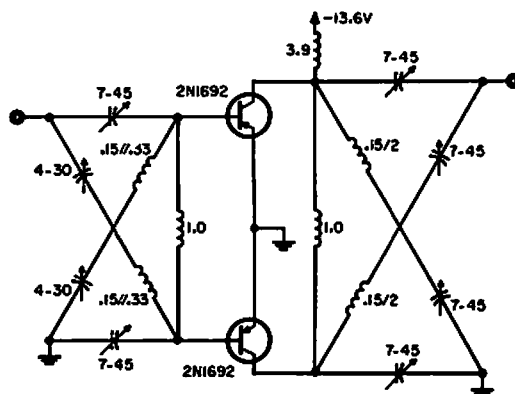


160-MC 15-W POWER AMPLIFIER—Simple three-stage r-f power transistor circuit provides 30.5 db power gain with efficiency of

62%, on 28-v supply.—Solid-State Power Amplifier Design (Motorola ad), Electronics, 39:14, p 48-49.

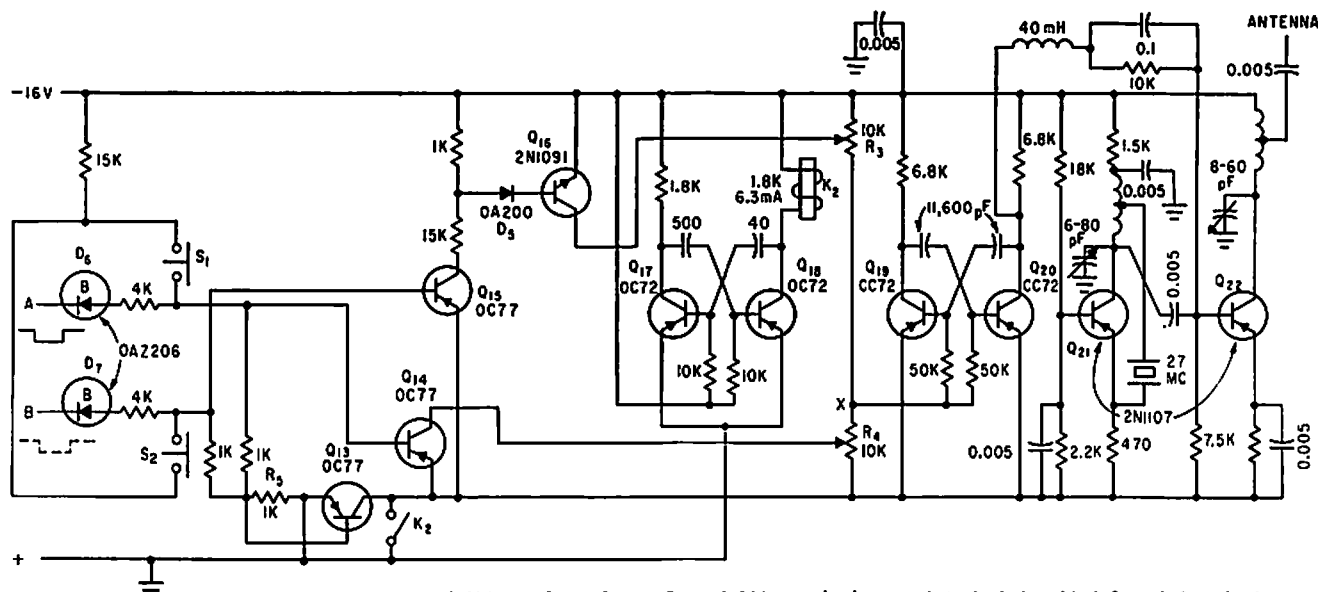


MINIATURE F-M TRANSMITTER—Single 2N499 transistor performs functions of r-f oscillator, frequency modulator, and audio amplifier for tiny portable transmitter having range of 200 feet. Suitable for use with public-address system.—D. E. Thomas and J. M. Klein, How to Construct a Miniature F-M Transmitter, *Electronics*, 32:31, p 80-81.



PUSH-PULL OUTPUT WITH LATTICE FILTERS—Preferred for frequencies above 30 Mc because lattice arrangement without transformers is much easier to construct and allan

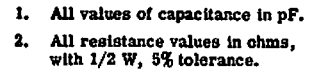
than standard push-pull circuit.—W. A. Rheinfelder, *Choosing the Best Transmitter Output Stage*, *EEE*, 11:10, p 48-53.



27-MC REMOTE-EVENT TRANSMITTER—Gives 400-cps modulation for pulse at input A.

1,400 cps for pulse at B, and 800-cps check pulses every 2 sec, for transmitting bird flight data to remote recorder.—P. A. Tove

and J. Czekaewski, Infrared Curtain System Detects and Counts Moving Objects, *Electronics*, 34:31, p 40-43.



Solid-State Marine Band Transmitter," Motorola Application Note AN-156, Feb. 1966.

TO NEGATIVE BIAS AND SECONDARY CENTER TAP OF T_1

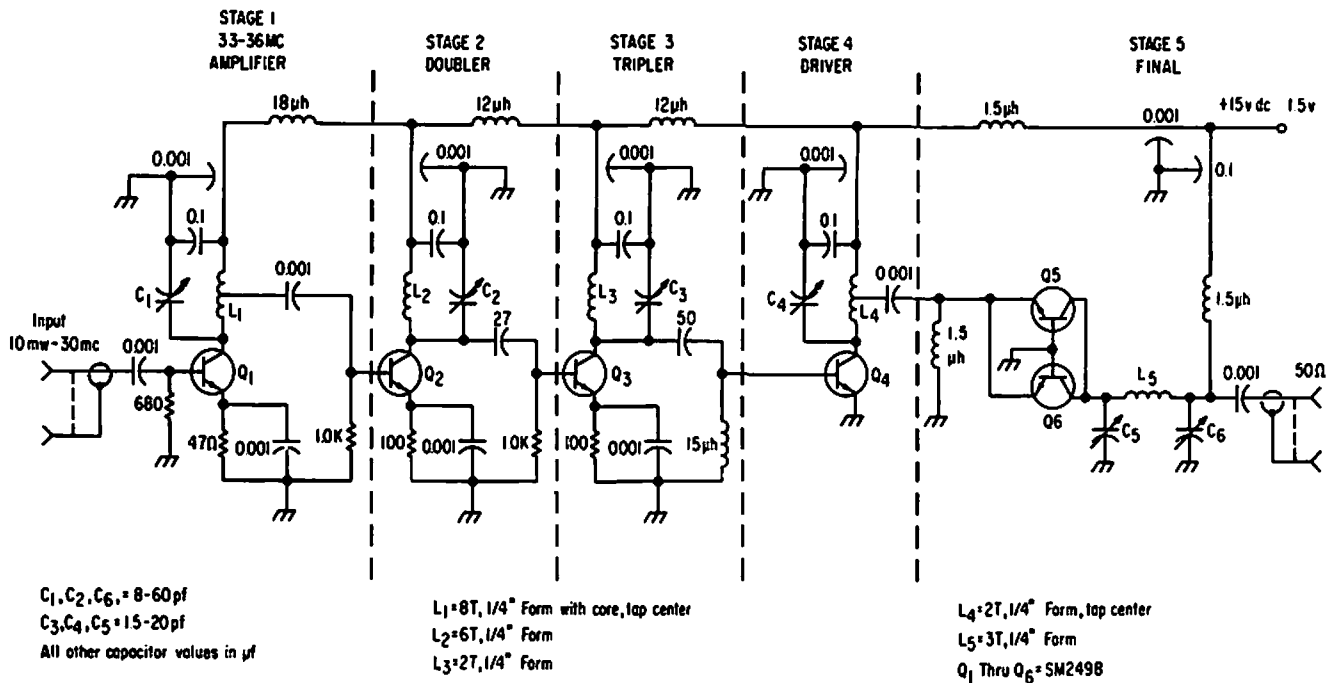
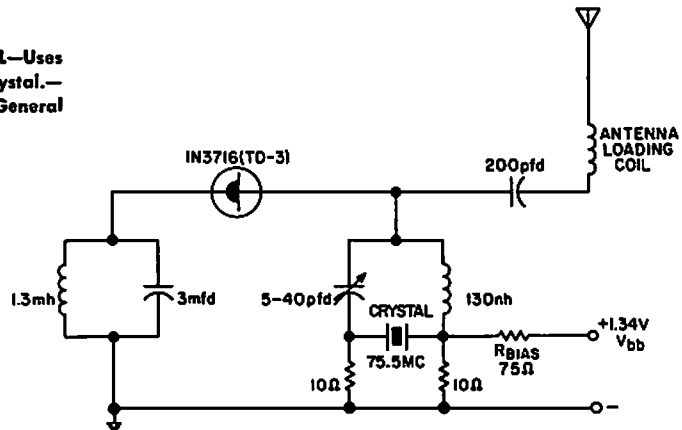
OUTPUT TO TRANSMITTER (VFO)

K $\times$ 1,000



"A Solid-State Transmitter with 40 Watts Output at 50 MHz," Motorola Application Note AN-172, Dec. 1965.

73.5-MC SELF-MODULATED CRYSTAL—Uses tunnel diode oscillator to modulate crystal.—
"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 357.



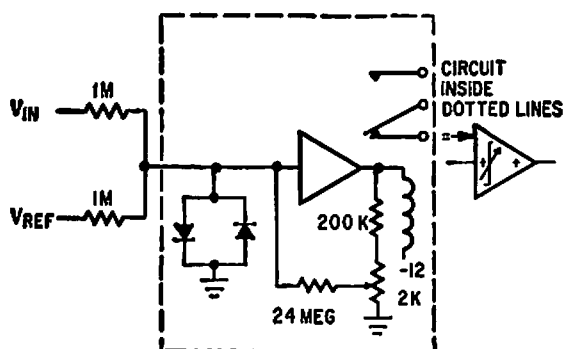
NARROWBAND 162-180 MC TRANSMITTER—
First stage acts as buffer for oscillator, while
Q2 and Q3 multiply frequency. Class-C

power amplifier using two SM2498 transis-
tors in parallel delivers 300 mw to 50-ohm
load.—Texas Instruments Inc., "Solid-State

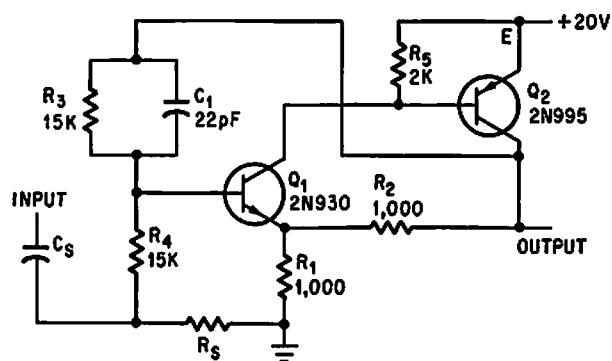
Communications," McGraw-Hill, N.Y., 1966,
p 325.

CHAPTER 95

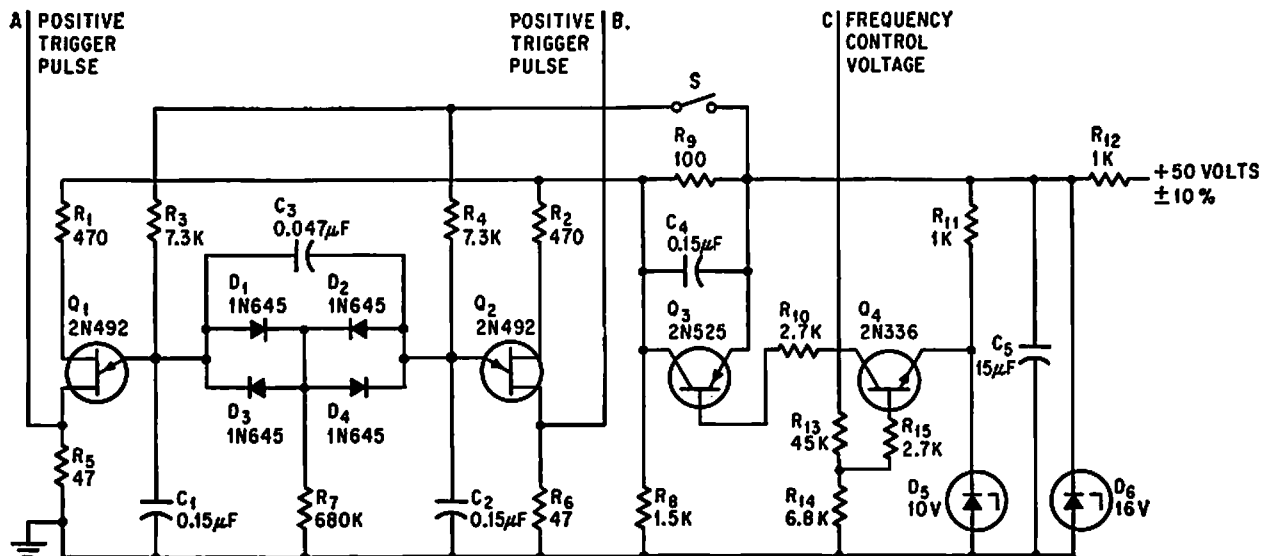
Trigger Circuits



OPERATIONAL TRIGGER—Combines some features of operational amplifier with those of Schmitt trigger. Input diodes prevent amplifier saturation.—P. Lefferts, Operational Trigger For Precise Control, *Electronics*, 37:28, p 50-55.



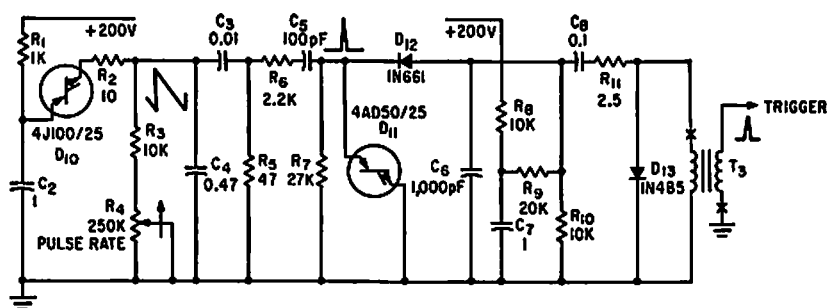
INPUT TURNS BOTH TRANSISTORS ON—Unlike Schmitt trigger, both transistors stop conduction when input is removed.—L. L. Kleinberg, Complementary Shaper Replaces Schmitt Trigger, *Electronics*, 37:26, p 66.



DUAL-PULSE TRIGGER—Dual triggers supply alternating pulses for driving d-c to d-c voltage converter connected to A and B. Fre-

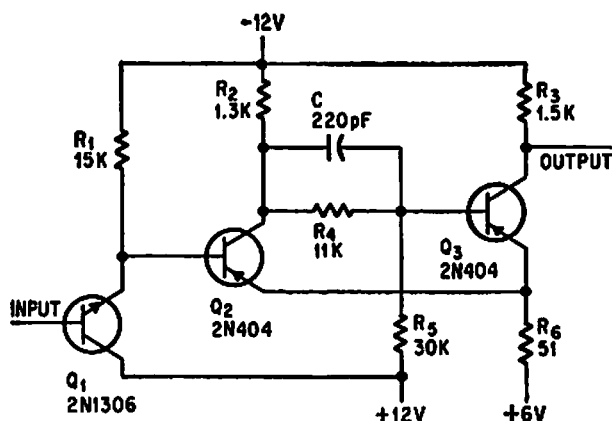
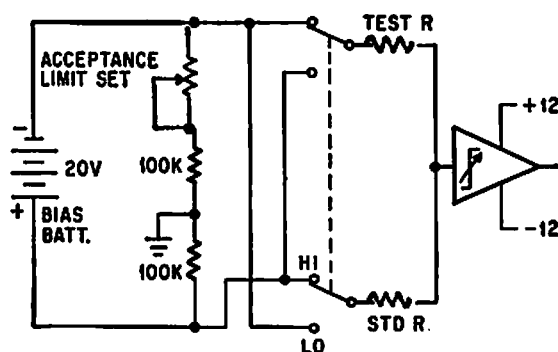
quency of triggering can be adjusted from 650 to 900 cps by varying base-to-base voltages of unijunction transistors.—T. Wilson,

Voltage Controls Dual-Pulse SCR Trigger, *Electronics*, 37:28, p 62-63.



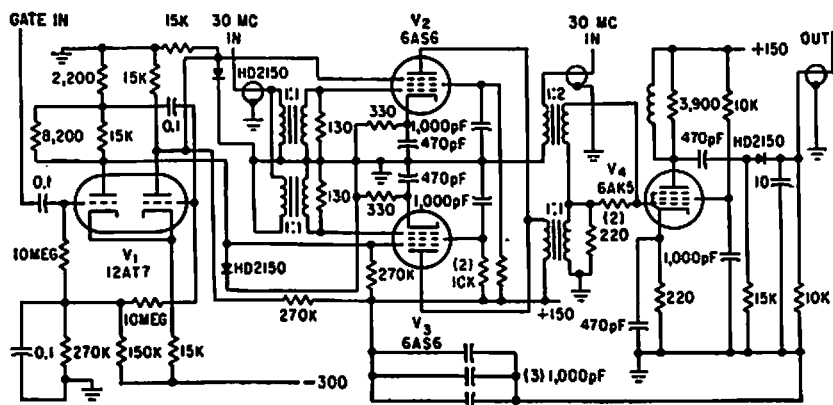
PULSER FOR 250-KW MODULATOR—Trigger generator for scr modulator uses two-layer and four-layer diodes to provide pulse burst repetition rates up to 25 kc.—H. G. Heard, Controlled Rectifier Produces Quarter-Megawatt Pulse Power, *Electronics*, 34:25, p 54-55.

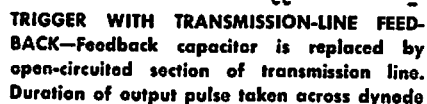
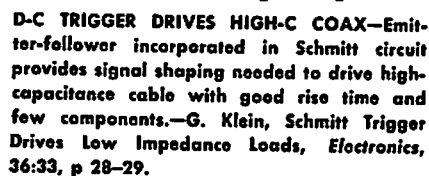
RESISTOR TESTER—Operational trigger is tripped by bridge circuits when resistor under test is 0.11% high for switch position shown. Reversing switch gives same sensitivity for low resistors.—P. Lefferts, Operational Trigger For Precise Control, *Electronics*, 37:28, p 50-55.



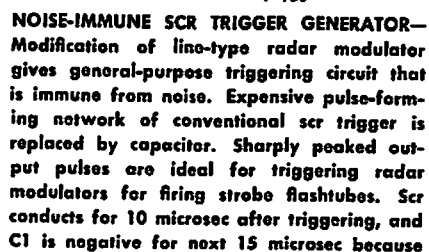
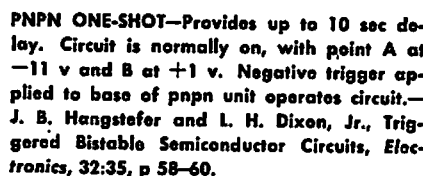
VOLTAGE-LEVEL TRIGGER—Output pulse appears when input is 6.0 v but not for inputs up to 7 volts above or below this level. Circuit then continues on until input drops below 5.4 v.—R. F. Woody, Precise 6-Volt Input Triggers Circuit, *Electronics*, 38:14, p 80.

CHOPPER AND PHASE DETECTOR—Input gate signal operates Schmitt trigger V1, to give identical but oppositely phased signals for phase detector of instrument for measuring phase differences between two signals.—R. T. Stevens, Precision Phasemeter for CW or Pulsed UHF, *Electronics*, 33:10, p 54-57.

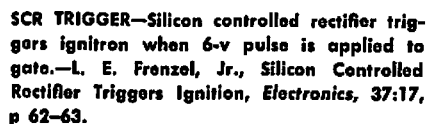
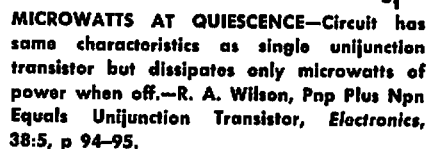


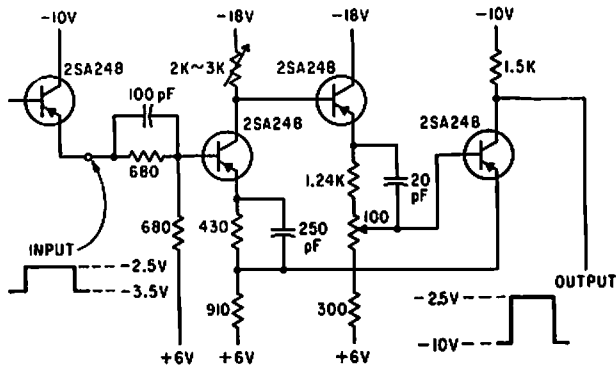


load of secondary-emission pentode is adjusted by varying line length.—E. J. Martin, Jr., How to Use the Secondary-Emission Pentode, *Electronics*, 33:41, p 60-63.



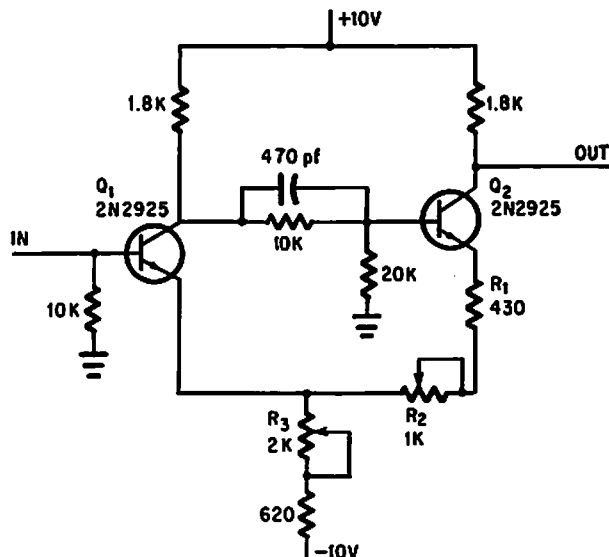
of ringing with L1, so false triggering can occur from low-level noise pulses for only last 5 microsec, by which time capacitor has charged enough to forward-bias scr so large triggering pulse is again required to turn it on. Circuit is ready for next trigger 60 microsec after C1 is discharged. Maximum ppr is 12 kc.—J. E. Curry, No Pulse-Forming Network in SCR Trigger Generator, *Electronics*, 39:18, p 97-98.





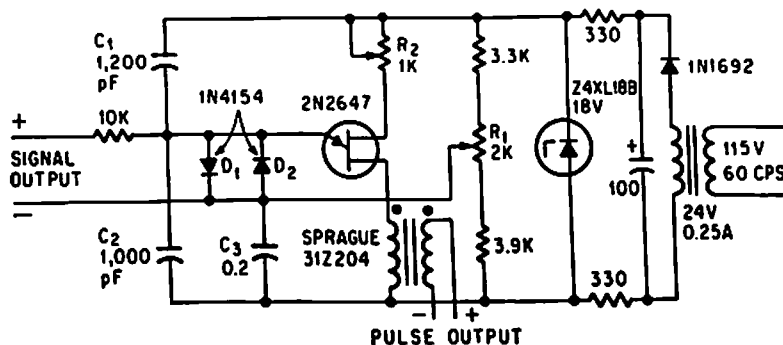
CUTOFF SCHMITT—Conventional current-feedback version, in which one of the two active transistor elements is generally cut off, performs reliably even though optimum operat-

ing regions for transistors cannot always be achieved.—H. Inose, Y. Yoshida, and H. Tada, Noncutoff Circuits Improve Trigger Switching, *Electronics*, 35:30, p 36-39.



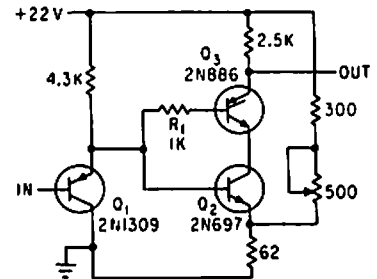
\$1.75 SCHMITT—Use of inexpensive transistors and fixed resistors in place of potentiometers keeps cost low. Hysteresis control R2 and trigger level control R3 are optional.

Output is 8 v peak to peak at 50 kc.—A. Pacola, Low-Cost Schmitt Trigger Reduces Hysteresis, *Electronics*, 38:24, p 63-64.

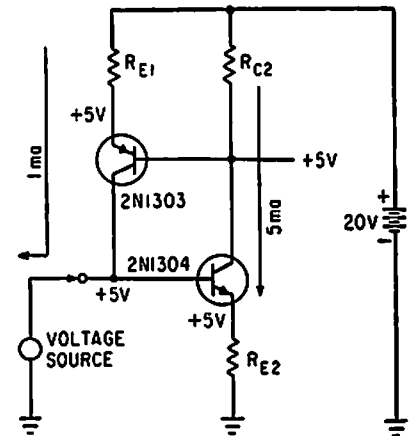


VOLTAGE SENSING—Unijunction transistor is triggered when input signal is slightly positive, and then generates pulses as long as input remains positive. Output can be used

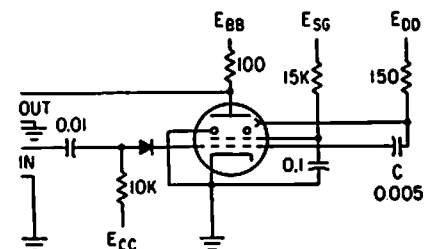
to trigger flip-flop or turn on scr's.—D. V. Jones, Quick-On-The-Trigger Design, *Electronics*, 38:12, p 105-110.



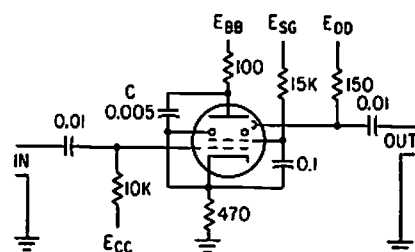
SCR IMPROVES SCHMITT TRIGGER STABILITY—Input signals to 200 kc give consistent triggering over wide range of temperature, source impedance, and input impedance, and hysteresis is reduced by order of 10.—M. Schmidt, Improved Schmitt Trigger Uses SCR, *Electronics*, 36:17, p 68.



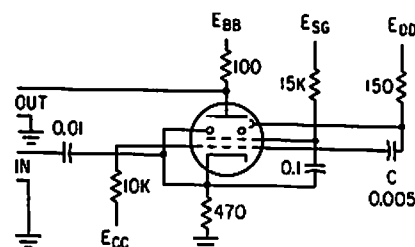
SERIES SCHMITT—Complementary transistors are either both on or both off, conserving power for trigger, comparator, flip-flop, one-shot, and oscillator applications. With 20-v supply, RE1 is 15 K, RC2 is 3K, and RE2 is 1K.—J. K. Skilling, New Complementary Transistors make Series Schmitt Circuits Practical, *Electronics*, 35:35, p 52-53.



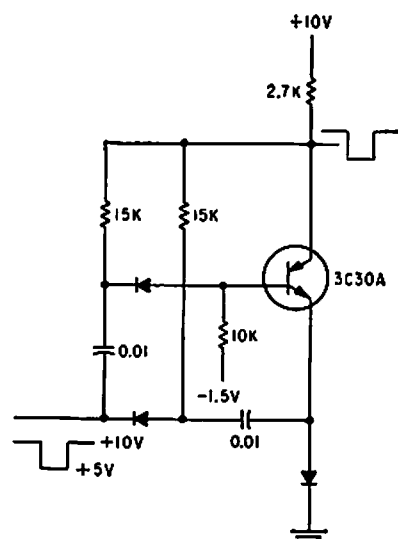
DYNODE-TO-GRID POSITIVE FEEDBACK—Produces negative output pulse across plate load of secondary-emission pentode. Feedback is from dynode to control grid, rather than from plate to cathode. Diode insures that feedback pulse does not affect other circuits, and makes feedback nearly independent of input generator impedance. Used in high-speed, short-duration pulse work.—E. J. Martin, Jr., How to Use the Secondary-Emission Pentode, *Electronics*, 33:41, p 60-63.



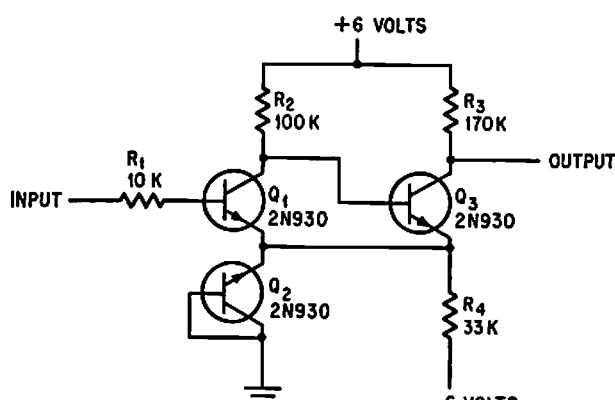
SECONDARY-EMISSION PENTODE TRIGGER—Produces positive output pulse across dynode load each time it is triggered by positive grid pulse. Used in high-speed, short-duration pulse work.—E. J. Martin, Jr., *How to Use the Secondary-Emission Pentode*, *Electronics*, 33:41, p 60-63.



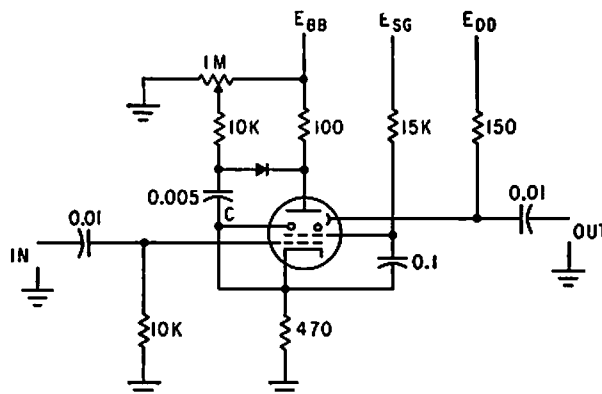
NEGATIVE-PULSE TRIGGER—Diode is not necessary in input circuit of secondary-emission pentode, since feedback is from dynode to control grid and negative trigger pulse is impressed on cathode. Used in high-speed, short-duration pulse work.—E. J. Martin, Jr., *How to Use the Secondary-Emission Pentode*, *Electronics*, 33:41, p 60-63.



PNP FLIP-FLOP BINARY COUNTER—Negative trigger pulses produce 2:1 operation. Can be driven from identical flip-flop or from collector of npn silicon transistor.—J. B. Mangstefer and L. H. Dixon, Jr., *Triggered Bistable Semiconductor Circuits*, *Electronics*, 32:35, p 58-60.

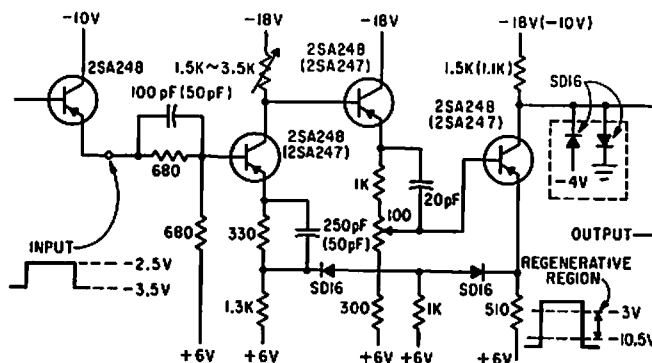


DIODE-COUPLED SCHMITT—Uses include pulse-width modulation of d-c voltage for switching amplifiers, wave shaping, and voltage or current monitoring. Low dynamic resistance of diode formed by Q2 keeps hysteresis (difference between turn-on and turn-off) down to 10 mv.—D. D. Robinson, *Diode-Coupled Schmitt Trigger*, *Electronics*, 37:31, p 50-51.



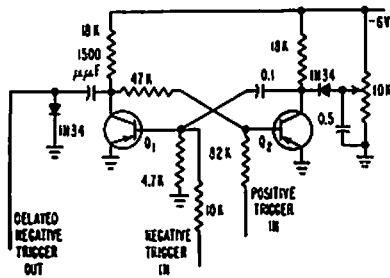
VARIABLE-SENSITIVITY TRIGGER—Biased diode in feedback circuit prevents regeneration. Feedback cannot occur until negative-going pulse greater than bias appears at plate of secondary-emission pentode. Diode bias

varies sensitivity, allowing use as pulse-height discriminator also.—E. J. Martin, Jr., *How to Use the Secondary-Emission Pentode*, *Electronics*, 33:41, p 60-63.

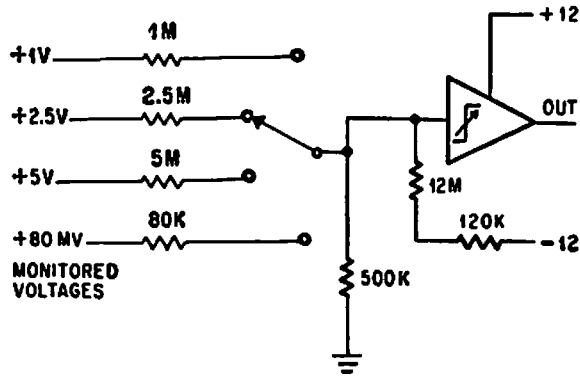


NONCUTOFF SCHMITT—Both amplifiers are always connected to their current sources, hence are never cut off and can operate in optimum region, with no risk of damage by inverse base-emitter voltage. Output voltage

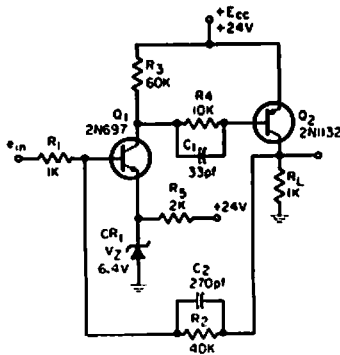
alternates between two levels.—H. Inose, Y. Yoshida, and H. Tada, *Noncutoff Circuits Improve Trigger Switching*, *Electronics*, 35:30, p 36-39.



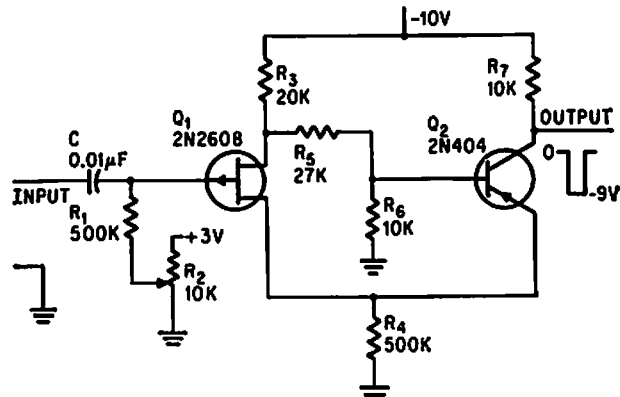
DELAYED TRIGGER GENERATOR—Provides controllable positive or negative delayed trigger. When used to see leading edge of multi-vibrator pulse on cto, trigger starts cto sweep and delay generator. After preset time, delay generator produces pulse used to trigger mvbr.—H. L. Armstrong, Transistorized Trigger and Delay Generators, *Electronics*, 31:3, p 96-98.



VOLTAGE MONITOR—Input voltage sensitivity better than 0.7 mv is obtained from operational trigger.—P. Lefferts, Operational Trigger For Precise Control, *Electronics*, 37:28, p 50-55.

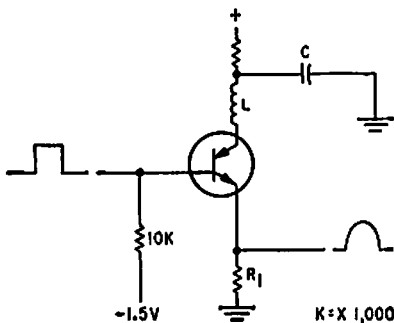


REGENERATIVE-SWITCHING TRIGGER—Advantages over conventional Schmitt include reduced power consumption (neither transistor conducts during off state), full-range output voltage swing, and low output impedance. Some input signal appears in output. Rise and fall times are 0.15 microsec.—R. K. Vieth, Trigger Circuit Gives Less P-diss, More V-out, *EEE*, 11:12, p 28.

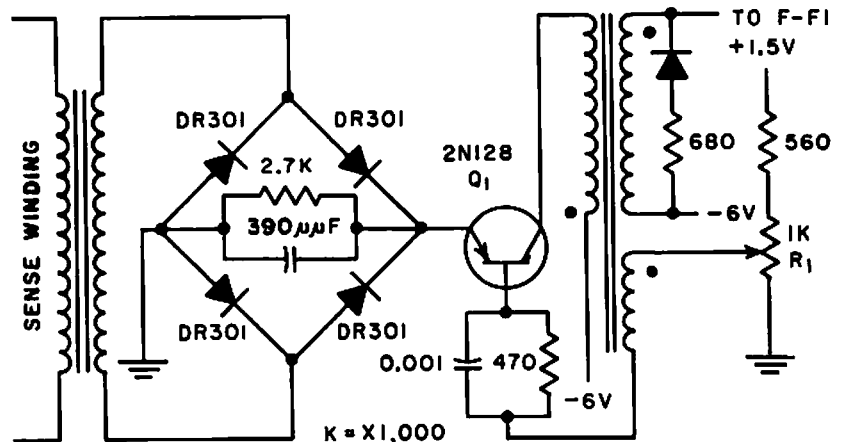


HIGH-IMPEDANCE SCHMITT—Use of fet Q1 for input stage gives high input impedance, as required for threshold detector circuit. Output pulse is square wave at up to 100 kc

triggering. Turnoff threshold is about 0.2 v below turnon.—L. R. Lett, FET Increases Schmitt Trigger Input Impedance, *Electronics*, 38:15, p 65.

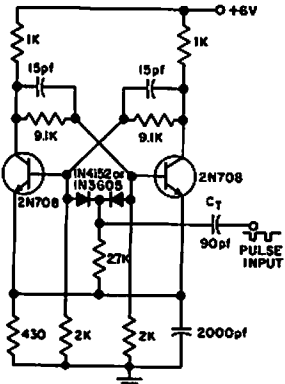


LC PULSE GENERATOR—Provides half-sinusoid output determined by L and C, when npn unit is triggered on by low-level positive pulse applied to its base.—J. B. Hangstefor and L. H. Dixon, Jr., Triggered Bistable Semiconductor Circuits, *Electronics*, 32:35, p 58-60.

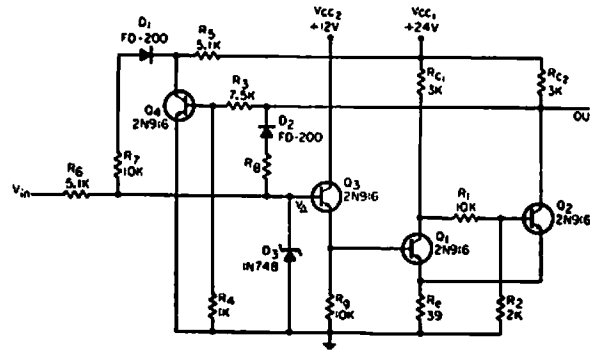


SENSE AMPLIFIER—Blocking-oscillator transistor amplifier is triggered by output of diode bridge network. Rectification assures that readout voltages of both polarities are sensed. Diodes attenuate small signals great-

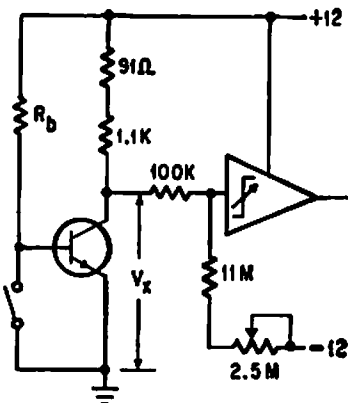
ly relative to large signals, increasing signal-noise ratio at rectifier output to about 20:1.—C. S. Warren, W. G. Rumble, and W. A. Helbig, Transistorized Memory Monitors Earth Satellite, *Electronics*, 31:3, p 66-70.



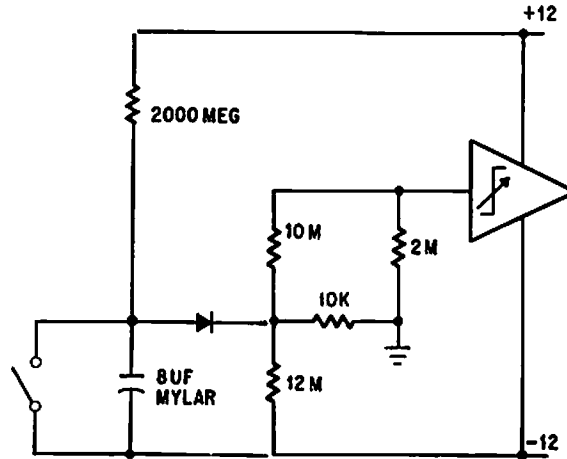
BASE-TRIGGERED FLIP-FLOP—Maximum trigger rate for steering circuit exceeds 5 Mc with negative trigger pulse amplitude from 0.75 to 2 v. Requires less trigger energy than collector triggering but more accurately controlled trigger amplitude.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 197.



VARIABLE-HYSTERESIS SCHMITT—R7 adjusts lower trigger point, and R8 adjusts upper trigger point.—R. S. Hughes, Variable-Hysteresis Schmitt Trigger, EEE, 13:7, p 41.

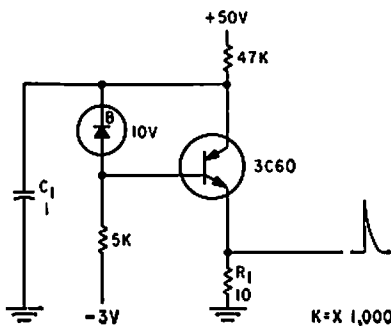


SATURATED TRANSISTOR MEASUREMENT—Transistor under test is biased to saturate collector to within 100 mv of ground for 10-ma collector load. Operational trigger then trips when V_x is 1 mv above 100 mv.—P. Loefferts, Operational Trigger For Precise Control, Electronics, 37:28, p 50-55.

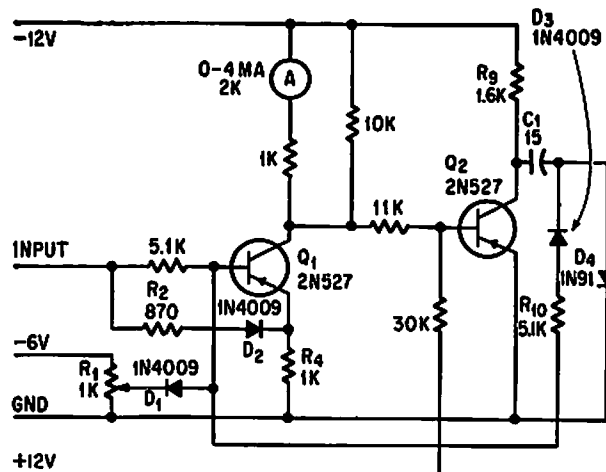


HOURS OF DELAY—Capacitor starts charging from -12 v to +12 v when switch is opened. Diode begins conducting at ground potential, and operational trigger trips when diode

passes 2 na. Timing accuracy is high.—P. Loefferts, Operational Trigger For Precise Control, Electronics, 37:28, p 50-55.

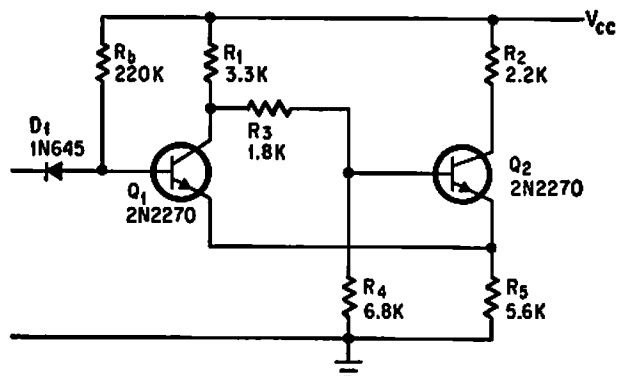


PULSE GENERATOR—Delivers 1-amp peak output current having duration of 10 microsec when pnp unit is triggered on by low-level positive pulse applied to its base.—J. B. Hangstefer and L. H. Dixon, Jr., Triggered Bistable Semiconductor Circuits, Electronics, 32:35, p 58-60.



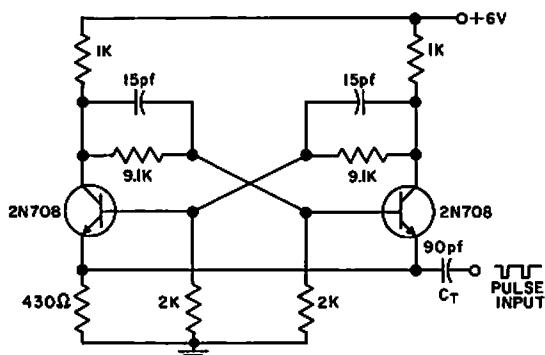
RANDOM-PULSE DETECTOR—Either positive or negative pulses above predetermined minimum amplitude force Q1 into saturation and turn Q2 off, causing the meter to deflect im-

mediately. Circuit may also be used as pulse stretcher or threshold detector.—C. F. Johnson and J. T. Loisello, Bipolar Pulse Detector Features Motor Display, Electronics, 38:24, p 63.



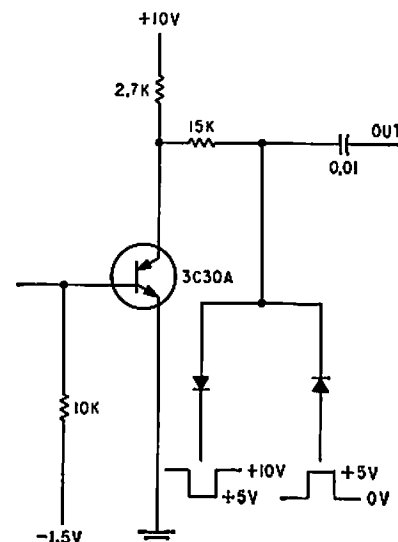
DIODE-MODIFIED SCHMITT TRIGGER—Addition of R_b and D_1 reduces loading on driving circuit when Q_1 is on, thereby preventing input signal from being clamped. Same

signal may therefore drive other Schmitt triggers having higher trigger levels.—J. Gaon, Diode and Resistor Increase Input Resistance of Schmitt, *Electronics*, 39:12, p 110-111.

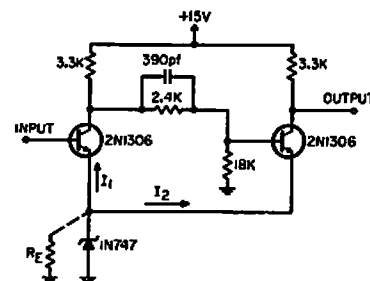


EMITTER-TRIGGERED FLIP-FLOP—Pulse input makes alternate sides of flip-flop conduct on alternate trigger pulses. Maximum trigger rate exceeds 2 Mc with trigger amplitude

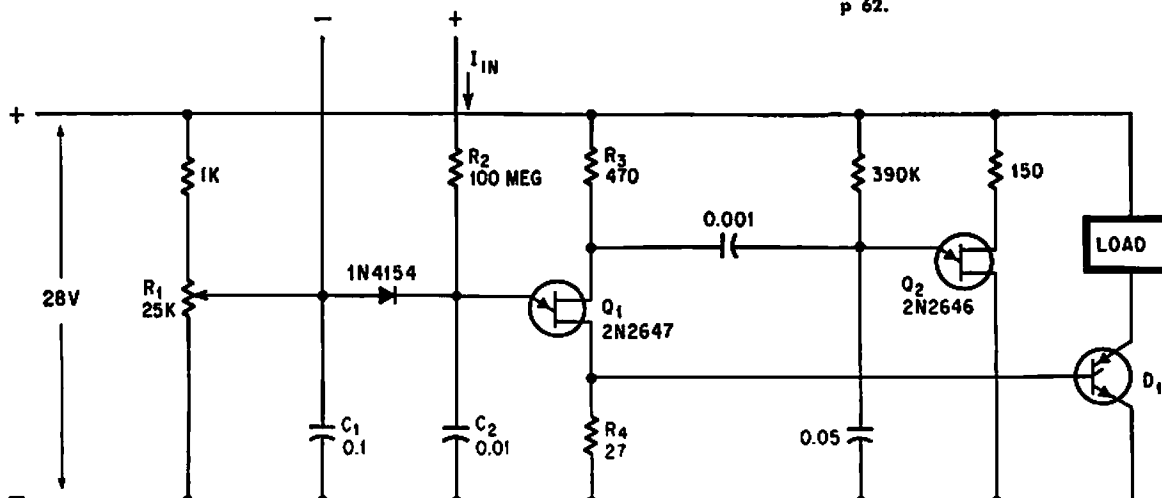
from 4 to 12 v. Chief limitation is high trigger current required.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 196.



BASIC PNP MEMORY—Delivers either positive or negative pulse output, accomplished by means of coupling diodes and transition memory capacitor.—J. B. Hangstefer and L. H. Dixon, Jr., Triggered Bistable Semiconductor Circuits, *Electronics*, 32:35, p 58-60.



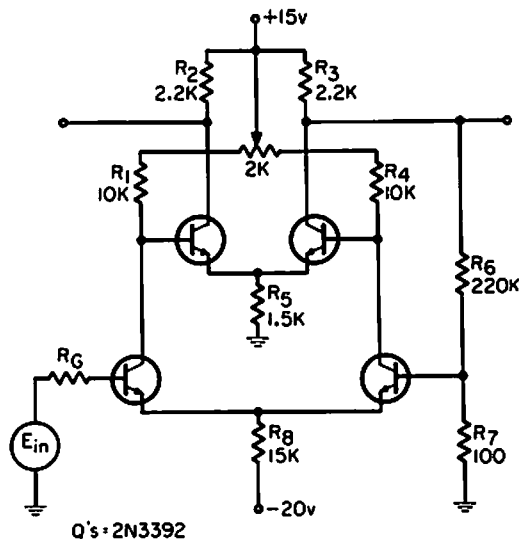
ZERO-HYSTERESIS SCHMITT—Turn-on and turn-off voltages are made identical by using zener diode in place of R_E .—R. A. Wilson, Zero-Hysteresis Schmitt Trigger, *EEE*, 13:2, p 62.



CURRENT SENSING—Input current of only 40 na will charge C_2 and raise voltage at emitter of Q_1 to triggering level. C_1 and C_2 then

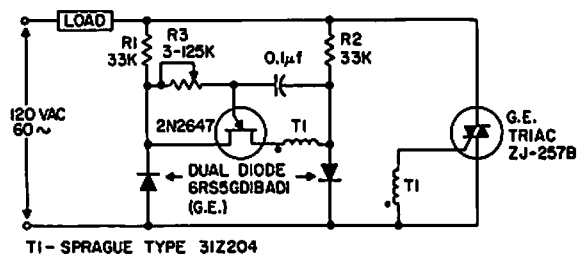
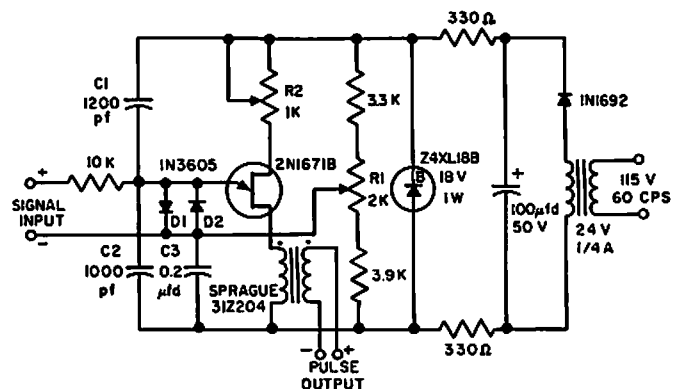
discharge through R_4 , and resulting generating pulse triggers scr D_1 in series with load. Recovery is rapid.—D. V. Jones, Quick-On-

The-Trigger Design, *Electronics*, 38:12, p 105-110.



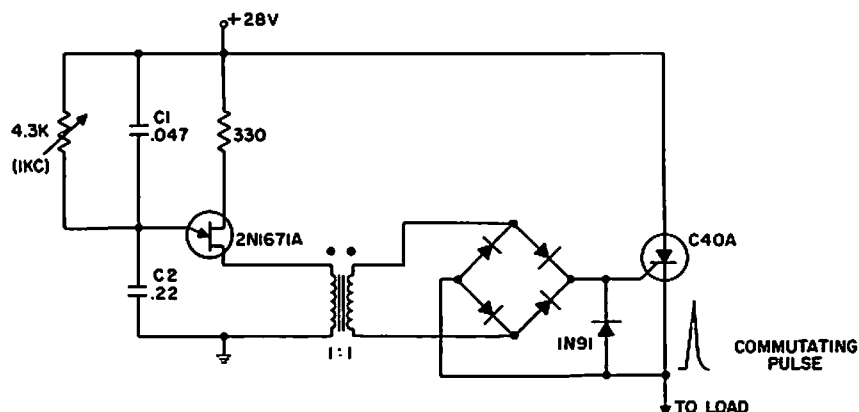
LOW-LEVEL THRESHOLD DETECTOR—Determines when millivolt-range signal exceeds adjustable threshold. Circuit is similar to Schmitt trigger. Forward gain of amplifier is increased by adding second differential amplifier stage having two low-cost transistors and three resistors. Hysteresis can be as low as 2 mv, as compared to 100 mv in standard Schmitt.—R. M. Muth, Stable Threshold Circuit With Low Hysteresis, *EEE*, 14:1, p 64.

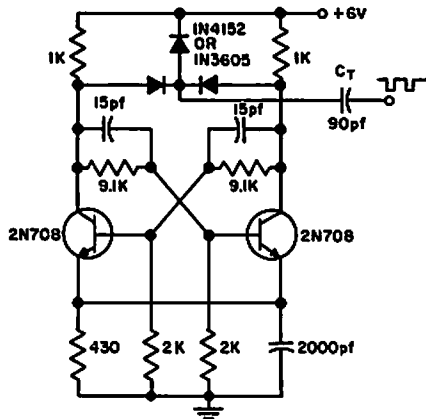
VOLTAGE-SENSING TRIGGER—Long-term stability is better than 10 mv, and can be improved still more by adding two silicon diodes in series with R2. Ideal for use in go-no-go applications.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 325.



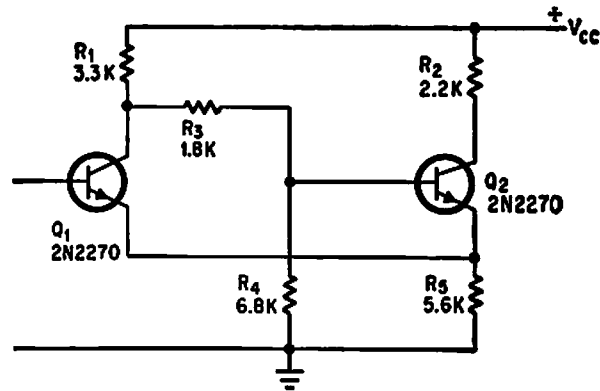
UJT/TRIAC PHASE CONTROL—Has wide range of stable control, without hysteresis or dependence upon supply voltage. Used in automatic feedback control systems, since ujt is essentially half of balanced bridge, with built-in unbalance detection.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 330.

TRANSIENT-ATTENUATING TRIGGER—Transformer coupling and diode bridge between ujt and scr greatly attenuate transients, to prevent premature triggering of ujt when used for impulse commutation in d-c choppers and invertors.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 333.

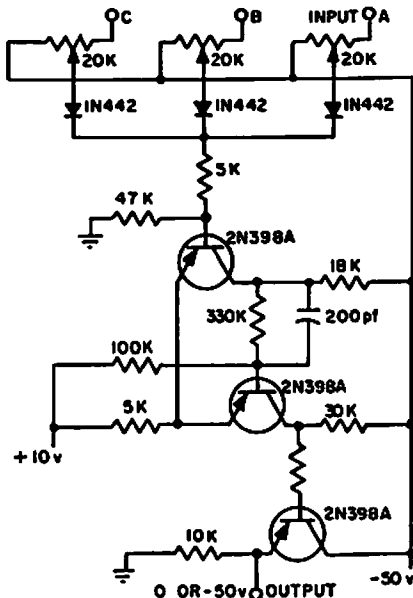




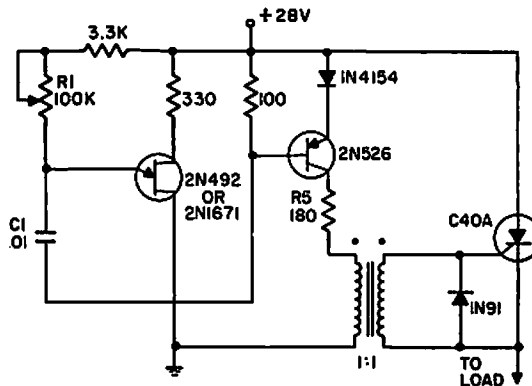
FAST-RECOVERY COLLECTOR TRIGGERING—Additional diode, used in place of resistor from midpoint of diode pair to +6 v, has high back impedance to prevent shunting of trigger pulse during triggering period, and has low forward impedance to insure fast recovery.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 198.



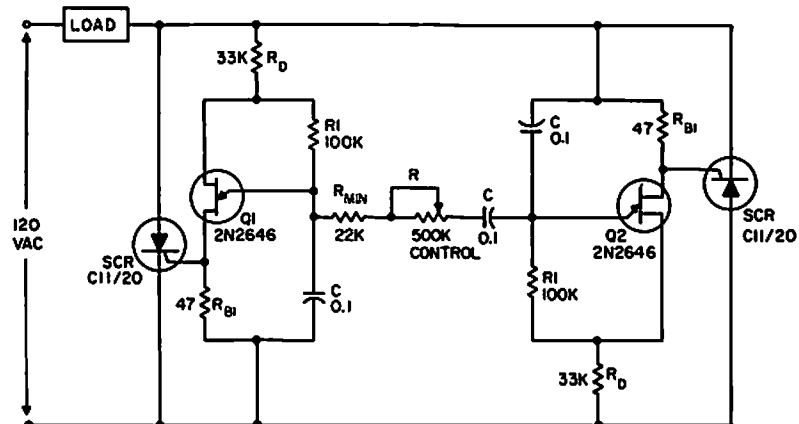
CONVENTIONAL SCHMITT TRIGGER—Q1 is turned on when input exceeds 6.5 v, and input remains clamped at this level. As a result, input signal cannot drive additional Schmitt circuits that may have higher trigger levels.—J. Gaon, Diode and Resistor Increase Input Resistance of Schmitt, *Electronics*, 39:12, p 110-111.



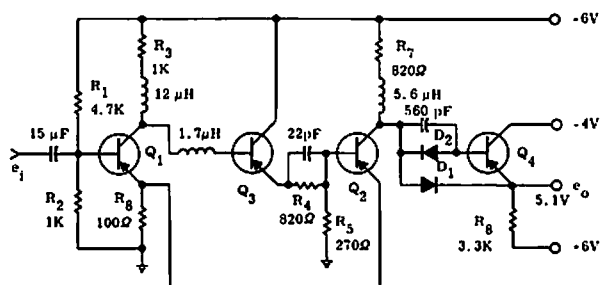
THREE-INPUT TRIGGER—Digital logic permits use of only one Schmitt trigger for monitoring number of variables that can have different voltage levels at which trigger action is desired. Potentiometers provide independent control of set points for each positive input. Each input then triggers circuit independently at its particular threshold, provided circuit was not previously triggered. Circuit is taken from NASA-SP-5022 Technology Utilization Report, Lewis Research Center.—Multiple-Input Trigger Circuit, *Electromechanical Design*, Nov. 1965, p 66.



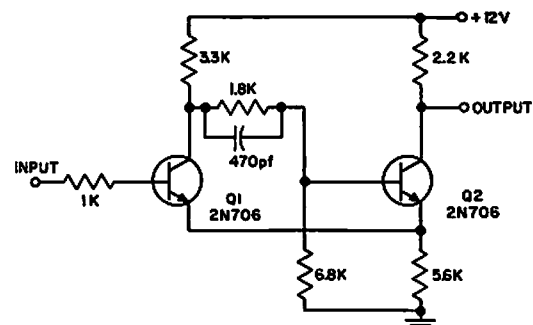
20-KC TRIGGER GENERATOR—Uses ujt to drive 2N526 transistor from cutoff to saturation. Since energy in C1 is not used to trigger scr, small capacitor can be used, thereby increasing operating frequency limit.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 333.



SIMPLIFIED FULL-WAVE UJT-SCR TRIGGER—Consists of two basic half-wave circuits placed back to back, with emitters of ujt's cross-coupled with network that exerts full-cycle phase control over both scr's.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 330.

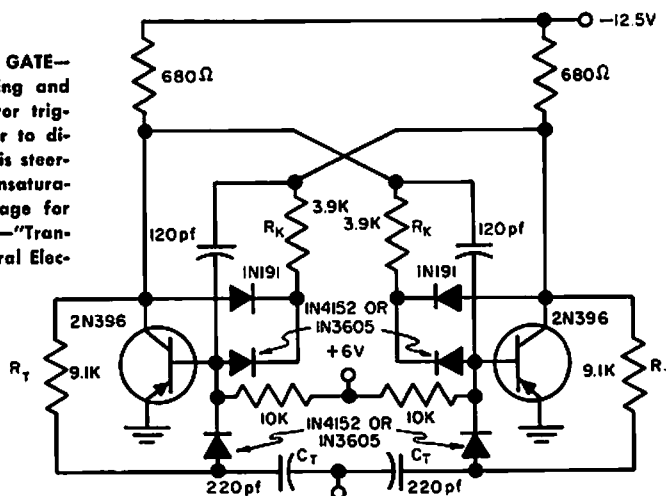


10-MC SCHMITT—Will operate as square-wave generator in range of 100 cps to 10 Mc, using 2N695, 2N705, or 2N711 mesa transistors.—P. A. McInnis, "Low-Cost Computer Circuits," Motorola Application Note AN-130, Nov. 1965.

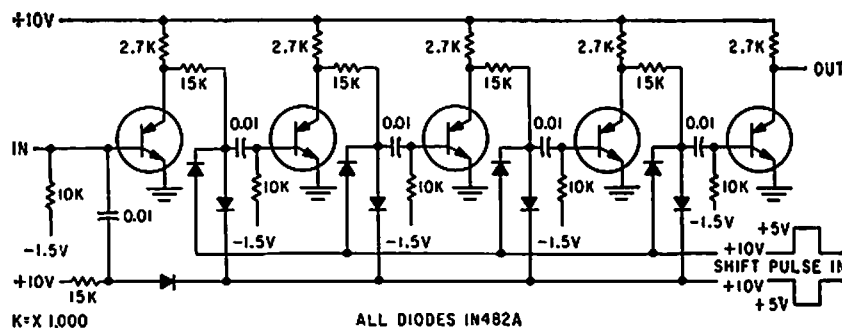


1-MC SCHMITT TRIGGER—Q1 conducts when input exceeds 6.8 v. Q2 always conducts if input is below 5.2 v. Ambient temperature range is 0 to 71°C. Output at collector has 2 v minimum level change.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 200.

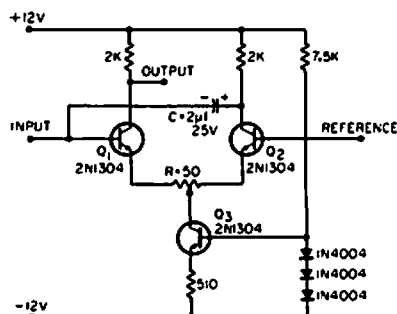
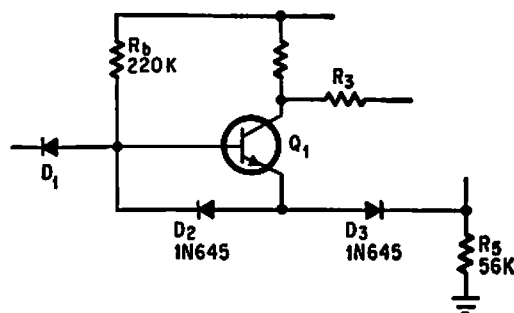
BASE TRIGGERING WITH HYBRID GATE—Combines sensitivity of base triggering and trigger amplitude variation of collector triggering. Bias potential varies in order to direct trigger pulse more effectively. This steering scheme is attractive for some nonsaturated circuits, when collector-base voltage for conducting transistor is very small.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 199.



PNPN SHIFT REGISTER—Consists of five one-bit memory elements connected in cascade.—J. B. Hangstefer and L. H. Dixon, Jr., *Triggered Bistable Semiconductor Circuits*, *Electronics*, 32:35, p 58–60.

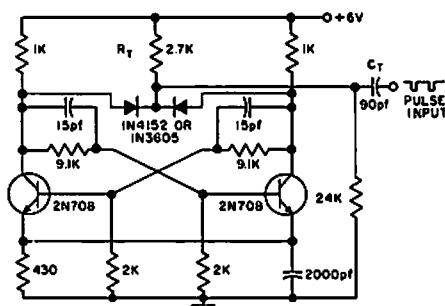
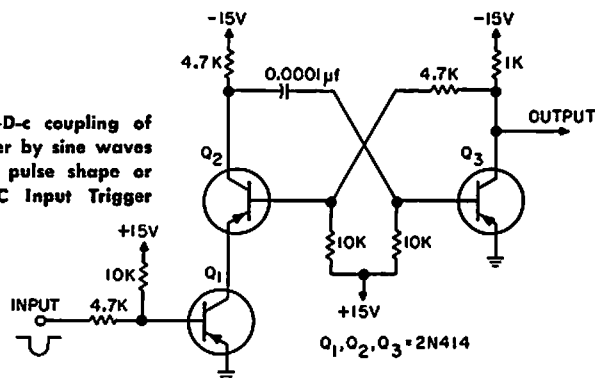


PROTECTIVE DIODES FOR SCHMITT TRIGGER—Addition of diodes D2 and D3 to modified Schmitt trigger having isolating diode D1 prevents reverse breakdown of emitter-base junction of Q1.—J. Gaen, *Diode and Resistor Increase Input Resistance of Schmitt*, *Electronics*, 39:12, p 110–111.

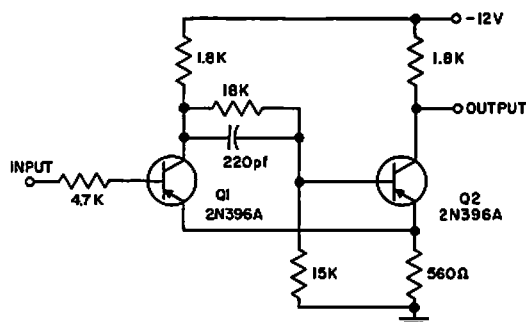


LOW-HYSTERESIS TRIGGER—Differential-amplifier pair, with constant-current source Q3 replacing emitter resistor, serves as level detector with low hysteresis. Good up to 60 kc.—D. B. Campbell, *Low-Hysteresis Trigger Circuits*, *EEE*, 13:1, p 76.

TRIGGER FOR ANY INPUT—D-c coupling of input permits triggering either by sine waves or pulses, independently of pulse shape or rise time.—P. L. Writer, *DC Input Trigger Circuit*, *EEE*, 10:9, p 29.

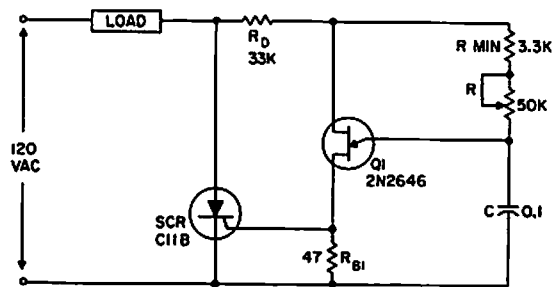


COLLECTOR-TRIGGERED FLIP-FLOP—Diodes with basic flip-flop serve as steering circuit, with negative input pulse being used to trigger collectors. Maximum trigger rate exceeds 5 Mc with trigger amplitude from 4 to 12 v.—“*Transistor Manual*,” Seventh Edition, General Electric Co., 1964, p 197.

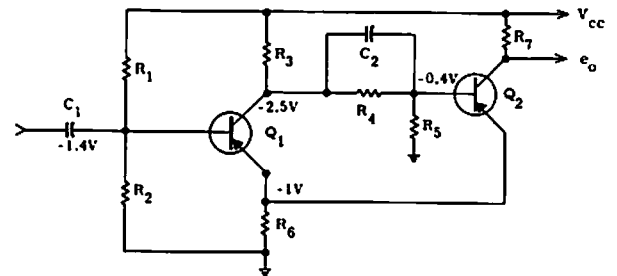


500-KC SCHMITT TRIGGER—Used for waveform restoration, signal level shifting, squaring, and d-c level detection. Q1 conducts if input is more negative than -5 v. Q2 con-

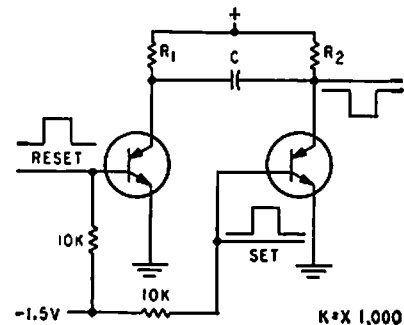
ducts when input is more positive than -2 v. Operating range is -55 to 71°C.—“*Transistor Manual*,” Seventh Edition, General Electric Co., 1964, p 200.



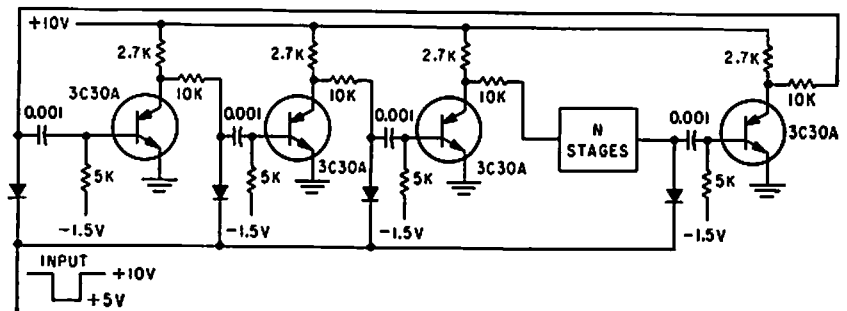
SIMPLIFIED UJT-SCR TRIGGER—Emitter timing circuit and base-2 of ujt are supplied directly from a-c line, with dropping resistor R_D keeping peak voltage on ujt within specifications.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 329.



LOW-COST BASIC SCHMITT—Developed for use with inexpensive 2N711 germanium pnp mesa switching transistors. Can serve as source of 10-Mc square waves, as pulse restorer, or as general-purpose square-wave generator.—P. A. McInnis, "Low-Cost Computer Circuits," Motorola Application Note AN-130, Nov. 1965.

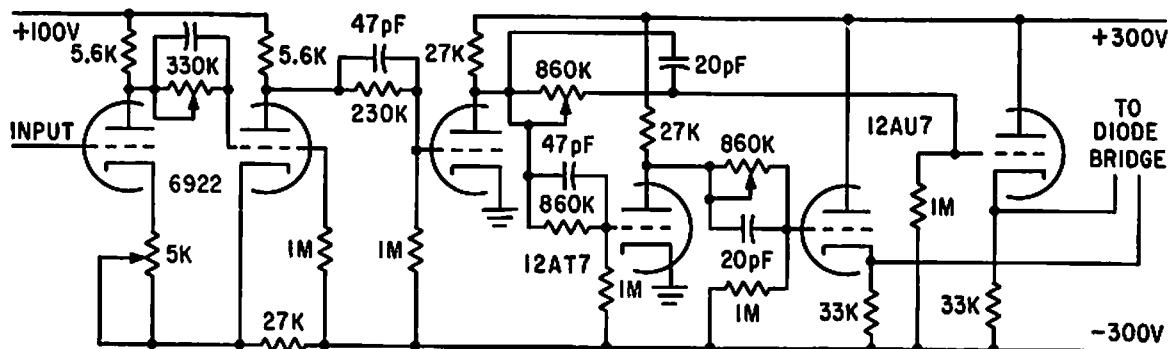


POWER FLIP-FLOP—Delivers square-wave output pulse of 1 amp when pnpn unit is triggered on by low-level positive pulse applied to its base.—J. B. Hangstefer and L. H. Dixon, Jr., Triggered Bistable Semiconductor Circuits, *Electronics*, 32:35, p 58-60.



N-STAGE RING COUNTER—Uses modified memory circuit, in which input pulse turns off all pnpn stages except that following on

stage.—J. B. Hangstefer and L. H. Dixon, Jr., Triggered Bistable Semiconductor Circuits, *Electronics*, 32:35, p 58-60.



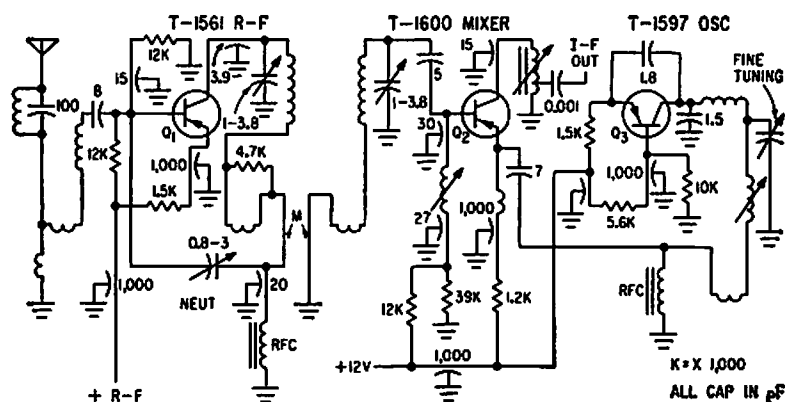
50-V PUSH-PULL PULSES FOR DIODE BRIDGE—Schmitt trigger acts as comparator about 0 v and provides input to two pulse amplifiers. Cathode followers furnish push-pull output

at low impedance necessary to drive a 6AL5 diode bridge. Permits two sample-and-hold circuits to be run as memory pair in analog computer.—T. A. Brubaker, *Precision Analog*

Memory has Extended Frequency Response, *Electronics*, 34:39, p 141-143.

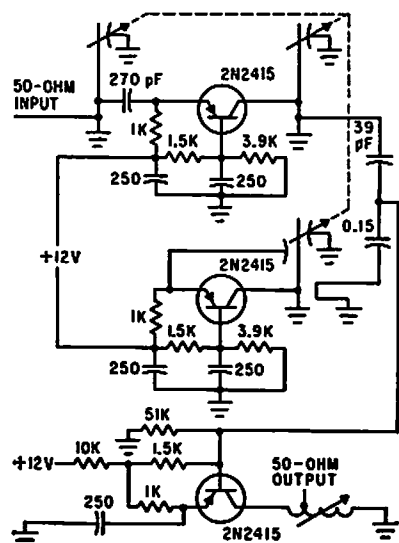
CHAPTER 96

Tuner Circuits

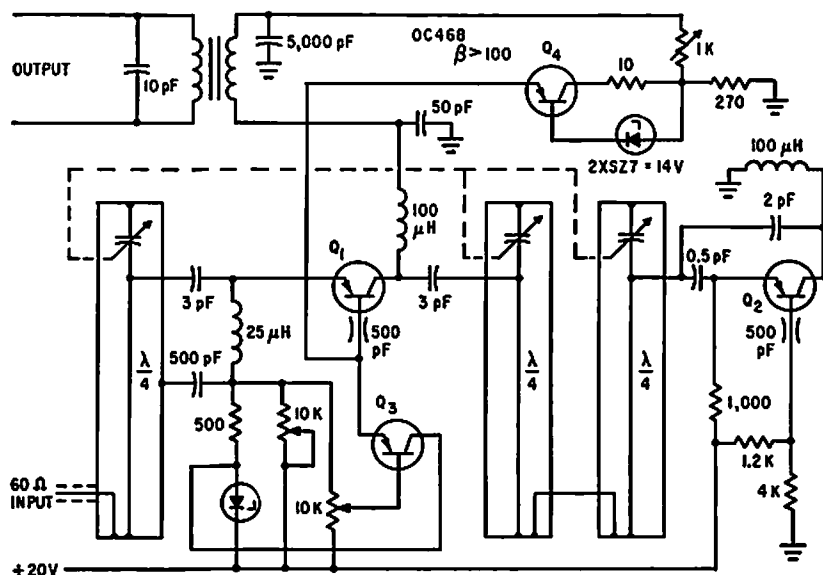


PORTABLE TV TUNER—Three transistors in vhf tuner provide 19 db power gain even for channel 13, with 12 db noise factor. V.

Mukai and P. V. Simpson, Transistorized Tuners For Portable Television, *Electronics*, 33:12, p 76-78.

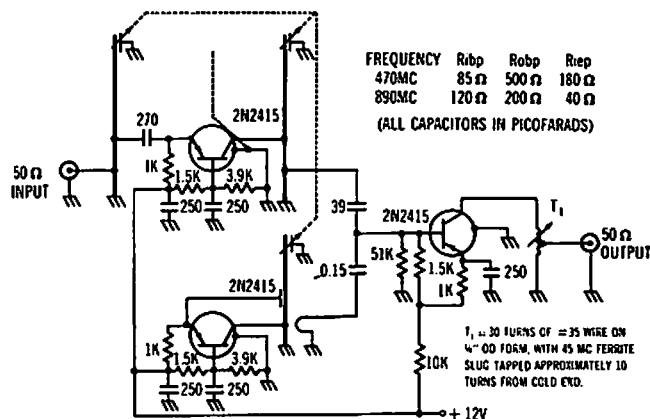
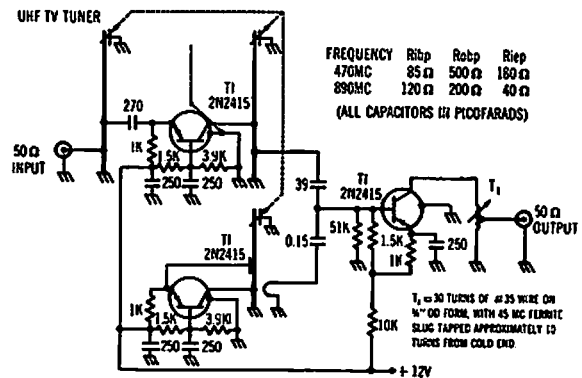
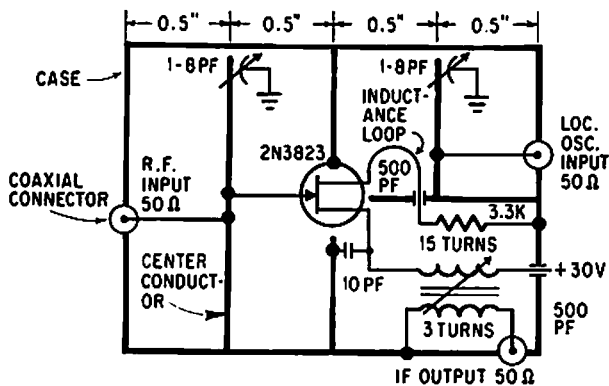
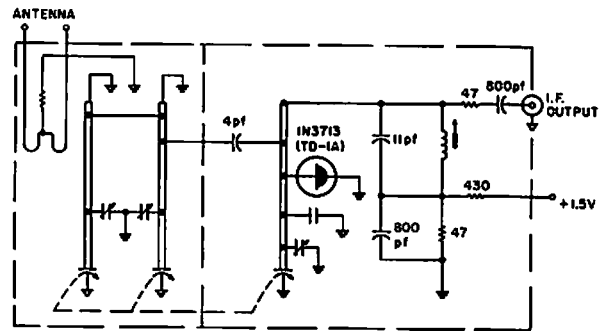
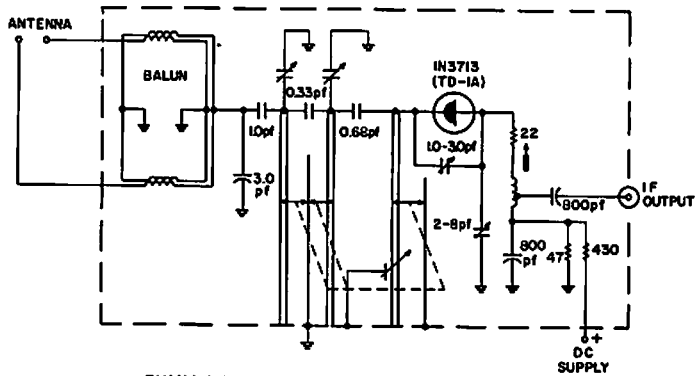


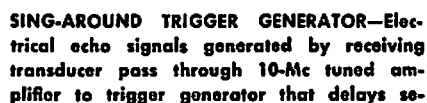
3-TRANSISTOR UHF TUNER—Conversion gain is up to 9 db and noise figure around 8 db, with drain of 18 ma at 12 v.—Transistors Provide Gain in TV Tuner, *Electronics*, 35:26, p 25.



TRANSIT-TIME DIODE UHF/SHF TUNER—Two electronically regulated voltage sources are required to control current-tuned condition and collector voltage, using transistors OC468 and TF65 with zener diodes. One

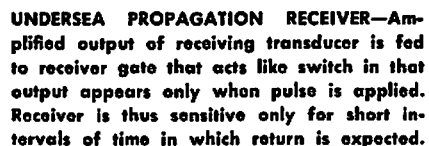
transistor operates in harmonic-generation mode as pump oscillator, comparable to parametric amplification.—U. L. Rhode, Pushing Transistors Above Their Frequency Limits, *Electronics*, 35:25, p 46-49.





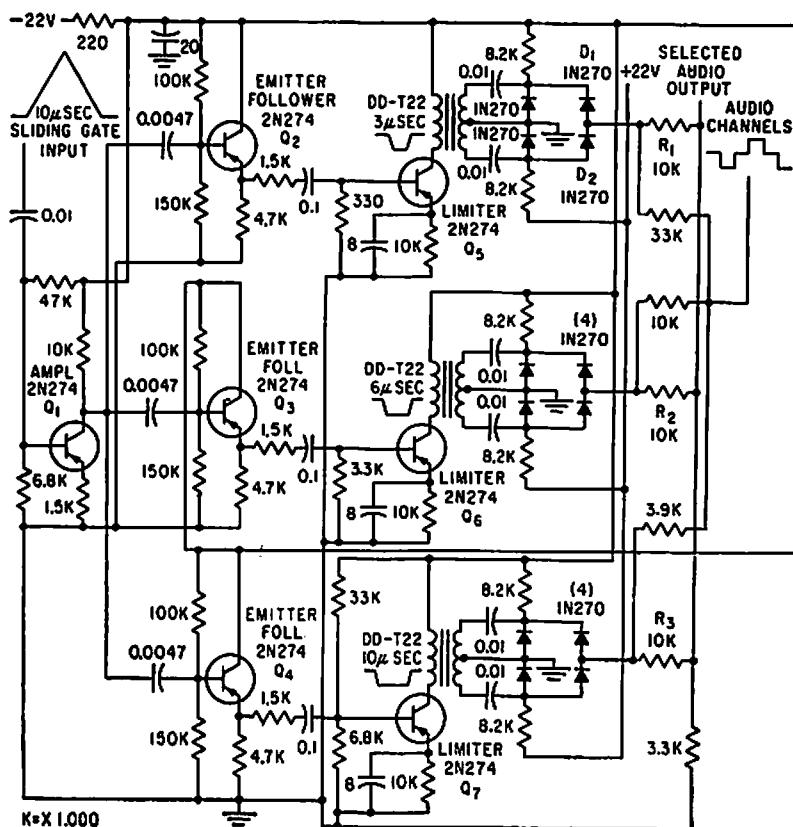
lected detected echo and combines it with undetected echo in fast series-transistor coincidence circuit to obtain trigger output pulse for transmitter of ultrasonic velocity meas-

uring system.—R. L. Forgacs, Precision Ultra-sonic Velocity Measurements, *Electronics*, 33:47, p 98-100.



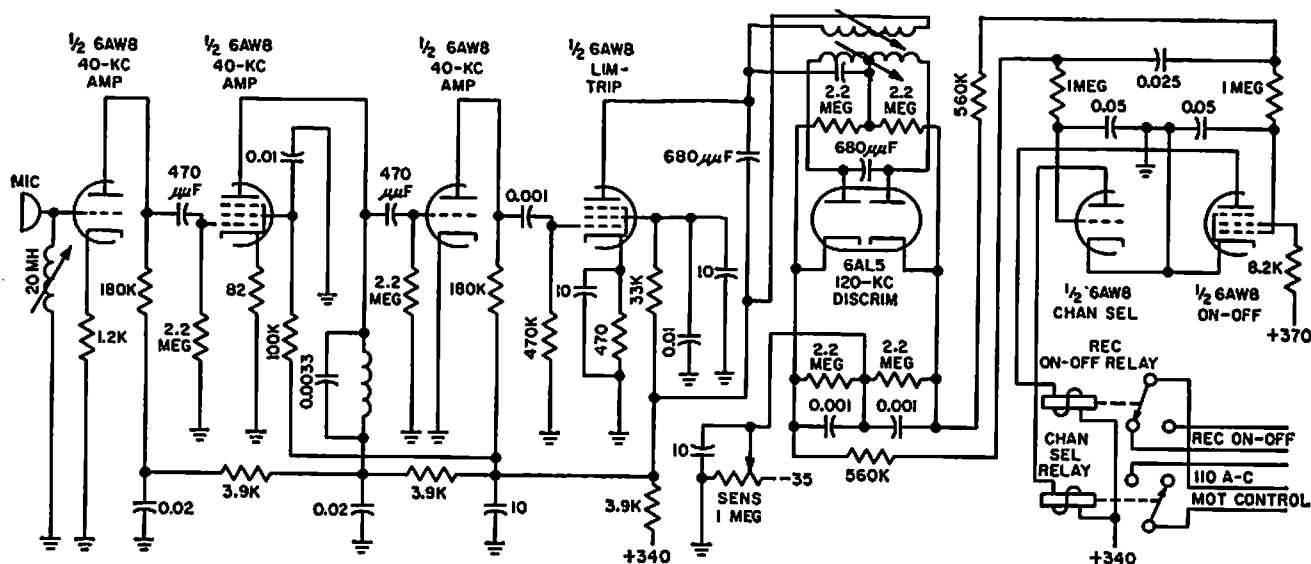
Output of receiver gate is detected by V8 and filtered to get pulse envelope for crt. Receiver pulse is also amplified by V9 and used to charge capacitor in boxcar generator, so amplitude of pulse is remembered in interval between pulses. To make boxcar

generator forget old amplitude when another pulse arrives, receiver gate is shaped into narrow pulse used to discharge capacitors through V11 just before arrival of next pulse.—W. C. Gore, Ultrasonics Tests Undersea Propagation, *Electronics*, 31:35, p 32-35.



SONAR AUDIO SELECTION GATE—Triangular sliding gate of sonar target classifier selects from channel positions the sample chosen for monitoring by sonar operator-trainee,

with smooth transition from one channel to another.—M. H. Damon, Jr., Tape Target Classifier Trains Sonar Operators, *Electronics*, 33:13, p 65-69.

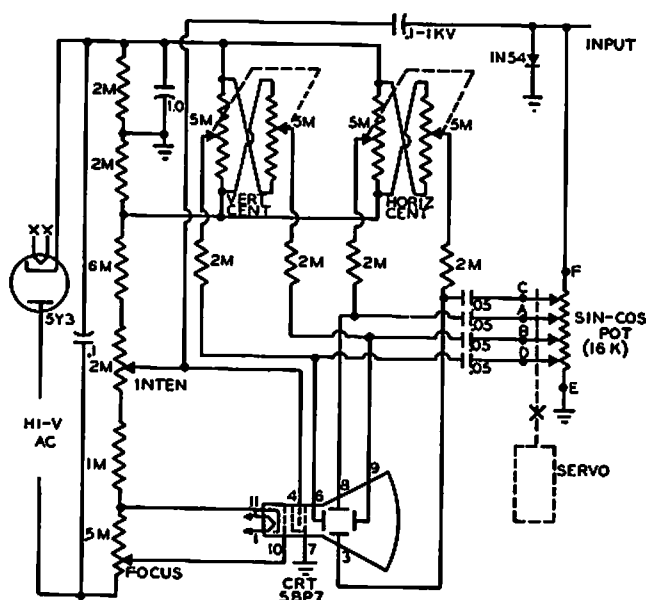
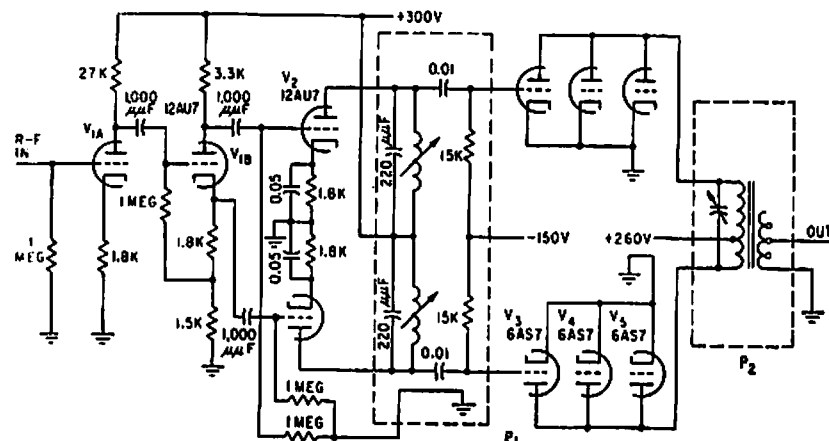


TV CONTROL RECEIVER—Uses barium titanate transducer as microphone, tuned with 20-mh coil to provide peaks at control frequencies of 38.5 and 41.5 kc. Balanced dis-

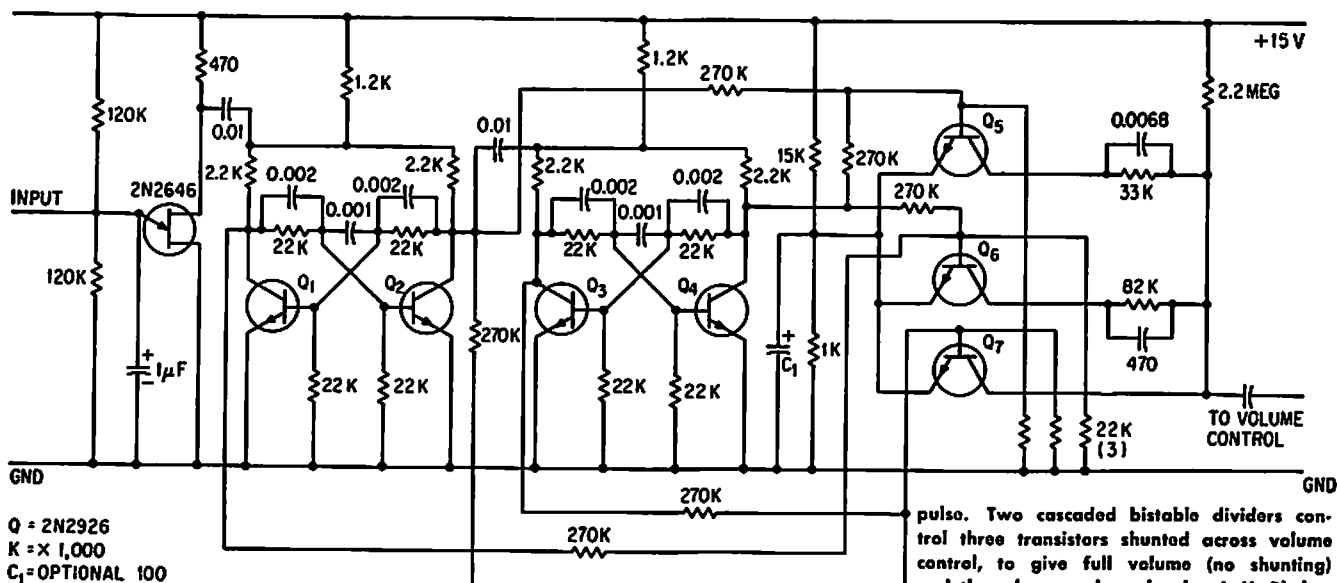
criminator detects the two ultrasonic tones. Frequency shift of continuous ultrasonic tone activates tuning-motor relay. Both audio and video are killed during tuning.

Also provides remote on-off control of power.—N. Frihart and J. Krakora, Ultrasonic Tones Select Tv Channels, *Electronics*, 31:23, p 68-69.

Develops 100-v peak signal across 100-ohm matched resistive load of crystal transducer. for mean power output of 50 w. Is fed by r-f oscillator through transmitter gate that generates voltage pulse whose width is variable from 10 to 5,000 microsec, to turn on transmitter for corresponding time. P1 and P2 are plug-in circuits that must be changed when operating frequency is changed in range of 25 to 150 kc.—W. C. Gore, *Ultrasonics Tests Undersea Propagation, Electronics*, 31:35, p 32-35.



SONAR BEARING INDICATOR—Visual indicator using ordinary cro can be synchronized with rotating directional underwater acoustic transducer, to indicate relative bearing of arriving signal from target. Transducer synchro output, proportional to bearing, drives small servo motor having standard 4-tap sine-cosine potentiometer. Detector output is applied across pot, and four vector outputs are fed to cathode-ray deflection plates. A-c voltage produces rotating bar, and diode clipping of half the signal converts bar to pointer emanating from center of screen. With target signal present, input amplitude is adjusted to produce line from center to edge of screen.—Target Bearing Indicator, "Electronic Circuit Design Handbook," Mactier Pub. Corp., N.Y., 1965, p 123.

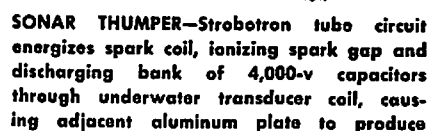
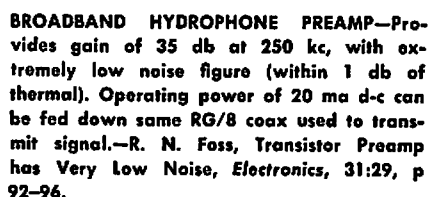


Q = 2N2926
K = X 1,000
C₁ = OPTIONAL 100

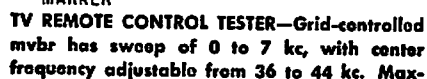
REMOTE TV VOLUME CONTROL—Ultrasonic
bursts from handheld transmitter are picked

up by microphone and fed to ujt threshold detector 2N2646 that also generates trigger

pulse. Two cascaded bistable dividers control three transistors shunted across volume control, to give full volume (no shunting) and three lower volume levels.—J. H. Phelps, *Transistors Instead of Relays Tune TV Volume*, *Electronics*, 37:9, p 32-33.

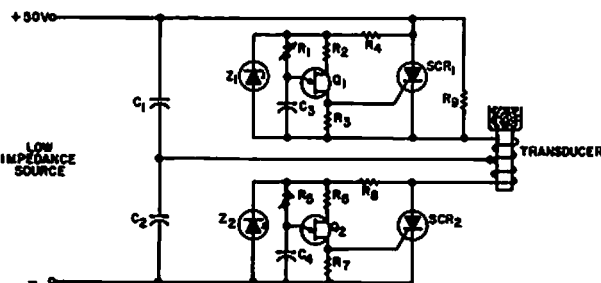


high-power sound pulse that penetrates sediment layers and bedrock for oceanographic research.—New Sonar Thumper Charts Ocean Subbottom, *Electronics*, 34:5, p 56-57.



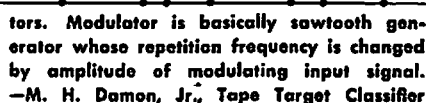
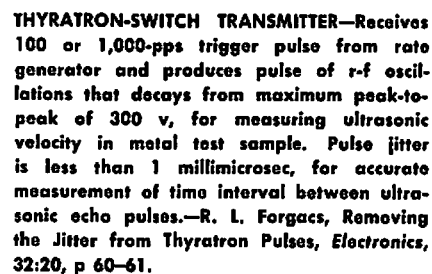
imum output is 85 v peak-to-peak. Neon-tube sweep generator operates at 1, 6, and 22 cps. Used for testing ultrasonic remote

controls.—G. Row, Sweep Generator Tests Ultrasonic Remote Controls, *Electronics*, 34:47, p 64-66.



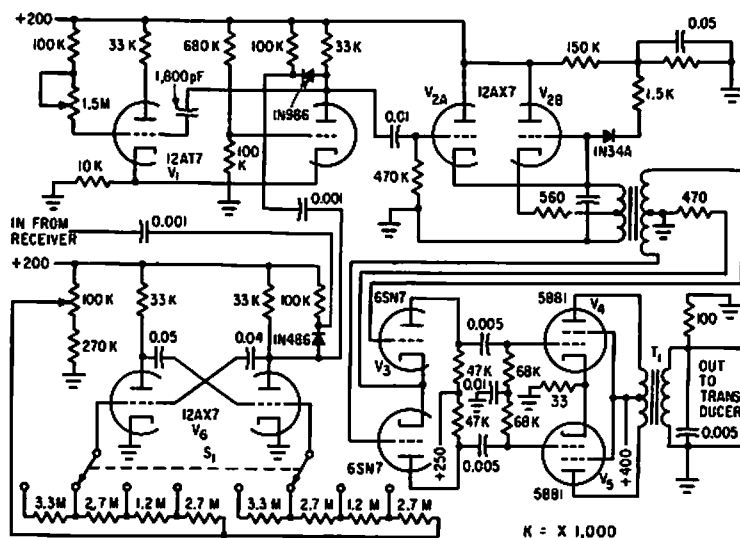
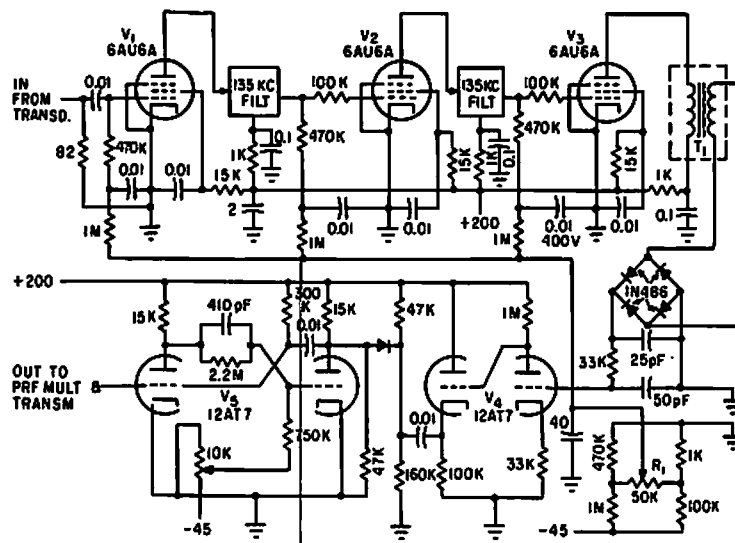
C ₉ -0.003μl, 100V PAPER	R ₄ -2.2KΩ, 2W	Q ₁ -6E 2N2847
C ₄ -0.003μl, 100V PAPER	R ₆ -25K, 2W POTENTIOMETER	Q ₂ -6E 2N2847
C ₁ AND C ₂ -0.8μl, 9E 25F330	R ₅ -100Ω, 1W	SCR ₁ -GE C200X3
R ₁ -25K, 2W POTENTIOMETER	R ₇ -100Ω, 1/2W	SCR ₂ -GE C200X3
R ₂ -150Ω, 1W	R ₈ -2.2KΩ, 2W	Z ₁ -24XL22
R ₃ -100Ω, 1/2W	R ₉ -10KΩ, 2W	Z ₂ -24XL22

Frequency-doubling mode permits use of inexpensive scr's. Load resistance is in magnetostriuctive transducer winding L. Circuit is actually 25-kc inverter triggered at 12.5-kc repetition rate.—"Silicon Controlled Rectifier Manual," Third Edition, General Electric Co., 1964, p 16



**Trains Sonar Operators, Electronics, 33:13,
p 65-69.**

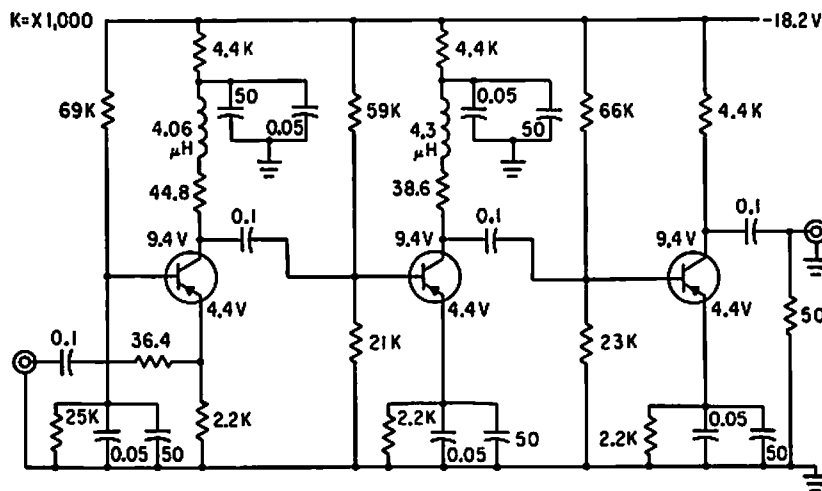
TRF RECEIVER—Operates at either 85 or 135 kc. Input impedance is 72 ohms to match transducer. Detected pulse of rectifier bridge is amplified in direct-coupled amplifier V4 and differentiated at its output, to trigger one-shot mvbr V5, which in turn triggers transmitter and prf multiplier in computing circuitry.—H. F. Messias, *Ultrasonics Measures Flow Velocity of Rivers, Electronics*, 34:41, p 56-59.



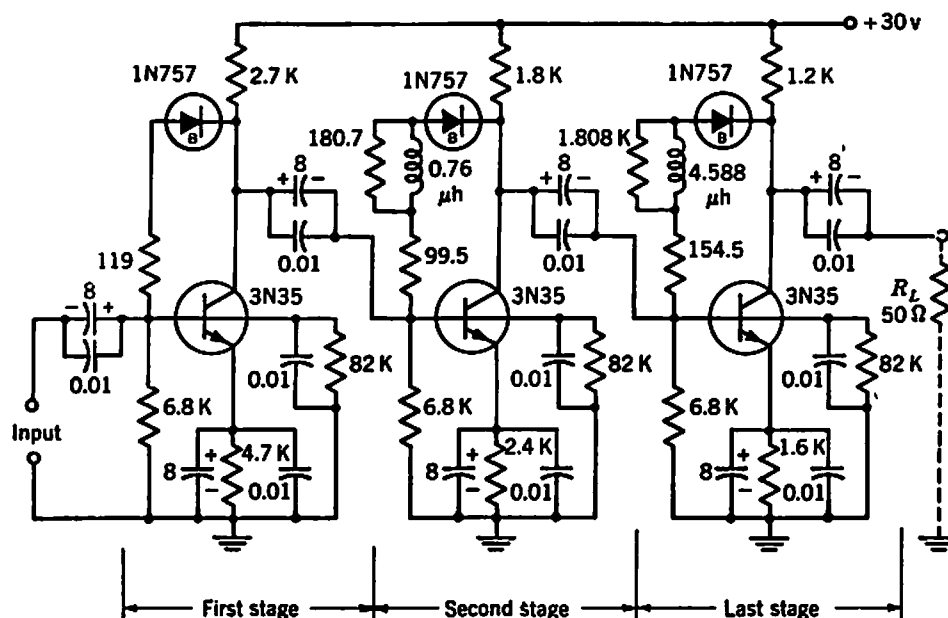
PULSED 30-W TRANSMITTER—Uses pulsed Hartley oscillator operating at either 85 or 135 kc, followed by push-pull driver amplifier, push-pull power amplifier, and power amplifier that feeds 72-ohm transducer through step-down transformer Q1. Oscillator operates for 1-millisecond period controlled by V1, which in turn is triggered by negative pulse coming from receiver through free-running monvibr V6.—H. F. Messias, *Ultrasonics Measures Flow Velocity of Rivers*, *Electronics*, 34:41, p 56-59.

CHAPTER 98

Video Circuits



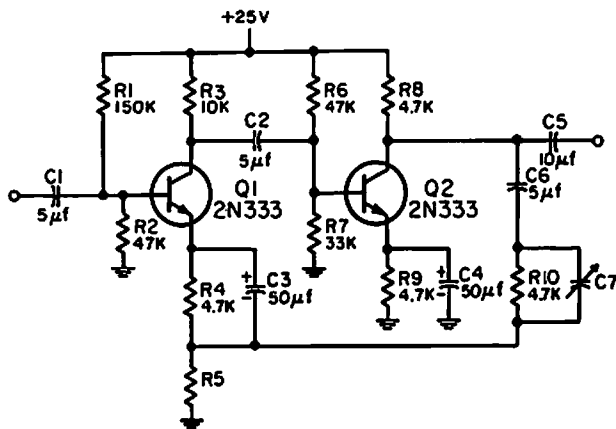
CASCADED SHUNT-PEAKED STAGES—Design procedure is given for n identical one-pole stages. Bandwidth of total cascaded amplifier is equal to bandwidth of single stage multiplied by shrinkage factor of 0.64 for two stages, 0.51 for three, and 0.44 for four. Two-stage example shown gives gain of 8.5 and bandwidth of 2.1 Mc.—R. S. Pepper and D. O. Pedersen, *Designing Shunt-Peaked Transistor Amplifiers*, *Electronics*, 33:49, p 68-70.



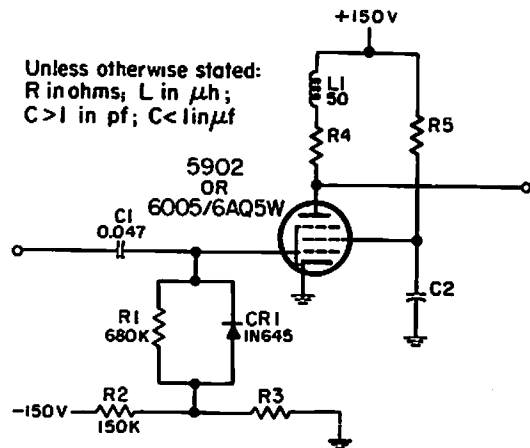
SILICON WIDEBAND VIDEO AMPLIFIER—Employs feedback around each of its three stages, with zener diode for stabilizing collector-emitter voltage. Voltage and current

amplification are 20 db, and useful frequency range is 3.2 kc to 32 Mc.—Texas Instruments

Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 267.



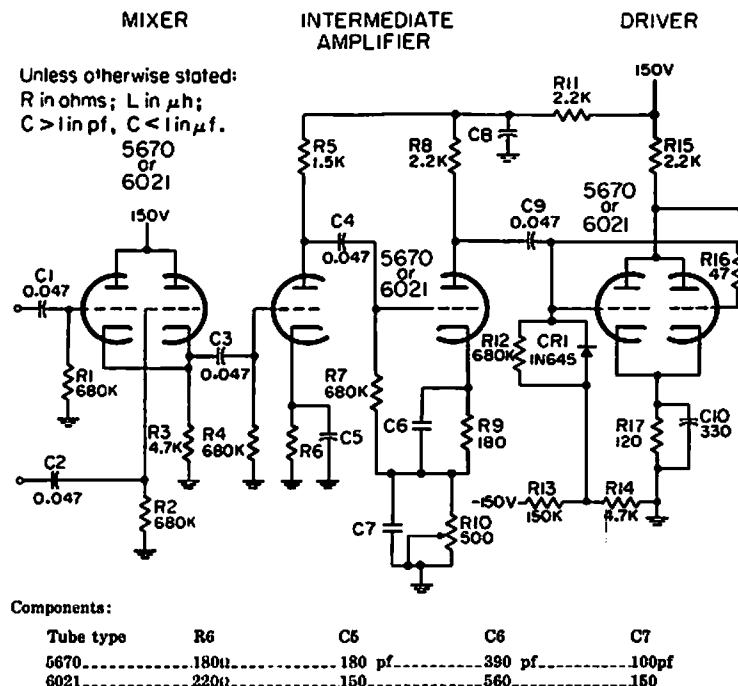
PREFERRED LOW-LEVEL AMPLIFIER—Serves as high-gain amplifier providing stable gain over wide temperature range, with maximum output of 2 v. Is noninverting, has input impedance of 20,000 ohms, and will operate into loads above 10,000 ohms. Several circuits may be cascaded. 2N333 has been dropped from Preferred List, but 2N335 can be used if operating point is adjusted for its larger beta. Voltage gains of 45, 20, or 10 are obtained for $R_5 = 100, 220$, and 470 ohms respectively.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 18 (originally PC 201), p 18-2.



Components:

	6005, 6AQ5W	5902
R3	10K Ω	18K Ω
R4	2.2K Ω	2.7K Ω
R5	0	47K Ω
C2	not used	0.1 to 2.0 μ f

PREFERRED BEAM-POWER DRIVER—Used in search radars to amplify video signals to 60-v level required for intensity modulation of cathode-ray indicator. Input polarity is positive. Amplification is 7.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 28, p 28-2.



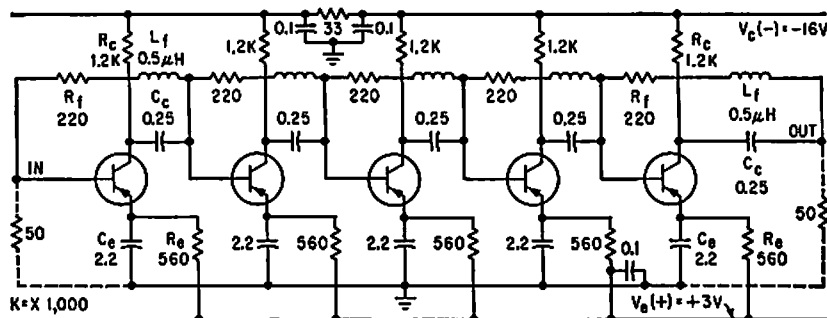
Components:

Tube type	R6	C5	C6	C7
5670	180 Ω	180 pf	390 pf	100pf
6021	220 Ω	150	560	150

PREFERRED VIDEO AMPLIFIER CHAIN—Designed for use in radar display system to mix positive radar video with positive marker pulses, to invert combined signals, and to

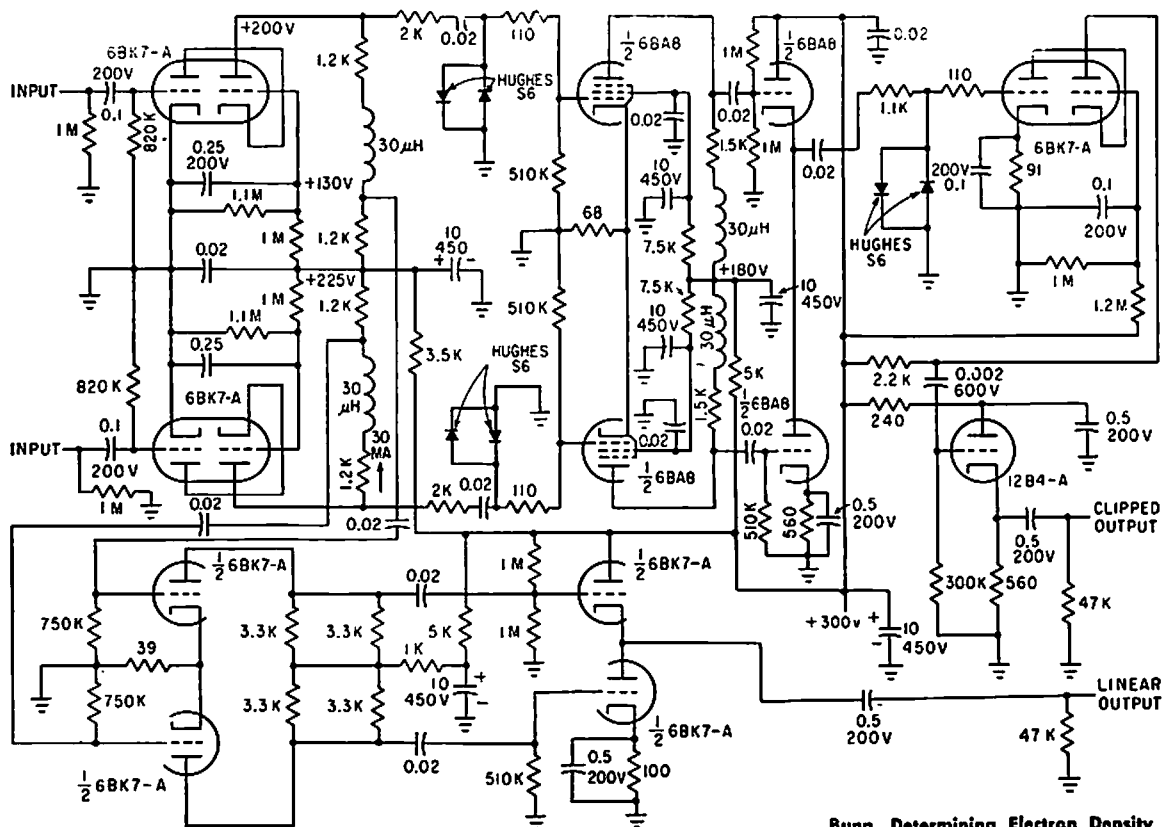
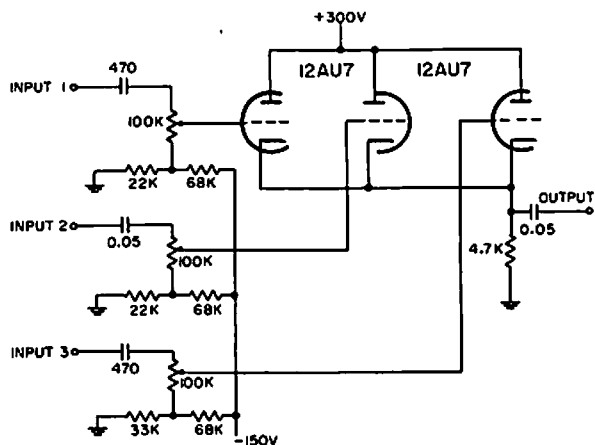
amplify them sufficiently to intensity-modulate cathode-ray indicator. Input polarity is negative. Maximum peak amplitude is 60 v. Amplification is variable from 30 to 60.—

NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. I, Electron Tube Circuits, 1963, PC 25, p 25-2.



100-MC-BANDWIDTH VIDEO AMPLIFIER—Shunt feedback networks around each stage reduce overall gain at low frequencies, trading gain for bandwidth, so that five cascaded stages give overall gain of 50 db. —J. C. de Broekert and R. M. Scarlett, Transistor Amplifier has 100 Megacycle Bandwidth, *Electronics*, 33:16, p 73-75.

TRIPLE-INPUT VIDEO MIXER—Each grid is biased to cutoff, so mixer accepts only positive-polarity pulses having sufficient amplitude to overcome this bias. —NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-2.

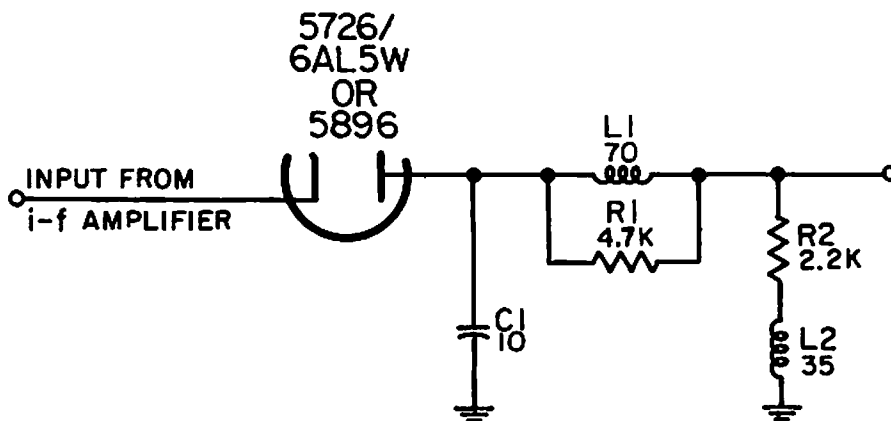
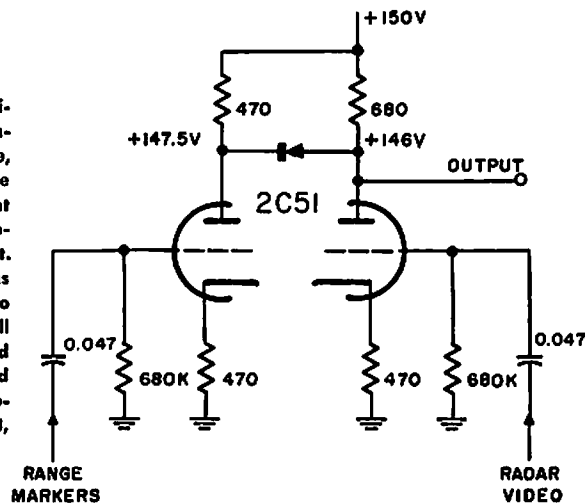


PUSH-PULL CLIPPER—Accepts balanced output of video preamp in microwave inter-

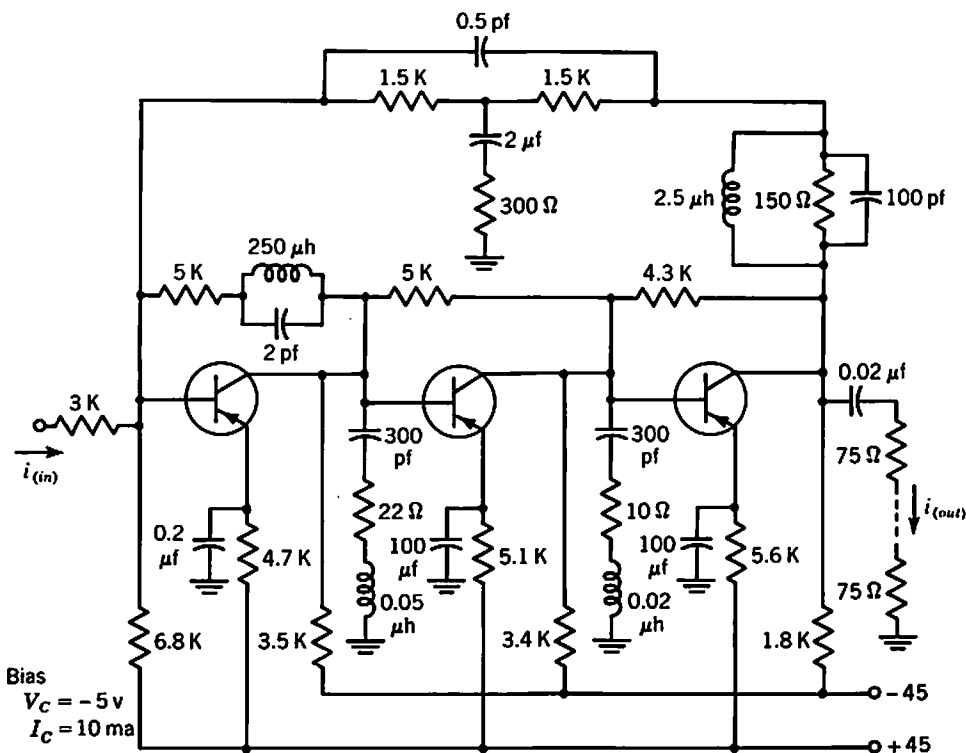
ferometer system and provides both linear and clipped outputs for oscilloscope. —H. L.

Bunn, Determining Electron Density and Distribution in Plasmas, *Electronics*, 34:14, p 71-75.

PARTIAL-ADDING MIXER—Semiconductor diode connected between plates in nonconducting direction, with 1.5 v back voltage, reduces additive factor of common-plate mixer. Marker input must have sufficient amplitude to overcome diode back-bias before marker signal can appear at output. Positive radar video at input appears as negative output. At coincidence, radar video biases diode to extent that only small amount of marker pulse can pass and add to radar video.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-9.



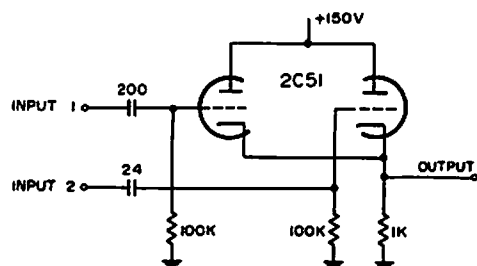
PREFERRED VIDEO DETECTOR—For demodulating pulse-modulated i-f signals in range between 20 and 70 Mc. Video output pulses are negative, with 40 nsec rise time and 70 nsec fall time.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 20, p 20-2.



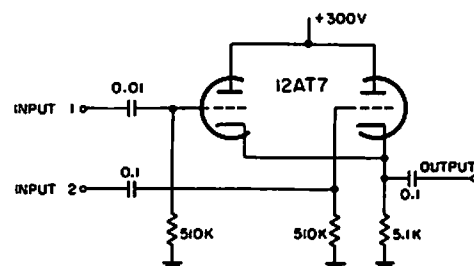
DIFFUSED-BASE GERMANIUM VIDEO AMPLIFIER—Use of standard stability criteria for wideband amplifiers gives current gain of 34 db up to 50 Mc when using any diffused-

base germanium mesa transistor similar to 2N2415. D-c biasing uses both series and shunt feedback to each stage, enabling circuit bandwidth to be extended to d-c if nec-

essary.—Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 268.

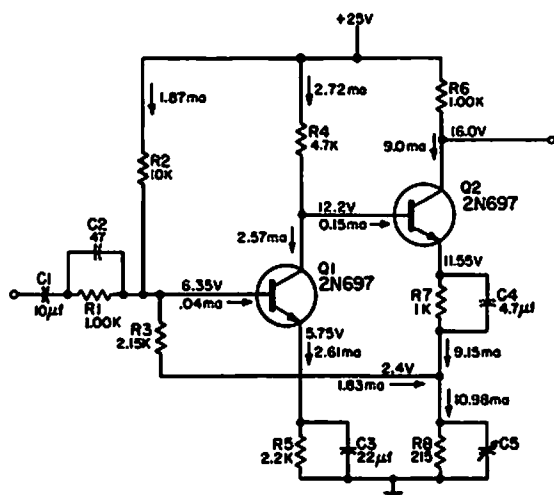
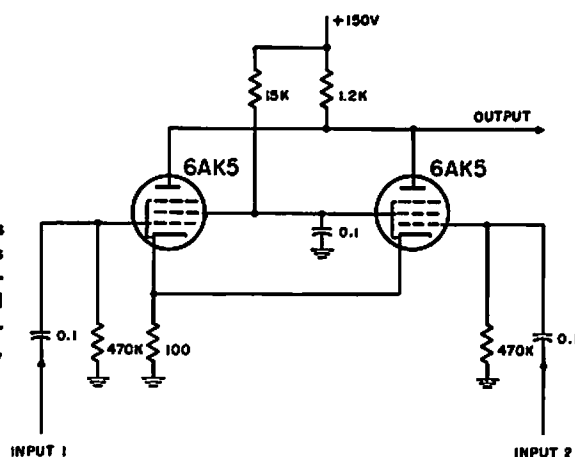


RANGE STROBE MARKER MIXER—Common-cathode dual-triode video mixer is used for combining two positive-polarity radar range strobe markers.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-2.

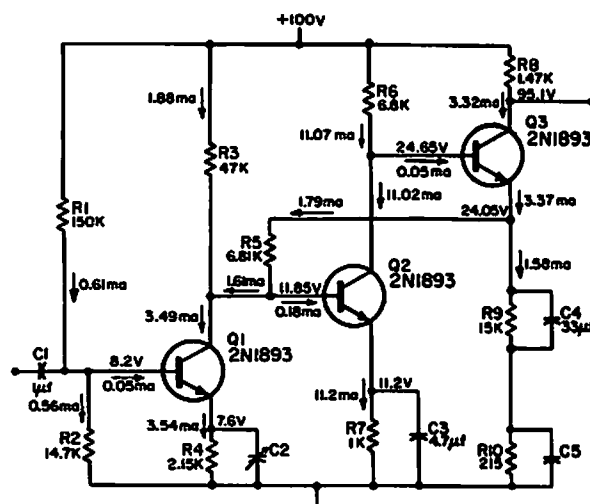


MARKER-IFF MIXER—Combines 9-v positive markers with iff signals from 2 to 10 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-2.

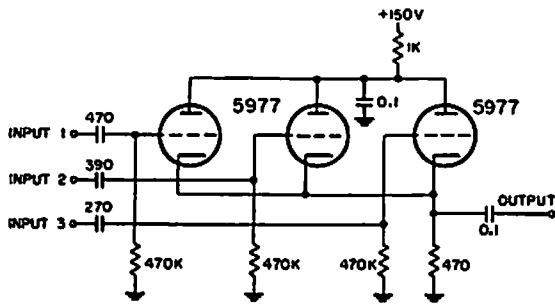
PENTODE COMMON-PLATE MIXER—Circuit is good adder for coincident inputs. Operates best with positive input pulses and negative-going output.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-7.



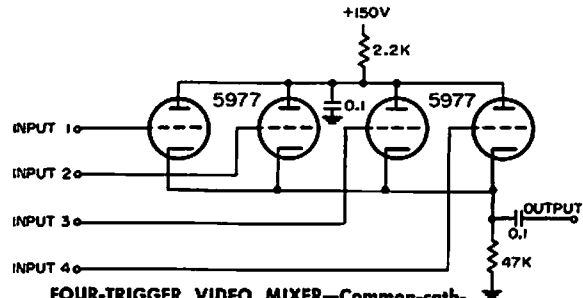
PREFERRED INTERMEDIATE-LEVEL AMPLIFIER—Is noninverting linear pulse voltage amplifier. May follow radar second detector. Minimum bandwidth is 3 Mc and maximum output 6 v. Signal polarity is positive input and positive output.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 19 (originally PC 219), p 19-2.



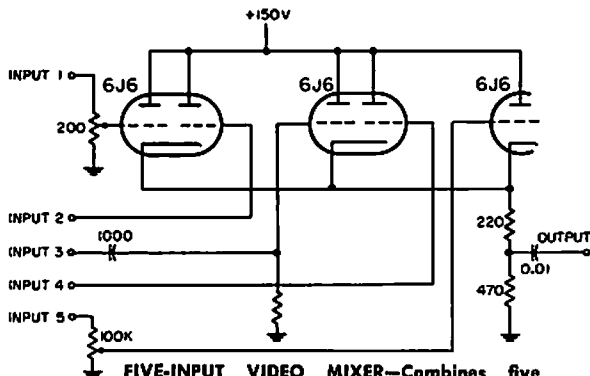
PREFERRED HIGH-LEVEL AMPLIFIER—Linear pulse voltage amplifier, designed primarily as crt intensity modulation device, has minimum bandwidth of 3 Mc. Takes positive input pulses and gives maximum negative output of 55 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, PSC 20 (originally PC 220), p 20-2.



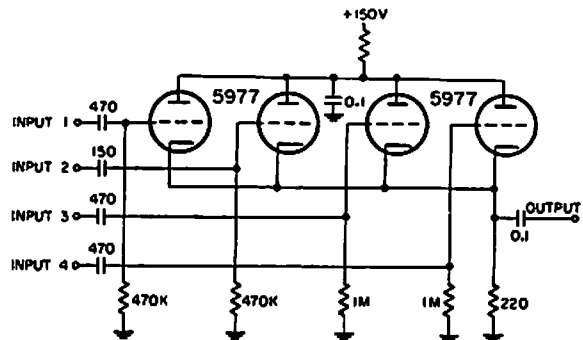
THREE-INPUT VIDEO MIXER—Used in radar systems for combining any three of the following: radar video, beacon, range markers, range strobe, and azimuth markers.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-1.



FOUR-TRIGGER VIDEO MIXER—Common-cathode arrangement with 47K cathode resistor allows nonadditive mixing of four positive trigger pulses having amplitudes in vicinity of 50 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-1.

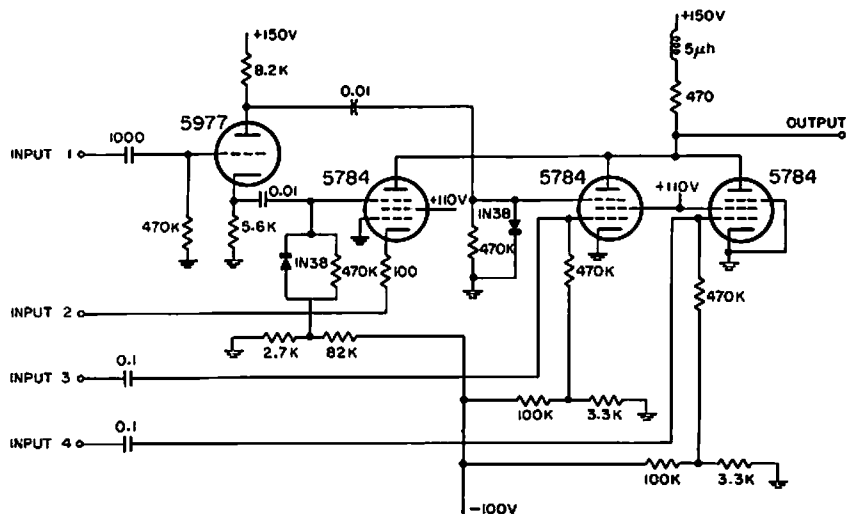


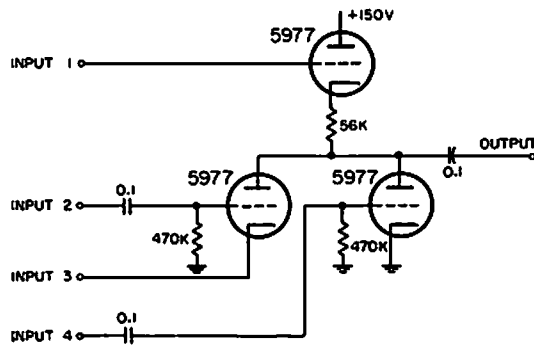
FIVE-INPUT VIDEO MIXER—Combines five radar marker inputs.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-2.



FOUR-INPUT VIDEO MIXER—Used for combining four different positive-polarity marker pulses in radar system.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-1.

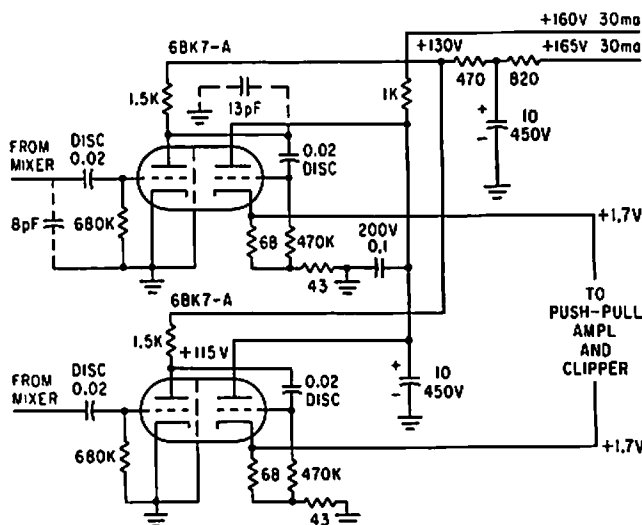
THREE-PENTODE MIXER—Triode is used as phase splitter. Both positive and negative signals are combined from four inputs. High-frequency compensation is used in common plate circuit of pentodes.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-5.





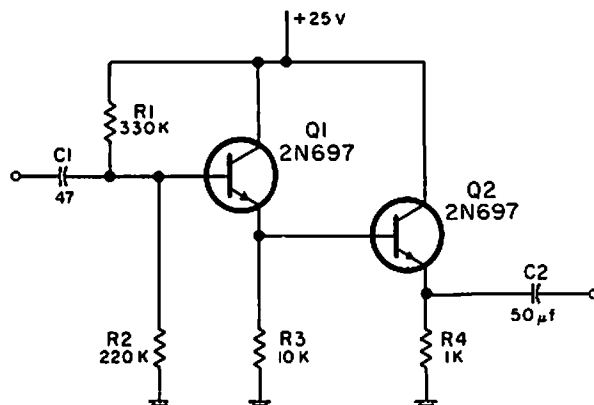
FOUR-INPUT HIGH-LEVEL PULSE MIXER—Triode in series with plate load of mixer provides for additional blanking pulse.—NBS,

"Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-3.



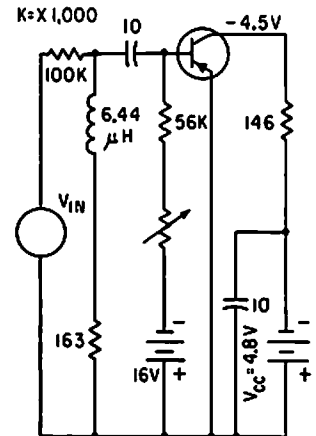
VIDEO PREAMP—Accepts output of balanced mixer of microwave interferometer system. Bandwidth is 3 Mc.—H. L. Bunn, Determining

Electron Density and Distribution in Plasmas, Electronics, 34:14, p 71-75.

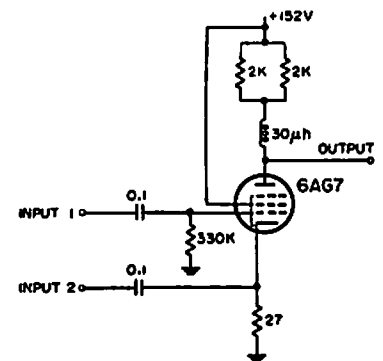


PREFERRED PULSE EMITTER-FOLLOWER—Two-stage cascaded emitter-follower is intended primarily as video line driver for positive pulses. Will drive load impedances as low as 50 ohms. Input impedance is about 80,000 ohms in parallel with 25 pf. May be modified for negative inputs by replacing Q1 and Q2 with complementary pnp types and

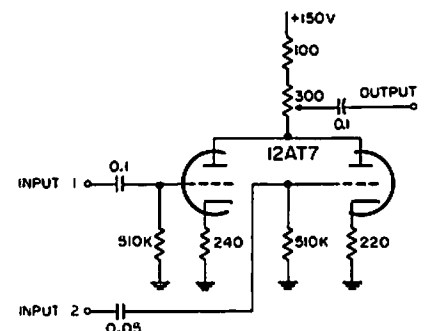
reversing polarity of collector supply. Voltage amplification is 0.975 and power gain is 30 db.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. II, Semiconductor Device Circuits, 1962, PSC 21 (originally PC 221) p 21-2.



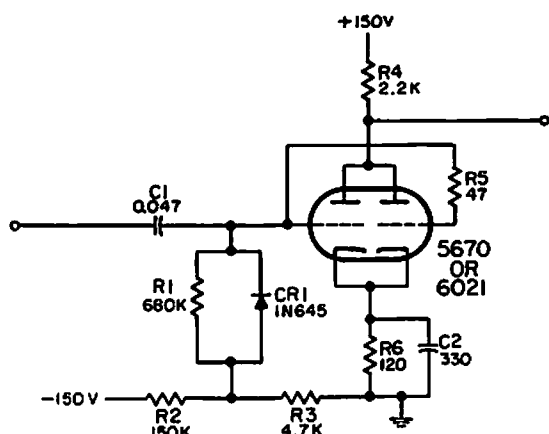
SHUNT-PEAKED INTERSTAGE—Pole-zero cancellation design procedure for using shunt peaking gives simple cascaded broadband video amplifier. Gain is 10.4 and bandwidth is 1.05 Mc.—R. S. Popper and D. O. Pederson, Designing Shunt-Peaked Transistor Amplifiers, Electronics, 33:49, p 68-70.



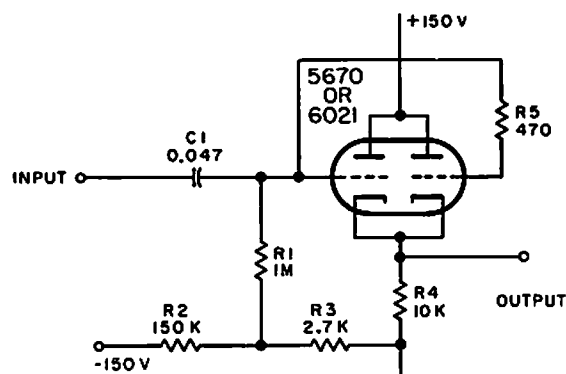
PENTODE MIXER—Negative video plus iff signals are inserted at grid, while range strobo, from cathode output of blocking oscillator, is applied to cathode.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-4.



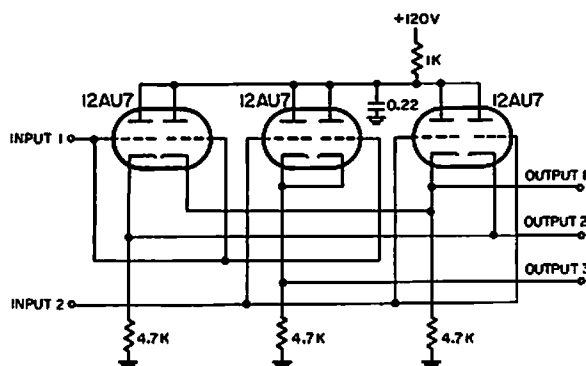
COMMON-PLATE TRIODE MIXER—Has two inputs, for combining mixed markers and radar video. Cathode resistors are unby-passed, for gain stabilization.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-3.



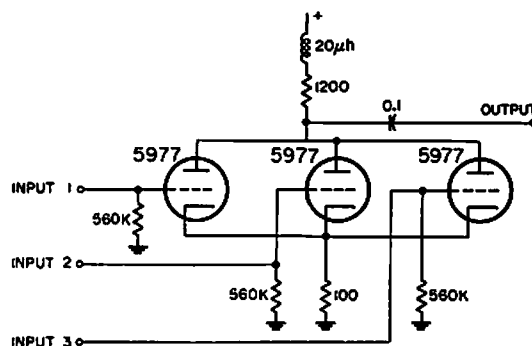
PREFERRED TRIODE DRIVER—Used to amplify video signals for intensity modulation of cathode-ray tube. Accepts positive inputs and gives negative output. Amplification is 5.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 27, p 27-2.



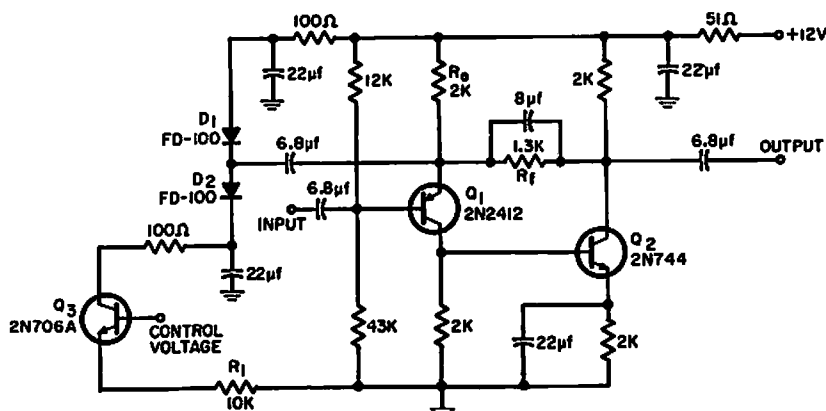
PREFERRED LOW-LEVEL PULSE CATHODE FOLLOWER—Used to couple output of low-level video stage to resistive load in applications where high-duty-factor signal makes direct coupling desirable.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 22, p 22-2.



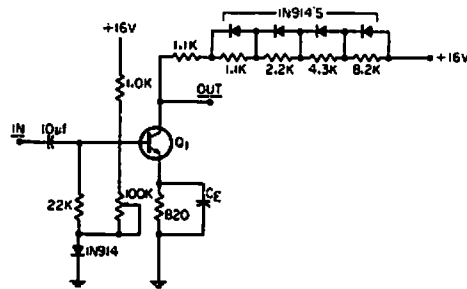
TWO-INPUT THREE-OUTPUT MIXER—Combines two inputs and distributes them to each of three independent outputs, which are connected to separate indicators.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-3.



THREE-INPUT IFF MIXER—Common-plate connection serves for combining three iff signals. Common cathode resistor provides some degeneration.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-3.

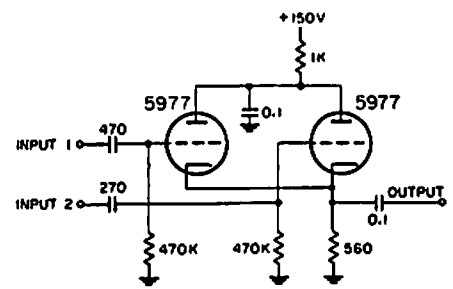


VOLTAGE-CONTROLLED GAIN—Silicon diodes serve to vary gain of wideband video amplifier over range from 2 to 32 db, with bandwidth remaining almost constant at 12 Mc and input impedance constant at 10K. —R. S. Hughes, *A Wideband Video Amplifier with Variable Gain*, *EEE*, 12:8, p 54-55.



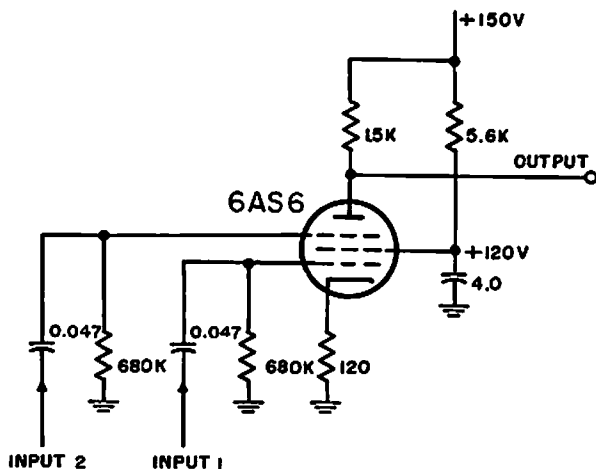
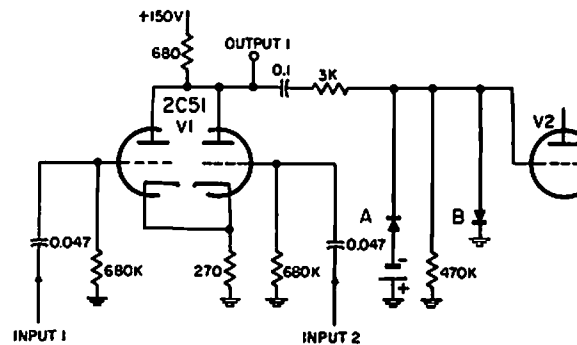
COMPRESSION AMPLIFIER—Single transistor serves as compression amplifier having 50 db dynamic range, for nonsaturating amplification of widely ranging video signals. Provides minimum output of 1 v for 20 mv input, but does not saturate with 6 v input.

Circuit gain is minimum of 1 and maximum of 15. Two circuits are cascaded in actual application.—R. W. Cotterman, One Transistor, 50 Db Dynamic Range Compression Amplifier, *EEE*, 13:5, p 46.

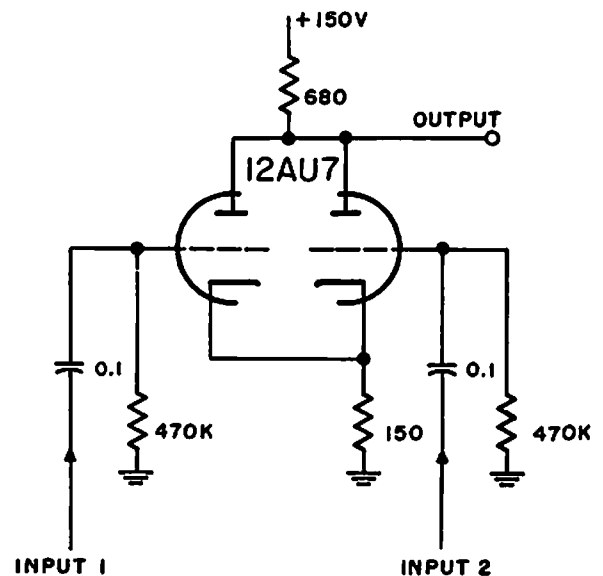


TWO-INPUT MIXER—Used to combine range and heading markers in radar system.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-1.

MIXER-LIMITER—Common-plate mixer uses diode-limiting coupling circuit to nullify adding feature. Bias voltage on diode sets limiting level.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-8.

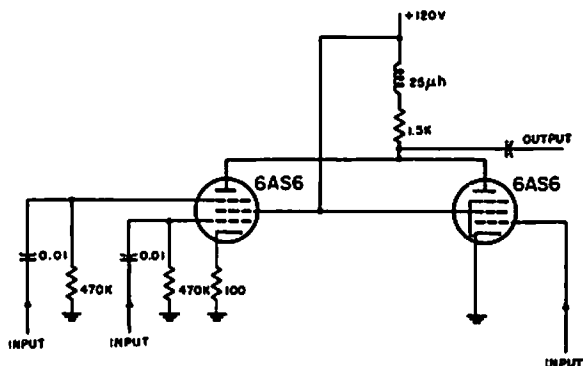
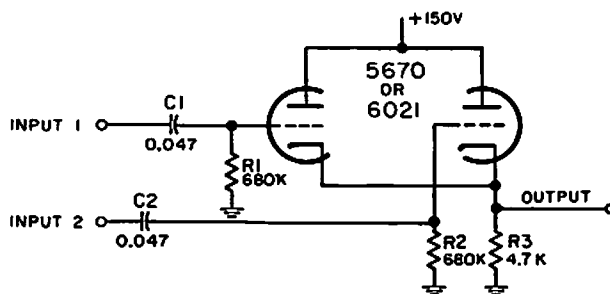


DUAL-GRID PENTODE MIXER—Uses 6AS6, in which suppressor grid has cutoff characteristic similar to control grid. Pulses of higher amplitude, such as markers, should be impressed on suppressor grid, since its transconductance is about one-fourth that of control grid. Chief drawback is need for large screen bypass capacitor.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-9.

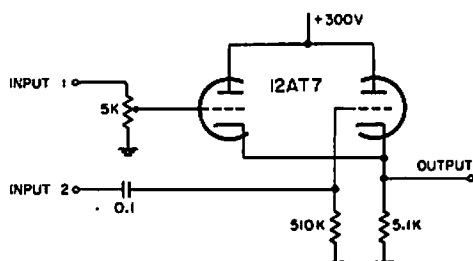


TRIODE COMMON-PLATE MIXER—Is good adder for coincident inputs. Proves unity gain. Generally preferred to pentode common-plate mixers.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-8.

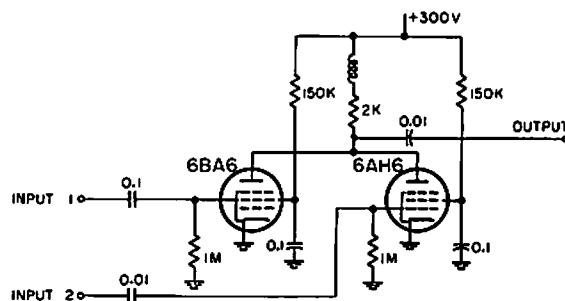
PREFERRED COMMON-CATHODE MIXER—Combines video signals and pulses from two inputs, as for radar video, beacon signals, range markers, range strobes, and azimuth markers. Mixer is nonadditive and noninverting, can handle fast rise times, but amplification is less than unity and it cannot handle negative inputs.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, PC 23, p 23-2.



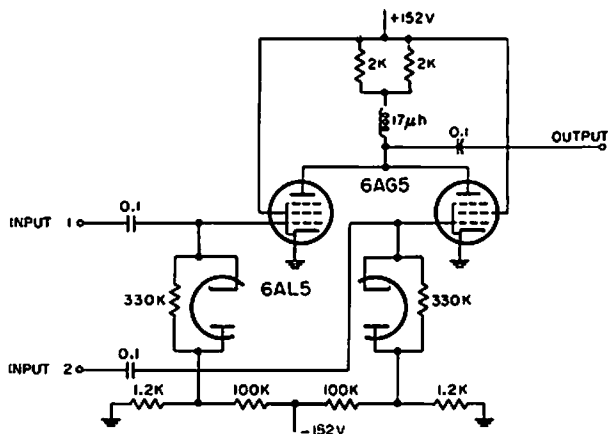
THREE-INPUT TWO-PENTODE MIXER—Distance markers and iff signals are inserted at separate grids on one tube, while radar video from input 3 is impressed on control grid of other pentode.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-4.



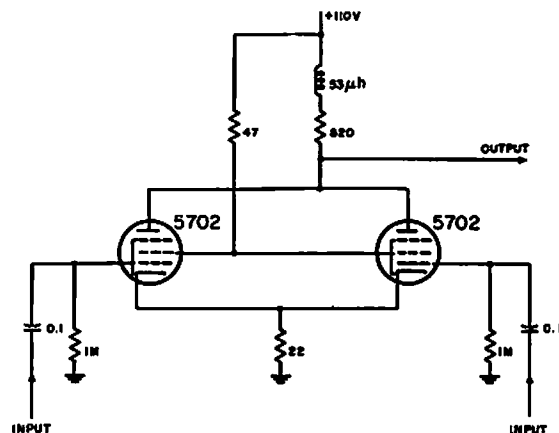
RANGE STROBE MARKER-IFF MIXER—Combines positive-polarity markers with iff signals, to give 8-v positive output.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-2.



RADAR AND BEACON MIXER—Two pentodes combine two inputs into single output.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-5.



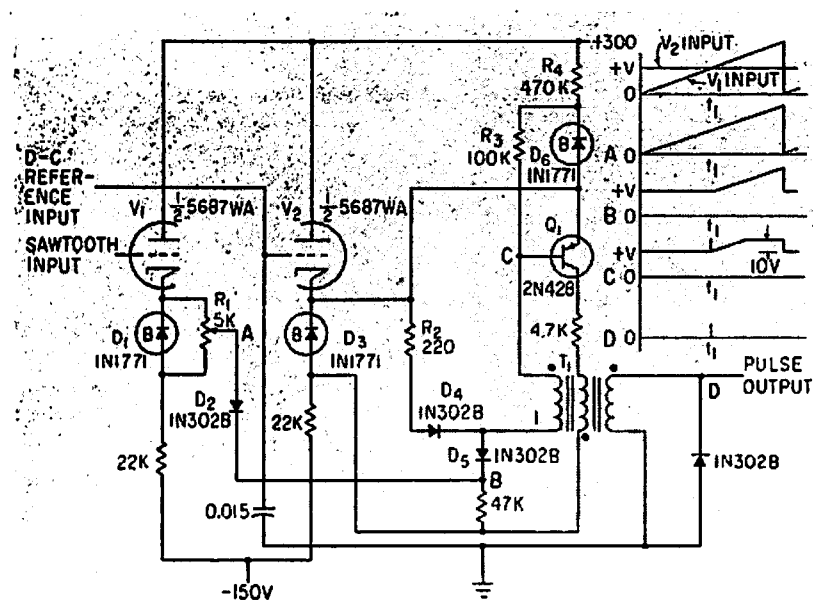
TWO-PENTODE RADAR AND IFF MIXER—Each grid is clamped by diode to establish base line of positive input pulse at -1.8 v.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-5.



TWO-PENTODE MIXER—Combines range markers and radar video. Compensated load improves high-frequency response.—NBS, "Handbook Preferred Circuits Navy Aeronautical Electronic Equipment," Vol. 1, Electron Tube Circuits, 1963, p N4-4.

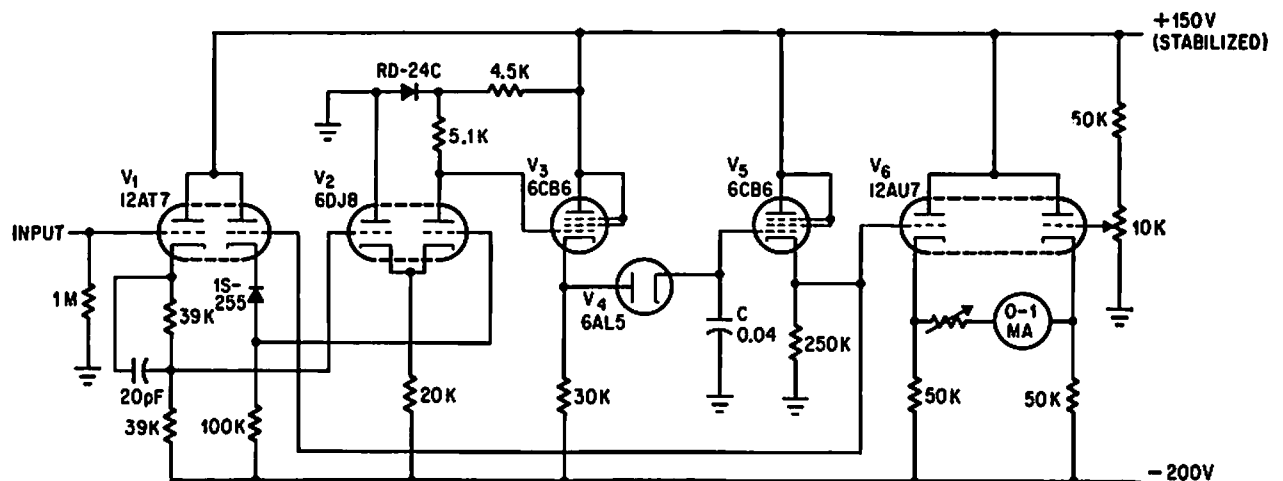
CHAPTER 99

Voltage Measuring Circuits



D-C VOLTAGE COMPARATOR—Compares long-duration sawtooth input with d-c reference and generates pulse when inputs

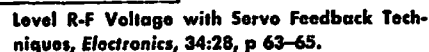
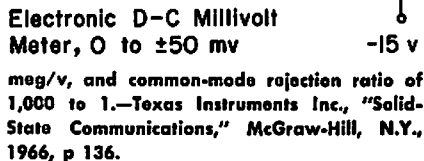
coincide.—R. D. Valentine, D-C Voltage Comparator Circuit Uses Tube and Transistor, *Electronics*, 34:24, p 66.

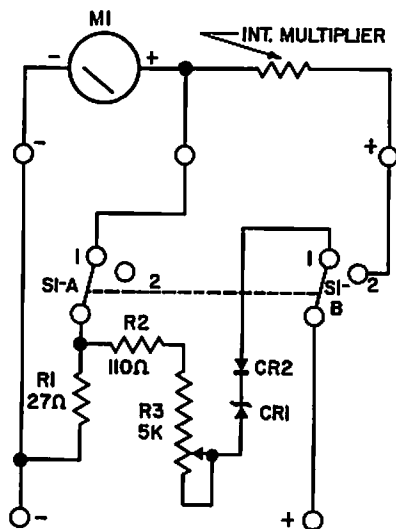


PEAK VOLTmeter FOR NARROW PULSES—Addition of dual-triode amplifier V2 to conventional peak voltmeter reduces charging

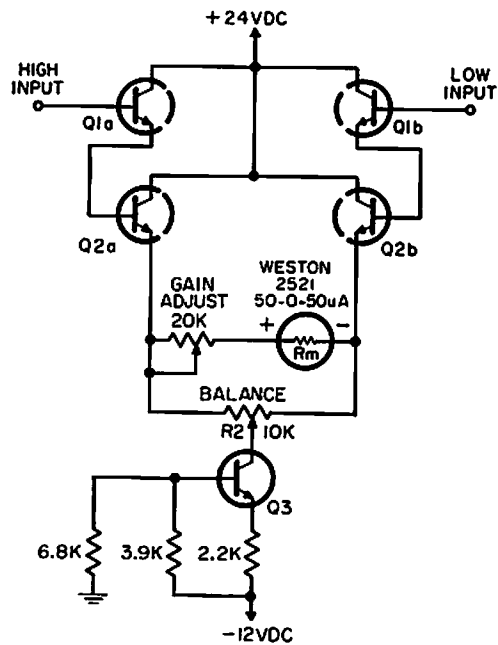
time constant while increasing available time for measuring peak value. Linearity is good up to 40 v.—M. Uno, Amplifier Improves

Peak Voltmeter Response, *Electronics*, 37:14, p 73.



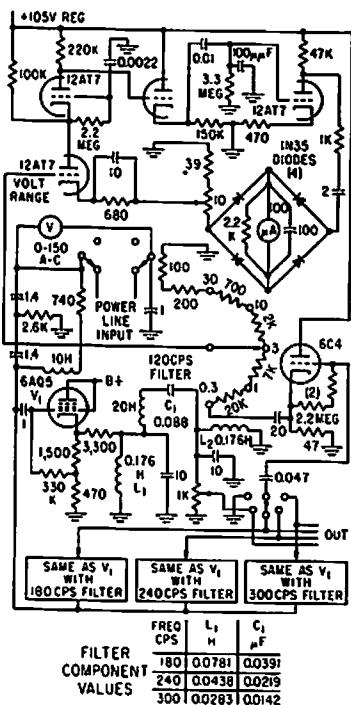


DUAL-RANGE D-C VOLTMETER—With switch in position 2, serves as standard 0-50 v d-c voltmeter. With switch in position 1, R1 is shunted across 50-mv, 1-ma meter to allow about 3 ma through 27-v, 1-w zener diode CR1. CR2 diode 1N540 is for temperature compensation. About 27 v is then held across the diodes, and meter scale represents 27 to 32 v, with sensitivity of 0.1 v d-c per division. R3 is used for calibration.—M. W. Raybin, *Dual Range DC Voltmeter*, *EEE*, 10:12, p 31.

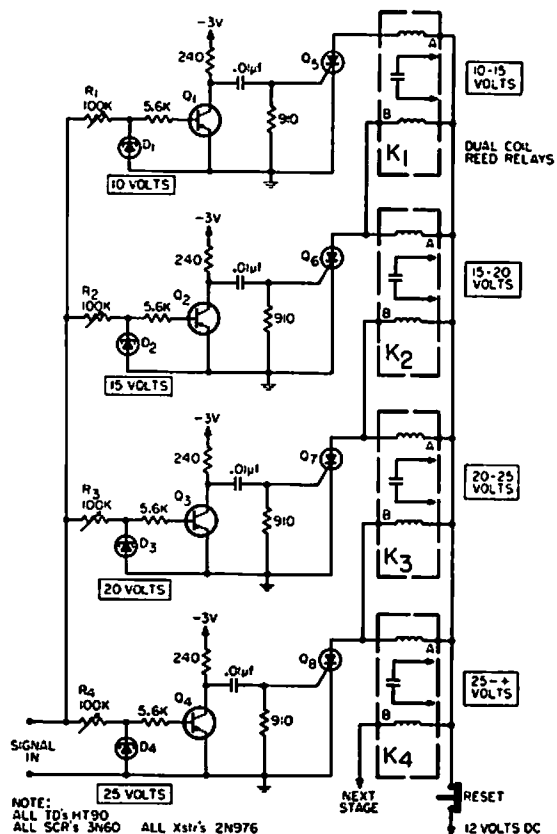


DIFFERENTIAL VOLTMETER—High-impedance differential-input transistorized panel voltmeter has stable zero point, eliminating need for undesirable zero control. Meter compares voltage under test with known zener-regulated reference voltage. Circuit is differential Darlington-connected emitter-follower

using pair of 2N2060's. Bias point is stabilized by constant-current sync connected to 2N1613 transistor. Accuracy is 1% if unmatched source impedance is less than 10K. —A High-Stability Differential Voltmeter, "Electronic Circuit Design Handbook," Macier Pub. Corp., N.Y., 1965, p 152.

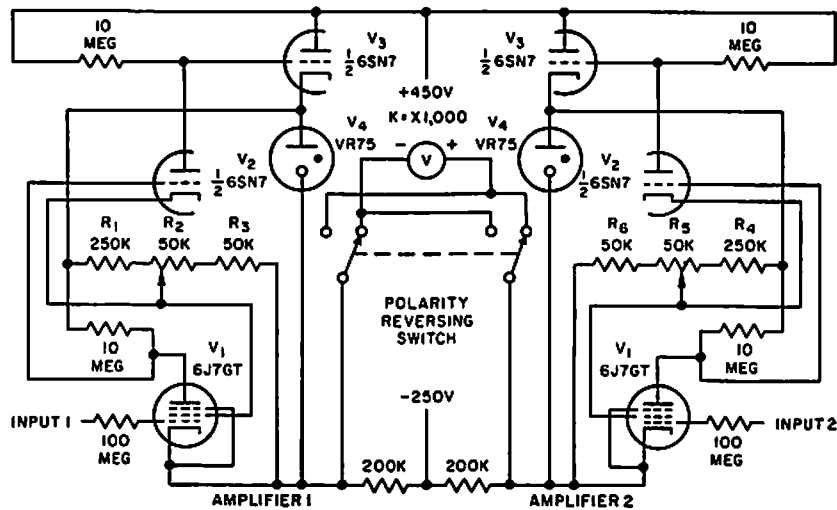


POWER-FREQUENCY HARMONIC METER—Has four bandpass filters, tuned to first four harmonics of 60 cps, and vtm that measures voltage at each filter output, in five ranges covering from 0.3 to 30 v full scale.—R. S. Brown, *Tuned Voltmeter Reads Harmonic Amplitude*, *Electronics*, 32:3, p 68.



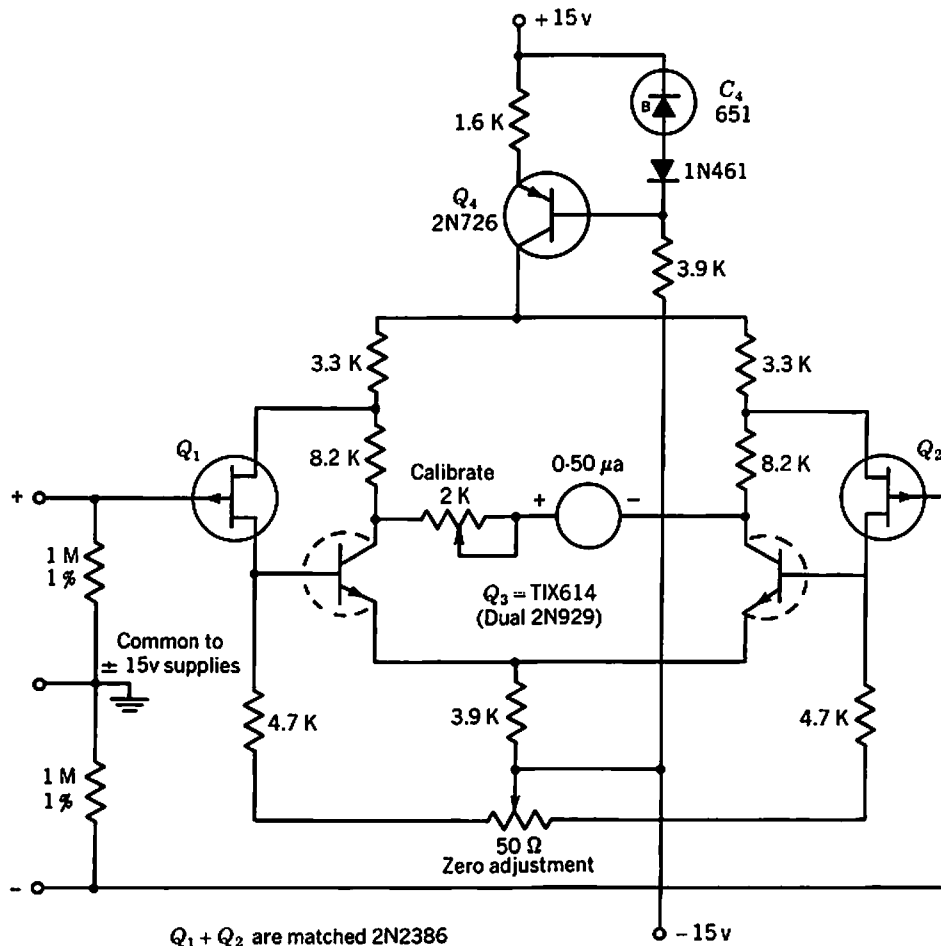
PULSE PEAK METER—Indicates peak of fast voltage pulse to within one of several predetermined voltage ranges established by tunnel-diode level-sensing circuit and indi-

cated by series of exclusive-or dual-coil reed relays.—J. C. Rich, *Pulse-Peak Indicator*, *EEE*, 13:2, p 61.



HIGH-IMPEDANCE DIFFERENTIAL VTVM—Infinite input impedance is obtained in direct-coupled d-c amplifier by continuously and automatically feeding back to input a bucking voltage equal to signal voltage. Use of both inputs permits differential measurements of small signal voltages at mean levels between -150 and $+300$ v, for measuring

grid-cathode potentials in high-impedance circuits or for balancing high-impedance push-pull circuits. Instead of input voltage dividers, range resistors are used in output circuit. Voltage supply need not be regulated.—V. D. Schurr, D-C Amplifier Expands Input Voltage Range, *Electronics*, 31:23, p 87-89.

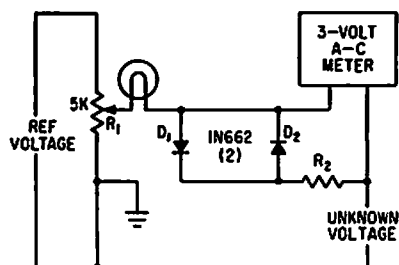


$Q_1 + Q_2$ are matched 2N2386

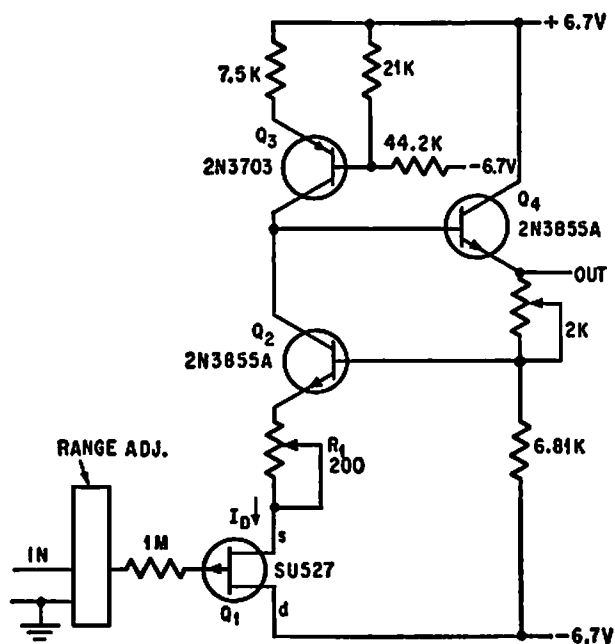
FET D-C MILLIVOLTMETER—Uses differential amplifier assembled from pair of simple two-stage feedback amplifiers having voltage

gain of 3. Input sensitivity of meter is 20 meg per v and common-mode rejection ratio is 1,000 to 1. Temperature characteristics are

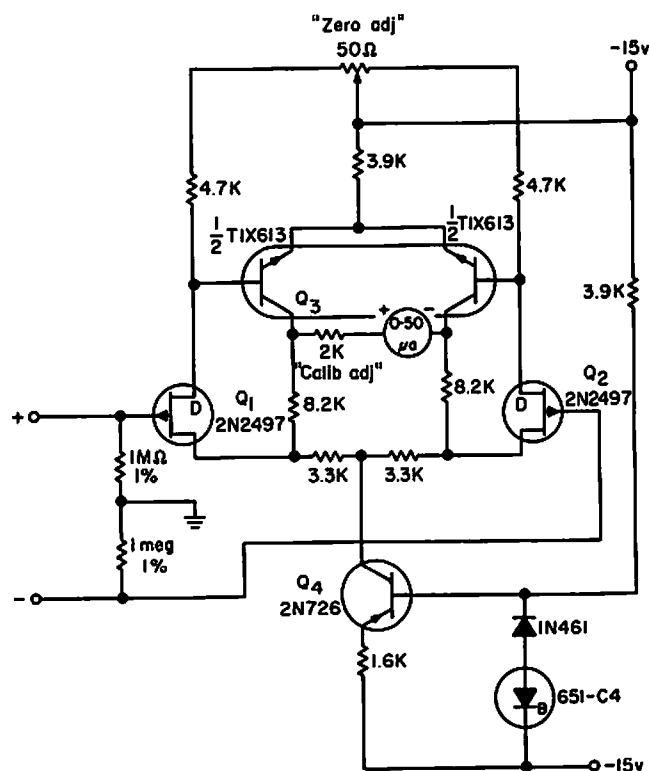
reasonably good when using matched fet's. —Texas Instruments Inc., "Transistor Circuit Design," McGraw-Hill, N.Y., 1963, p 522.



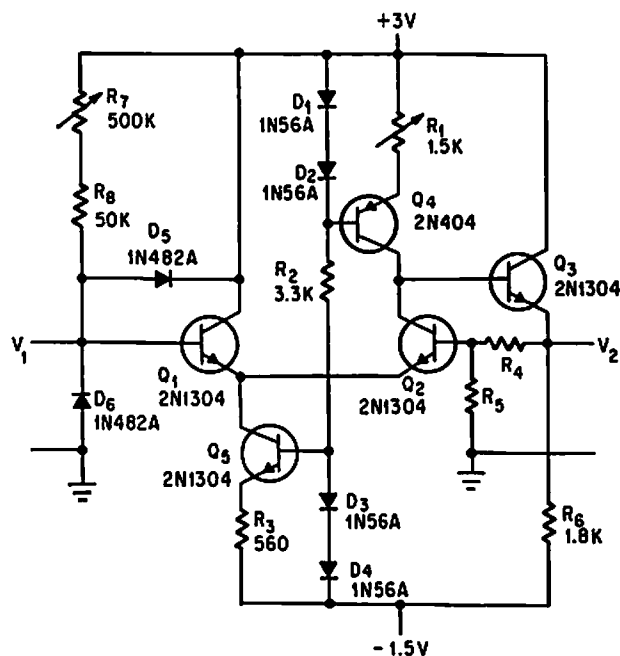
VOLTAGE RATIO METER—Simple circuit, having resolution better than 0.1% for measuring ratio of two voltages, also serves as accurate null detector when difference voltage is less than 0.5 v. A 115-v, 6-w lamp limits voltage applied to meter when difference voltage exceeds 0.5 v. R2 is chosen to give full-scale deflection when difference between the two voltages is maximum.—P. A. Lenk, *Circuit Permits Accurate Voltage Ratio Measurements*, *Electronics*, 34:52, p 56-57.



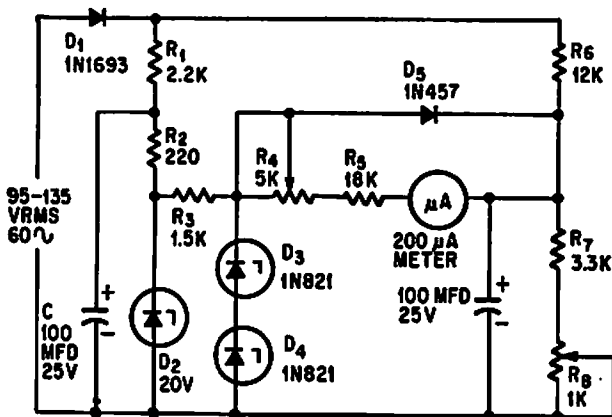
DIRECT-COUPLED AMPLIFIER—Gives high input impedance and low drift at low cost, with approximately unity gain. Uses one fet and three bipolar transistors. Suitable for d-c voltmeter having 0.1 v full scale on lowest range. Temperature drift is low.—J. M. Colwell, *Direct-Coupled Amplifier Cuts Cost of D-C Voltmeter*, *Electronics*, 39:12, p 109-110.



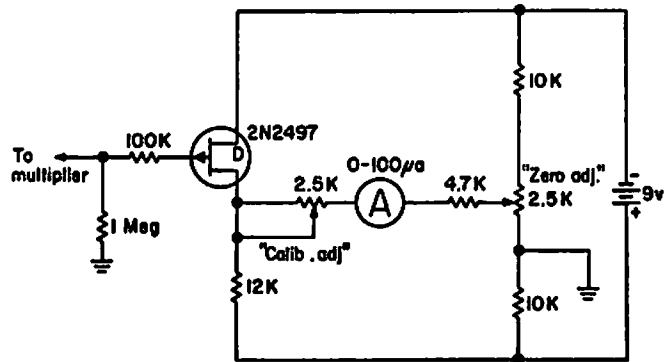
FET D-C MILLIVOLTMETER—Consists of two circuits much like bootstrapped source-follower, differentially connected and fed by active current source. Input of 50 mv produces full-scale deflection, making sensitivity 20 meg/v.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 110.



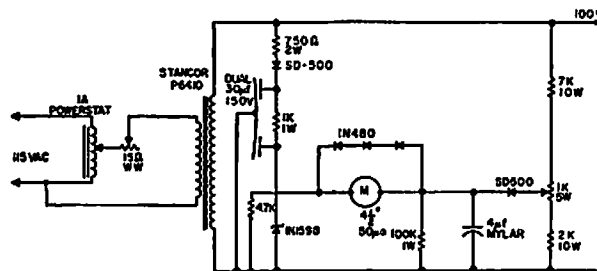
HIGH-GAIN D-C PREAMP FOR VTVM—Use of current source as load resistor of Q2 minimizes battery supply effect on zero setting. Additional current source Q5-R3-D3-D4 serves in place of common-emitter resistance for Q1-Q2 to reduce sensitivity to negative supply voltage excursions and boost open-circuit gain above 300. Preamp will extend 1.5-v range down to 500, 150, and 50 mv full-scale for voltage measurements in semiconductor circuits without damaging transistors.—A. K. Scidmore, *Low-Cost Emitter-Follower Extends Voltmeter's Range*, *Electronics*, 39:3, p 87.



PRECISION A-C VOLTMETER—Measures a-c voltages between 95 and 135 v with 0.6% accuracy while using ordinary 2% accuracy meter. Zener diodes provide reference voltage.—D. S. Belanger, *Simple Circuit Increases Measurement Accuracy*, *Electronics*, 38:22, p 69.

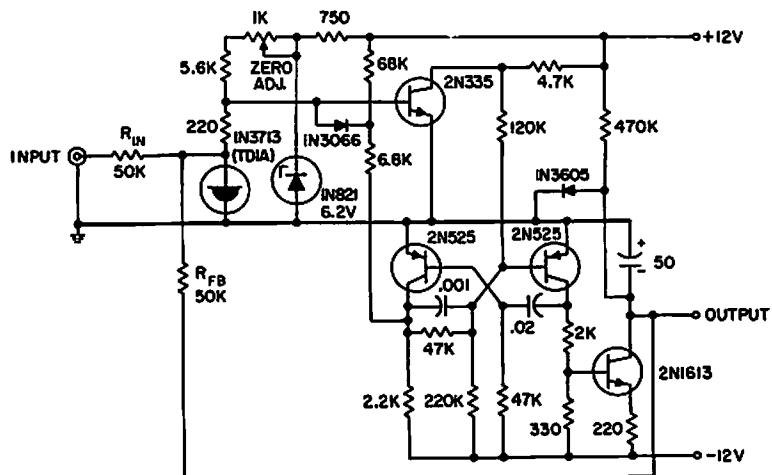


SIMPLE FET VOLTMETERS—Uses single active device to indicate full scale at 1 v input, for sensitivity of 1 meg per volt. Bias is at 300 microamp drain current, approximately point of zero drift.—L. J. Sevin, Jr., "Field-Effect Transistors," McGraw-Hill, N.Y., 1965, p 109.



100-V A-C REFERENCE—Accurate 100-v rms source is used as reference voltage for divider to correlate vacuum-tube voltmeters. Meter is altered to zero-center, with new scale indicating voltages up to 1.825 v on each side of 100-v center value. D-c voltage on one side of meter is held constant by zener diode, and is compared with positive voltage applied to other meter terminal by divider action without stabilization.

Initial standardization is done by adjusting input controls for 100-v output as determined by reference standard. Output potentiometer is then adjusted to make meter correspond (center of scale). Three diodes protect meter when unit is turned on.—Standardized AC Voltage Reference Source, "Electronic Circuit Design Handbook," MacTier Pub. Corp., N.Y., 1965, p 151.

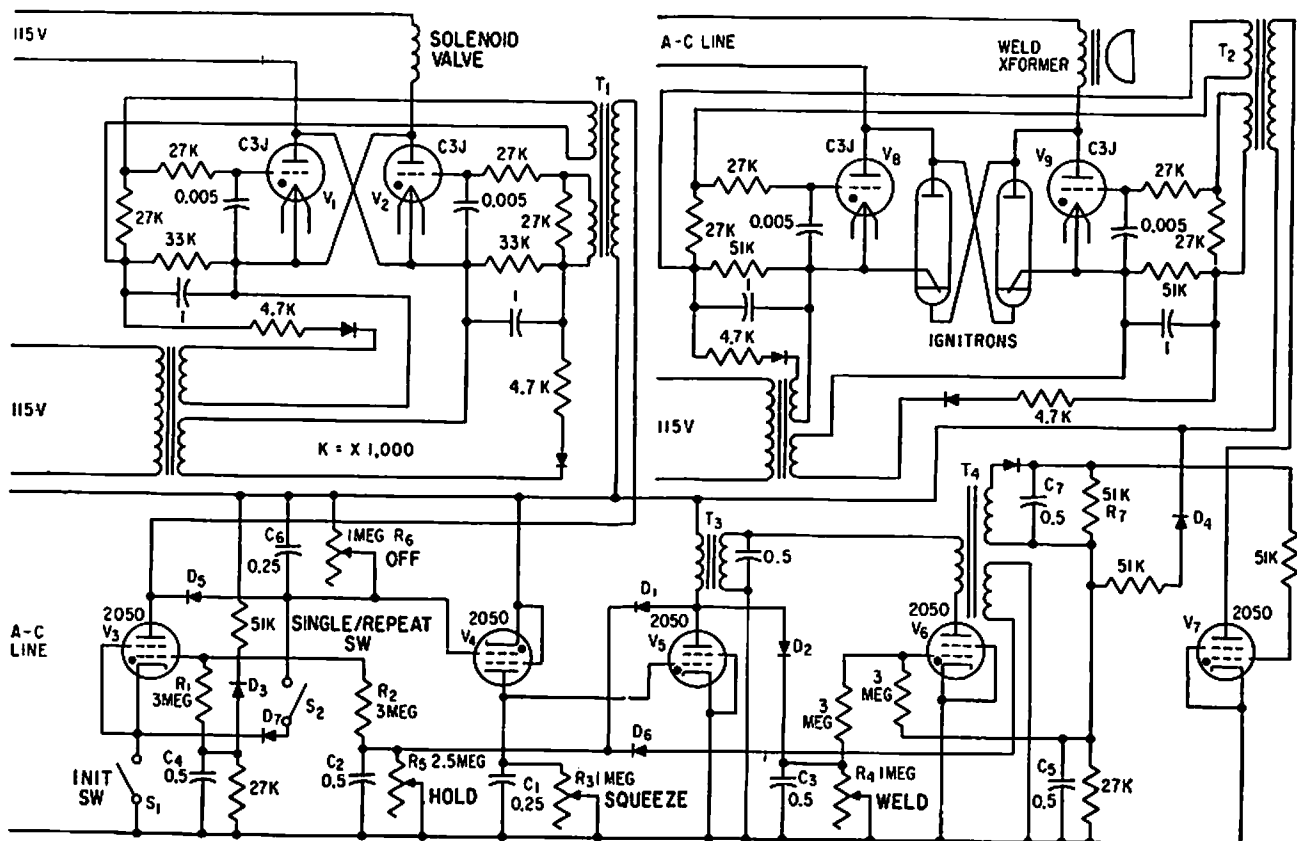
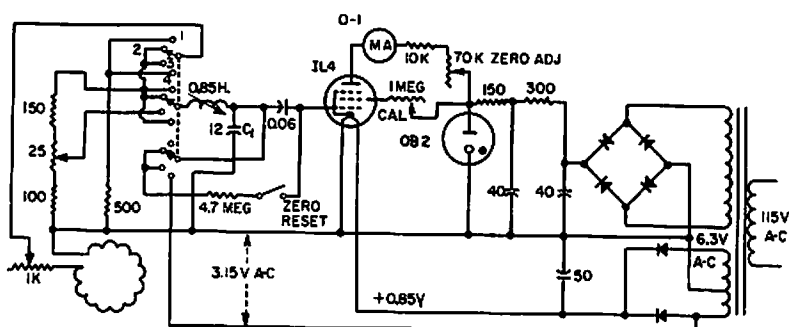


PEAK-SENSING OPERATIONAL AMPLIFIER—Operates as slideback sensing circuit to give d-c output that is proportional to positive peak of repetitive input signal. Will measure peaks of pulses as narrow as 1 nsec.—"Transistor Manual," Seventh Edition, General Electric Co., 1964, p 371.

CHAPTER 100

Welding Circuits

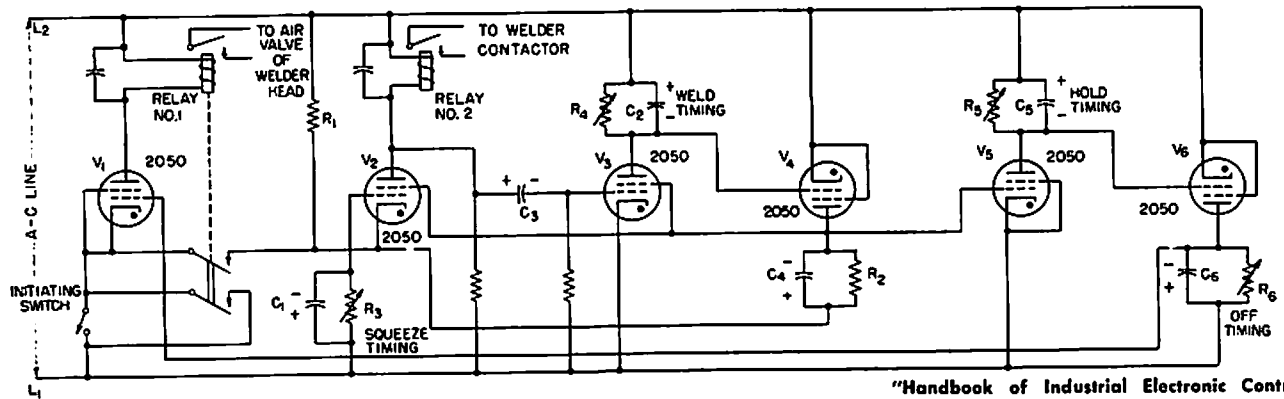
MEASURING SPOT-WELDING CURRENT—Toroid placed around one of welder electrodes develops voltage that is function of rate of change of magnetic flux produced by alternating current flowing through weld. Peak-reading a-c electronic voltmeter is used to measure resulting voltage across toroid. Selector switch positions are: 1—no signal input; 2—0 to 15,000 amp; 3—0 to 30,000 amp; 4—calibration.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, 1959, p 326.



TIMER FOR RESISTANCE WELDING GUN—Provides exact timing control for squeeze, weld, and hold, as well as fast-repeat re-

cycling, by control of thyratrons. Can be operated anywhere from single-shot to over 600 spots per minute. Timer is designed

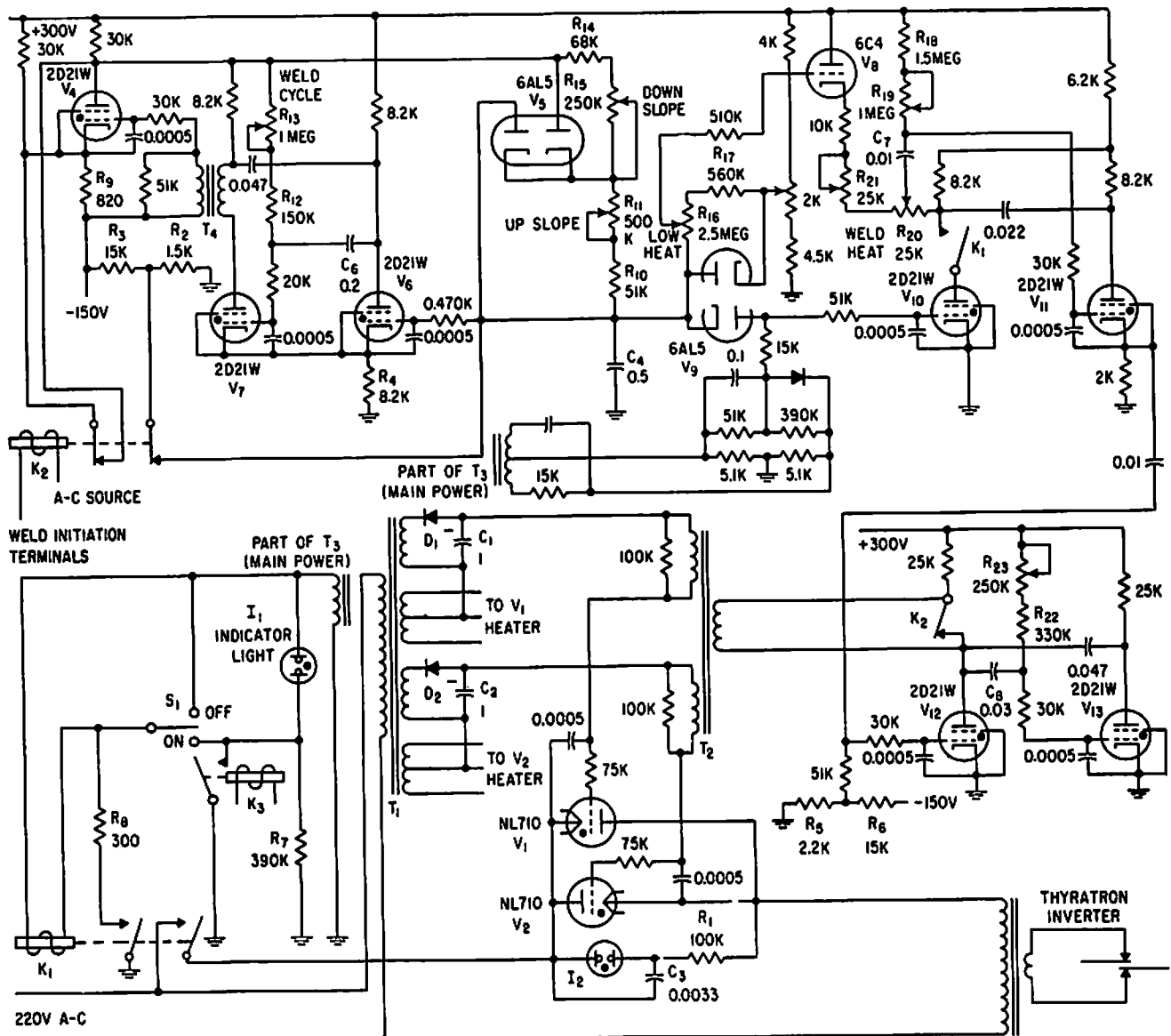
for fail-safe operation.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, New York, 1959, p 322.



SPOT-WELDING TIMER—Five-thyratron sequence timer for resistance-type spot welder

meets auto industry requirements for efficiency and reliability.—J. Markus and V. Zeluff,

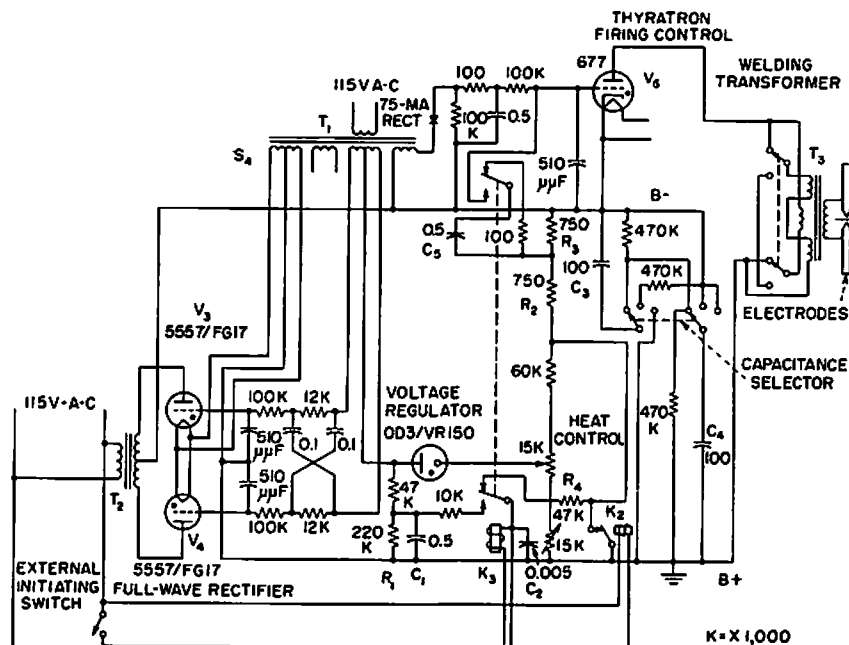
"Handbook of Industrial Electronic Control Circuits," McGraw-Hill, New York, 1956, p 343.



HEAT PROGRAM TIMER—Controls weld energy for production-line welding of electron tubes and other small components. Func-

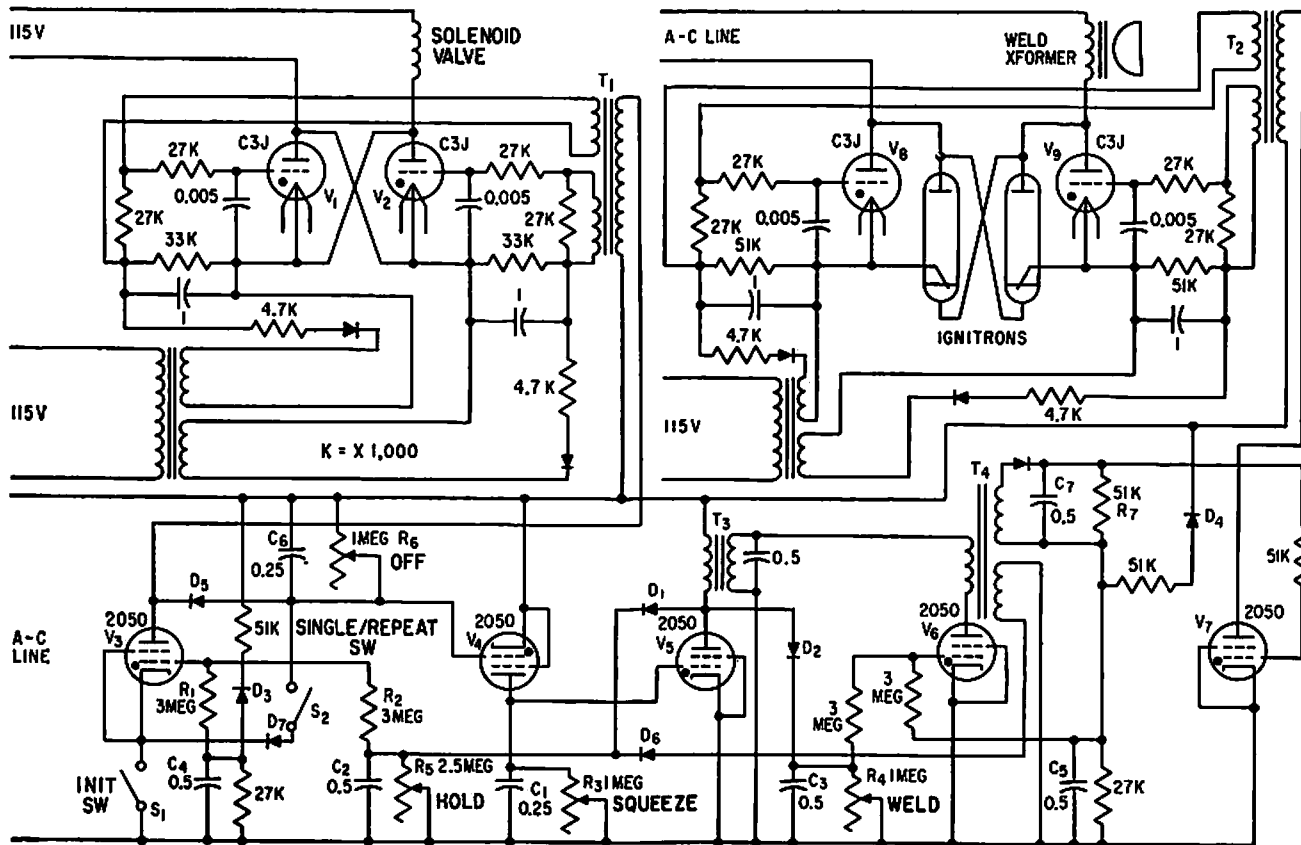
tions controlled are low heat, weld heat, up-slope time, weld time, and down-slope time. Adjustable potentiometers permit

changing each of these times.—A. V. Ranis, Heat Program Timer Controls Weld Energy, *Electronics*, 31:23, p 76-78.



METAL-FOIL SPOTWELDING CONTROL—Permits precise control of high-energy capacitor discharge used in welding extremely thin and highly conductive foils or fine wires. Heat control provides range of 650 to 1,500

v for level at which energy is stored, and selector switch gives choice of 50, 100, and 200 mfd for storage capacitor.—J. Markus, "Handbook of Electronic Control Circuits," McGraw-Hill, New York, 1959, p 321.

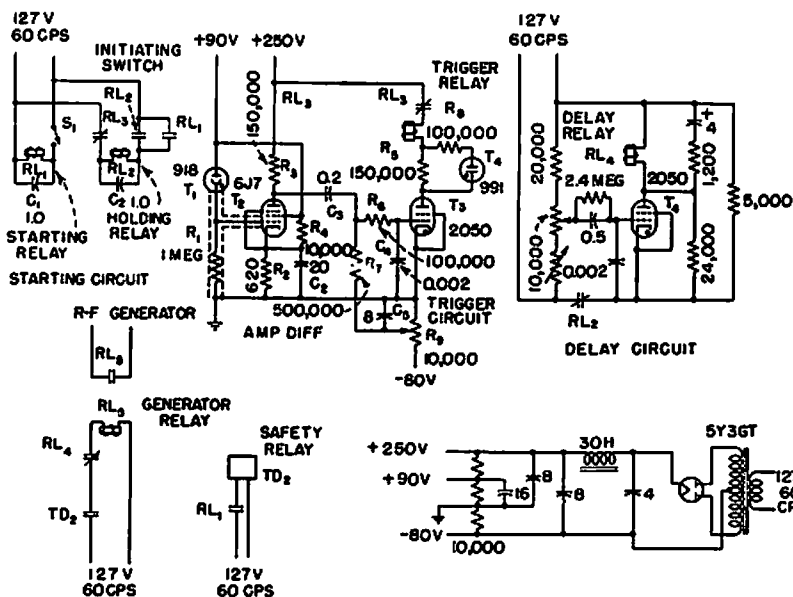
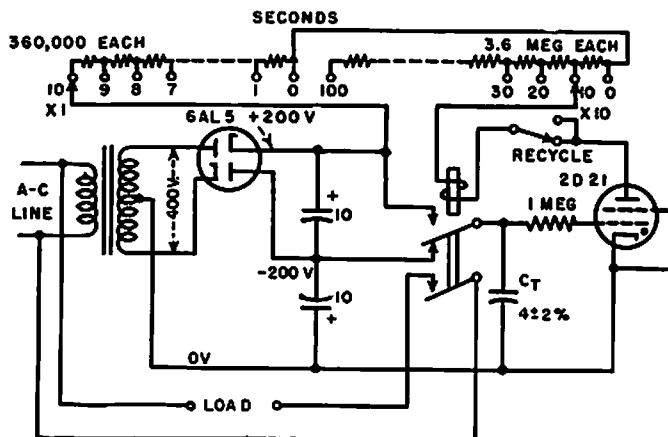


FOUR-FUNCTION WELDING TIMER—Thyristors serve as relays for controlling squeeze time, weld time, hold time, and off time in high-speed resistance welding. Control pro-

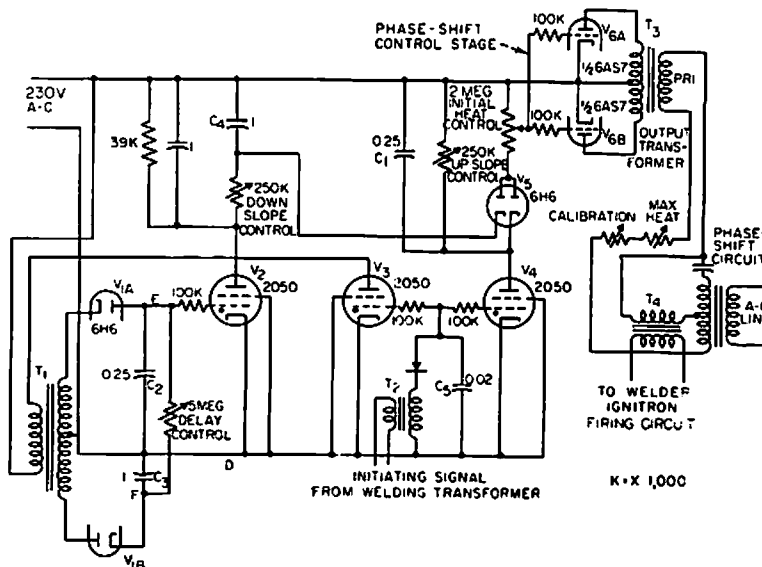
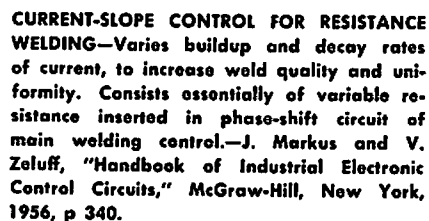
vides fail-safe operation, reduces transients by accurately adjusting ignitron firing angle, and gives accurate repetition of timing cycle.—S. C. Rockafellow, *Electronic Control Times*

High-Speed Welding Cycle, *Electronics*, 31:33, p 70-73.

PRECISION WELD INTERVAL TIMER—Stable timer provides intervals repetitive to accuracy of 0.75%, from 1 to 110 sec in 1-sec increments. Can be used for welder, enlarger, and other industrial controls.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, New York, 1956, p 292.



PHOTOELECTRIC WELD MONITOR—Shuts off r-f generator automatically after copper flows when welding exhaust tubulation to metal vacuum tube. Light is reflected by molten copper into phototube that initiates shutdown, with 0.6 sec delay introduced intentionally to allow copper to flow around entire seal.—J. Markus and V. Zeluff, "Handbook of Industrial Electronic Control Circuits," McGraw-Hill, New York, 1956, p 344.



Index

- Acceleration, measurement, 731
- Accelerometer, d-c motor control, 406, 407
 - digital-analog converter, 32
 - distance measurement, 307
 - falling-sphere switch, 424
 - motor-controlling, 408, 410
- Acoustics, measuring auditorium, 309, 787
- Adapter, compactron f-m stereo, 693
 - fat curve tracer, 764
 - f-m stereo multiplex, 691, 694
 - marker beacon, 80
- Adder, cascade, 82
 - drain-coupled, 81
 - full, 123
 - half, 121
- Afc (see Automatic frequency control)
- Agc (see Automatic gain control)
- Air navigation (see Beacon)
- Air traffic control, beacon decoder, 78
- Aircraft, 75-Mc marker beacon receiver, 77
 - 75-Mc three-light marker beacon receiver, 76
 - 243-Mc crash beacon, 80
 - automatic multiple-antenna phaser, 467
 - autopilot fader control, 130
 - autopilot failure alarm, 118
 - capacitance-type fuel gage, 86
 - five-zone passenger public-address amplifier, 43
 - fuel-flow gage, 350
 - one-light marker beacon, 79
 - three-light marker beacon adapter, 80
 - (See also Beacon)
- Alarm, 27.12-Mc diathermy frequency drift, 212
 - 200-Mc r-f radiation, 6
 - analog computer overvoltage, 5
 - autopilot malfunction, 118
 - avalanche warning transmitter, 2
 - balanced-capacitance fence, 86
 - battery standby supply, 507
 - bootstrap integrator, 312
 - capacitance-bridge intrusion, 88
 - capacitance-sensitive intrusion, 87
 - computer fault-location, 3
 - differential voltage or current, 6
 - electronic doorbell, 7
 - flying-spot scanner sweep failure, 696
 - high-voltage, 5
 - illumination rise, 7
 - intrusion fail-loop, 1
 - lamp burnout, 7
 - locked-up gyro, 6
 - low-voltage, 5
 - microwave system coder matrix, 3
 - microwave system fault, 272
 - missing-pulse, 6
 - multiple-input overvoltage, 4
 - multiple-input scs, 6
 - over-or-under-voltage, 119
 - parking meter, 293
 - phase-sensitive infrared intrusion, 5
 - photographic film break, 131
 - power-failure, 1
 - power loss on one three-phase line, 474
 - proximity-switch intrusion, 86
 - pulse-coded microwave system fault, 3
 - radiation, 560
 - radiation stopped-count indicator, 567
 - resistance-triggered, 7
 - road icing, 758
 - short-circuit, 4
 - signal dropout, 7
 - signal-powered, 7
 - siren power amplifier, 4
 - siren preamplifier, 2
 - supply voltage failure, 502
 - temperature rate-of-rise, 1
 - transient spike pulse, 4
 - transmitter vswr, 362
 - varying-frequency tone generator, 4
 - vlf intrusion, 4
 - voltage-sensing scs, 5
 - wailing-siren, 5
 - wobble generator, 3
 - zone coding generator, 2
 - (See also Protection)
- Alternator, frequency-control discriminator, 203
 - static solid-state, 314
- Altimeter, aneroid-driven capacitor in datum stabilizer, 87
- Ammeter, integrating, 308, 369
- Amplidyne control, electric spark machining, 129
- Amplifier, 0.25 kc strain gage, 366
 - 0.250 kc unity-gain, 15
 - 0.01-100,000 cps differential, 117
 - 0.2-5 cps direct-coupled, 582
 - 0.3-70,000 cps eeg, 380
 - 1 cps-30 kc micropower, 13
 - 1 cps-40 kc microvolt-signal, 21
 - 1 cps-500 kc low-noise, 22
 - 1 cps-1 Mc direct-coupled, 9
 - 2-cps foetal heartbeat detector, 389
 - 2 cps-20 kc, 9
 - 5-10,000 cps, 385
 - 5 cps-72 kc broadband, 10
 - 6 cps-300 kc gain-stabilized, 17
 - 6.5-cps air-leak detector, 17
 - 10-cps push-pull, 362
 - 10 cps-10 kc bootstrapped source-follower, 10
 - 10 cps-100 kc microphone, 498
 - 10 cps-1 Mc cascaded emitter-follower, 22
 - 10 cps-1 Mc unity-gain fet, 20
 - 15-15,000 cps lock-in, 568
 - 15 cps-200 Mc cascade mos fet, 13
 - 15 cps-200 Mc common-gate mos fet, 8
 - 17 cps-35 kc RC-coupled fet, 23
 - 30-1,500 cps passive-redundancy, 28
 - 46-cps with 8-cps bandpass, 294
 - 50, 60, and 400-cps servo, 656
 - 50-2,000 cps feedback-stabilized, 589
 - 50 cps-1 Mc complementary current multiplier, 24
 - 50 cps-3.5 Mc emitter-follower, 27
 - 60-cps bandstop, 18
 - 100 cps-230 kc with high input impedance, 25
 - 100 cps-500 kc diode-stabilized bias, 28
 - 100-1,000 cps, 390
 - 400-cps high-impedance, 81, 82
 - 400-cps inverter power, 319
 - 400-cps servo, 653
 - 400-cps variable-gain inverter, 315
 - 1 kc-500 kc high-input-impedance, 27
 - 3.2 kc-32 Mc silicon video, 838
 - 7-kc cable-driving, 370
 - 16-kc operational, 29
 - 50-kc operational, 29
 - 50-kc push-pull class-S, 320
 - 50-100 kc, 171
 - 100-kc wide-band 4-w, 28
 - 200-kc fixed-gain, 64
 - 200-kc high-impedance parametric, 27
 - 500-kc cascade, 248
 - 500-kc delay-line-tuned, 282
 - 1-Mc-bandwidth discriminator, 9
 - 2-Mc unneutralized common source, 10
 - 2.2-Mc radar power, 548
 - 3-Mc bandwidth, 846
 - 3.58-Mc color burst, 753
 - 4.7-Mc two-stage 2N1937, 22
 - 8-Mc limiting, 331
 - 10-Mc tuned fet, 15
 - 10-Mc unity-gain video, 628
 - 30-Mc 8-w linear ssb, 13
 - 30-Mc 20-w single-sideband, 8
 - 30-Mc double-tuned, 19
 - 30-Mc gain-controlled tetra, 60
 - 30-Mc hybrid gain control, 59
 - 30-Mc low-noise, 12
 - 30-260 Mc grounded-grid broadband, 11
 - 35-Mc steerable antenna control, 466
 - 50-Mc 40-w single-sideband, 8
 - 50-Mc 2N2410 power, 29
 - 50-Mc diffused-base video, 841
 - 60-Mc 2N743, 25
 - 60-Mc low-noise, 12
 - 60-Mc small-signal, 12
 - 60-90 Mc voltage-tuned, 18
 - 70-Mc 0.5-w, 14
 - 70-Mc neutralized gain-controlled, 58
 - 75-Mc marker beacon, 77
 - 100-Mc bandwidth, 745
 - 100-Mc bandwidth cro, 94
 - 100-Mc-bandwidth video, 840
 - 100-Mc cascaded three-transistor, 20
 - 100-Mc insulated-gate fet, 8
 - 100-Mc neutralized common-source, 10
 - 140-Mc 2-w f-m, 254
 - 140-Mc 15-w power, 255
 - 160-Mc 15-w three-transistor power, 803
 - 160-Mc wideband class C-16
 - 173-Mc 400-mw power, 21
- Amplifier, 175-Mc f-m mobile, 796
 - 175-Mc r-f stage, 578
 - 180-Mc class C power, 806
 - 190-400 Mc broadband, 242
 - 200-Mc gain-controlled, 63
 - 200-Mc neutralized fet with 12-Mc bandwidth, 14
 - 200-Mc noise-measuring, 358
 - 200-Mc pulsed distributed, 554
 - 250-Mc 12-w, 9
 - 250-Mc military vhf band, 20
 - 250-Mc power, 29
 - 400-Mc low-noise, 145
 - 445-Mc tunnel-diode, 18
 - 450-Mc, 26
 - 450-Mc aeronautical navigation band, 25
 - 450-Mc gain-controlled, 59
 - 470-890 Mc, 823
 - 500-Mc neutralized, 23
 - 500-Mc small-signal, 24
 - 600-Mc cascade, 26
 - 1,000-Mc wideband video, 512
 - 50-db dynamic-range compression, 848
 - 100-db gain for distortion monitor, 27
 - 250,000-gain integrator, 306
 - 0.5-ohm source impedance, 23
 - 50-meg input impedance, 12
 - 100-meg input impedance, 15
 - 250,000-meg input impedance, 174
 - 10-nsec pulse, 126
 - 12-v power supply reference, 614
 - 21-v power supply reference, 593
 - 1-w differential audio, 297
 - 2-w servo, 646
 - 5-w servo, 651
 - 6-w servo, 656
 - 200-w siren, 4
 - a-c coupled low-noise cascade, 19
 - age fet, 62
 - attack control amplifier, 676
 - audio with 40-db agc, 58
 - beta-independent gain, 782
 - boosting input impedance, 16
 - bootstrapped fet source-follower, 19
 - bootstrapped fet with 30-meg input impedance, 18
 - bootstrapped source follower, 10
 - broadband differential bootstrap, 82
 - broadband radar speed motor, 545
 - buffer and logic, 301
 - cascaded common-base uhf doubler, 259
 - cascaded shunt-peaked video, 838
 - cascode follower, 12
 - cascode integrated-circuit chip, 301
 - cascode video tape, 715
 - cascode nujistor-transistor, 734
 - CB transmitter, 795
 - CCTV receiving terminal, 740
 - character-generator deflection, 102
 - chopper-type d-c, 472
 - chopper-type summing, 103
 - chopperless differential, 174
 - color-tv luminance, 751
 - complementary-transistor Darlington, 24
 - compounded emitter-follower, 13
 - computer readout, 121
 - constant-current, 768
 - constant-current diode load, 18, 29
 - constant-gain differential, 12
 - constant-Q, 442
 - core-sensing, 673
 - counter bootstrap, 685
 - cro marker pulse, 96
 - crt horizontal, 98
 - crt unblanking, 95
 - current-derived stabilization, 9-11
 - current-difference, 591
 - current-summing operation, 177
 - Darlington beta-squaring, 24
 - Darlington broadband, 14
 - Darlington output stage, 194
 - Darlington-pair servo, 645
 - Darlington sync distributor, 737
 - d-c lamp-driving, 387
 - difference, 356, 387, 628
 - difference for cctv microscope, 32
 - differential, 62, 140, 388, 415, 506, 596, 723
 - differential a-f, 143
 - differential analog, 30

Amplifier, differential battery-charging, 74
 differential current compression, 174
 differential drive for transfluxor, 30
 differential low-hysteresis trigger, 819
 differential low-voltage alarm, 5
 differential memory-sensing, 124
 differential pulse width measurement, 630
 differential regulated power supply, 592
 differential video, 744
 digital-analog compute, 34
 digital-analog hold, 37
 digital data read, 721
 digital data write, 722
 direct-coupled a-c, 10, 11
 direct-coupled mos fet, 15
 double-bootstrapped fet, 21
 emitter-squared follower, 13
 error-rate compensation, 663
 expanded-scale swr meter, 373
 fet source-follower, 11
 flux-controlled transistor, 12
 gain-controlled logarithmic, 26
 gain-stabilized conversion feedback, 645
 gated telemetry receiver, 723
 gyro pitch servo, 659
 gyro vibration, 98
 Hall-effect field coil drive, 413
 Hall-effect plate drive, 416
 high-gain clipping, 544
 high-gain video, 843
 high-voltage pulse, 512
 i-f transistor cascade with agc, 63
 incremental R-C oscillator, 669
 improved Darlington, 298
 infinite-input-resistance mos fet, 26
 infinite-input-impedance unity-gain, 28
 infrared horizon sensor, 289
 infrared width gage, 290
 integrated-circuit sense, 301
 integrated d-c operational, 305
 integrated output buffer, 305
 integrated voltage reference, 614
 integrating operational, 379
 inverter, 96
 light-pen, 92
 logarithmic 0.5-6 v, 724
 logarithmic radar, 551
 logarithmic reactor startup, 128
 logic-inverting, 339
 low input capacitance, 10
 low-noise contour-map design procedure, 442
 low-noise fet audio with agc, 11
 low-pass adjustable 190-280 kc bandwidth, 29
 magnetic, 105
 magnetic drum read, 587
 magnetic drum write, 582
 magnetic half-square, 355
 magnetic tape recorder, 718
 magnetostrictive delay line, 836
 magnified d-c level control, 576
 memory logic sense, 340
 memory readout, 120
 memory-sensing, 123
 memory X-pulse, 343
 memory Y-pulse, 349
 microphone, 498
 mos fet with junction transistor, 15
 mos fet two-stage, 14
 myoelectric signal, 390
 nanosecond thyatron driver, 513
 narrow-band radar, 547
 negative-positive-feedback servo, 653
 nonneutralized-emitter-inductance h-f, 797
 neutron counter, 726
 nonlinear feedback with varistor, 14
 null in integrated-circuit tester, 303
 nulling, 644
 operational, 308, 807
 operational Darlington-input integrated, 298
 operational sampling, 626
 operational two-stage integrated, 302
 p-most integrated broadband, 302
 paralleled-f feedback, 81, 82
 period reactor startup, 128
 photodiode, 481, 484
 photodiode noise-suppressing, 478
 photoelectric agc, 476
 photomultiplier, 726
 photomultiplier gain stabilizer, 641
 piezoelectric gage, 9
 plate-cathode follower, 27
 preferred high-level cathode follower, 24
 pressure-sensitive resistor, 388
 pulse-shaping a-f, 367
 push-pull frequency-shift keying, 451
 push-pull optoelectronic, 484
 push-pull pulse, 820
 push-pull video, 840
 Q-multiplier, 24
 rail flaw detector, 584, 585
 read-write, 336
 redundant analog, 175
 reflex for sensitive relay control, 137
 remote gain-control, 14, 620
 saturating bootstrap, 566
 secondary-emission-pentode cathode follower, 16
 sense, 196
 servo chopper, 115
 servo integrated-circuit, 304
 servo synchronous filter, 650
 shunt-peaked video, 846
 shunt-series feedback, 543
 signal-level-controlled gain, 59
 signal-voltage-stabilized, 22
 simple differential, 816

Amplifier, single-ended video input, 844
 solar-cell strobe-generating, 17
 squaring for cctv microscope, 32
 stable superregenerative, 16
 stereo i-f, 690
 sweep power, 585
 temperature-compensated Darlington, 16
 temperature-stable complementary-transistor, 9
 thin-film memory sense, 124
 thin-film pulse, 300
 three-triode constant-current, 767
 transformerless linear modulator, 402
 transistor tv r-f, 769
 transistorized chroma, 747
 tuned r-f transistor stage, 572
 tv video output, 746
 two-stage wideband video, 844
 uhf doubler, 260
 ultrasonic, 409
 unity-gain for active filter, 217
 unity-gain buffer, 11
 unity-gain isolation, 17
 unity-gain triode cathode follower, 16
 varactor-tuned, 571
 vibration-measuring, 369
 video with agc, 737
 video inverter, 309
 voltage-controlled attenuator, 15
 voltage-controlled gain, 58
 voltage-difference, 391
 wideband cable-driving, 16
 wideband fet, 11
 wideband i-f with agc, 62
 wideband twin-T, 20
 wideband video, 641
 wideband voltage-controlled gain, 847
 wire-locating, 770
 Z-axis, 99
 zero-crossing-detecting, 278
 (See also all specialized circuits using amplifier stages)
 Amplifier control, operational, 704
 Amplitude probability density function, measurement, 441
 Analog, four-quadrant multiplier, 414
 four-quadrant square-law magamp multiplier, 353
 probability multiplier, 416
 redundant d-c amplifier, 175
 square-law magamp multiplier, 351, 354
 triangle multiplier, 416
 (See also under Analog-digital; Digital-analog)
 Analog computer, 10-bit digital-analog converter, 35
 exponential function generator, 267
 integrating operational amplifier, 379
 ladder-type network decoder, 31, 36
 multiplier, 399
 overvoltage alarm, 5
 potentiometer error compensation, 35
 pulse-amplitude multiplier, 416
 pulse-width demodulator, 36
 pulse-width modulator, 416
 random-noise source, 535
 voltage multiplier, 415
 Analog counter, staircase, 685
 Analog-digital converter, 300-Mc sampling circuit, 32
 binary encoder, 337
 calibrator, 31
 coding digit card, 36
 comparator, 36
 d-c amplifier, 30
 encoder, 34
 fast-response, 628
 gate, 274
 magnetic-core readout, 33
 magnetic data, 708
 multiplexer, 37
 polarity detector, 34
 sample-and-hold flip-flop, 625
 summing and weighting network, 30
 transfluxor driver, 30
 tunnel-diode, 32
 video from cctv microscope, 32
 video sampling amplifier, 268
 voltage comparator, 33
 voltmeter, 35
 weighting circuit, 33
 Analog memory, sample-hold circuit, 36
 Analog ratio computer, Hall multiplier, 415
 Analog voltage source, computer, 492
 Analyzer, 15-cps i-f amplifier, 284
 eeg waveform, 384
 frequency, 248, 669
 gasoline engine rotation, 67
 nuclear flight time interval, 783
 neutron time of flight, 560, 564
 pulse height, 115
 radar noise clipper-limiter, 332
 random noise, 268
 Anesthesia, servo control, 387
 shock spectrum, 376
 signals and noise, 441
 stepper motor, 777, 778
 And gate (see Logic)
 Anemograph, decoder drive, 733
 Antenna, audio-controlled phaser, 467
 automatic phasing control, 467
 beam-steering, 474
 impedance measurement, 674
 variable-directivity phase-shift control, 466
 Antenna pattern, radar with logarithmic amplifier, 551
 Antennafier, dipole fixed-gain, 569

Antennafier, dipole phase-shift, 474
 dipole variable-gain, 63
 slot, 573
 varactor-tuned, 571
 Anticoincidence, arc protection, 509
 Anticoincidence circuit, scintillation counter, 565
 Anticoincidence detector, nonoverlapping pulses, 118
 Aperture correction, tv camera, 735
 Arc protection, comparator, 509
 Astronomy, variable-star detector, 641
 Attenuator, logarithmic, 362, 373
 recording galvanometer input signal, 587
 transistor, 61
 two-transistor electronic, 58
 voltage-controlled mos fet, 15
 Audio, 400-cps oscillator, 670
 Audio, 1,000-cps wobulated speech simulator, 683
 1,800-cps alarm tone generator, 6
 1-Mc modulated tunnel-diode oscillator, 449
 10-w stereo amplifier power supply, 694
 30-w amplifier overload protection, 594
 active adjustable-bandpass filter, 216
 adjustable tuning-fork filter, 218
 alarm beep or tone generator, 6
 a-m/f-m portable, 570
 amplitude-stable transducer-exciting oscillator, 583
 artificial larynx, 388
 attack control amplifier, 676
 audible auto turn-signal indicator, 65, 70
 automatic f-m stereo switch, 689
 automatic impedance-matching preamp, 498
 background noise suppressor, 446
 balanced variable-threshold control switch, 441
 bass boost, 51
 battle stations signal generator, 3
 bridge-stabilized oscillator, 457
 cascaded filters, 222
 chatter-jamming tone generator, 461
 codan control, 812
 collision signal generator, 3
 compactron f-m stereo adapter, 693
 cube-root signal compressor, 267
 d-c voltage-controlled pulse generator, 145
 delta tv sound, 743
 driver, 693
 driver for fader, 39
 electronic fader, 39
 electronic music demonstrator, 671
 fet squaring circuit, 265
 flip-flop controlled switch, 710
 horn signal generator, 3
 loudness control, 51
 low-cost discriminator, 251
 low-noise agc, 61
 low-pass d-c to 1-cps filter, 219
 magnetic-tape print-through squelch, 720
 magnetic tape recording amplifier, 719
 measuring auditorium acoustics, 309, 787
 microwatt oscillator, 530
 noise-actuated avc, 793
 noise-free keying, 481
 nonlinear voltage-controlled oscillator, 463
 percussion music synthesizer, 214
 Q multiplier, 141
 siren preamplifier, 2
 siren warble generator, 3
 siren zone code generator, 2
 snap-action power level switch, 127
 sonar selection gate, 827
 sound level meter, 444
 sound level meter rectifier, 446
 sound pulse generator, 833
 stereo multiplex transmitter, 690
 three-transistor class A color tv, 752
 tone control, 50
 tuning motor noise-actuated squelch, 412
 two-stage discriminator, 201
 two-tone electronic doorbell, 7
 variable-sweep signal generator, 676
 varying-frequency alarm tone generator, 4
 voltage-controlled dropping resistor for filter, 220
 voltage-controlled vfo, 454
 volume compressor, 721
 wailing-siren, 5
 (See also Amplifier; Preamplifier; Receiver; Recorder; Servo; Stereo; Tape Recorder; Transceiver; Transmitter; Ultrasonics)
 Audio amplifier, 0.5-100 cps tunable, 49
 1 cps-30 kc micropower, 13
 2 cps-20 kc, 9
 20-100,000 cps low-noise, 48
 100-350 cps cravasse-detector receiver, 51
 130-800 cps, 477
 800-1,000 cps tuned, 38
 40-db-range agc, 58
 60-db low-noise fet, 45
 6-v phono, 41
 9-v phono, 42
 0.5-w 3-transistor, 48
 1-w fet, 40
 1-w integrated, 297
 2-w preferred power, 41, 51
 2.5-w transformerless, 46
 8-w silicon-transistor, 44
 10-w direct-coupled, 42
 10-w single-ended push-pull output, 41
 10-w stabilized power, 49
 12-w transformerless, 48
 15-w transformerless, 47
 20-w single-ended push-pull output, 40
 25-w power, 51
 45-w quasicomplementary-symmetry, 52
 45-w series-type, 52

- Audio amplifier, 50-w two-stage output, 53
70-w, 47
200-w siren, 4
class-B push-pull output, 42
complementary-symmetry transformerless, 41
cutting odd-harmonic class-B distortion, 52
Darlington, 51, 52
doppler radar, 531
fet-pnp Darlington, 49
five-zone aircraft public address, 43
high input resistance, 40, 52
high voltage gain, 22
integrated-circuit power, 304
integrated output stage, 302
line-operated two-transistor phono, 46
low-current mos fet, 50
low-noise bootstrap fet, 49
low-noise cascade fet, 38, 46
low-noise fet with agc, 11
low-noise fet output, 53
low-noise low-level high-gain, 45
output transistor protection, 44
preferred twin-triode voltage, 42
Q multiplier, 41
single-ended class-B output, 47
single-transistor, 53
single-transistor push-pull, 51
with squelch, 444, 445
stable three-transistor, 44
stereo headphone, 694
telephone-channel thick-film, 43
temperature-compensated for spacecraft
decoder, 50
three-stage cascaded common-emitter, 46
two-stage cascaded common-emitter, 48
two-transistor R-C coupled, 40
- Audio oscillator, conversion to square-wave
generator, 531
five-tone, 731
- Audio preamplifier, bootstrapped stereo, 689
building-block stage, 47
capacitor mike, 49
ceramic-cartridge-phono, 48
four-control, 44
hi-fi fet, 43
npn magnetic-cartridge, 53
phono, 38, 40
phono-tape-microphone, 39
stabilized, 50
tape and microphone, 45
underwater hydrophone, 50
velocity-response phono, 46
- Auto radio, five-transistor a-m, 569
signal-seeking tuner, 824
- Autocorrelation, spectrum invariance, 672
- Automatic brightness control, flying-spot scanner,
639
- Automatic direction finder, agc amplifier, 64
agc without clipping, 63
balanced modulator, 395
synchronous filter, 650
- Automatic frequency control, 23-Mc telemetry
discriminator, 56
30-Mc preferred pulse-modulated i-f, 54
automatic search and control, 662
collector-voltage-control, 55
diode-phantasticon control for airborne radar
klystron, 54, 56
emitter-current-control, 55
klystron, 193
mobile color tv pickup, 750
reactance-transistor, 740
receiver klystron local oscillator, 55
thyatron control for radar klystron repeller,
55, 56
varactor-controlled, 55
voltage-tuned f-m oscillator, 822
- Automatic gain control, 12-Mc video amplifier, 744
30-Mc gain-controlled tetrad, 60
30-Mc hybrid gain control, 59
30-Mc stage, 60
70-Mc neutralized, 58
200-Mc amplifier, 63
450-Mc amplifier, 59
40-db-range audio, 58
adi receiver, 63, 64
ambient light level, 476
amplified, 57
antennafier, 63
bridge with agc for on diode, 62
bridge with control of both diodes, 63
camera video, 737
cascaded fixed-gain amplifier, 64
communication receiver, 64
diode L-attenuator, 64
diode T-attenuator, 64
double-conversion-f amplifier, 57
double-gated with regulated bias supply, 64
fct amplifier, 62
keyed, 59
logarithmic, 26
low-noise audio, 61
noise-actuated, 793
preferred audio squelch, 62
preferred tube circuit, 62
signal-controlled color-film scanner, 59
slicer and gate, 60
squelch in a-f amplifier, 444, 445
superregenerative amplifier, 16
television camera video, 737
transistor i-f amplifier, 62, 63
transistor receiver, 61
transistorized color tv, 753
vlf preamp, 499
voltage-controlled a-c amplifier, 58
- Automatic gain control, voltage-controlled capaci-
tor diode network, 64
voltage-controlled capacitors in diode bridge, 64
voltage-controlled capacitors in diode T-network,
63
voltage-controlled phase-shift oscillator, 469
Automatic volume compression, tape recorder, 721
Automatic volume control, magnetic-tape print-
through squelch, 720
noise-actuated, 793
- Automotive electronics, 60-cps 200-w 115-v
inverter, 316
456-kc vehicle identification beacon, 79
30,000-rpm capacitance transducer for
tachometer, 87
400,000-rpm turbine tachometer, 70
12-v d-c to 300-v d-c converter, 71
30-kv ignition pulse generator, 71
air-suspension leak detector, 17, 456
audible turn-signal indicator, 65, 70
automatic timing advance, 65
capacitive-discharge ignition, 68
cylinder gas temperature measurement, 67
d-c power supply for fluorescent lamp, 169
electronic tachometer, 65
engine heat detector, 293
engine road test simulator, 66
gas-tube electronic ignition, 66
gate-opening turbine tachometer, 69
generator regulator, 169
headlight dimmer, 66, 71
ignition-pickup tachometer, 68
infrared suspension-leak detector, 654
quick-disconnect tachometer, 69
road icing alarm, 758
rotation-analyzing integrator, 67
sealed transmission tachometer, 71
single-pulse tachometer generator, 66
single-transistor tachometer, 71
speed-controlling chopper for battery-powered
vehicle, 107
tachometer amplifier, 69
tachometer gate, 68
telemetry d-c amplifier, 70
telemetry display strobo, 89
transistor fuel pump drive, 67
transistor ignition, 69, 711
ultralinear high-precision tachometer, 67
zero-power tachometer, 68
(See also Battery charger)
- Autopilot, clock generator, 665
comparator-alarm, 118
d-c amplifier, 197
fader, 130
gyro control, 130, 136
preamplifier, 500
unity-gain inverter, 322
- Avalanche alarm, trip-wire, 2
Avc (see Automatic volume control)
- Backscatter, ground pulse blanker, 95
tone signal rejector, 570
- Backward-wave oscillator, regulated filament
supply, 593
- Balancer, gyro, 89, 98
- Balinverter, noise-cancelling, 138
- Battery charger, automatic control with reference
battery, 73
constant-current motor-controlled rheostat, 75
control and protection circuit, 72
control with stabilized reference supply, 75
nickel-cadmium, 75
photoflash, 72, 74
portable cro, 74
scr regulator, 75
thyatron-controlled, 73
two-rate, 73
welder, 74
- Battery standby, alarm power supply, 507
- Beacon, 1.686-Mc high-altitude balloon, 76
75-Mc marker, 77
75-Mc one-light airborne marker, 79
75-Mc three-light aircraft marker adapter, 80
75-Mc three-light aircraft marker receiver, 76
100-140 or 220-260 Mc emergency, 78
243-Mc aircraft crash, 80
decoder, 78
gated-diode modulator, 80
rotating-lamp, 223
signal-powered transponder, 80
spacecraft recovery, 77
two-frequency SOS, 79
vehicle identification, 79
- Beam intensity control, crt, 95
- Beam switching, capacitance transducers, 705
recorder cores, 581
- Beam-switching tube, decade counter, 165
pulse generator, 516
storing accumulated count, 156
- Beepers, recording telephone conversation, 586
- Beta squaring, complementary-transistor
Darlington amplifier, 24
temperature-compensated Darlington, 16
- Binary, arithmetic, 564
basic neutron analyzer, 560
cascaded, 336
RC-coupled, 348
- Binary-analog, converter, 31
- Binary-command control system, 596
- Bionics, neuron model, 343
neuron model without integrator, 346
- Bistable circuit, triangular-wave, 631
- Black light, portable inverter, 321
- Blender, three-speed motor control, 412
- Blinker, neon roadside or Halloween-pumpkin, 230
(See also Flasher)
- Blocking oscillator, 0-2,000 pps preferred pulse-
frequency divider, 454
0-2,000 pps preferred series-triggered, 453
200-2,000 pps preferred astable, 459
2,000-83,000 pps preferred parallel-
triggered, 461
adjustable-prr trigger generator, 814
cascaded distance-mark divider, 243
diode sampling bridge drive, 82
distance-mark generator, 549, 551, 552, 553, 557
preferred distance-mark divider, 453
inverted-output switching voltage regulator, 609
pulse-frequency divider, 244, 245
sense amplifier trigger, 812
voltage limit monitor, 858
- Blood pressure, automatic measurement, 386
- Bolometer, amplifier and detector, 289
preamp, 291
- Bolometer bridge, ergometer, 370
- Bone disease, early diagnosis by osteograph, 382
- Bootstrap, wide-range linear time base, 634
- Bootstrap sweep, Darlington-linearized, 699
positive and negative-going, 701
variable controllable, 702
- Boatstrapping, 0.5 cps-700 kc fet preamp, 499
- Boxcar, capacitor-storage feedback, 116
modulation envelope detector, 192
- Boxcar detector, radar, 557
- Boxcar generator, ultrasonic receiver, 826
- Brazing, 45-kw r-f pulse control, 862
- Bridge, 3-kc oscillator amplitude stabilization, 383
15-kc impedance, 367
100-kc hybrid balanced modulator, 400
300-kc fet gate, 273
300-kc tandem fet gate, 271
a-c fet, 84
a-c gate, 273
a-c line regulator, 598
amplifier stabilization, 10
armature tester, 774
automatic servo balanced capacitance, 663
beam-tube-switched, 705
blood pressure indicator, 384
capacitance, 357
cascode-adder null detector, 82
chopper-driven thermistor, 762
constant-voltage regulated power supply, 592
conveyor belt slip detector, 353
crt light-pen pulse height detector, 81
d-c level-shifting, 83
diode sampling, 82
drain-coupled-adder null detector, 81
enamel thickness monitor, 87
gyro temperature control, 758
impedance, 387
intrusion-alarm capacitive, 88
inverter, 325
line-voltage-sensing, 501
liquid level measurement, 363
magnetoresistor scr control, 83
magnetoresistor triggering, 84
memory core sense amplifier, 812
microvolt-switching, 83
millivolt pulse sampling, 84
optoelectronic resistor, 83
overtone crystal parameters, 83
phase-difference detector, 81
plate-cathode follower drive for
Wien oscillator, 27
pulse-energy-measuring bolometer, 370
R-C oscillator, 674
resistance thermometer, 761
resistor testing, 808
scr capacitor-charging, 169
scr regulated power supply, 507
scr single-phase motor control, 352
self-balancing shunt-type, 83
static-inverter regulator, 316
strain-gage amplifier, 366
telephone baseband amplifier, 82
temperature-compensating, 757
temperature-control, 761, 762
thermal, in variable-frequency power
supply, 589, 593
thermistor, 759
thermocouple tester, 768
three-phase inverter, 315
transformerless scr inverter, 323
tunnel-diode a-c gate with toroid, 274, 280
variable-frequency Wien oscillator, 670
Wheatstone in enlarger exposure indicator, 489
Wien fet, 675
Wien pulsed oscillator, 530
Wien rejection filter, 222
- Bridge discriminator, phase, 201
- Brightness control, ujt-scr for 500-w lamp, 137
- Buffer, infinite-impedance fet for 60-cps
notch filter, 18
ring counter, 160
unity-gain, 11
- Buffering, 10-Mc flip-flop, 152
- Burglar alarm (see Alarm)
- Butterworth attenuation, active audio filter, 216
- Butterworth filter, active low-pass R-C, 217
- Butterworth triple, radar clutter simulator, 546
- Cable fault finder, marker generator, 764
time-base, 769
transmit pulse generator, 772
- Cable propagation delay, measurement, 249
- Cable wire, locator, 769, 770

Cadmium sulfide-neon inverter, 349
 Cadmium sulfide-neon latch, 328
 Calibration, frequency standard with WWV, 111
 modulation percentage meter, 416
 Calibrator, digital voltmeter, 31
 portable cro, 98
 Calling system, citizens band, 572
 Camera, 16-mm sound-on-film drive, 581
 control, 490
 control receiver, 615
 underwater flash, 227
 Camera (See also Photography)
 Capacitance, measuring, 368
 Capacitance bridge, liquid level measurement, 363
 Capacitance control, 30,000-rpm tachometer
 transducer, 87
 aircraft fuel gage, 86
 continuously variable electronic, 88
 extrusion of plastic on wire, 87
 fence alarm, 86
 in-circuit capacitor tester, 88
 intrusion alarm, 87, 88
 lamp, 85
 proximity switch, 86
 sensing cane for blind, 88
 stabilizer for radar altimeter, 87
 wire enamel thickness monitor, 87
 Capacitor, 0.1-100 mfd variable electronic, 88
 voltage-variable, 394, 397, 398
 Capacitor bank, firing switch, 241
 Capacitor charger, constant-current, 169
 Capacitor forming, power supply, 591
 Capacitor tester, in-circuit, 88
 Car (see Automotive)
 Carcinotron, control for receiver, 572
 Card scanner, mark-sensing, 478
 Cardiac pacemaker, batteryless, 392
 two-transistor, 383
 Carrier-current control, receiver, 617
 Cascode, 45-Mc, 285
 multipurpose integrated-circuit chip, 301
 Cascode amplifier, i-f with agc, 62, 63
 Cascode circuit, mvbr, 422, 423, 425, 430
 Cascode follower, 5-cps, 12
 Cascode tube, noise-figure measurement, 358
 Cathode follower, secondary-emission pentode, 16
 Cathode ray, 50-kc vertical deflection, 90
 8-22 Mc marker generator, 91
 800-v sweep, 698
 7-kv magnetic-deflection power supply, 491
 7-kv preferred dual-triode crt supply, 494
 alignment correction, 96
 alphanumeric generator, 102
 armature tester, 774
 beam intensity control, 95
 chopper-produced multitrace display, 107
 coarse-deflection switch, 101
 cro as sonar bearing indicator, 829
 deflection control, 207
 display numeral generator, 101
 fine-deflection switch, 101
 focus current regulator, 92
 grid-1 bias regulator, 99
 ground pulse blanker, 95
 gyro vibration amplifier, 98
 high-speed hybrid bootstrap sweep, 697
 high-voltage supply, 90
 horizontal amplifier, 98
 hybrid bootstrap sweep, 702
 intensity-modulation pulse amplifier, 842
 intensity-modulation radar video chain, 839
 level-controlled strobe, 89
 light-pen amplifier, 92
 light-pen preamp, 99
 light-pen pulse height detector, 81
 linear integrated-circuit tester, 295
 low-frequency differential amplifier with
 400-v peak swing, 193
 low-frequency sync oscillator, 99
 marker pulse amplifier and mixer, 96
 measuring axis-crossing interval, 90
 monostable unblanking gate, 420
 nanosecond display, 208
 number generator, 100
 portable cro calibrator, 98
 preferred triode driver, 847
 radar intensity-modulating crt driver, 839
 raster display, 91
 regulator-charger for portable cro, 74
 sampling pulse stretcher, 92
 single-frame tv photography timer, 489
 sonar display, 204
 staircase generator, 688
 strobe pulse generator, 96
 sweep trigger, 89
 tangent-function sweep, 698
 tape waw and flutter measurement, 716
 tone signal rejector, 570
 trace brightness equalizer, 99
 trace intensifier, 98
 transistor beta display, 93
 triangular-wave sweep, 96
 trigger multivibrator, 94
 tunnel-diode curve-tracer, 94, 781
 tunnel-diode vhf synchronizer, 97
 two-signal display, 89
 unblanking amplifier, 95
 unblanking pulse, 542
 unblanking pulse generator, 99
 unblanking signal generator, 106
 variable-length radar sweep, 551
 variable prr and pulse width generator, 532
 wide-band deflection amplifier, 94
 Z-axis amplifier, 99

Cathode ray, Z-axis modulation, 99
 (See also Display)
 Cathode-ring, diode gate, 273
 Catv, up converter, 146
 Cave, magnetic-induction mapping, 374, 376
 Celestial guidance, active low-pass filter, 220
 automatic threshold control, 216
 Cement setting, timer, 359
 Ceramic cartridge, preamp, 39, 40
 Ceramic filter, i-f amplifier, 283, 287
 Channel selector, discriminator, 200
 Character generator, alphanumeric, 102
 cathode-ray numerals 1-7, 100
 coarse-deflection switch, 101
 deflection amplifier, 102
 fine-deflection switch, 101
 harmonic oscillator, 102
 numeric, 100
 overlapping-dot, 208
 pulsed-oscillator, 100
 signal combiner, 101
 Character reader, alphanumeric, 640
 dilating circular scan, 640
 largest-signal selector, 343
 script, 336
 solar-cell, 114
 vertical positioner, 640
 Charger, constant-current, 169, 699
 Chopper, 0-1 kc voltage-controlled oscillator, 202
 10-kc, 496
 100-kc, 108
 analog switch, 103
 asynchronous square-wave, 106
 balanced silicon-diode lattice, 55
 data amplifier, 172
 d-c operational amplifier, 103, 106
 d-c voltage comparator, 118
 difference circuit and integrator, 307
 display switching in integrated-circuit tester, 303
 fet shunt, 106
 gain-controlling, 477
 gate-turnoff d-c, 108
 Hall-generator, 108
 hybrid d-c feedback amplifier, 107
 infrared receiver, 292
 lamp-photo cell, 106
 mechanical with 400-cps amplifier, 103
 mechanical with fet amplifier, 108
 multitrace cro display, 107
 normally-off gate-turnoff, 107
 normally-on gate-turnoff, 108
 null indicator, 472
 pentode, 103
 phasemeter, 808
 photoelectric, 108, 858
 printed-circuit Bright, 107
 servo comparator amplifier, 115
 solar-cell regulated power supply, 105
 speed-controlling for battery-powered vehicle,
 107
 starved d-c input amplifier, 177
 temperature-regulating, 762
 transient-suppressing trigger, 816
 transistor pulse-width regulator, 105
 two-transistor dewpoint-measuring, 480
 Chopper stabilization, d-c amplifier, 104
 strain-gage amplifier, 366
 Chroma amplifier, transistorized, 747
 Chroma control, tv, 751
 Chromatograph, integrator, 308
 Chrominance, French SECAM, 748
 Chronometer, electronic, 113
 Chronotron, measuring coax delay, 365, 372
 Circuit breaker, d-c gate-turnoff, 708
 Citizens band, 27-Mc a-m transmitter, 795
 27-Mc collector-modulated transmitter, 397
 class-B transceiver, 794
 class-C transmitter, 794
 crystal oscillator, 462
 decoder, 795
 interference filter, 792
 squelch, 794, 795
 tone-operated calling system, 572
 transmitter without relay, 794
 tunnel diode-crystal oscillator, 454
 Clamp, 1-Mc d-c restorer, 544
 keyed tv camera, 735
 millivolt pulses, 84
 pulse generator, 519
 radar ppl sweep, 557
 radar yoke driver, 550
 Clipper, heater-voltage-regulating, 598
 phase-demodulating, 191
 power-supply, 352
 pulse height analyzer, 536
 radar modulator, 551
 six-diode sine-wave, 518
 video, 840
 Clock, 2,500-cps phase synchronizer, 112
 100-kc Pierce crystal, 109
 600-kc pulse shaper, 125
 809.11-kc oscillator, 112
 10-Mc counter, 152
 10-Mc oscillator, 113
 10-Mc three-phase, 113
 12-Mc four-phase, 112
 150-Mc gate, 269
 500-Mc gate, 279
 16-bit binary word generator, 681
 ambiguity-preventing latch, 328
 cesium beam-tube control for 5-Mc primary
 standard, 109
 cesium resonance synthesizer, 111
 computer testing, 110
 crystal-controlled mvbr, 110

Clock, digit square-wave amplifier, 110
 disk-track pulse generator, 112
 driver gate, 121
 electronic, 113
 fallout arrival time, 560
 frequency-dividing WWV calibrator, 112
 fuse timer, 111
 one-shot mvbr, 423
 pulse amplifier, 110
 sidereal regenerative oscillator, 113
 synchronizing to 300-pps trigger, 113
 WWV check, 111
 WWV-controlled, 111
 Clock pulse generator, 25-nsec 0.5-amp, 109
 Clutch control, timer, 790
 Coating, thickness measurement, 362, 365
 Coax, drive amplifier, 515
 Cockcroft-Walton multiplier, radiation survey
 meter, 563
 Codan, improved crystal-controlled, 812
 Code generator, alarm siren, 2
 double-pulse, 556
 Coded pulse comparator, radar beacon, 78
 Coding generator, fire alarm system, 2
 Coincidence, nuclear particle timer, 783
 phototube pulses, 479
 Coincidence circuit, echo pulses, 826
 telephone data readout, 122
 tunnel-diode, 566
 Coincidence control, fluorescent-lamp display, 206
 Coincidence detector, 100-microamp pulses, 539
 energy-loss telescope, 726
 pulse, 539, 561
 Cold-cathode tube, 6-kc counter, 154
 counter, 149
 numerical display, 206
 shift register, 150
 Color-burst regenerator, video tape, 748
 Color tv, automatic fine tuning, 751
 Colpitts oscillator, 3-Mc, 392
 Combiner, telemetry diversity, 730
 Communication, delta-sigma modulator, 393
 light-beam, 401, 402
 Commutator, analog switch, 103
 Hall generator, 623
 matrix-controlled, 789
 time-division telemetry, 730
 Commutator simulator, telemetry, 678
 Compactron, f-m stereo adapter, 693
 Comparator, 20-kc control amplifier, 118
 amplitude, 119
 analog voltage, 586
 analog-digital converter, 36
 autopilot dual-accelerometer, 118
 battery-charging voltage, 73
 beacon radar code, 78
 bilateral-transistor voltage, 119
 capacitor charging voltage, 117
 capacitor-storage feedback boxcar, 116
 chopper-type d-c voltage, 118
 code conversion, 116
 d-c voltage, 850
 differential voltage, 117
 digital-voltmeter, 114
 diode switching time, 766
 frequency, 66
 frequency with WWV, 568
 go-no-go voltage, 119
 insulation resistance tester, 772
 missile count-down switch, 706
 nonoverlapping pulse, 118
 or gate, 343
 over-or-under-voltage, 119
 pdm keyer, 728
 pulse, 119, 156, 509
 pulse amplitude, 114
 pulse voltage, 115
 random noise amplitude, 268
 resistance, 375
 retina welder control, 381
 series-Schmitt amplitude, 810
 servo amplifier, 115
 tunnel-diode voltage, 117
 two-stage load current switch, 118
 video signal, 119
 voltage, 33, 606
 Compensation, low audio frequency, 551
 magnetic tape playback, 717
 temperature in induction receiver, 573
 Compensator, servo frequency, 647
 Compression, 50-db dynamic-range video, 848
 telemetry signal, 724
 Compressor, audio volume, 721
 cube-root dynamic audio to megacycle, 267
 Computer, 5-Mc memory driver, 121
 10-nsec amplifier, 126
 10-nsec gate and mixer, 123
 10-nsec register-driver, 667
 25-nsec 0.5-amp clock pulse generator, 109
 0.3-microsec-recovery delay, 184
 16-bit word generator, 681
 alphanumeric output display, 102
 analog display at 50 kc, 96
 analog overvoltage alarm, 5
 analog-ratio Hall multiplier, 415
 analog sample-and-hold circuit, 36
 analog-type ratio, 265
 analog voltage multiplier, 415
 analog voltage source, 492
 arithmetic cell, 120
 bilateral-transistor digital comparator, 119
 choke-controlled differentiator, 125
 clock gate, 121
 clock pulse shaper, 125
 double-bridge magamp open-fuse detector, 354

- Computer, eeg waveform analyzer, 384
 fault-location alarm, 3
 full adder, 123
 function generator, 266
 half-adder, 121
 Hall-effect squaring multiplier, 415
 low-cost basic inverter, 342
 magamp open-fuse detector, 351
 magnetic-spoke disk memory pulse generator, 125
 magnetic-spoke disk read-write circuit, 125
 megapulse test generator, 110
 memory inhibit driver, 122
 memory-plane driver, 123
 memory readout amplifier, 120
 memory-sensing amplifier, 123, 124
 memory strobe generation, 122, 124
 operational d-c amplifier, 177
 paired-inverter, 231
 phone-line interconnection, 732
 potentiometer error compensation, 35
 power switching, 125
 power transistor switching, 708
 reciprocal circuit, 120
 register indicator, 664
 ringing-type pulse generator, 528
 switching diode tester, 777
 symmetrical saturated 2N711 flip-flop, 666
 telephone traffic data recorder, 122
 thin-film memory current driver, 124
 thin-film memory sense amplifier, 124
 thin-film pulse generator, 121
 time-shared troubleshooting scope, 122
 universal NBS diode logic, 335
 wiring short-circuit alarm, 4
- Conductivity, testing, 781
 Contacts, testing under vibration, 779
 Continuity tester, wire harness, 765
 Control, 100-cps phase-shift oscillator, 310
 20-channel audio tone decoder, 132
 a-c power outlet interlock, 713
 acceleration, 408, 410
 alternator frequency, 203
 autopilot, 130
 autopilot gyro, 130
 balanced-triode sine-wave modulator, 399
 battery charger turn-on and turn-off, 73
 blood pressure, 385
 body-capacitance switch, 85
 cable driver-receiver, 133
 camera shutter, 485, 489, 490
 carbon dioxide in iron lung, 388
 carrier-powered, 7
 chopper-type modulator, 399
 continuously variable capacitance, 88
 conveyor shutdown for jams, 134
 crystal lapping machine, 249
 Control, d-c voltage level detector, 762
 Diamond modulator, 396
 differential d-c amplifier, 198
 dual-function bistable reactor scram, 130
 duo-diode half-wave switch modulator, 403
 electro spark machine, 128, 129
 feedback-type regulator amplifier, 129
 filament emission in gas analyzer, 127
 film-break detector, 131
 fluorescent lamp dimming, 136
 frequency-sensitive photoelectric, 483
 full-range 500-w lamp dimmer, 137
 full-wave general-purpose td-scr, 135
 gyro balancer, 89
 gyro torquing switch, 136
 heart-lung machine, 377
 heart rate, 390
 high-pot tester, 775
 induction heater, 210
 inverter idling current, 321
 lamp brightness, 128
 laser modulator current, 167
 liquid-cooling, 128
 liquid level, 136, 363
 machine-gun firing rate, 131
 magnetic tape skew, 356
 metal-forming, 134
 missile-destruct f-m receiver, 619
 missile guidance, 127
 mobile radio repeater, 442
 motor transient anticipator for counter, 135
 parking meter, 293
 phase discriminator for heating and cooling, 200
 power-summing d-c amplifier, 177
 power supply output voltage, 495
 printed-circuit assembler, 652
 printing register, 148
 programmed lamp preheater, 129
 proximity, 132
 pulse length of intervalometer, 544
 punch press, 706
 quartz crystal film thickness, 249
 reactor startup, 128
 remote gain, 14
 resistance-triggered, 7
 retina welder output, 379, 381, 391
 ring modulator, 402
 rocket roll channel, 132
 saturable-core voltage-monitoring, 134
 saturating-mvbr on-off, 131
 scr lamp dimmer, 133
 sensitive relay with reflex circuit, 137
 skew, 356
 snap-action a-f power level switch, 127
 telescope servo, 662
 thin-fail welder, 862
 time-delayed Schmitt sensor, 136
 timer, 786, 790
- Control, timer motor, 790
 triac lamp dimmer, 133
 triode clamp modulator, 401
 ujt latch for inductive-load scr, 329
 variable-current-overcurrent protection, 133
 variable-plate-resistance modulator, 402
 voice-activated, 133
 zero-crossing for scr's, 136
 Control (See also Motor control; Photoelectric; Remote control)
 Conversion gain, measurement, 358
 Converter, 10-Mc dynamic notch filter, 222
 27-Mc CB transmitter, 139
 30 Mc to 5.5 Mc, 143
 88-108 Mc f-m tunnel-diode with afc, 145
 255-Mc oscillator, mixer, 144
 400-Mc low-noise, 145
 420-Mc oscillator-mixer, 140
 450 Mc to 30 Mc, 144
 450 Mc to 105 Mc with 2N2415, 139
 450 Mc to 105 Mc with 2N2996, 139
 1,000 Mc to 200 Mc, 441
 11-bit digital to analog, 32
 12-300 v d-c, 71
 28-162 v d-c at 10 w, 145
 28-400 v d-c, 496
 28-420 v d-c at 125 w, 147
 30-325 v d-c, 138
 5,000 v d-c, 496
 analog tv to pulse code, 342
 autodyne, 569
 autodyne with 455-kc i-f, 147
 balanced-input noise-canceling, 138
 battery supply for instruments, 610
 capacitively tuned tunnel-diode tuner, 823
 cciv for bar graph display, 206
 Citizens band, 138
 Citizens band crystal-controlled, 146
 Civil Air Patrol, 147
 community-tv, 146
 d-c to audio, 145
 d-c/d-c, 313, 491
 d-c/d-c 7-kv crt supply, 491
 d-c/d-c auto ignition, 68
 d-c dual-pulse drive, 807
 d-c level shifter, 542
 d-c signal level, 768
 d-c voltage, 143
 differential-mvbr magnetic, 326
 digital-analog, 31
 digital logic level, 144
 dual-triode magnetic, 326
 flowmeter frequency, 247
 frequency to d-c, 146
 frequency-to-voltage, 146
 Gray binary code, 207
 grounded-collector Hartley, 456
 height-to-time, 726
 high-power stable mvbr, 433
 integrated-circuit, 198
 linear voltage-frequency, 421
 mobile receiver, 578
 mos fet, 144
 negative-impedance, 141
 one-shot rz to nrz, 426
 phase inverter, 323
 photoflash, 225
 power, 146
 pulse height to digital signal, 142
 pulse height to time, 143, 542
 pulse height to width, 275
 pulse width to analog, 36
 pulse width-sequence, 423
 random-pulse digital-analog, 35
 ringing-choke d-c to d-c, 141, 142
 sine to sawtooth or square wave, 147
 superregenerative quench, 286
 temperature-frequency, 761
 transducer-controlled 70-Mc crystal oscillator, 140
 transducer voltage to resistance, 84
 transistor-magnetic, 322
 triangular to sine, 542
 tunnel-diode uhf tuner, 823
 uhf tv, 823
 ultralinear voltage to pulse width, 636
 voice to pcm, 144
 voltage-current, 537
 voltage-frequency, 140, 142, 143, 145, 629
 voltage-pulse width, 145
 voltage-time, 35
 (See also Analog-digital converter; Digital-analog converter; Mixer)
 Conveyor, bolt slip detector, 353
 jam detector, 482
 Cooling-liquid-density gage, 128
 Core, pulse-width-measuring, 374
 Core readout, 5-aperture, 33
 Coronameter, polarized-light, 476
 Correlator, polarity-coincidence multiplier, 414
 receiver noise, 440
 Coulometer, 30 sec-350 hr delay, 188
 Counter, 1-kc glow-tube, 161
 4-kc glow-tube, 154
 20-kc scs ring, 165
 40-kc bistable mvbr, 154
 40-kc ujt bistable ring, 164
 50-kc gas tube with transistors, 155
 100-kc clock, 109
 100-kc gas tube, 155
 500-kc flip-flop, 666
 0.5-Mc pulso, 565
 1-Mc complementary-transistor, 158
 1-Mc gas tube, 163
 5-Mc tunnel-diode, 166
- Counter, 10-Mc buffered flip-flop, 152
 10-Mc dynamic notch filter for noise rejection, 222
 200-Mc, 788
 500-Mc biquinary, 151, 158
 500-Mc high-speed quinary sync control, 164
 10-channel multiplexer, 152
 7-digit binary, 166
 400-pps decade scaler, 157
 400,000-rpm digital tachometer, 69
 2,000-sec, 665
 10-stage ring, 204
 15-stage low-current-drain, 160
 adf decade chain, 151
 adjustable strobe, 224
 alpha particle, 563, 567
 anode-triggered ring, 160
 antiduplication circuit, 150
 backward and forward, 160
 base-gated binary, 165
 basic binary, 156
 batching, 150
 bed, 156
 bed conversion, 151
 beam-switching decade, 165
 bidirectional ring scr, 158
 binary high-speed, 788
 binary neon, 208
 binary npn flip-flop, 811
 binary scaler stage, 149
 biquinary, 153
 birds in flight, 148, 150
 bistable ring, 161
 boosting electromechanical speed, 149, 150
 buffer circuit, 160
 closed-ring data-sampling, 624
 coincidence-triggered decade, 155
 cold-cathode blocking-oscillator, 625
 cold-cathode ring, 157
 collector-triggered with trigger amplifier, 814
 constant-amplitude doppler oscillator, 448
 conveyor, 149
 decade driver, 262
 decimal-coded, 163
 digital phase meter, 468
 diode matrix decimal, 159
 divide-by-5 ring, 240
 doppler cycle, 23
 electromechanical drive for Geiger-Müller, 561
 flip-flop, 165
 four-stage hybrid ring, 154
 four-stage npn binary, 814
 four-transistor binary flip-flop, 153
 frequency-measuring, 191, 192
 frequency-multiplying 1,000 times, 258
 gate turnoff ring, 164
 Geiger-Müller, for tracers, 564
 Geiger-Müller drive for scaler, 541
 gunshot, 149
 halogen-type Geiger, 558
 integrated-circuit quad block binary, 304
 lamp-flashing ring, 223
 low-power scs ring, 163
 magnetic-core frequency-dividing, 241, 243
 magnetic-core ring, 517
 motor transient suppressor, 135
 multiaperture-core, 159
 multijunction semiconductor decade, 152
 neutron, 726
 Nixie-tube scs ring, 166
 nonoverlapping pulses, 118
 nuclear tracks, 639
 npn ring, 820
 predetermimed-pulse, 312
 preset neurosurgery pulse, 390
 proximity-detector, 132
 reversible decade, 162
 reversible ten-stage ring, 157
 ring, 729
 ring-of-7, 159
 ring-of-10, 162
 ring with visual readout, 161
 sampling-period time recorder, 625
 scale-of-ten divider, 240
 Schmitt input trigger, 814
 Schmitt trigger for buttons, 148
 scs binary, 162
 shift register, 154
 staircase, 685
 static alternator, 314
 steering for reversible decade, 153
 stepper-motor pulse-generating, 411
 storing accumulated count, 156
 strontium-90 photorelay, 560
 symmetrical saturated flip-flops, 666
 tachometer auto-locking gate, 68
 tachometer display, 70
 teleprinter character, 787
 three-phase ring, 314
 transformer-controlled, 165
 transformer-controlled shift register, 668
 ultrasonic sing-around transmitter, 834
 unijunction ring, 164
 unijunction transistor, 164
 variable scale factor, 162
 variable-timing ring, 163
 voltage dips, 858
 (See also Scaler; Timer)
 Counting-rate meter, Geiger-Müller, 562
 Coupler, R-C directional, 357
 Coupling, light-beam, 709
 Crovasco detector, audio oscillator, 452
 Crowbar, ignition control, 765
 stellarator, 708
 Crowbar circuit, 300-kw tube tester, 776

- Cryogenics, critical current test set, 778
- Crystal, impedance meter, 776
 - lapping control, 249
- Crystal detector, biasing circuit, 574
- Crystal oven, temperature control, 739
- Current control, 0-100 ma, 171
 - 2.8-microamp, 170
 - 0.5, 5, or 50 ma, 168
 - 1-ma, 171
 - 1 ma for 10-to-400 v input with scr, 167
 - 10-ma, 167
 - 15,000-v regulated power supply, 736
 - adjustable rise and fall times, 523
 - amplitude director, 167
 - auto generator, 169
 - battery charger, 74, 75
 - capacitor-charging, 143
 - capacitor-charging pulse, 169
 - collector load, 171
 - constant a-c, 168
 - d-c logarithmic amplifier, 174
 - d-c regulator for fluorescent lamps, 169
 - intrusion alarm, 1
 - laser modulator, 167
 - magnetic core tester, 767
 - overload, 171
 - precision reference-amplifier regulated supply, 170
 - pulse generator, 780
 - pulse height-time converter, 143
 - reactor scram system, 133
 - resistance welder, 863
 - servo response speed-up, 645
 - single 50,000-amp pulse, 170
 - superconducting contacts, 778
 - temperature-compensated, 170
 - temperature-compensated matched-transistor, 168
 - transistor protection, 170
 - trigger, 815
 - variable d-c supply, 496
- Current detector, nanampere sensing, 364
- picampere, 369
- Current measurement, micromicroammeter, 371
- Current regulator (see Regulated power supply)
- Curve tracer, fet adaptor, 764
- transistor and diode, 770
- tunnel-diode, 94, 781
- tunnel-diode adaptor, 766
- waveform generator, 688
- Damping, voice-operated transmitter, 801
- Darlington, sweep linearity correction, 699
- Darlington amplifier, 100-meg input impedance, 15
- servo, 636
- Darlington circuit, broadband amplifier, 10
 - fet timer, 785
 - shift register driver, 667
- Darlington connection, 12-v precision regulated supply, 602
 - series regulated power supply, 607
- Darlington driver, low-noise fet audio, 53
- Darlington pair, integrated-circuit servo amplifier, 304
 - servo amplifier, 645
- Darlington series regulator, 12-v power supply, 614
- Data amplifier, dual-chopper, 172
- Data transmission, Kita diode switching, 706
- phone line, 732
- D-c amplifier, 400-cps summing and power, 176
 - 20-channel audio tone decoder, 132
 - 250,000-meg input impedance, 174
 - 51-db power gain, 197
 - active redundant, 175
 - actuating relay for input voltage change, 176
 - analog-computer operational, 177
 - analog-digital converter, 30
 - auto-testing telemetry, 70
 - balanced differential operational, 198
 - balancing, 172
 - cascade differential, 198
 - chopper-stabilized operational, 106
 - chopper-type operational, 103
 - crystal oscillator control, 459
 - current-summing operation, 177
 - d-c voltmeter, 854
 - differential, 193, 194
 - dual-chopper transistorized, 172
 - electrometer, 375
 - electronic fuse, 508
 - fet with mechanical chopper, 108
 - fet operational, 174
 - fet sample-hold, 172
 - four-stage differential, 195
 - Hall-generator chopper, 108
 - high-input-impedance, 176
 - hybrid chopper-stabilizer, 104
 - hybrid chopper-type, 107
 - integrated wideband, 296
 - logarithmic current compressor, 174
 - low-drift 400-cps mechanical chopper, 103
 - low-drift chopperless, 174
 - low-impedance transducer, 176
 - low-level complementary-pair, 173
 - oceanography recorder, 583
 - power-summing, 177
 - R-C directional coupler, 357
 - regulated power supply, 593, 614
 - starved low-noise, 177
 - stereo f-m tuner, 695
 - telemetry summing, 177
 - temperature-stable unity-gain, 173
 - triangular-wave generator, 629
 - tunnel-diode discriminator, 535
- D-c amplifier, two-stage differential with common-mode feedback, 198
 - unity-gain differential, 195
 - unity-gain high-input-impedance, 175
 - unity-gain temperature-stabilized fet, 173
 - varistor control, 175
 - voltage level detector, 762
 - watch timer, 790
 - wide-dynamic-range differential, 196
- D-c restorer, 500 kc to 1 Mc, 544
- Decade counter, gate turnoff, 164
 - reversible, 153
- Decoder, 11-bit, 32
 - 20-channel audio tone, 132
 - C8 receiver, 795
 - ladder-type digital-analog, 31, 36
 - radar ppi, 78
 - reference switch, 37
 - serial, 34, 37
 - television remote-control receiver, 622
 - twin-phantastron, 522
- Decommutator, video recorder, 394
- Delay, 500-kc amplifier-tuning, 282
 - 0-190 mila nonprecise phantastron, 184
 - 5-175 mila nonprecise phantastron, 184
 - 5-nsec complementary-transistor integrated-circuit logic, 305
 - 0-2,440 microsec bootstrap, 188
 - 0-2,440 microsec phantastron, 184
 - 0.5-microsec-recovery, 184
 - 0.58 to 4.65 microsec, 181
 - 50-350 microsec bootstrap, 186
 - 50-350 microsec phantastron, 188
 - 0.4 millise-1 min scr-uit, 183
 - 0.4 millise-4 min, 180
 - 0.4 millise-4 hr, 180
 - 1-300 millise nonstable-mvbr, 187
 - 6 millise-1.5 sec variable, 182
 - 8 millise-5 sec line-operated, 179
 - 30 sec-350 hr, 188
 - 1-4 min, 185
 - 15-min, 786
 - 1-hr voltage-sampling, 186
 - 5-hr, 187
 - 30-hr pulse-stretching, 541
 - a-c operated scs, 187
 - adjustable sanatron, 230
 - blocking-oscillator, 338
 - cascaded complementary td-transistor, 187
 - continuously variable ferrite-core, 179
 - continuously variable phase, 466, 472
 - digital pulse, 185, 186, 189
 - eighth-pulse-gated, 272
 - expanded nonstable mvbr, 183
 - fast-recharging phantastron, 185
 - fast-recycling relay, 187
 - fixed bootstrap, 185
 - fixed mvbr, 186
 - four-transistor phantastron, 183
 - interval timer, 244
 - isolating-diode nonstable mvbr, 186
 - linear pulse amplifier, 512
 - measuring nearly simultaneous events, 180
 - nonstable mvbr, 189
 - mvbr turnoff, 417
 - phantastron, 680
 - phantastron in radar target acceleration simulator, 683
 - phantastron integrator, 185, 189
 - phasometer, 471
 - photoelectric, 484
 - precision 3-35 microsec, 188
 - precision 3-min scr, 187
 - preferred fast-recovery phantastron, 181
 - preferred nonstable mvbr, 181
 - preferred phantastron, 182
 - pulse generator output, 529
 - pulse pair, 178
 - pulse-stretching, 543
 - random-time, 227
 - relay break, 186
 - relay dropout, 188
 - relay make, 180, 182
 - ring-starting, 188
 - scs load-current, 187
 - scs turnoff, 189
 - six-step ring unit, 181
 - solid-state with a-c output, 179
 - temperature alarm, 1
 - temperature-compensated phantastron, 182
 - three-phase inverter, 314
 - three-range nonprecise phantastron, 184
 - time-base, 769
 - trigger generator, 826
 - uit relay-closing, 188
 - ultrasonic driver, 462
 - variable turn-on and turn-off, 178
 - voltage-controlled radar, 178
 - (See also Timer)
- Delay line, driver, 715
 - pulse shaper, 543
 - radar clutter simulator, 546
- Delay measurement, digital circuit, 524
- Demodulator, antisideband, 192
 - asymmetry, 133
 - boxcar-type modulation envelope, 192
 - color tv, 747, 752
 - diode quad, 403
 - flip-flop, 130
 - f-m data, 191, 192
 - f-m stereo, 694
 - f-m stereo matrixing, 691
 - gyro pitch channel, 659
 - linear, 192
 - low-frequency f-m, 717
- Demodulator, microwave radiometer, 190
 - phase, 191
 - phase-sensitive, 190
 - ppm multiplexer, 395
 - ppm telemetry, 729
 - pulse width to analog, 36
 - synchronous, 115
 - synchronous color tv, 751
 - synchronous radar, 191
 - transformerless full-wave, 191
 - transistorized 8-Y, 754
 - uhf scatter klystron, 192
 - vertical acceleration recorder, 584
 - (See also Detector)
- Densitometer, flying-spot integrating, 364
- Density gage, liquid, 128
- Depth indicator, portable, 836
- Detection, backbones, 440
 - envelope, 440
 - slope, 766
 - weak i-f signals in high noise, 414
- Detector, 27.255-Mc superregenerative remote control, 621
 - 200-Mc r-f radiation, 6
 - 100-microamp coincident pulses, 539
 - 6.0 v, 808
 - anticoincidence, 540
 - armature faults, 774
 - bipolar pulse, 813
 - boxcar, 360
 - bridge null, 81
 - C8 40-kc quench, 793
 - core saturation, 374
 - cosmic-ray neutron, 726
 - crack and void, 367, 370
 - crt blip, 81
 - current amplitude, 167
 - current-controlled threshold, 422
 - current-level, 203
 - d-c level, 762
 - differential voltage or current, 6
 - eeg waveform zero, 383
 - engine heat, 293
 - foil-loop intrusion, 1
 - gamma-ray, 564
 - hold-sample-hold, 472
 - icoborg, 190
 - illumination rise, 1
 - integrating, 390
 - magnetic drum read amplifier, 587
 - magnetic tape flaws, 779
 - millimeter-wave, 574
 - millivolt threshold, 816
 - missing-pulse, 6, 502
 - missing-pulse and d-c dropout, 6
 - multichannel signal, 545
 - noise-filtering, 440
 - nuclear explosion flash, 559
 - oscillating linear-slope tv, 743
 - paper flaws, 479
 - phase-difference bridge, 81
 - phase-measuring, 466, 472
 - phase-sensitive, 440
 - phase-sensitive diode, 88
 - phase-sensitive impedance-measuring, 387
 - photographic film breaks, 131
 - picampere current, 369
 - polarity, 34
 - preferred a-m receiver, 333
 - preferred pulse-modulated video, 841
 - projectile glow, 483
 - pulse coincidence, 118, 539
 - pulse sequence, 269, 540
 - pulse train absence, 540
 - radar boxcar, 557
 - Schmitt-trigger threshold, 812
 - sensitive aural-visual parasitic, 449
 - signal dropout, 7
 - signal level, 531
 - sine-wave zero-crossing, 538
 - sonar peak amplitude, 832
 - square-law with 40-db dynamic range, 266
 - ssb, 575
 - synchronous with reed-switch control, 311
 - synchronous tape playback, 714
 - target Y-position, 430
 - time-constant tv remote control, 619
 - transient-pulse, 329
 - transient spike pulse, 4
 - ultrasonic reactor slug cladding flaw, 830
 - vlf intrusion, 4
 - voice for pcm, 144
 - voltage limit, 535
 - waveform time-sample, 773
 - weak-current, 815
 - weak signal in noise, 90
 - zero-crossing, 519, 810
 - zero-crossing for scr power control, 136
 - (See also Demodulator)
- Dew-point hygrometer, alpha-counting, 558, 563
- Dowpoint measurement, photoelectric servo, 480
- Diamond circuit, measuring a-f phase shift, 361
- Diamond modulator, d-c input signal, 396
- Diathermy, frequency drift alarm and control, 212
- (See also Electronic heating; Medical electronics)
- Dictating machine, battery-operated, 719
- Dielectric heater, 27.12-Mc crystal-controlled, 209
- Difference circuit, two-pole chopper, 307
- Differential amplifier, 230-kc radar control, 193
 - 51-db power gain, 197
 - 1-ma constant-current scr supply, 167
 - 8-v voltage regulator, 606
 - 400-v output swing with transistors, 193
 - balanced operational, 198
 - canceling supply variations, 198

- Differential amplifier, chopperless, 194
common-mode feedback, 198
d-c millivoltmeter, 853
direct-coupled, 194
four-stage, 195
integrated-circuit, 195-199
memory sense, 296
pill-tracing integrator, 391
single-stage integrated, 198
unity-gain d-c, 195
variable-gain, 199
voltage-regulating inverter, 197
voltage regulator, 194
wide-dynamic-range, 196
wide-range variable delay, 182
- Differential voltage, signal comparator, 117
- Differential voltmeter, high-stability, 852
- Differentiator, choke-controlled, 125
pulse, 374, 766
ultrasonic pulse, 828
- Diffractionmeter, neutron, 559
- Digital-analog converter, 31
10-bit, 35
11-bit, 32
accelerometer, 32
compute amplifier, 34
current-subtracting modulator, 646
hold amplifier, 37
ladder-type decoder, 31, 36
random-pulse, 35
reference switch, 37
- Digital communication, modulator, 393
- Digital computer (see Computer)
- Digital data, phone-line transmission, 732
rz to nrz, 426
- Digital voltmeter, calibrator, 31
- Dimmer, fluorescent lamp, 136
light-activated scr, 473
scr, 133, 476
silicon symmetrical switch, 472
triac, 133
uht-scr full-range 500-w lamp, 137
- Diode, harmonic generator, 452
light-emitting, 226
light-emitting modulator, 401, 402
- Dip meter, tunnel diode, 365
- Direction finder, 15-500 kc, 498
antenna servo, 649
degree counter, 151
goniometer motor amplifier, 405
phasometer, 465
r-f switch, 704
- Discriminator, 3 cps-300 kc prf, 201
23-Mc crystal, 56
alternator frequency control, 203
amplitude ratio indicator, 67
channel-selecting, 200
crystal oscillator control with WWV, 111
crystal reference frequency-hunting, 792
data sampler, 624
delta tv sound, 743
differential, 203
digital f-m, 299
f-m, 331
Foster-Seeley, 248
Foster-Seeley color tv sound, 749
Foster-Seeley mobile receiver, 330
Foster-Seeley radar, 56
four-channel, 725
heart sound, 389
level, 536
low-cost f-m, 251
modified Travis, 286
phase, 200, 201
phase-locked frequency, 202
phase-modulated, 192
phase-synchronizing, 112
prf, 203
pulse amplitude, 534, 565
pulse-counting, 202
pulse-counting f-m, 203
pulse height, 534, 811
pulse length measuring, 539
pulse-level, 6
pulse width, 81
pulse width-height, 534
radar pulse width, 552
reference-frequency sampler, 626
servo, 643
solar x-ray pulse height, 533
tunnel-diode, 535
tunnel-diode pulse-height, 536
tunnel-diode with d-c amplifier, 535
two-stage audio, 201
voltage or current pulse height, 533
voltage-difference, 534
Weiss radar, 55
(See also Detector)
- Display, 50-kc cathode-ray, 90
400,000-rpm tachometer, 70
alphanumeric, 101, 102
auto-locking gate, 68
binary-controlled electroluminescent, 205
binary neon counter, 208
bistable indicator lamp driver, 436
cathode-ray numeral generator, 101
chopper-produced multitrace, 107
cold-cathode numerical, 206
crt control, 207
crt numeric, 100
crt unblanking pulse stretcher-expander, 542
electroluminescent ring counter drive, 204
fluorescent-anode triode, 237
gate-opening oscillator, 208
Gray-binary code converter, 207
- Display, lamp driver, 769, 779
lamp readout inversion, 207
lamp with transistor drive, 207
light-pen amplifier, 92
light-pen preamp, 99
magnetic contours, 708
movable marker generator, 275
nanosecond pulse, 208
overlapping-dot character generator, 208
pulse coincidence control, 206
radar unblanking switch, 553
radiation alarm failure, 567
signal ratio, 205
sonar cathode-ray, 204
tick-tock-toe neon-lamp, 337
transistor beta on cro, 93
tv pulse-cross generator, 746
two-intensity crt, 95
two-signal cathode-ray, 89
voltage bar graphs on cctv, 206
(See also Cathode ray)
- Display limit control, video, 330
- Distortion, cutting class-B audio, 52
monitor, 675
- Diversity combiner, pulse reshaper, 732
telemetry, 723, 730
- Divider, cascaded bistable, 829
frequency for f-m/f-m telemetry, 243
(See also Frequency divider)
- Doorbell, two-tone electronic, 7
- Doppler, constant-amplitude oscillator, 448
cycle counter, 23
- Doppler radar, demodulator, 191
- Dosimetry, electrometer, 564
- Double, Cockcroft-Walton voltage, 492
f-m multiplexer, 692
frequency, 692
mobile receiver, 578
telemetry transmitter, 730
voltage, 496
(See also Frequency multiplier; Voltage multiplier)
- Driver, 5-Mc tunnel-diode memory, 121
10-nsec pulse, 667
0.5-ohm source impedance, 23
beam-power video, 839
clock output, 111
core, 682
horizontal output, 744
light-emitting diode, 401, 402
line printer hammer, 515
mechanical counter for G-M, 561
mos fet photo preamp emitter-follower, 38
phone-line digital data, 732
scaler for G-M tube, 541
scr power-switching, 321
shift register, 341
thin-film memory plane, 123
- Dry cell, current stabilization, 359
- Dynamometer, magnetic tape controlled, 66
- Eddy-current, flaw detector, 367, 370
- Eeg waveform, analyzer, 384
zero detector, 383
- Electrocardiograph, amplifier, 380
- Electroluminescent display, binary control, 205
- Electrolytic capacitor, forming, 591
- Electromagnet, fine servo regulator, 648
rough servo regulator, 649
- Electrometer, electron density measurement, 376
preamplifier, 497
radiation signal amplifier, 564
reactor startup, 128
wide-band feedback amplifier, 375
- Electron resonance spectrometer, wide-band amplifier, 20
- Electronic heating, 27.12-Mc crystal-controlled di-electric heater, 209
27.12-Mc diathermy frequency-drift alarm, 212
hydrogen-thyratron control, 211
hydrogen-thyratron induction, 211
isolation oscillator, 209
power oscillator, 210
regulator for high-power induction heating, 212
repetition rate control, 211
sawtooth-sync pulse generator, 210
- Electronic music, attack control amplifier, 676
neon-oscillator synchronizer, 215
neon tone generator, 213
organ outphased-tone generator, 215
organ pedal generator, 214
organ swell shoe, 213
organ voicing panel, 213
synthesizer, 214
tone timbre demonstrator, 671
uht frequency divider for organ, 215
uht metronome, 214
- Encoder, 1-Mc oscillator, 789
binary, 337
commutator, 789
counter and limit trigger, 791
ferrite-core digital recorder, 581
five-bit parallel, 731
output circuits, 791
pulse width, 374
shaft-position, 481, 484
Envelope generator, tunnel diode, 338
Ergometer, pulse energy measurement, 370
Exciter, 10-meter ssb, 798
stepped frequency, 804
- Facsimile, 12-Mc distribution amplifier, 744
- Facsimile recorder, sweep generator, 584
- Facsimile recorder, sweep power amplifier, 585
sync pulse shaper, 586
- Fader, autopilot, 130
driver, 39
electronic, 39
- Fading control, uhf scatter link, 192
- Failure indicator, 0-20 v power supply, 595
- Fathometer, portable, 836
- Feedback, 1,000-Mc video amplifier, 512
Hall-effect field coil amplifier, 413
- Fencing, fail touch timer, 785
- Ferrite switch, microwave radiometer, 190
- Fiber optics, high-voltage, 226
- Filament supply, traveling-wave tube fail-safe, 506
- Filter, 7-cps active bandpass, 220
60-cps parallel-T notch with feedback, 218
130-cps synchronous, 650
800-cps active bandpass, 220
4,900-5,100 cps slot, 271
8-kc Q multi-tar, 413
100-kc magnetostriuctive, 460
325-kc nullisaction bridged-T, 219
455-kc mechanical, 798
20-channel L-C audio tone, 132
active adjustable-bandpass audio, 216
active low-pass R-C, 217
active parallel-T 800-cps, 221
active zero-crossing-sensing, 278
adaptive, 516
adjustable 400-cps tuning-fork, 218
airborne marker beacon, 79
automatic threshold control, 216
band-rejection, 442
bandpass photoread, 481
cascaded a-f, 222
C8 electromechanical, 792
ceramic in i-f stage, 287
diode voltage-regulating, 492
dynamic notch, 222
frequency analyzer, 669
general bandpass-reject, 222
lattice coupling, 221
low-pass active shot noise suppressing, 220
low-pass d-c to 1-cps, 219
magnetostriction bandpass, 219, 222
nonreactive, 542
organ formant, 213
parallel-T amplifier feedback, 294
parallel-T feedback in 800-cps oscillator, 221
power-line harmonic voltmeter, 852
remotely tuned crystal i-f, 217
sample-and-hold nonlinear filter, 440
single-transistor smoothing, 222
synchronous lag-load, 647
telemetry decoder notch, 733
transmitter output lattice, 803
triangular-to-sine conversion, 542
triple-tuned 90-ohm input, 221
triple-tuned 90-ohm output, 220
win-T 60-cps notch, 18
win-T 400-cps modulator, 221
two-section low-pass R-C, 284
unity-gain amplifier for third-order active low-pass, 217
variable-bandwidth 848-kc crystal, 221
variable-bandwidth radar preamp, 498
variable electronic capacitor, 88
vestigial sideband operation, 555
voltage-controlled dropping resistor, 220
Wien-bridge rejection, 222
Zobel band-elimination, 218
Zobel band-pass, 217
Zobel high-pass, 219
Zobel low-pass, 219
- Firing-angle control, scr, 608
- Fish fence, ignitron-pulsed, 707
- Fish shocker, d-c pulse generator, 524
pulse generator control, 525
- Flash, 30 w-sec supply with charger, 74
200 w-sec symmetrical inverter supply, 72
adjustable strobe, 224
blocking-oscillator drive for light-emitting diode, 226
blocking-oscillator supply, 225
high-voltage pulsor, 223
low-cost stroboscope, 225
measurement, 482
nuclear explosion detector, 559
ratemeter, 229
strobe oscillator-trigger, 224
synchronizing timer, 228
thyatron trigger, 225
two-lamp shared-cycle strobe, 230
underwater camera, 227
- Flash circuit, light-emitting diode, 223, 226, 228
timing-mark generator, 228
- Flash control, scr, 227
- Flasher, 1-cps scs, 229
50% duty cycle, 226
adjustable-delay sanatron, 230
intensity-controlling drive, 230
low-current construction barricade, 229
neon Halloween-pumpkin eyes, 230
random-time, 227
retina electrical stimulator, 385
road icing alarm, 758
rotating-beacon, 223
triggered blocking oscillator, 224
variable duty cycle, 228
(See also Blinker)
- Flash lamp, measuring output, 481
pulse-forming modulator, 395
variable square-wave source, 528
- Flash tube, firing circuit, 486

Flash tube, noise-immune scr trigger, 809
nuclear blast simulator, 682
power supply, 229
Flaw detector, magnetic tape, 779
paper, 479
zirconium wire, 772
Flight recorder, timing motor supply, 585
vertical acceleration, 584
Flip-flop, 1.5-25 cps solenoid pump pulser, 237
10-kc capacitor-commutated, 237
250-kc, 235
500-kc counter-shift register, 666
1.5-Mc, 345
5-Mc temperature-compensated, 236
10-Mc buffered, 152
10-Mc wide-temperature-range, 234
27-picosec tunnel-diode, 236
12-millimicrosec direct-coupled, 238
20-millimicrosec diode-coupled, 238
40-millimicrosec resistor-coupled, 238
2 sec-10 min timer, 298
adjustable-hysteresis level-detecting, 236
analog-digital converter, 34
base-triggered, 813
base-triggered with hybrid gate, 818
basic bistable, 667
basic saturated, 239
binary neon, 208
bistable thyatron, 781
closed-ring sampling counter, 624
collector-triggered, 819
collector-triggered with trigger amplifier, 814
complementary, 235
compute amplifier, 34
cross-connected integrated-circuit inverters, 302
Darlington-linearized sweep, 699
dctl, 239
decimal counter, 156, 159
direct-coupled, 238
emitter-triggered, 815
fast-recovery collector-triggered, 817
feedback-stabilized, 236
follower count-down, 231
frequency-dividing, 370
frequency-dividing 1.5-kc, 243
frequency-dividing for firing capacitor bank, 241
gated current-mode logic, 233
gyro control, 130, 136
heater control, 480
high fan-out, 234
high-speed, 338
high-temperature saturated, 238, 239
indicator triode, 237
integrated, 300
JK memory-drum recorder, 239
lamp drive, 237
load-sharing control for welder ignitrons, 862
low-dissipation 1-Mc, 664
magnetic drum read control, 587
magnetic drum recorder control, 587
magnetic drum write amplifier, 582
microsec fall time for astable mvbr, 426
neon, 765
neon indicator, 207
nor-function reset, 233
nonsaturated design procedure, 235
nonstalling for capacitive load, 231
paired inverters using 2N711, 231
pdm keyer, 728
peak voltmeter, 857
phase-difference meter, 473
phase-locked frequency discriminator, 202
photoelectric band-switching in distress
transmitter, 79
pnnp binary counter, 811
radiation alarm, 560
raster display, 91
relay-driving, 375
reversible binary counter, 160
reversible decade counter, 153
ring counter, 165
ring-of-five counter, 204
sample-and-hold, 625
scale-of-two frequency divider, 232
series-Schmitt set-reset, 810
shift register, 665, 666, 668
single-acs, 238
starter for free-running, 233
stepper relay, 230
stopped-count indicator, 567
superconducting contact test set, 778
symmetrical saturated 2N711, 666
three-phase ring counter, 314
transformer-controlled counter, 165
transformer-controlled shift register, 668
transistor-testing, 232
trigger circuit, 810
tunnel diode-coupled, 234
transistor turn-on for triggering, 232
two-counter, 156
unsymmetrical pulse generator, 424
variable controllable sweep, 702
(See also Logic)
Flow gage, radioactive jet fuel, 567
Flowmeter, frequency converter, 247
portable transistorized, 367
Fluorescent lamp, d-c power supply, 169
dimmer, 136
inverter, 313
Flutter, measurement, 716
Flux, Millar integrator, 312
Flying-spot microscope, bone section scanner, 382
Flying-spot scanner, ujt raster generator, 640
F-m (see Frequency modulation)
Focus coil, current regulator, 92

Fotoscope, 2-cps amplifier, 389
Fail, automatic welding, 862
Football helmet, telemetry transmitter, 725
Forcing function, random stepped generator, 672
Franklin superregenerative receiver,
remote control, 620
Frequency analyzer, filter and squarer, 669
two-phase oscillator, 248
Frequency control, mobile receiver, 578
Frequency divider, 0.25 cps-250 kc scs, 242
360-cps to 60-cps phantastron, 244
8:1 for 80-Mc oscillator, 244
10:1 stabilized mono mvbr, 241
0-2,000 pps preferred blocking oscillator, 454
blocking-oscillator pulse, 244, 245
cascaded distance-mark, 243
cascaded Eccles-Jordan, 240
cascaded ujt relaxation-oscillator, 242
clock calibrator, 112
Dekatron, 69
digital magnetic core, 241, 243
digital synthesizer, 242
divide-by-5 ring counter, 240
fet flip-flop, 232
flip-flop, 375
independent pulse-width control, 244
organ pedal generator bistable mvbr, 214
pcm telemetry, 243
phasemeter, 471
preferred blocking-oscillator distance-mark, 453
saturable reactor, 784
series flip-flop, 241
timing signal generator, 673
tunnel-diode, 113
ujt for electronic organ, 215
Frequency doubler, velocimeter, 367, 370
Frequency measurement, 1-cps f-m deviation, 248
10-480 Mc, 246
55-4,000 Mc single-pulse, 247
cable delay, 249
crystal lapping, 249
film deposition, 249
flowmeter, 247
pulse rate, 248
quick-disconnect tachometer, 69
tachometer amplifier, 69
vlf time-signal preamp, 499
Frequency motor, 0.2 to 10 cps, 246
Frequency modulation, 460-kc wireless
microphone receiver, 571
1-Mc oscillator, 251
4.5-Mc low-cost sound discriminator, 251
10.7-Mc two-chip i-f strip, 282
24-Mc varactor-modulated oscillator, 252
50-Mc voltage-tunable oscillator, 254
92-Mc transmitter, 732
95-Mc f-m/f-m telemetry, 724
100-Mc integrated-circuit receiver, 299
100-Mc oscillator, 254
100-Mc varicap oscillator, 251
160-Mc 2-w power amplifier, 254
160-Mc 15-w power amplifier, 255
175-Mc transmitter, 796
200-Mc voltage-controlled oscillator, 252
230-Mc modulated oscillator, 252
400-Mc varicap oscillator, 255
406-549 Mc remote-control receiver, 619
460-Mc receiver, 615
afc for local oscillator, 822
data demodulator, 191, 192
double-conversion superhet, 574
exciter tripler-doubler for 700-1,200 Mc, 262
fet stereo tuner, 691
football-helmet shock transmitter, 725
linear modulator, 403
low-cost single-chip tuner, 254
lunar probe transmitter, 727
modulation percentage monitor, 416
multipurpose integrated-circuit chip, 301
one-transistor transmitter, 803
pam sample-and-hold, 626
pulse-counter discriminator, 203
recorded-signal discriminator, 626
stereo demodulator, 694
stereo matrixing, 691
stereo multiplex tuner, 253
stereo usos pin diode, 695
strain-gage oscillator, 255
tape-recorder demodulator, 717
telemetry modulator, 730
two-chip tuner, 250
variable-bandwidth 848-kc crystal filter, 221
voltage-tunable oscillator or modulator, 255
weak-signal capture trap, 250
wireless microphone, 802, 803
Frequency-modulation stereo, compactron
adapter, 693
Frequency multiplier, 60-420 cps septupler, 258
400-800 cps ceramic-dielectric oscillator, 262
100 kc to 5 Mc, 260
3 Mc to 54 Mc doubler-triplers, 259
21-42 Mc doubler, 263
40-160 Mc varactor quadrupler, 258
50-100 Mc push-push doubler, 260
50-100 Mc push-push varactor doubler, 261
50-150 Mc tripler, 262
50-100 Mc varactor doubler, 264
50-200 Mc quadrupler, 263
50-400 Mc varactor quadrupler, 260
54-162 Mc tripler, 256, 257
60-960 Mc doubler-quadrupler, 259
80-160 Mc transmitter output doubler, 796
121.5-243 Mc doubler, 263
125-250 Mc parametric doubler, 264

Frequency multiplier, 150-450 Mc tripler, 256
162-180 Mc transmitter, 806
200-600 Mc varactor tripler, 261
223-Mc transmitter, 800
500-1,000 Mc varactor doubler, 263
500-4,000 Mc varactor octupler, 261
1-gc tripler, 261
1,000-times, 258
a-f doubler, 264
cascaded uhf doubler, 259
cesium clock synthesizer, 111
decode driver, 262
pulse shaper, 256
SOS beacon, 78
transmitter push-push output doubler, 804
trippler-doubler for 700-1,200 Mc, 262
uhf doubler, 260
velocimeter, 367
vhf varactor quadrupler, 264
white noise, 679
Frequency-shift keyer, 5-kc
Colpitts oscillator, 451
flip-flop controlled, 710
Frequency synthesizer, uhf transceiver, 472
Fuel flow, magnetic-amplifier gage, 350
Fuel pump, transistor blocking oscillator drive, 67
Function generator, cosine/sine, 267
cube-root signal compressor, 267
exponential, 267
high-accuracy fet squarer, 267
linear pulse width amplifier, 268
radar target simulator, 266, 443
random-noise analyzer, 268
sine-cosine, 265
sine-cosine approximator, 268
spectrum-invariant random, 672
square-law, 266
squaring, 265
three-dimensional radar target simulator, 681
transistor-diode, 266
(See also Generator; Pulse generator; Pulse
processing; Pulse shaping; Sawtooth
generator; Signal generator; Simulator;
Staircase generator)
Fuse, d-c amplifier electronic protection, 508
Fuse circuit, transistor protection, 507, 508
Fuze, electronic timer, 111, 113
Gage, photoelectric, 479
Gain tester, transistor, 779
Gallium arsenide lamp, driver flip-flop, 237
Gamma-ray spectrometer, underwater, 475
Garage door, control transmitter, 618
Gas analyzer, filament emission control, 127
Gas tube, noise-generator, 677
Gato, 8-650 kc positive transmission, 280
300-kc fet bridge, 273
300-kc tandem fet bridge, 271
5.5-Mc crystal oscillator, 277
5.5-Mc varicap with dual triodes, 276
30-Mc amplifier, 281
50-Mc balanced tunnel diode, 345
100-Mc tunnel-diode, 275
150-Mc current-robbing, 269
200-Mc interval timer, 269
200-Mc pumped tunnel-diode computer
logic, 278
500-Mc computer clock, 280
500-Mc tunnel-diode voltage divider, 280
2 and 4 input-pulse nor, 341, 343
5-fan-in nand/and/or, 339
0.7-nsec rise time, 279
10-nsec pulse, 123
analog, 274
analog fet chopper, 280
analog sample-and-hold multiplex, 625
and, 200
and with nor logic for inversion, 278
audio-controlled, 214
balanced shoring-switch, 278
basic integrated, 304
basic nor, 280
basic pnp, 275
bidirectional counter, 158
bistable, 156
cascaded agc amplifier, 60
cascaded monostable tunnel-diode and, 276
cascaded monostable tunnel-diode or, 276
clock ambiguity-preventing latch, 328
coincidence-anticoincidence, 565
color-burst, 753
computer clock driver, 121
current-mode logic flip-flop, 233
diode-coupled nand/nor, 341
diode between ring counter and
scope display, 273
double nor, 347
dual four-input, 297
emitter-coupled and, 342
event sequence detector, 269
ferrite-core recorder drive, 583
flash-tube trigger, 225
four-layer switching diodes, 277
high-noise-immunity integrated-circuit, 303
high-speed nor, 341
interval-sensitive 4,900-5,100 cps, 271
logic-controlled 32-Mc bursts, 277
majority, 348
memory driver, 348
memory strobe generator, 124
memory strobing pulse, 122
micromodule logic, 270
missile tracker, 270
missing-pulse detector, 6

- Gate, mono mvbr generating step function, 427
monostable, 320
multiplexer, 270
multiplexer sampling, 37
mvbr, 274, 381
mvbr-controlled bilateral, 279
neon photoconductor, 271
neon-triode, 273
nipo nor, 349
nor logic using series transistors, 274
npn and logic, 347
npn nor, 280
npn series-transistor, 277
output inverter, 271
overvoltage transient, 583
parallel-transistor, 344
phantastan delayed, 279
phase inversion without inverter, 279
phase-shift keyer, 805
pino nor, 341
pnp series-transistor, 277
power transistor tester, 770
ppm demodulator, 729
pulse-coded fault alarms, 272
pulse coincidence detector, 539
pulse height to width converter, 275
pumped tunnel-diode nor, 338
radar ppi sweep, 557
radar video integrator, 309
RCTL nand/nor, 341
r-f, 520
RTL nand/nor, 337
sampling, 360
scs protection, 446
selective-calling oscillator, 272
self-closing synchronized, 277
self-setting servo, 276
series-transistor, 347, 349
sliding, 389
sonar audio selection, 827
superconducting contact test set, 778
switching time test circuit, 300
switching transistor, 729
tachometer auto-locking, 68
teaching-machine reward dispenser, 272
tick-tack-too neon-lamp logic, 337
time-controlled, 279
transistor-coupled nand/nor, 346
trigger generator, 826
tunnel-diode a-c bridge, 273
tunnel-diode a-c bridge with toroid, 274, 280
tunnel-diode and, 348
ultrasonic pulse, 828
ultrasonic receiver, 826
universal NBS diode logic, 335
variable-width mvbr, 275
voltage failure protection, 502
welding pulse generator, 862
word switch, 345
zero-crossing-sensing, 278
(See also Logic; Switching)
- Gate turnoff, 100-kc chopper, 108
d-c chopper, 107, 108
- Gated diode, beacon pulse modulator, 80
- Generator, 20-40 kc ultrasonic, 830
300-kc tone burst, 271, 273
8-22 Mc cw marker, 91
100-Mc pulse, 519
16-bit word, 681
2.8-microamp constant-current, 170
64-step staircase, 686, 687
alternating-pulse, 807
constant-current for linear sawtooth, 632
constant-current mvbr, 423
constant-current timing, 787
cosine-wave, 102
current pulse, 767
current-voltage regulator, 169
free-running sawtooth, 699
high-power sonar pulse, 833
memory clock pulse, 112
mti clutter signal, 682
programmed digital, 673, 682
radar echo, 680
radar time interval, 554
ramp, 360
random noise, 443
random-noise pulses, 535
sine-wave, 102
square wave, 422, 430, 544, 807
squaring-function magnetic, 355
stereo subcarrier, 695
sync, 519
timer ramp, 710
triggered sawtooth, 698
(See also Oscillator; Pulse generator)
- Geiger-Müller counter, audio-visual indicating, 562
cosmic-ray, 562
radiation survey meter, 563
three-level power supply, 495
- Glow tube, 1-kc counter, 161
4-kc counter, 154
- Goldberg drift correction, d-c amplifier, 104
- Golf cart, pulse width modulation motor control, 408
- Goniometer, r-f switch, 704
- Gate pair, locked, 342
- Gray code, encoder, 34
- Gyro, d-c motor control, 406, 407
lock-up alarm, 6
magamp indicator servo, 350
pitch or yaw servo, 659
roll channel amplifier, 132
temperature control, 758
- Gyro balancer, cw sweep trigger, 89
- Hall-effect multiplier, field coil drive, 413
plate drive, 416
- Hall generator, amplifier chopper, 108
commutator, 623
multiplier, 413
- Hall multiplier, analog ratio computer, 415
squaring, 415
- Handwriting, reading spelled-out digits, 336
- Harmonic generator (see Oscillator)
- Harmonic voltmeter, power frequency, 852
- Headlight dimmer, photoelectric, 66, 71
- Heart, pacer, 390
- Heat (see Temperature control)
- Heater supply, regulator, 598
- High-frequency heating (see Electronic heating)
- Horizon sensor, infrared, 289
- Horizontal deflection, germanium-transistor tv, 741
- Hydrogen thyatron, laser pumping switch, 395
- Hydrophone, preamplifier, 50, 825
- Hydrophone preamp, noise-suppressing fet, 828
- Hygrometer, alpha-counting, 558, 563
- Hysteresis graph, magnetic materials, 310
- Hysteresis control, Schmitt trigger, 810
- Iceberg, microwave-radiometer detector, 190
- Icing, alarm, 758
- Identification friend or foe, mixer with range strobe marker, 849
mixer with radar marker, 842
- I-f (see under Intermediate frequency)
- Iff (see Identification friend or foe)
- Ignition, 30-kv pulse generator, 71
capacitive-discharge auto, 68
electronic automotive, 65
gas-tube electronic, 66
magnetic-pulse, 69
transistor switch, 711
(See also Automotive)
- Ignitron, 360-kw switching circuit, 707
crowbar, 763
discharge-shorting, 708
pulse-switching, 229
- Illumination (see Photoelectric)
- Illumination control, photoelectric, 475, 480, 482
- Indicator, cathode-ray sonar bearing, 829
chopper-type d-c, 472
computer register, 664
crt blanking, 686
fencing foil time and sequence, 785
fluorescent-anode triode, 237
four-electrode neon, 207
fuel-flow, 350
gated amplitude ratio, 67
incorrect polarity, 325
line-voltage dip, 329
off-limit transducer, 643
power-line overvoltage, 505
scr-controlled lamp, 769, 779
self-balancing shunt bridge, 83
switch position, 521
three-lamp switching circuit, 207
transistor-driven lamp, 207
transistor-failure lamp, 595
(See Character generator; Display; Voltmeter)
- Inductance control, differentiator, 125
free-running mvbr, 426
one-shot mvbr, 428
- Induction field, 460-kc f-m wireless microphone, 800
- Induction heater, closed-loop regulator, 212
control oscillator, 209
keyer and pulser, 212
photographic dryer, 487
power oscillator, 210
- Induction radio, 9-kc oscillator stabilizer, 799
magnetic tape playback compensation, 717
- Industrial control (see Control)
- Infrared, 6.5-cps amplifier for air-leak detector, 17
46-cps amplifier, 294
60-cps servo amplifier for air-leak detector, 654
air-leak detector, 456
engine gas temperature measurement, 67
engine heat detector, 293
horizon sensor, 289
mine-detector preamp, 294
monochromator preamp, 291
object detector-counter, 150
Perkin-Elmer analyzer, 289
phase-sensitive intrusion alarm, 5
power monitor, 291
space vehicle communication, 292
steel strip width, 294
transceiver, 290
video modulator, 293
width gage amplifier, 290
width gage preamp, 292
width gage translator, 294
- Infrared scanner, tangent-function sweep, 698
- Instrumentation, calibrating a-c meters to standard cell, 781
magnetic recorder, 715
magnetic tape reproducer, 714
- Insulation, controlling extrusion on wire, 87
- Integrated circuit, 10.7-Mc two-chip f-m i-f strip, 282
15-Mc telemetry command receiver, 300
1-w audio amplifier, 297
a-f preamp, 305
alternating and-or gate, 339
audio output, 302
- Integrated circuit, audio power amplifier, 304
basic gate, 304
basic RCTL logic inverter, 299
bipolar pulse generator, 516
buffer and logic, 301
camera sync distributor, 737
chopper drive for linear integrated-circuit tester, 303
complementary-transistor logic, 305
cross-connected inverters as flip-flop, 302
Darlington audio preamp, 52
Darlington-input operational amplifier, 298
d-c operational amplifier, 305
differential amplifier, 195, 196, 199
dual four-input gate, 297
five-voltage power supply for tester, 604
flip-flop, 300
f-m limiter, 197
harmonic mixer, 198
high-noise-immunity gate for logic, 303
improved Darlington, 298
line driver, 305
logic-inverting, 339
low-cost f-m tuner, 254
measuring and gate switching time, 300
measuring gate switching time, 300
measuring performance with crosstalks, 295
memory sense amplifier, 296
multipurpose chip, 301
nonlinear-mode harmonic mixer, 296
null amplifier and chopper for tester, 303
optoelectronic coupling, 484
or gate, 339
output buffer, 305
p-most amplifier, 302
pressure-sensitive monostable mvbr, 297
pressure-sensitive mvbr, 297
pressure-sensitive oscillator, 301
quad block, 304
Schmitt trigger, 300, 303
sense amplifier, 196, 301
servo amplifier, 304
servo frequency compensator, 647
single-frequency 100-Mc f-m receiver, 299
single-stage differential amplifier, 198
staircase generator, 686
thin-film pulse amplifier, 300
timer, 298
two-chip f-m tuner, 250
two-stage operational amplifier, 302
wideband amplifier, 296
- Integrated transistor-zener reference amplifier, 614
- Integrator, 100-cps phase-shift oscillator, 310
20-Mc bandwidth-compensated R-C, 307
250,000-gain amplifier, 306
analog-computer blood flow-rate, 379
automatic search and control for servo, 662
bootstrap, 312
chopper-stabilized d-c amplifier, 307
clearing circuit, 312
current pulse, 308
d-c to 10 kc, 308
d-c voltage, 391
diode pump, 367
distance measurement, 307
electrical-readout solen, 310
magnetic characteristics, 310
magnetic core flux, 312
Memistor, 312
Miller, 381, 545
noise dosimeter, 309
operational amplifier controlled by synchronous reed switch, 311
peak voltmeter, 857
phantastan delay, 185, 189
proportional amplifier, 311
pulse, 146
radar video, 309
rotation-analyzing staircase, 67
shmoo memory curve plotter, 306
solen linear time base, 309
time-base cycle-counting, 462
triangular-wave generator, 629
- Intensifier, cro, 93
- Intensity control, crt, 95
- Interference, receiver susceptibility, 440, 520
- Interference protection, scs, 446
- Interference suppression, ionospheric sounding, 570
- radar, 552
- Intermediate-frequency amplifier, 15-cps spectrum analyzer, 284
455-kc mobile, 288
455-kc single-transistor, 288
500-kc delay-line tuning, 282
848-kc crystal filter, 221
5.5-Mc germanium, 287
5.5-Mc high-gain low-noise, 287
6.25-Mc, 77
10-Mc superregenerative, 286
10.7-Mc two-chip f-m strip, 282
22-Mc design, 288
30-Mc with 2N2188, 285
30-Mc with 2N2410, 285
30-Mc pulsed-gated, 281
30-Mc radar, 549
30-Mc three-transistor, 285
45-Mc cascade fet, 285
60-Mc with 2N743, 288
60-Mc grounded-grid triode, 283
60-Mc logarithmic, 284
60-Mc logarithmic thin-film, 288
60-Mc silicon tetrode stage, 287
60-Mc tetrode, 282

Intermediate-frequency amplifier, 70-Mc low-noise, 284

70-Mc neutralized, 281

105-Mc, 139

105-Mc with 2N2966, 286

500-Mc stagger-tuned, 283

a-m/f-m strip, 570

ceramic-filter stage, 287

complete optimized design, 283

double-conversion, 57

ground pulse blanker, 95

low-noise for backward-diode detector, 285

optimum design, 284

out-of-baseband noise, 440

preferred 30-Mc pulse-modulated afc, 54

symmetrical limiting, 286

three-transistor color tv sound i-f, 749

three-transistor color video, 754

video, 286

(See also Receiver; Transceiver; Transmitter)

Intermediate frequency filter, remotely

tuned crystal, 217

Intervalometer, low-cost ujt-scr, 791

pulse width controller, 544

Intrusion alarm, capacitance-bridge, 88

(See also Alarm)

Inverter, d-c to 18 kc to d-c, 315

d-c to 50 kc to d-c, 313

1.5-25 cps solenoid pump pulser, 237

60-cps 20-w, 318

60-cps 100-w, 318

60-cps 200-w 115-v, 316

60-cps 250-w dual-transformer, 319

60-cps 400-w dual transformer, 317

400-cps fork control, 324

400-cps power amplifier, 319

400-cps variable-gain amplifier, 315

2-kc parallel, 322

9.6-kc fluorescent, 313

50 kc at 100 w, 320

10-nsec pulse, 123

12 v d-c to 8.5 kc, 145

12 v d-c to 250 kc, 143

24 v d-c to square-wave a-c, 320

28 v d-c to 400-cps a-c, 324

28 v d-c to 10 kc, 147

4 w at 150 v from flashlight cell, 326

basic RCTL logic integrated circuit, 299

black-light supply, 321

capacitor turnoff, 325

code converter phase, 323

cross-connected integrated-circuit

as flip-flop, 302

cross-coupled as flip-flop, 231

d-c voltage, 96

delay circuit, 314

differential transistor-magnetic, 353

differential-mvbr magnetic, 326

dual speed-up windings, 323

dual-triode magnetic, 326

frequency standard control, 468

induction motor drive, 407

lamp-flashing, 226, 228

logically redundant, 324

low-cost computer logic, 342

low-voltage high-current, 320

neon-photoconductor, 349

nondifferential transistor-magnetic, 354

parallel, 778

parallel reactive-load, 322

positive-feedback fast-switching, 324

power-switching output stage, 326

propagation time measurement, 366

protection from incorrect polarity, 325

pulse delay, 185, 186, 189

pulse polarity, 516

pulse sequencing of scr parallel, 325

pulse-width modulator, 315

reducing idling current, 321

reference element, 323

rotating-beacon, 223

scr driver, 321

scr static alternator, 314

scr trigger, 426

self-excited half-bridge, 325

self-excited three-phase, 315

speed-up winding, 323

square-wave drive, 322

symmetrical photoflash, 72

three-phase regulator, 316

three-phase ring counter, 314

three-transistor, 339

transformerless scr bridge, 323

transient-suppressing trigger, 816

transistor-magnetic, 322

transistor tester, 770

two-capacitor fast-switching, 319

unijunction control, 425

unity-gain autopilot, 322

voltage regulator, 197

ionization, cosmic-ray, 362

ionosonde, long-duration stepped-sweep, 685

ionosounder, 2.2-Mc transmitter, 548

ionosphere, sounding transmitter simulator, 677

ionospheric sounding, tone signal suppression, 570

isolation, magnetoresistor, 84

Jam detector, conveyor, 482

Jammer, conversation, 461

Jitter monitor, radar mil, 546

Jitter suppression, digital receiver, 623

Keyer, distress-frequency transmitter, 79

874

Keyer, induction heater, 210

induction heater oscillator, 212

neurosurgery oscillator, 382

phase-shift, 805

squelch-actuated repeater, 442

Keying, oscillator, 451

paging transmitter, 801

transient-free photoelectric, 481

Keying monitor, sensitive broadband c-w, 449

Klystron, color tv pickup reference oscillator, 752

regulated magnetic-amplifier supply, 592

Lamp, burnout alarm, 7

dimmer, 472

driver, 769, 779

flasher circuit, 226, 228

fluorescent inverter, 313

intensity control, 475

programmed preheater control, 129

radiation alarm failure, 567

scr brightness control, 128

scr dimmer, 133

transistor failure-indicating, 595

triode dimmer, 133

Lamp dimmer, light-activated scr, 473

scr, 476

Lamp display, readout inversion, 207

Lamp drive, tumor-scanner, 387

Lamp driver, bistable, 436

scr, 664

Lamp preheater, light-activated scr, 478

Langmuir probe, sporadic-E ionization, 376

Lapping, automatic shutoff control, 249

Larynx, artificial, 388

Laser, modulator current control, 167

receiver preamp, 499

Laser beam, preamp, 500

Laser flash tube, power supply, 229

Laser tv, audio driver, 739

Latch, 120 v a-c Triac, 329

a-c static using toroids and scr's, 327

clock pulse register, 328

fet light-activated relay, 328

lamp-switching, 328, 329

magnetic-firing a-c 1.7-kw scr, 328

neon-photoconductor, 328

saturable-reactor, 709

stepper relay reset coil, 328

transient-pulse detector, 329

ujt, 327

ujt for inductive-load scr, 329

latching circuit, lamp, 4

Leak detector, 60-cps servo amplifier, 654

automotive air-suspension system, 17, 456

Leakage, capacitor and diode testing, 782

Leakage current, measurement, 369

Leakage resistance tester, operational trigger, 771

Leakage tester, transistor, 770, 779

Level control, liquid, 136

Life test, microwave triodes, 773

Light (see Photoelectric)

Light beam, communication, 401, 402

Light-beam communication, current-mode

switch, 223, 228

Light control, ambient-level a/c, 476

Light pen, high-gain amplifier, 92

preamp, 99

Light transients, measuring, 481

Limiters, 500-kc, 439

6-Mc-bandwidth high-linearity, 331

27-Mc CB receiver, 334

80-db dynamic range, 331

a-f noise, 332

audio dynamic-range, 267

bipolar, 334

current, 331

duty-cycle, 332

fast-acting, 333

f-m integrated-circuit, 197

mobile color tv pickup, 332

mobile receiver, 330

nonadditive video mixer, 848

preferred a-m receiver noise, 333

preferred video, 332

pulse, 374

radar noise analyzer, 332

series noise, 333

series-parallel audio noise, 334

symmetrical i-f, 286

telemetry low-level amplifier, 331

video signal, 330

Line driver, integrated, 305

Liquid level, measurement, 363

Load switching, flying-spot scanner, 639

flying-spot scanner, 642

Locking, coherent oscillator, 672

Logarithmic attenuator, pulses, 362, 373

Logarithmic diode, reactor startup, 128

Logic, 1.5-Mc flip-flop, 345

300-Mc pin-diode, 346

500-Mc clock gate, 279

2 and 4 input nor gate, 341, 343

alternating and-or gate, 339

anticoincidence detector, 540

arithmetic cell, 120

basic integrated gate, 304

basic parallel-transistor npn, 344

basic parallel-transistor pnp, 344

basic RCTL integrated-circuit inverter, 299

basic series npn, 347

basic series pnp, 349

binary adder, 335

binary encoder, 337

bird-direction-of flight, 150

Logic, bistable and circuit, 345

blocking oscillator control, 338

complementary RDTL nor, 341

complementary-transistor integrated-circuit, 305

current-mode gated flip-flop, 233

dcl flip-flop, 239

digit line decoupler, 340

digit line driver, 344

diode-coupled nand/nor gate, 341

double nor gate, 347

drive for bilateral transistor, 725

drivers, 341

emitter-coupled astable driver, 430

emitter-coupled and gate, 342

envelope generator, 338

exclusive-or, 345

full adder, 123

half-adder, 121

high-speed binary, 336

high-speed flip-flop, 338

hybrid nor, 340

integrated-circuit amplifier, 301

integrated line driver, 305

inverting amplifier, 339

largest-signal selector, 343

level converter, 144

level shifter, 335

locked-pair tunnel-diode, 342

low-cost inverter, 342

majority-decision 50-Mc gate, 345

majority gate, 348

memory information-extracting driver, 348

memory information-inserting driver, 346

memory X-pulse amplifier, 343

memory Y-pulse amplifier, 349

microenergy flip-flop, 234

monostable or circuit, 349

multi-emitter-transistor or gate, 339

nand/and/or gate, 339

neon-photoconductor inverter, 349

neon with pnp exclusive-or, 347

neuron model, 343

neuron model without integrator, 346

nor, 347, 348

nor up to 8 Mc, 347

nor gate, 238, 708

nnp and gate, 347

odd-even, 346

or, 338, 343

paired-inverter flip-flop, 231

pnp exclusive-or, 347

pulse sequence detector, 540

pulse train generator, 531

pumped tunnel-diode nor gate, 338

random-noise analyzer, 268

RC-coupled binary, 348

RCTL nand/nor gate, 341

read-write amplifier, 336

RTL nand/nor gate, 337

script word recognition, 336

sense amplifier, 340

single-supply nor to 1 Mc, 349

Magnetic tape, skew control, 356
 (See also Audio; Recorder; Tape Recorder)
 Magnetic tape handler, photoelectric alarm, 7
 Magnetic tape recorder, playback of event-timing signal, 586
 Magneto, zero-power tachometer, 68
 Magnetometer, 325-kc bridged-T filter, 219
 proton precession, 368
 Magnetoresistor bridge, 84
 Magnetoresistor control, scr, 83
 Magnetostriction, tv remote control, 830
 Magnetostriction delay line, serializer, 180
 Magnetostriction filter, oscillator, 219, 222
 Magnetostriction oscillator, 100-kc, 460
 Magnetron, 2-kv modulator switching module, 398
 300-kw modulator, 397
 injection electrode supply, 592
 magnetic pulser, 554
 radar four-diode modulator, 401
 regulated filament supply, 614
 series-diode modulator, 400
 transformer-triggered modulator, 400
 X-band radar transmitter, 556
 Magnetron beam tube, switching circuit, 537
 Manometer, photoelectric, 386
 Marker, 8-22 Mc cr, 91
 Marker beacon, 75-Mc receiver, 77
 Matching, gamma, 569
 Matrix, counter, 159
 electroluminescent display, 204
 microwave system alarm, 3
 translation circuit, 343, 349
 Matrixing, f-m stereo adapter, 691
 Measurement, 10 cps to 1 Mc, 146
 3-260 Mc tunnel-diode dip meter, 365
 200-Mc transistor noise figure, 441
 1-nsec pulse peak amplitude, 855
 a-c resistance with milliohm meter, 375
 a-f phase shift, 361
 amplifier distortion, 27, 359, 675
 amplifier linearity, 629
 amplitude probability density function, 441
 antenna impedance, 674
 antenna radiation pattern, 802
 auditorium acoustics, 309, 787
 auto engine speed, 71
 axis-crossing interval, 90
 bird flight speed, 150
 blood flow, 377
 blood flow rate, 267, 379
 blood oxygenator volume, 380
 blood pressure, 378, 384, 386
 body temperature, 758
 cable fault location, 764, 769, 772
 cable propagation delay, 249
 calibrating a-c meters to standard cell, 781
 capacitance, 368
 capacitance-type aircraft fuel gage, 86
 cell-growth microphotometer, 481
 cement setting time, 359
 cladding or plating thickness, 367, 370
 coating thickness, 362, 365
 coax delay, 365, 372
 coherent oscillator jitter, 546
 cosmic-ray neutrons, 726
 crt pulse height, 81
 daylight intensity, 484
 d-c voltage comparator, 850
 dewpoint, 480
 dielectric properties, 663
 dielectric strength, 775
 differential transformer core displacement, 645
 digital circuit delay, 524
 dimensions of rotating parts, 479
 distance with accelerometer, 307
 dynamic notch filter for noise rejection, 222
 electron density, 376
 engine gas temperature, 67
 engine rpm, 65, 68
 eyeball pressure, 378
 eye pupil movements, 642
 fet pinchoff voltage, 369
 film density, 364
 flashlamp output, 481
 flip-flop peak voltmeter, 857
 flowmeter frequency converter, 247
 f-m modulation percentage, 416
 f-m strain-gage oscillator, 255
 frequency-dependent noise, 442
 frequency jitter, 156
 frozen-food temperature, 758
 fuel-flow, 350
 gas flow, 462
 gyro vibration, 89, 98
 high-impedance source voltage, 174
 high-speed multiplexing counter, 624
 home and outdoor temperatures, 759
 infrared energy absorption, 289
 infrared horizon sensor, 289
 infrared power, 291
 instantaneous and peak core flux, 312
 instrument insulation resistance, 772
 insulation resistance, 782
 integrating circuit time interval, 786, 790
 invisible flaws in wire, 772
 ionosphere electron density, 548
 jet fuel flow, 567
 leakage resistance, 771
 light flashes, 482
 linear integrated-circuit performance, 295
 line voltage dip, 329
 liquid density, 128
 liquid level, 363
 logarithmic attenuator, 362, 373
 low-energy space particles, 566
 Measurement, magnetic characteristics, 310
 magnetic fields in space, 368
 magnetic-induction cave mapper, 374, 376
 magnetic materials at narrow pulse widths, 525
 magnetic tape flutter, 360
 metal tension and elongation, 134
 meter protection from pulsed transducer, 507
 micromicroammeter, 371
 micromicroampere currents, 375
 millivolt d-c voltages, 858
 missile position and velocity, 23
 moisture in powder, 359
 Mossbauer-effect spectrometer, 356
 movie camera frame rate, 485
 nanoampere sensing circuit, 364
 neutron, 564
 neutron analyzer, 560
 neutron diffraction, 559
 noise dosimeter, 309
 noise figure, 358, 439, 779
 nuclear particle flight time, 783
 ocean depth, 835
 overtone crystal parameters, 83, 776
 paper flaws, 479
 parallel-mode transducer, 643, 649
 phase, 201, 648
 phase at 15 to 400 Mc, 466
 phase up to 2,000 Mc, 472
 phase angle, 470, 473
 phase shift at 30 Mc, 469
 pH meter, 372
 picoampere, 369, 497
 position of encapsulated metal part, 225
 power supply temperature coefficient, 614
 prf, 203
 printed circuit widths, 477
 projectile acceleration, 731
 projectile nose pressure, 731
 pulse amplitude, 538
 pulse energy content, 370
 pulse energy in coulombs, 308
 pulse frequency, 191, 192
 pulse height, 115, 534
 pulse rate, 248
 pulse-reference phase angle, 468
 pulse width, 374, 539, 630
 pulse width with linear bootstrap time base, 634
 radar auto speed meter, 545
 radiation at 0.5-50 milliroentgen per hour, 563
 radiation exposure, 564
 radiation of high-altitude alpha particles, 558, 563
 radio propagation, 804
 radioactive dust particles, 560
 radioactivity of tracers, 564
 radio bearing, 465
 radio wave direction, 498
 random time interval, 789, 791
 receiver noise, 440, 520
 recorder wow and flutter, 201
 relay chatter time, 774
 resistance, 808
 resistance comparator, 375
 resistance in milliohms, 781
 r-f power, 362
 r-f voltage, 851
 river flow velocity, 836, 837
 rocket surface temperature, 756, 757, 760
 satellite position and velocity, 448
 scr turnoff time, 778
 self-balancing strain-gage bridge, 83
 semiconductor resistivity, 168
 servo system angular lag with photodiode pickup, 481
 shock spectrum, 376
 short-term frequency stability, 672
 signal phase difference, 470, 474, 808
 small phase angles, 471
 solar x-rays, 533
 sound level, 444
 sound level meter, 44
 speed of inaccessible rotating parts, 71
 speed of sound in water, 367, 370, 371
 spot-welding current, 859
 stale stability under vibration, 460
 steel strip width, 290, 292
 strain-gage amplifier, 366
 superconductor critical current, 778
 swr, 373
 tachometer amplifier-frequency meter, 69
 tape recorder wow and flutter, 716
 temperature, 757, 758, 763
 temperature by nuclear resonance, 760
 thickness of moving wire, 363
 thin magnetic films, 628
 tissue impedance, 387
 tool running time, 785
 transducer-controlled crystal oscillator, 140
 transistor alpha cutoff, 375
 transistor beta, 358, 774
 transistor conversion gain, 358
 transistor forward-current transfer ratio, 360
 transistor h-fe at 100 Mc, 782
 transistor leakage, 770
 transistor noise figure at 200 Mc, 444
 transistor open-circuit output admittance, 361
 transistor power gain, 362, 782
 transistor propagation time, 366
 transistor reverse-voltage transfer ratio, 371
 transistor short-circuit input resistance, 360
 trawl net depth, 825, 831
 transistor thermal data, 359
 tunnel-diode characteristic curve, 94
 tunnel diode curves, 700, 771
 tunnel diode d-c parameters, 373
 Measurement, tunnel-diode negative resistance, 374
 ultrasonic delay line characteristics, 828
 ultrasonic propagation time in sea water, 826, 834
 ultrasonic velocity, 825, 826, 834
 underwater gamma radiation, 475
 vacuum, 564
 valve closure time, 784
 vertical acceleration, 584
 vibration, 369
 voltage, 812, 813
 voltage dips, 858
 voltage in semiconductor circuit, 854, 856
 vswr, 357, 362
 water velocity, 367
 wind velocity, 727
 wire enamel thickness, 87
 (See also Counter; Frequency measurement; Meter; Pulse height analyzer; Radiation; Sampling; Telemetry; Tester; Timer; Voltage measurement; Voltmeter)
 Medical electronics, 3-kc three-output oscillator, 383
 13-kc ultrasonic responder for blind, 832
 15-Mc receiver for brain command signals, 300
 27.12-Mc diathermy frequency-drift alarm, 212
 anesthesia control, 387
 artificial-hand amplifier, 390
 artificial-hand servo, 388
 artificial larynx, 388
 automatic blood pressure monitor, 386
 batteryless pacemaker, 392
 blood flow-rate integrator, 267, 379
 blood pressure indicator, 384
 blood-pressure monitor, 378
 blood-pressure regulator, 385
 blood-volume servo, 380
 bone section scanner, 382
 capacitance-sensing cane for blind, 88
 cardiac pacemaker, 383
 ecg amplifier, 380
 ecg telemeter, 388
 ecg waveform analyzer, 384
 ecg waveform zero detector, 383
 electronic tonometer, 378, 392
 eye-motion recorder, 386
 eye-motion tracker, 483
 fetal heartbeat detector, 389
 heartbeat telemeter, 377
 heart-block pulse amplifier, 385, 391
 heart-lung machine control, 377
 heart pacer, 390
 heart rate regulator, 390
 heart sound selector, 389
 heart-sound zero-crossing detector, 278
 intravenous infusion bubble detector, 131
 iron-lung air intake control, 388
 low-cost intervalometer, 791
 measuring eye pupil movements, 642
 measuring tissue impedance, 387
 muscle potential telemeter, 391
 myoelectric stimulator, 385
 nerve-fiber-potential recorder, 721
 nerve potential transmitter, 389
 neuron simulator, 683
 neurophysiology stimulator, 384
 neurosurgery oscillator keyer, 382
 neurosurgery ultrasonic oscillator, 386
 neurosurgery vivum, 392
 pill tracer, 391
 preset neurosurgery pulse counter, 390
 recording manometer, 386
 retina stimulator, 385
 retina welder, 379
 retina welder control, 381, 391
 timing-signal playback for biomedical research, 586
 tumor-scanner amplifier, 387
 video processor for blood-cell scanner, 32
 x-ray tube pulser, 381
 Memistor, integrator, 312
 Memory, 5-Mc driver, 121
 analog sample-hold, 36
 basic npn trigger, 815
 clock pulse generator, 112
 digit line decoupler, 340
 digit line driver, 344
 exclusive-or logic for shmoo curve plotter, 345
 fast-readout 500-Mc gate, 280
 fast-rise current driver, 705
 ferrite time-of-magnetization, 126
 French SECAM color tv one-line, 748
 high-gain readout amplifier, 120
 information-extracting driver, 348
 information-inserting driver, 346
 inhibit driver, 122
 integrated-circuit sense amplifier, 296
 integrator for shmoo curve plotter, 306
 limit detector for shmoo curve plotter, 535
 marker-pulse blocking oscillator, 125
 matrix readout, 122
 nonstalling flip-flop drive for capacitive load, 231
 peak capacitor voltage, 536
 peak voltage, 376
 peak voltage for 0-10 cps, 856
 pnpn ring counter, 820
 power transistor switching, 708
 pulse generator, 121
 pulsed drive current amplitude detector, 167
 read-write blocking oscillator, 125
 sense amplifier, 123, 124, 812
 shmoo curve plotter, 432
 storage-diode selection matrix, 342

- Memory, strobe pulse generation, 124
 strobing-pulse gate, 122
 thin-film current driver, 124
 thin-film sense amplifier, 124
 tick-tack-toe logic, 344
 transistor switching circuit, 713
 tunnel-diode temporary bistable, 711
 word switch, 345
- Memory core, tester, 767
- Memory tester, linear ramp generator, 637
- Meter, capacitance, 368
- Meter, expanded-scale swr, 373
 portable power tool running time, 785
 pulse rate, 248
- Metronome, two-transistor, 215
- Microammeter, Hall-generator, 108
- Microcoulometer, 30 sec-350 hr delay, 188
- Micromicroammeter, servo-controlled, 371
- Micromodule, flip-flop, 235
 (See also Integrated circuits)
- Microphone, preamplifier, 45
 wireless, 802, 803
- Microphone preamplifier, capacitor, 49
- Microphotometer, stained-cell transmittance, 481
- Microwave, diode harmonic generator, 452
 fault alarm, 272
 Kita diode switching, 706
 measuring projectile acceleration, 731
 receiver klystron afc, 55
- Microwave link, regulated klystron supply, 592
- Microwave relay, afc for mobile color pickup, 750
 color tv pickup reference oscillator, 752
 lost-carrier generator, 459
- Microwave system, fault alarm, 3
- Miller action, phantastron sweep, 698, 699
- Miller integrator, sawtooth generator, 636
 triggered sweep, 698
- Miller superregenerative receiver, remote control, 618
- Milli ohmmeter, transistors as rectifiers, 375
- Millivoltmeter, d-c fet, 853
- Mine detector, preamp, 294
- Mines, underwater ultrasonic telemetry, 831, 832
- Missile, beam-rider radar target simulator, 443
- Missile fuze, electronic timer, 111, 113
- Missile guidance, acquisition relay, 127
- Missile tracker, display gate generator, 270
- Mixer, 27-Mc CB transmitter, 139
 30 Mc to 5.5 Mc, 143
 120 Mc to 10.7 Mc nonlinear-mode integrated harmonic, 296
 255-Mc, 144
 420-Mc, 140
 450 Mc to 30 Mc, 144
 10-nsec pulse, 123
 balun, 138
 common-plate video, 844, 846
 cutoff-biased triple-input video, 840
 diode tv, 746
 distortion monitor, 359
 dual-grid pentode video, 848
 fet uhf tv, 823
 five-input radar marker, 845
 four-input high-level video pulse, 846
 harmonic integrated-circuit, 198
 marker-iff, 842
 mos fet, 144
 nonadditive common-plate video, 844
 nonadditive video, 848
 partial-adding video, 841
 pentode common-plate video, 842
 pentode video, 846
 preferred common-cathode video, 849
 preferred low-level common-plate video, 843
 radar and beacon video, 849
 radar range strobe marker, 842
 radar video amplifier chain, 839
 range strobe marker-iff, 849
 silicon-transistor, 798
 three iff signals, 847
 three-input two-output video, 843
 three-input two-pentode video, 849
 three-pentode video, 845
 transistor tester, 771
 triode common-plate video, 848
 two-input three-output video, 847
 two-pentode radar and iff, 849
 two-pentode video, 849
 uhf tv, 823
- Mobile radiotelephone, CB decoder, 795
- Modulation, light-emitting diode, 228
- Modulation suppression, diode quad, 403
- Modulator, 9-kc induction radio, 799
 25-kc trigger generator, 808
 25-75 kc frequency, 720
 100-kc hybrid balanced, 400
 100-kc prr, 398
 2-kv switching module, 398
 250-kw ten-diode radar, 548
 300-kw magnetron, 397
 10-Mc CCTV carrier, 745
 23-Mc voltage-controlled, 460
 100-Mc varicap f-m oscillator, 251
 150-Mc telemetry carrier, 731
 230-Mc self-reactance, 232
 243-Mc beacon, 80
 400-Mc varicap f-m oscillator, 255
 0-200 pps variable-width pulse, 403
 a-c for photomultiplier, 477
 balanced, 649
 balanced adf, 395
 balanced silicon-transistor, 798
 balanced-triade sine-wave, 399
 beacon transmitter, 80
 blocking-oscillator X-band pulse radar, 555
- Modulator, Boynton-Scholt, 396
 capacitor-balanced ssb, 397
 cascade angular, 394
 CB direct R-C coupled common-emitter, 396
 CB transformer-coupled base, 403
 CB transformer-coupled collector-modulated transmitter, 397
 CB transformer-coupled series d-c, 398
 chopper, 399
 delta-sigma, 393
 Diamond, 396
 diode quad, 403
 double-balanced ring, 695
 duo-diode half-wave switch, 403
 ferrite-core microwave, 731
 flat-display tv, 734
 f-m/p-m, 398
 f-m telemetry, 730
 full-wave scr a-c switch, 864
 infrared, 290
 infrared television transmitter, 293
 large-range pulse width, 399
 laser pumping, 395
 light, 476
 light-emitting diode, 223, 226, 401, 402
 linear f-m, 403
 linear voltage-frequency converter, 142
 lino-type 250-kw peak, 393
 low-frequency f-m, 721
 magnetic pulse-time, 399
 missile transponder, 548
 modem pulse shaper, 396
 myoelectric stimulator, 385
 pam video recorder, 394
 phase, 733
 photochopper, 851
 ppm multiplexer, 395
 pulse amplitude, 416
 pulse ratio, 394
 pulse train amplitude, 402
 pulse width, 315, 396, 415, 416, 811
 radar four-diode magnetron, 401
 R-C coupled base, 403
 r-f tv camera, 736
 ring, 402, 693
 satellite transmitter, 728
 sawtooth ramp, 636
 Schmitt trigger, 303
 series-diode magnetron, 400
 servo current-subtracting, 646
 shaft position servo, 661
 stabilized signal generator, 672
 stereo phase, 693
 synchronous-detection balanced, 440
 temperature control, 759
 three-tone, 803
 three-tone telemetry, 618
 transformerless base, 401
 transformerless collector, 400
 transformerless linear, 402
 transformer-triggered magnetron, 400
 triode clamp, 401
 tv laser, 739
 twin-T 400-cps filter, 221
 variable-plate-resistance, 402
 variable rectangular-wave, 618
 variable-width pulse, 398
 voltage-tunable f-m, 255
 wide-band transformer, 396
 wide-range voltage-controlled oscillator, 455
- Modulator tube, protection, 765
- Moisture meter, current-stabilized, 359
- Monitor, distortion, 27, 359
 leakage current, 770
 line-voltage failure, 1
 missile performance, 727
 missing-pulse and d-c dropout, 6
 r-f power and vswr, 362
 slow-scan tv, 742
 telemetry command pulse, 726
 transducers, 643, 649
 voltage, 812
 voltage bar graph display, 206
 voltage and current, 811
- Monochromator, missile plume, 291
- Mossbauer effect, spectrometer, 356
- Motor, 440-cps power supply, 585
- Motor control, 400-cps servo magamp drive, 352
 0.5-hp shunt wound d-c, 408
 1-kw a-c phase, 471
 3-w servo power amplifier, 661
 acceleration-sensing switch, 408, 410
 adf generator, 405
 adjustable d-c field supply, 352
 balanced-bridge reversing drive, 411
 commutator noise actuates muting system, 412
 constant-current battery-charger rheostat, 75
 constant-speed 400-cps, 404
 conveyor belt slip, 353
 crystal oscillator, 111
 directional d-c, 412
 four-transistor inverter, 407
 full-wave a-c or d-c, 404
 full-wave push-pull a-c servo, 407
 full-wave push-pull shunt motor, 405
 golf cart drive, 408
 governor-transistor for 0.5-hp d-c, 411
 milling machine, 410
 mvbr modulator for full-wave scr power switch, 864
 one-source gyro, 407
 overhead crane, 404
 reversible d-c shunt motor, 410
 reversing, 406, 412
 reversing full-wave d-c, 409
- Motor control, reversing series-wound universal, 410
 scr noise suppressor, 445
 scr zero-crossing synchronizer, 136
 series actuator solenoids, 407
 sidereal-time clock, 113
 small permanent-magnet d-c motor, 409
 speed-controlling chopper for battery-powered vehicle, 107
 stopper pulse generator, 411
 stopping, 412
 switching-mode, 406
 synchronizer, 619
 td-scr with speed feedback, 412
 three-phase scr, 405
 three-speed blonder, 412
 timer motor clutch, 790
 Triac a-c starter, 329
 Triac-Diac reversing a-c, 411
 two-source gyro, 406
 ultrasonic, 409
 universal a-c/d-c, 410
 vhf intrusion alarm, 4
 voice-actuated, 133
 (See also Control; Servo)
- Moving-target indicator, signal simulator, 682
- Multiplex, stereo tuner, 253
- Multiplexer, 10-channel satellite, 152
 20-channel sampling counter, 624
 analog-digital converter, 37
 f-m stereo, 692
 four-channel ppm modem, 395
 low-level telemetry, 730
 pcm telemetry, 729
 transistor series switch, 270
- Multipplier, 8-kc Q, 413
 amplifier input impedance, 28
 analog-computer pulse-amplitude modulator, 416
 analog-computer pulse-width modulator, 416
 analog-ratio Hall-effect, 415
 analog voltage, 415
 four-quadrant analog, 414
 four-quadrant square-law magamp, 353
 gain-controlled logarithmic, 26
 half-square magnetic, 355
 Hall-effect, 413
 Hall-effect field coil drive, 413
 Hall-effect plate drive, 416
 Hall-effect squaring, 415
 polarity-coincidence, 414
 probability analog, 416
 Q, 24, 141, 416
 quarter-wave magnetic, 355
 simultaneous logic, 337
 square-law magamp, 351, 354
 triangle analog, 416
 two-quadrant analog, 399
 voltage, 141, 142, 492
 voltage sextupler, 193
 (See also Frequency multiplier; Q multiplier; Voltage multiplier)
- Multivibrator, 40-cps 0.01-microwatt, 435
 40-cps astable, 687
 100 cps to 1 Mc astable, 433
 125-cps, 190
 210 cps-210 kc voltage-controlled astable, 434
 400-cps synchronized 68.4-kc astable, 459
 400-cps vjt, 434
 850-cps constant-amplitude sine wave from square wave, 675
 2-kc monostable, 427
 10-kc power supply, 491
 20-kc astable, 769
 20-kc paging transmitter, 801
 31.5-kc crystal oscillator, 736
 50-kc free-running, 431
 150-kc bistable, 418
 250-kc monostable, 435
 500-1,000 kc pressure-sensitive astable, 297
 750 kc-7 Mc, 419
 1-Mc counter input, 163
 1-Mc magnetic transfluxor, 526
 5-Mc, 423
 10-Mc tunnel diode, 421
 200-Mc astable, 252
 10-microsec monostable, 418
 600-microsec output-pulse monostable, 434
 5 to 600 millisecc astable, 434
 25-nsec 0.5-amp clock pulse generator, 109
 0.3 sec astable, 359
 1-5 sec one-shot timer, 784
 15 sec-2 min thermal mono, 422
 1-4 min one-shot delay, 185
 200-400-800 pps prf generator, 428
 adjustable delay, 389
 astable high-power, 433
 astable hybrid td-transistor, 531
 astable stimulator, 385
 astable wailing-siren, 5
 asymmetric current control, 736
 asymmetric free-running, 759
 basic hybrid vjt-pnp transistor, 784, 789
 bistable flowmeter, 367
 bistable hybrid td-transistor, 438
 bistable indicator lamp driver, 436
 bistable main-gate, 437
 bistable ppm demodulator, 729
 bistable in pulse-width modulator, 399
 bistable transistor-testing, 232
 blocking oscillator 10:1 sync, 447
 cascaded bistable, 166
 cascaded one-shot, 427
 cascade, 423, 425
 cathode-coupled main-gate, 432, 437
 cathode-coupled trigger, 419

Multivibrator, cathode-follower-coupled main-gate, 435
 choke-controlled free-running, 426
 choke-controlled one-shot, 428
 clean square-wave, 672
 cold-cathode, 480
 combined with Schmitt trigger, 814
 complementary, 165
 computer fault alarm, 3
 constant-current charging, 421
 constant pulse-width one-shot, 412
 construction barricade flasher, 229
 continuously variable prr, 429
 continuous phase control, 467
 counting-rate meter, 562
 cro trigger, 94
 crystal-controlled, 419
 crystal-controlled clock, 110
 d-c controlled switching voltage regulator, 603
 demodulator, 717
 differential magnetic, 326
 double-coupled main-gate, 432, 435
 dual-input flip-flop, 430
 dual-triode, magnetic inverter, 326
 Eccles-Jordan, 427
 Eccles-Jordan auto-latching gate, 68
 emitter-coupled, 425
 emitter-coupled astable logic driver, 430
 emitter-coupled linear sawtooth, 636
 emitter-coupled mono design, 421
 emitter-coupled sawtooth, 632
 expanded-delay monostable, 183
 fast-recovery hybrid one-shot, 436
 fast-recovery one-shot, 429
 fast-recovery variable-width one-shot, 431
 fast-reset, 423
 fast-turnoff mono, 433
 fet monostable timer, 785
 free-running, 143, 517
 free-running astable 1 cps-250 kc, 434
 free-running cascade, 422, 430
 free-running with Colpitts, 771
 frequency-dividing for f-m/f-m telemetry, 243
 frequency-multiplying, 836
 gate-controlling saturated, 279
 gated, 274
 gated delay, 272
 Geiger recorder keep-alive, 562
 high-power one-shot, 437
 high triggering sensitivity, 420
 improved one-shot, 438
 improved ultra-long monostable, 436
 isolating-diode monostable delay, 186
 line-triggered bistable, 70
 long-pulse mono, 432
 magnetic inverter, 322
 magnetically coupled, 425
 main-gate with diode limiter, 428
 main-gate with triode limiter, 437
 mechanical counter drive for G-M, 561
 microsec fall time, 426
 missile count-down switch, 706
 modulator for full-wave scr motor control, 864
 mono, 422
 mono with adjustable pulse width, 201
 mono with delay, 417
 mono with improved triggering, 433
 mono with negative recovery time, 438
 monostable, 200, 537, 692
 monostable 1-300 millisecc delay, 187
 monostable auto ignition, 65
 monostable chart recorder drive, 583
 monostable delay, 189
 monostable with driver as first stage, 425
 monostable noncutoff, 422
 monostable overload-protecting, 594
 monostable pulse length control, 488
 monostable quad block, 304
 monostable with transistor protection, 505
 monostable vertical sweep, 93
 monostable voltage-time converter, 35
 monostable zero-crossing detector, 114
 noise-immune bistable, 424
 noise-suppressing mono, 428
 on-off relay control, 131
 one-shot, 314, 423, 538
 one-shot battery-charging, 74
 one-shot cro raster display, 91
 one-shot telemetry, 723
 one-shot ultrasonic synchronizer, 828
 one-transistor one-shot, 425
 photocell-triggered chain, 483
 photo-to-grid coupled monostable main-gate, 427, 436
 pnpn triggered one-shot, 809
 power one-shot, 432
 precision time delay, 188
 preferred bistable, 419
 preferred monostable, 420
 preferred monostable delay, 181
 preferred nonsaturating bistable, 419
 preferred prf generator, 418
 preferred saturating bistable, 424
 pressure-sensitive monostable, 297
 pulse duty cycle limiter, 431
 radar ppi sweep generator, 549
 radar prf generator, 420, 421
 random-time delay, 227
 reducing one-shot recovery time, 436
 reducing reset power level, 429
 relay control, 438
 relay-only, 421
 relay-only for a-c, 426
 relay-type with diodes, 419
 remote-control transmitter keyer, 617

Multivibrator, retreating welder control, 381
 scr bridge inverter, 323
 scr switching, 428
 scr trigger, 426
 self-completing gated astable pulse train generator, 531
 series-Schmitt one-shot, 810
 shmo curve plotter, 432
 siren warble generator, 3
 stabilized bistable, 117
 stabilized mono frequency divider, 241
 stabilized one-shot, 424
 strobe trigger, 224
 superconducting test set, 778
 switch-actuating, 520
 switching control, 712
 switching-diode one-shot, 277
 symmetrical, 690, 691
 symmetrical twin-triode, 528
 synchronized astable main-gate, 435
 teaching-machine reward dispenser, 272
 temperature-compensated, 425
 testing tape transport, 718, 720, 722
 thermistor-compensated one-shot, 431
 time-controlled gate, 279
 transformer-controlled free-running, 417
 transient-immune mono, 434
 transistor-uit, 438
 triangular waveform generator, 635
 trigger-shaping, 391
 tunnel-diode, 348
 tunnel-diode monostable, 437
 tv horizontal sweep, 417
 uit, 434
 uit bistable with clamp, 438
 uit bistable with diode decoupling, 437
 ultra-long monostable, 427, 431
 ultrasonic control tester, 833
 ultrasonic prf, 836
 ultrasonic transmitter, 837
 ultrasonic trigger generator, 826
 unclamped saturated, 148
 unijunction control, 425
 unsymmetrical, 424
 variable-delay monostable, 772
 variable gate generator, 275
 variable mark-space, 423
 variable-pulse-width mono, 433
 variable-time one-shot, 429
 voltage-controlled astable duty-cycle limiter, 332
 wide-range mono, 430
 zero-recovery-time one-shot, 426
 (See also Flip-flop)
 Muscle potential, telemetry transmitter, 391
 Music, attack control amplifier, 676
 Fourier synthesis demonstrator, 671
 Muting, actuated by tuning motor noise, 412
 NAND/nor gate (see Logic)
 Navigation system, reverse-phase protection, 503
 Negative resistance, diode bistable switch, 709
 diode oscillator, 462
 diode pulse amplifier, 512
 diode pulse generator, 520
 Neon, 0.2-2,500 cps pulse generator, 384
 binary flip-flop, 208
 chopper, 108, 858
 crt bias regulator, 99
 Halloween pumpkin blinker, 230
 lamp-control oscillator, 85
 off-on-gate, 273
 argon tone generator, 213, 215
 photoconductor gate, 271
 photoconductor inverter, 349
 photoconductor latch, 328
 pulse generator, 521
 relaxation oscillator, 1
 ring-of-five oscillator, 450
 stabilizing photomultiplier dynode voltages, 486
 ultrasonic fathometer, 836
 Neon lamp, capacitor-discharging switch, 709
 Network, 800-1,000 cps ladder tuning, 38
 inverter despiking, 323
 parallel-T, 442
 variable pulse-forming, 229
 Neuron, simulator, 683
 Neuron model, without integrator, 346
 receptor-type, 343
 Neutralization, 200-Mc fet amplifier, 14
 emitter inductance, 797
 Neutron diffractometer, 559
 Noise, 10-Mc dynamic notch filter, 222
 audio squelch, 444, 445
 automatic cancellation, 380
 balanced variable-threshold control switch, 441
 cancellation with balinverter, 138
 constant-Q measuring amplifier, 442
 contour-map design procedure for low-noise amplifier, 442
 correlator, 440
 detector and squelch, 439
 drone target simulator for missile radar, 443
 gate for high-noise-immunity logic, 303
 generator for low-frequency white, 679
 lock-in amplifier, 568
 measurement, 446
 measurement for 200-Mc transistor, 441
 measuring amplitude probability density functions, 441
 nonlinear rejection filter, 440
 out-of-band logarithmic amplifier noise, 440
 protection against scs firing by rf, 446
 pulse generator, 680
 rejection in scr trigger, 818

Noise, series limiter with audio detector, 333
 shunt limiter with audio detector, 334
 sound level meter, 444
 squelch-actuated mobile repeater, 442
 squelch for double superhet, 445
 squelch in mobile receiver, 446
 statistical analysis, 268
 suppressing with photodiode, 478
 suppression in receiver with synchronous scr switch, 709
 suppression in scr circuit, 445
 zenor-diode generator, 446
 Noise analyzer, probability amplitude density, function, 536
 radar clipper-limiter, 332
 Noise figure, measurement, 358
 transistor test set, 439, 779
 transistors at 200 Mc, 444
 Noise figure meter, 1,000 Mc to 200 Mc converter, 441
 Noise generator, 25 to 450 Mc, 443
 radioactive source, 535
 Noise limiter, a-f, 332
 preferred a-m receiver, 333
 Noise reduction, sense amplifier, 340
 Noise suppression, automatic threshold control, 216
 background audio, 446
 improved codon, 812
 Noise suppression factor, cro display, 205
 Nor gate (see Logic)
 Nor logic, single-transistor, 343
 Nuclear flight interval analyzer, 783
 Nuclear radiation (see Radiation)
 Nuclear resonance, temperature telemeter, 760
 Nucleonics, ultrasonic slug cladding flaw detector, 830
 (See also Radiation)
 Null detector, phase-sensitive, 660
 Null detector, voltage, 854
 Oceanography, ruggedized d-c amplifier, 583
 underwater flash, 227
 underwater preamplifier, 50
 Octupler (see Frequency multiplier)
 Operational amplifier, chopper-stabilized, 106
 control switch, 704
 reed-switch synchronous detector, 311
 servo, 653
 Optical modulator, tv laser, 739
 Optical scanner, line locator, 640
 mark-sensing, 478
 Optoelectronics, high-voltage protection, 226
 neon chopper, 108
 switch, 709
 Or gate (see Logic)
 Organ, neon tone generator, 213
 outphased-tone generator, 215
 pedal generator, 214
 swell shoe, 213
 tone generator, 215
 voicing panel, 213
 (See also Electronic music)
 Oscillator, 0-100 cps relaxation, 143
 0-25 kc Wien bridge, 366
 0.01-100,000 cps stabilized, 117
 0.1-1,000 cps two-phase, 670
 0.1-1,000 cps voltage-controlled, 142
 0.2-2,500 cps neon relaxation, 384
 0.25 cps-250 kc relaxation frequency-dividing, 242
 below 1 cps, 766
 1.5-25 cps unijunction relaxation, 237
 2-cps uit relaxation, 186
 4 cps-350 kc Sulzer bridged-T, 454
 5 cps-300 kc capacitance-coupled, 455
 10-30 cps blocking, 828
 10-cps four-mesh fet phase-shift, 458
 10-cps phase-shift, 362
 10-cps phase-shift fet, 448
 50-2,000 cps power, 589
 50 cps-8.5 Mc emitter-coupled astable, 430
 60-cps saturating-core inverter, 316
 100-cps square-wave, 425
 100-350 cps bridged-T audio, 452
 200-400 cps transistor, 462
 400-cps ceramic-dielectric, 262
 400-cps Hartley, 762
 400-cps tuning-fork, 324
 450-cps dual-triode crt supply, 494
 500-1,000 cps, 292
 1-kc Colpitts speech simulator, 683
 1-kc phase-shift, 310
 1-kc precision Colpitts, 645
 1-kc tone for SOS beacon, 78
 1-100 kc Colpitts sine-cosine function, 265
 1.39-kc Hartley, 242
 1.4-kc telephone recorder beeper, 586
 1.5-2.5 kc voltage-controlled sine wave, 458
 1.5-3 kc electronic music, 671
 1.7-kc square-wave, 554
 2-kc unijunction, 322
 3-kc three-output L-C, 383
 3-kc trigger, 426
 3-kc unijunction transistor, 425
 4-100 kc, 518
 5-kc transistor Wien-bridge, 378
 7-kc blocking, 371
 8-170 kc plug-in crystal, 459
 9-kc stabilizer, 799
 9.1-kc variable-frequency crystal, 447, 460
 10-kc single-transistor Colpitts, 459
 10-kc stable Colpitts, 457
 10-kc uit relaxation, 640
 13-25 kc relaxation, 396

Oscillator, 15-kc flowmeter, 367
 18-kc, 315
 19-kc crystal, 692
 20-kc chopper-driving d-c voltage comparator, 118
 20-kc mbvr, 801
 20-40 kc ultrasonic, 830
 21-kc water depth telemeter, 831
 31.5-kc crystal, 736
 38-kc blocking, 692
 40-kc bias-erase, 716
 50-kc crystal-controlled blocking, 722
 50-kc Hartley induction telemeter, 391
 50-kc square-wave, 320
 52-kc crystal and blocking marker-generator, 764
 52.5-kc tetra remote-control transmitter, 620
 68.4-kc astable mvbr synchronized at 400 cps, 459
 80-kc magnetic tape erase and bias, 721
 100-kc Clapp L-C, 109
 100-kc gate-opening, 208
 100-kc magnetostriuctive ultrasonic, 460
 100-kc Pierce crystal, 109
 122-kc grounded-collector Hartley, 456
 230-kc c-w radar, 193
 400-kc triggered blocking, 97
 453-kc silicon-transistor, 798
 456-kc three-tone vehicle identification beacon, 79
 500-kc crystal, 309
 500-kc and 8.325-Mc distress transmitter, 79
 634-kc crystal, 445
 670-kc crystal-transistor, 799
 800 kc-3 Mc crystal or capacitor, 464
 809.11-kc clock, 112
 900-kc ultrasonic flaw detector, 830
 900 kc-5 Mc ultrasonic neurosurgery, 386
 0.45-Mc sine-wave tunnel-diode, 464
 0.5-2 Mc tunnel-diode relaxation, 459
 0.5-20 Mc tunnel-diode delay-line, 462
 0.75-Mc sine-wave tunnel-diode, 464
 0.8-20 Mc preferred Colpitts crystal, 449
 0.8-20 Mc preferred electron-coupled Colpitts, 449
 1-Mc audio-modulated tuned tunnel diode, 449
 1-Mc crystal-controlled clock, 110
 1-Mc crystal time interval standard, 554
 1-Mc encoder, 789
 1-Mc f-m, 251
 1-Mc high-efficiency relaxation, 451
 1-Mc inductive telemetry, 768, 771
 1-Mc proximity detector, 363
 1-Mc stable battery-powered, 447
 1-Mc transfluxor, 526
 1.1-Mc tunnel-diode temperature-sensing, 761
 1.5-Mc clock, 112
 1.65-Mc CB crystal, 139
 2-Mc, 469
 2-Mc crystal, 98
 2-Mc triggered blocking, 98
 2-Mc wide-range voltage-controlled, 455
 2.50 Mc crystal, 802
 2.185-Mc Colpitts, 805
 3-Mc Colpitts, 450
 3-Mc Colpitts crystal, 392
 3-3.5 Mc Lampkin, 573, 577
 3-260 Mc tunnel-diode, 451
 3.522-Mc silicon-transistor, 798
 3.58-Mc color burst, 748, 753
 3.8-Mc transistor negative-resistance, 309
 4.5-Mc crystal, 378
 5-Mc servo-controlled crystal primary standard, 109
 5-15 Mc Clapp sweep-frequency, 674
 5.5-Mc crystal, 277
 6-Mc infrared air leak detector, 456
 7-Mc crystal-controlled Colpitts, 805
 7-Mc tunnel-diode, 793
 10-Mc CCTV carrier, 745
 10-Mc clock, 113
 10-Mc crystal, 457
 11.155-Mc Colpitts crystal, 578, 800
 12-Mc crystal, 674
 12-22 Mc electrically tuned, 463
 12-22 Mc tunnel diode-varicap voltage-controlled, 451
 18-Mc blocking, 110
 20-Mc Colpitts power, 457
 20-480 Mc crystal frequency standard, 246
 23-Mc two-transistor push-pull, 452
 23-Mc voltage-controlled, 460
 24-Mc Clapp, 453, 462
 24-Mc varactor-modulated f-m, 252
 25.7-Mc silicon-transistor, 798
 27-Mc crystal, 803
 27-Mc crystal CB, 397
 27.12-Mc crystal-controlled electron-coupled, 379
 27.255-Mc crystal-controlled tunnel-diode remote control, 618
 27.255-Mc crystal remote control, 617
 27.255-Mc tunnel diode-crystal, 454, 620
 28-Mc cement-setting timer, 359
 30-Mc, 369
 30-Mc coherent radar, 547
 30-Mc wide-temperature-range, 448, 454
 31.05 to 54.95 Mc stepped, 804
 32-Mc burst gate, 277
 35-Mc reactance-tube-controlled, 466
 40-Mc, 463
 40-Mc collector-voltage-control afc, 55
 40-Mc damped, 462
 40-Mc emitter-current-control afc, 55
 40-Mc varactor-controlled, 55
 47.1-Mc tunnel diode-crystal, 464
 54-Mc telemetry transmitter, 733
 60-Mc common-base, 455, 456
 68.75-Mc marker beacon, 77

Oscillator, 70-Mc transducer-controlled crystal, 140
 73.5-Mc self-modulated crystal, 617
 73.5-Mc td-modulated crystal, 806
 74.13-Mc crystal lost-carrier generator, 459
 90-Mc voltage-variable, 470
 90.3125-Mc crystal-transistor reference, 460
 96-110 Mc tunnel-diode f-m wireless mike, 800
 100-Mc Colpitts, 464
 100-Mc crystal, 766
 100-Mc fet Hartley, 464
 100-Mc tunnel-diode telemetry transmitter, 729
 100-Mc varicap f-m, 251
 100-Mc voltage-controlled f-m, 254
 100-140 or 220-260 Mc emergency transmitter, 78
 108-Mc telemetry transmitter, 730
 118-145 Mc crystal, 643
 200-Mc temperature-stable, 463
 200-Mc voltage-controlled, 252
 200-400 Mc electrically tuned, 461
 220-260 Mc dual-triode, 77
 230-Mc pam/f-m, 252
 230-Mc self-reactance modulation, 252
 243-Mc beacon, 80
 255-Mc, 144
 345 Mc, 139
 345-Mc Colpitts, 139
 400-Mc varicap f-m, 255
 400-5,000 Mc tuner, 821
 425-845 Mc, 823
 480-Mc, 140
 500-Mc Colpitts, 463
 600-1,200 Mc voltage-tuned, 462
 1,000-Mc uhf tuner, 824
 1,200-Mc converter, 441
 2,000-Mc harmonic frequency conversion, 452
 920-channel mobile radio crystal reference, 792
 80-flash-per-minute relaxation, 226
 3.6-kv crt power supply, 492
 4.8-kv crt power supply, 496
 7-kv crt power supply, 494
 7-kv and 380-v crt power supply, 494
 14-kv and 385-v crt power supply, 495
 0-2,000 pps preferred series-triggered blocking, 453
 200-300-800 pps radar prf generator, 527
 200-2,000 pps preferred astable blocking oscillator, 459
 200-2,000 pps radar prf generator, 555, 557
 220-4,550 pps voltage-controlled variable-frequency audio, 454
 2,000-83,000 pps preferred parallel-triggered blocking, 461
 500-w square-wave, 105
 a-m/f-m tuner, 574
 amplitude-stable transducer-exciting, 583
 artificial larynx, 388
 audible auto turn-signal indicator, 65, 70
 backward-wave shf sweep, 697
 beat-frequency in sensing cone for blind, 88
 blocking, 321, 338, 518, 527, 563
 blocking 10:1 sync, 447
 blocking for G-M power supply, 495
 blocking for raster crt, 97
 blocking siren-warble, 3
 carrier-signal-powered, 80
 carrier synthesizer, 675
 cascade sine-wave tunnel-diode, 458
 CB crystal, 462
 cesium clock synthesizer for 5-Mc primary standard, 111
 chatter-jamming audio tone, 461
 code practice, 448, 449
 coherent, 672
 coherently switched for f-m/f-m telemetry, 243
 cmo jitter monitor, 546
 color tv pickup reference, 752
 Colpitts crystal frequency monitor, 249
 Colpitts telemetry subcarrier, 728
 common-emitter Hartley, 798
 constant-amplitude doppler radar, 448
 crystal chronometer, 113
 crystal-controlled pulse, 520
 crystal-controlled tunnel-diode CB converter, 146
 crystal impedance meter, 776
 current-controlled subcarrier, 731
 daylight-intensity telemeter, 484
 d-c to d-c 590-v power supply, 493
 dip meter, 365
 drive for cold-cathode counter, 625
 eight-tone phase-shift, 404
 electric fuel pump drive, 67
 electronically swept 400-600 kc, 671
 feedback-stabilized, 458
 fet-controlled pulse, 412
 five-tone series-tank, 731
 f-m/f-m subcarrier, 725
 four-layer diode, 461
 four-waveform generator, 634
 free-running segmented-sweep, 687
 free-running sine-wave cascade, 460
 free-running time-delay, 180
 frequency checking with WWV, 568
 frequency-shift-keyed, 451
 frequency-synthesizing, 580
 fuze-timing, 111
 gated selective-calling, 272
 Hartley distance-mark generator, 549, 551-553, 555, 557
 Hartley remote-control tone, 620
 heart-pacer relaxation, 390
 high-frequency mobile, 578
 high-stability bridged-T, 463
 high-stability vacuum-tube, 461

Oscillator, hold-sample-hold phase detector for vco, 472
 hydrogen-thyatron induction heater, 211
 incremental-tuning R-C, 674
 incremental-tuning R-C output, 669
 induction heater, 210
 isolation, 209
 level-detecting relaxation, 531
 linear-biased oscillator control, 466
 locked color tv, 753
 locking to WWV, 111
 low-frequency cwo sync, 99
 machine-gun firing control, 131
 marker-pulse blocking, 125
 mesa-transistor tv tuner, 740
 microwatt a-f, 530
 Miller-effect transistor, 721
 millimicrosecond blocking, 365, 372
 mos fet crystal, 458
 multi-crystal clutter generator, 682
 multi-waveform voltage-controlled, 530
 negative-feedback uhf tuner, 822
 negative-resistance diode, 462
 neon relaxation, 521, 566
 nonlinear voltage-controlled sine-wave a-f, 463
 nonsaturating triggered blocking, 526
 parallel-T feedback filter, 221
 pentode a-f for 5-kv crt supply, 493
 pentode a-f for 7-kv crt supply, 494, 496
 phase-locked frequency control, 202
 photomultiplier power supply, 492
 photoread, 484
 Pierce tetrode-transistor, 676
 pnp plus npn, 809
 preferred parallel-triggered blocking (below 2,000 pps), 456
 pressure-sensitive integrated, 301
 proximity-control, 132
 pulsed sinusoidal, 100
 read-write blocking, 125
 relaxation, 517
 relaxation for lamp-flashing beacon, 223
 relaxation neon, 1
 relaxation scr control, 598
 resistance-controlled subcarrier, 732
 resistance strain gage, 255
 resistance-transducer-controlled, 455
 r-f for 10-kv regulated crt supply, 601
 r-f-a-f, 670
 ring-of-five neon, 450
 ring-type magnetic core, 517
 saturable-reactor, 462
 sawtooth chirp-radar, 700
 sawtooth relaxation, 556
 sawtooth-triggered blocking, 586
 scs relaxation, 523
 scs RLC, 450
 self-quenched superregenerative, 286
 series-Schmitt, 810
 shorted-coax tunnel-diode, 524
 sidereal-clock regenerative control, 113
 sine-square-wave phase-shift, 520
 sinusoidal high-voltage converter, 496
 square-wave tunnel-diode, 524
 stabilized horizontal tv, 746
 stabilized Wien-bridge, 457
 strobe trigger, 224
 switched Hartley distance-mark generator, 525
 switching-transistor Hartley, 530
 telemetry anemovane audio, 727
 temperature-sensing, 762
 three-terminal neon, 215
 transistor radio autodyne converter, 147
 transistorized magnetostriiction filter, 222
 transistorized phantastor, 523, 697, 701, 702
 triggered blocking, 224
 triode magnetostriiction filter, 219
 tunnel-diode CB converter, 138
 tunnel-diode Civil Air Patrol converter, 147
 tunnel-diode monostable, 437
 tv camera r-f, 736
 twin-phantastor, 522
 twin-triode a-f for 6.5-kv crt supply, 493
 two-phase frequency analyzer, 248
 two-lana electronic doorbell, 7
 two-transistor metronome, 215
 ujt, 460, 757
 ujt metronome, 214
 ujt relaxation, 178
 ujt relaxation with pulse shaper, 544
 ujt sawtooth, 635
 variable-amplitude tunnel-diode, 464
 variable-duty-cycle relaxation, 228
 variable-frequency tone-burst, 461
 variable-frequency Wien-bridge fet, 670
 variable pulse width, 518
 variable Wien-bridge fet, 675
 varistor-controlled voltage-tuned, 465
 varying-frequency alarm tone generator, 4
 vco for frequency synthesizer, 242
 vhf intrusion alarm, 4
 video-recorder blocking, 715
 voltage-controlled 0-1 kc, 202
 voltage-controlled frequency-multiplying, 258
 voltage-controlled Shockley diode, 140
 voltage-controlled temperature telemeter, 756
 voltage-controlled wide-range phase-shift, 469
 voltage-tunable 50-Mc f-m, 254
 voltage-tunable f-m, 255
 voltage-tunable swept vhf, 676
 wailing-siren, 5
 X-band pulse radar modulator, 555

- Oscillator, zero-speed tape playback, 718
(See also Audio; Electronic heating; Electronic music; Frequency divider; Frequency modulation; Frequency multiplier; Function generator; Inverter; Modulator; Multivibrator; Pulse generator; Radar; Receiver; Remote control; Sawtooth generator; Signal generator; Staircase generator; Sweep; Telemetry; Television; Transceiver; Transmitter; Ultrasonic; Video)
- Oscillator-doubler, 8-12 Mc crystal-controlled, 520
- Oscilloscope, battery-operated, 90, 94, 95, 98
- crt blip detector, 81
- raster, 93, 97, 98
- time-shared computer troubleshooter, 122
(See also Cathode-ray)
- Osteograph, bone-marrow ratio scanner, 382
- Oven, temperature-controlled, 761
- Oven control, crystal oscillator, 759
- Overlay transistor, CB transmitter, 795
- f-m transmitter, 796
- Overload (see Protection)
- Overload protection, transistor, 170
- Overshoot suppression, accelerometer, 410
- Overvolting, electromechanical counter, 149, 150
- Pacemaker, batteryless cardiac, 392
- cardiac, 383
- Paging, resonant-reed receiver, 568
- single-loop transmitter, 801
- Panoramic receiver, electrically tuned, 822
- Paramagnetic coating, thickness measurement, 362, 365
- Parametric amplifier, 200-kc high-impedance, 27
- level control, 576
- test set, 766
- Parking motor, infrared reset, 293
- Pattern recognition, dilating circular scan, 640
- Peak memory, 0-10 cps, 856
- Pedestal generator, airborne radar, 696
- Peltier-effect, liquid-density gage cooling, 128
- pH, Beckman meter, 372
- Phantastron, delayed gate, 279
- modified, 701
- Phantastron, 360-cps to 60-cps frequency divider, 244
- current pulse generator, 767
- delay integrator, 185, 189
- equivalent transistorized sweep, 699, 702
- fast-recharging solid-state, 185
- four-transistor delay, 183
- preferred delay, 182
- preferred fast-recovery delay, 181
- print timer, 490
- radar target acceleration simulator, 683
- sweep generator, 523
- temperature-compensated delay, 182
- three-transistor sawtooth sweep, 697, 701, 702
- timing signal generator, 673
- tv pulse-cross generator, 746
- two-transistor sweep, 698
- two-triode sweep, 699
- variable time-delay, 680
(See also Delay; Timer)
- Phantastron oscillator, voltage-controlled, 522
- Phase control, 90-Mc voltage-variable oscillator, 470
- 1-kw general-purpose a-c, 471
- 180° transformerless, 466
- 180° variable, 474
- 360-deg, 466
- ambiguity resolver, 474
- antenna array, 474
- audio-controlled antenna phaser, 467
- automatic antenna phaser, 467
- balanced-bridge reversing servo drive, 411
- cascaded two-tube 255° range, 473
- coincident-slicer phase meter, 470
- d-c supply output voltage, 470
- Diac-Triac for 5-amp load, 473
- digital phase meter, 468
- free-running mvbr, 467
- frequency standard, 468
- high-gain vjt, 473
- hold-sample-hold detector, 472
- lamp-dimming, 472
- linear-biased oscillator control, 466
- low-power scr, 476
- measurement from 15 to 400 Mc, 466
- measurement at 30 Mc, 469
- measurement up to 2,000 Mc, 472
- measuring with flip-flop, 473
- miniature using light-activated scr, 473
- polyphase, 471
- pulse-chain phase meter, 471
- radio direction finder, 465
- reversing full-wave d-c motor, 409
- reversing series-wound universal motor, 410
- shunt-wound d-c motor, 408
- snap-action a-c, 473
- steerable antenna, 466
- Triac-vjt, 473
- universal a-c/d-c motor, 410
- voltage-controlled wide-range oscillator, 469
- voltage-tuned oscillator, 465
- voltage-variable capacitor, 398
- Phase detection, magnetic drum read amplifier, 587
- Phase discriminator, a-c bridge, 201
- Phase equalizer, a-f amplifier, 690
- Phase indicator, three-phase power line, 474
- Phase-lock microwave system, 662
- Phase rotation indicator, reverse-phase protection, 503
- Phase shift, 10-cps fet oscillator, 448
- Phase shift, 10-cps oscillator, 458
- bridge-rebalancing servo, 663
- crystal i-f filter, 217
- current-controlled oscillator, 731
- goniometer motor amplifier, 405
- measuring a-f, 361
- phasemeter chopper, 808
- servo quadrature voltage rejection, 646
- square-wave generator control, 70
- two-stage a-f discriminator, 201
- varistor control amplifier, 175
- vertical acceleration recorder, 584
- Phase shifter, color tv, 752
- transistor-testing, 768
- Phase-shifting amplifier, 90-deg, 653
- Phase splitter, pulse, 538, 539
- shift register, 666
- Phasemeter, ambiguity resolver, 474
- chopper and phase detector, 808
- coincident-slicer, 470
- polarity-coincidence multiplier, 414
- pulse-chain, 471
- voltage-variable oscillator, 470
- Phono line, radar relay, 555, 556
- Phono amplifier, 6-v, 41
- 9-v, 42
- line-operated two-transistor, 46
- Phono preamplifier, 40-12,000 cps, 39
- ceramic-cartridge, 38, 40, 48
- npn magnetic-cartridge, 53
- velocity-response ceramic-cartridge, 46
- Photocell, bridge element, 83
- Photoconductive cell, sunset switch, 482
- Photoconductor-neon inverter, 349
- Photoconductor-neon latch, 328
- Photoelectric control, 6217 photomultiplier preamp, 500
- 7326 photomultiplier preamp, 499
- a-c modulator for photomultiplier, 477
- age for ambient light, 476
- alarm transmitter tester, 2
- antenna-pattern function generator, 266
- automatic caliper, 477
- automatic camera shutter control, 485
- bandpass filter, 481
- bird counter, 150
- blip detector, 81
- character reader, 640
- chopper, 106, 858
- cold-cathode amplifier, 482
- conveyor jam detector, 482
- conveyor shutdown for jams, 134
- coranometer, 476
- dewpoint sensor, 480
- distress transmitter code-whistle flip-flop, 79
- earlobe pulse transmitter, 377
- exposure indicator for enlarger, 489
- eye-motion mvbr chain, 483
- fet latching relay, 328
- flash-activated scr delay, 484
- frequency-sensitive control, 483
- full-wave id-scr control, 135
- gaging rotating parts, 479
- gain-controlling servo, 651
- gamma-ray spectrometer, 475
- headlight dimmer, 66, 71
- illumination control, 480
- illumination telemeter, 484
- keying control, 481
- lamp control, 475
- lamp failure alarm, 7
- light-activated scr lamp preheater, 478
- light-activated scr phase control, 476
- light-activated scr time delay, 484
- light-beam coupling, 709
- light detector and alarm, 7
- light-frequency converter using 1,000-cps phase-shift oscillator, 310
- light meter, 482
- light-source stabilizer, 482
- mark sensor, 478
- measuring angular lag of servo, 481
- measuring flashlamp output, 481
- measuring length, 539
- microphotometer, 481
- monochromator preamp, 291
- movie camera frame-rate checker, 485
- nuclear explosion flash detector, 559
- nuclear track scanner, 639
- optical communication amplifier, 23
- optoelectronic chopper, 108
- paper flaw detector, 479
- photodiode amplifier, 484
- photodiode noise suppressor, 478
- photomultiplier gain stabilizer, 641
- photoreed oscillator, 484
- programmed lamp preheater control, 129
- programmed-tape tool control, 410
- projectile glow detector, 483
- pulse interval control, 483
- punched paper tape reader, 476
- punched-tape pulse comparator, 119
- pupillograph with flying-spot scanner, 642
- push-pull optical coupling, 484
- recording manometer, 386
- reflected-light photodiode amplifier, 481
- resistance-triggered alarm, 7
- r-f welder shutoff, 863
- scr paper tape reader, 480
- signal subtractor, 641
- strontium-90 photorelay source, 560
- time-delayed Schmitt sensor, 136
- Photoelectric amplifier, starved-transistor, 40, 52
- Photoelectric control, character reader, 640
- conveyor, 149
- Photoelectric control, scanner load switching, 639, 642
- Photoelectric counter, buttons and beads, 148
- Photofeedback, high-temperature pyrometer, 756
- phototransistor control, 760
- Photoflash, high-efficiency supply, 225
(See also Flash; Photography)
- Photography, 0.1 sec-2 hr timer, 486
- 16-mm sound-on-film drive, 581
- bootstrap timer, 488
- contrast-density timer, 490
- crt trace brightness equalizer, 99
- dryer temperature control, 487
- electronic shutter control, 485
- exposure indicator for enlarger, 489
- exposure timer, 488
- Kerr-cell shutter, 487
- magnification-compensating darkroom timer, 487
- movie camera frame-rate checker, 485
- nuclear track scanner, 639
- photomultiplier timer for enlarger, 486
- precision interval timer for enlarger, 863
- projectile shadowgraph trigger, 483
- rocket-sled camera control, 490
- scr developer temperature control, 762
- shutter control, 489
- slave unit, 486
- tumor-scanning amplifier, 387
- tv single-frame timer, 489
- underwater flash, 227
- variable program timer, 488
(See also Flash)
- Photomultiplier, 2-kv battery-powered supply, 492
- automatic caliper, 477
- gain-controlling, 651
- gain stabilizer, 641
- gain-switched a-c modulator, 477
- microphotometer, 481
- power supply, 492
- projectile glow detector, 483
- shot noise suppression, 220
- underwater gamma radiation-measuring, 475
- Photoreed, bandpass filter, 481
- frequency-sensitive, 483
- oscillator, 484
- Photoreistor, audio keying, 481
- Phototransistor, high-temperature pyrometer, 757
- photofeedback, 756
- Phototube, sunset switch, 480
- Piezoelectric gage, amplifier, 9
- Plasma generator, ionization-gage, 708
- thyatron pulser, 493
- Plasma instrumentation, timing-mark flash, 228
- Plasma research, wideband integrator, 307
- Plate-cathode follower, drive for Wien-bridge oscillator, 27
- Polarity detector, analog-digital converter, 34
- Pole-zero cancellation, shunt-peaked video amplifier, 838, 846
- Potentiometer, error compensation, 35
- Power supply, 420-cps, 258
- 440-cps 115-v, 585
- 9,600-cps fluorescent, 313
- 27-Mc pump frequency, 27
- 0-1,200 v photomultiplier, 492
- 20 v at 60 amp variable remotely controlled, 495
- 28 v d-c to 25 and 50 v d-c, 495
- 28 v d-c/400 v d-c, 496
- 100 v a-c reference, 855
- 590-v oscillator-type d-c to d-c, 493
- 900, 1,000, and 1,100 v for portable G-M tube, 495
- 2-kv photomultiplier, 492
- 3.6-kv screen for dark-face crt, 492
- 4.5-kv electric spark machining, 128
- 4.8-kv oscillator-type crt, 496
- 5-kv a-f oscillator-type crt, 493
- 5-kv d-c, 496
- 6.5-kv twin-triode a-f oscillator-type crt, 493
- 7-kv a-f oscillator-type crt, 494
- 7-kv magnetic-deflection crt, 491
- 7-kv preferred dual-triode crt, 494
- 7-kv and 380-v oscillator-type crt, 494
- 7-kv and 450-v a-f oscillator-type crt, 496
- 14-kv and 385-v oscillator-type crt, 495
- 10-w stereo amplifier, 694
- adjustable d-c motor field, 352
- analog voltage source, 492
- auto ignition, 66
- battery-charging, 73, 75
- battery standby circuit, 507
- constant-current, 591
- constant-current a-c output, 168
- constant-current with adjustable rise and fall times, 523
- constant-voltage, 591
- d-c to 400-cps a-c, 324
- d-c converter, 143
- d-c for fluorescent lamps, 169
- d-c motor control, 412
- d-c/d-c inverter, 313, 315
- d-c/d-c regulated, 491
- dual-polarity variable, 496
- error cancellation in d-c amplifier, 198
- flash-tube pulser, 223
- high-voltage era, 90
- induction heater bias, 210
- laser flash tube, 229
- low-power amplifier, 516
- minimum-dissipation regulated, 506
- output voltage phase control, 470
- overload protection, 507
- radio signal energy, 576
- short-circuit protection, 508
- single-transistor smoothing filter, 222

- Power supply, slow sweep for classroom demonstrations, 700
 temperature-compensated constant-current, 170
 three-phase inverter, 314
 transistor circuit tester, 493
 triggered thyatron pulser, 493
 underwater flash tube, 227
 voltage monitor, 134
 (See also Battery charger; Inverter; Regulated power supply)
- Power switching, 400-cps a-c, 712
 scr, 712
- Power transistor, pulse-type tester, 768, 770
- Preamplifier, 0.5 cps-700 kc, 499
- 1 cps-40 kc microvolt-signal, 21
 10 cps-100 kc microphone, 498
 10-cps-1 Mc magnetic transducer, 715
 100 cps-8 Mc tape recorder, 499
 13-24 kc vlf, 499
 15-500 kc direction finder, 498
 18-kc low-drain silicon transistor, 500
 21-kc ultrasonic, 825
 250-kc broadband ultrasonic, 833
 30-Mc variable-bandwidth i-f, 499
 30-260 Mc grounded-grid broadband, 11
 3-50 na current, 500
 amplification-15 servo, 654
 amplification-70 servo, 657
 amplification-300 servo, 654
 amplification-1,200 servo, 663
 automatic impedance-matching, 498
 autopilot, 500
 ceramic-cartridge phono, 38, 40
 Darlington audio, 51, 52
 direct-coupled servo, 657
 dual-beam oscilloscope, 499, 500
 electronic siren, 2
 fat hydrophane, 828
 fat-pnp bootstrapped source-follower, 497
 infrared mine detector, 294
 infrared transceiver, 290
 infrared width gage, 292
 integrated a-f, 305
 light pen, 99
 low-noise radar i-f, 549
 magnetic tape playback, 717
 photomultiplier, 499, 500
 stabilized audio, 50
 tape recorder fet, 716
 uvicon tv camera, 734
 variable-bandwidth radar, 498
 vibrating-capacitor electrometer, 497
 video recorder, 715
 vvm transistor-protecting, 854, 856
 (See also Amplifier; Audio; Audio preamplifier; Phono preamplifier; Receiver)
- Preregulator, 31-v series-regulated power supply, 590
- Prescaler, divide-by-5 ring counter, 240
- Printed circuit, automatic caliper, 477
 conductor tester, 781
 programmed servo component inserter, 652
- Printer, electrostatic high-speed, 116
- Processor, video signal, 330
- Product detector, ssb, 575
- Programmer, neutron diffractometer, 559
- Projectile, glow detector, 483
 measuring acceleration, 731
 nose pressure telemetry, 731
- Propagation delay, measurement, 249
- Proportional amplifier, integrator, 311
- Protection, 12-v battery charger control, 72
 28-v power supply overload, 594
 30-w audio amplifier overload, 504
 adjustable overload, 510
 a-f output transistor, 44
 capacitance-sensitive intrusion alarm, 87
 capacitive overload for series regulator, 510
 conveyor belt overload, 353
 conveyor jams, 134
 correct a-c line voltage, 501
 current overload, 507
 current overload for series-pass regulator, 503
 d-c amplifier overload, 508
 d-c power supply short-circuit, 508
 from false alarm due to power failure, 507
 high-voltage system, 226
 high-voltage testing, 509
 hot-chassis, 509
 meter from transducer current spikes, 507
 missing-pulse detector, 502
 monostable mvbr transistors, 505
 multiple supply voltage failure, 502
 overload, 171
 overload with ripple clipping, 503
 overload and short-circuit, 503
 paralleled power transistors, 508
 power amplifier pulse duty cycle limiter, 431
 power-line overvoltage, 505
 power supply overload with gate-turnoff scr, 504, 505
 power supply overload and overvoltage, 504
 power supply overload with power reduction, 510
 power supply short-circuit, 590
 punch press safety, 706
 reactor undercurrent-overcurrent, 133
 regulated power supply, 506, 509
 regulated power supply against capacitive-load charging current, 502
 regulated power supply overload, 501
 regulated power supply short-circuits, 510
 reverse-phase control, 503
 scanner sweep failure, 696
 scr regulated power supply, 507
- Protection, series voltage regulator overload with tunnel diode, 505
 short-circuit on power supply, 510
 short-circuit-proof regulated power supply, 508
 telemetry low-level amplifier overload, 331
 thyatron in voltage-controlled high-voltage switch, 506
 transistor overload, 170
 transistor overvoltage, 507, 508
 transmitter, 362
 travelling-wave tube filament regulator, 506
 welder ignitor with load-sharing flip-flop, 862
 (See also Alarm)
- Proximity detector, ferrous or non-ferrous, 132
- Proximity switch, capacitance control, 86
- Public address, aircraft five-zone, 43
- Pulse (see Chopper; D-c amplifier; Flip-flop; Gate; Inverter; Latching circuit; Logic; Pulse amplifier; Pulse generator; Pulse height analyzer; Pulse processing; Pulse shaping; Radar; Switching; Trigger)
- Pulse amplifier, 50 cps-3.5 Mc emitter-follower, 27
 100-Mc bandwidth, 511
 1,000-Mc wideband video, 512
 50-ohm driver, 515
 beam-intensity modulation, 99
 coaxial cable driver, 515
 current-integrating, 308
 fast-acting limiter, 333
 fast-acting nonlinear feedback, 514
 fast rise time, 514
 heart-block correction, 385, 391
 intermediate-level video, 842
 inverting, 514
 keyer, 212
 line printer hammer, 515
 linear, 512
 linear high-level, 842
 logarithmic, 513
 low-level video, 839
 magnetostrictive delay, 836
 nanosecond thyatron driver, 513
 negative-resistance diode, 512
 noninverting, 514
 preferred cascaded emitter-follower, 846
 preferred low-level cathode follower, 847
 pulse code groups, 513, 514
 reduced standby current, 511
 Van de Graaff accelerator, 512
 wireless telemetry link transmitter, 729
 (See also D-c amplifier)
- Pulse amplitude, sampling, 538
- Pulse analyzer, fast-recovery one-shot mvbr, 431
- Pulse communication, amplifier, 513, 514
- Pulse delay, inverter, 185, 186, 189
- Pulse dropout detector, 6
- Pulse generator, 0.05 cps-10 kc, 532
 0.2-2,500 cps neon-transistor, 384
 0.5 cps-60 kc variable square-wave, 528
 60-cps square-wave in neutron diffractometer, 559
 60-1,000 cps variable-duty rectangular-wave, 529
 100-cps square-wave, 425
 13-25 kc prf, 396
 20-kc trigger, 817
 600-kc clock, 125
 1-Mc square-wave transfluxor, 526
 10-Mc clock, 113
 10-Mc Schmitt square-wave, 818
 12-Mc four-phase clock, 112
 18-Mc clocking, 110
 30-Mc suppressor-gated i-f, 281
 100-Mc, 519
 140-Mc tunnel-diode, 522
 200-Mc voltage-controlled, 522
 220-260 Mc beacon, 77
 1-amp power flip-flop, 820
 1-amp trigger, 813
 45-kw r-f brazing, 862
 250-kw, 393
 1-megawatt, 552
 0.2-0.00 pps preferred series-triggered blocking oscillator, 453
 0.5 ppm-2,000 pps, 531
 1-pps clock, 665
 200-2,000 pps preferred astable blocking oscillator, 459
 200-300-800 pps, 527
 200-400-800 pps mvbr prf, 428
 2,000-83,000 pps preferred parallel-triggered blocking, 461
 25-nsec 0.5-amp clock, 109
 2,000-sec counter, 665
 100-v ramp, 633
 500-v, 703
 1,000 v, 521
 30-kv ignition, 71
 adjustable duty cycle, 518
 adjustable rise and fall, 523
 adjustable time-base delay, 769
 armature tester, 774
 astable hybrid td-transistor, 531
 basic bistable, 667
 basic hybrid square-wave mvbr, 784, 789
 basic low-cost Schmitt square-wave, 820
 batteryless pacemaker, 392
 blanking with linear pulse width control, 539
 blocking-oscillator, 338
 calibrated millimicrosecond, 529
 cardiac pacemaker, 383
 clock, 423
 combination triangular, square, and sawtooth, 632
- Pulse generator, constant duty cycle, 537
 constant-width high-current, 524
 control for fish shaker, 525
 cosine/sine function, 267
 cosine-squared radar, 550
 counter-testing, 517, 527
 cro marker, 764
 crt unblanking, 99
 crystal-controlled avalanche, 520
 current, 767
 delay line driver, 715
 delayed output, 529
 digital, 673, 682
 diode-matrix program, 338
 double-pulse, 556
 eight-pulse train, 142
 electric fish fence, 707
 electroexplosive tester, 780
 eleven-pulse burst, 427
 fish shocker, 524
 four-waveform, 634
 frequency exciter, 804
 gated-beam square-wave, 528
 gyro balancer, 89
 half-sinusoid, 812
 hard-tube radar modulator, 554
 Hartley oscillator, 530
 high-accuracy rectangular, 526
 high-voltage, 587
 high-voltage flash tube, 229
 integral number of pulses, 531
 interference pulses, 520
 ionospheric sounder simulator, 684
 keyed d-c or tone signals, 434
 laser pumping, 395
 level detector, 531
 light-controlled, 483
 line-triggered square-wave, 70
 linear ramp, 637
 long-time-constant ramp, 638
 low-cost square-wave from sine wave, 531
 low-frequency random-noise, 679
 low-repetition-rate neon-triode gate, 273
 magnetic core testing, 525
 magnetron modulator, 397, 400
 memory disk track, 112
 memory-plane driver, 123
 microwave tube life-test, 773
 millimicrosecond, 365, 372
 modulator trigger, 808
 monostable, 519
 monostable mvbr with adjustable duty cycle, 431
 multi-waveform, 530
 negative-resistance diode, 520
 neon-transistor, 521
 neuron simulator, 683
 neurosurgery oscillator keyer, 382
 nine-bit word, 526
 noise, 446
 one-shot, 521
 phantatron, 523
 positive or negative ramp slope, 635
 predetermined number of pulses, 680
 preferred parallel-triggered blocking oscillator (below 2,000 pps), 456
 preferred prf mvbr, 418
 preferred search radar distance mark, 525
 with pulse inverter, 516
 pulsed 100 cps-100 kc Wien-bridge oscillator, 530
 punched card reader, 17
 push-pull rectangular, 415
 radar distance mark, 549, 551-553, 555, 557
 radar prf, 420, 421
 ramp-staircase strobe, 632
 random-time, 227, 516
 rectangular parts, 318
 retina-stimulating, 385
 ring-type, 517
 ringing-type, 528
 sawtooth, 641
 sawtooth-sync for induction heater, 210
 scs, 523
 scs RLC, 450
 scs single-pulse, 66
 scs square-wave, 530
 self-resetting pulse stretcher, 543
 sine to sawtooth or square-wave converter, 147
 sine-square-wave, 520
 single 100-amp pulse, 777
 single-pulse, 524
 single-step, 688
 six-diode square-wave, 518
 specific number of pulses, 522
 square-wave, 232, 544, 672
 square-wave pair, 517
 square-wave tunnel-diode, 524
 square-wave tunnel-diode discriminator, 535
 stellarator-controlled, 520
 stepper motor, 411
 stepping switch drive, 530
 superregenerative amplifier control, 16
 sync, 519
 sync oscillator, 99
 temperature-control, 758
 thin-film memory, 121
 thin-film welder, 862
 transistor tester, 773
 triangular, 641
 triangular-wave, 629
 triangular waveform, 635
 trigger, 538
 triggered blocking oscillator, 526
 triggered square-wave, 527
 triggered tunnel-diode, 249
 tunnel-diode, 117

- Pulse generator, tunnel-diode rectangular, 526, 529
- twin-phantastron, 522
- uJT triangular-wave, 637
- ultra-long mono mvbr, 427
- ultrasonic receiver boxcar, 826
- unsymmetrical, 424
- variable-delay, 772
- variable gate, 275
- variable mark-space, 423
- variable prr and pulse width, 532
- variable pulse width, 529
- variable rectangular, 618
- variable-slope, 524
- variable-slope ramp, 638
- variable triangular, 631
- variable-width, 518
- vertical triggering for raster cro, 97
- voltage comparator, 850
- voltage-controlled pulse spacing, 529
- voltage-controlled ramp/trigger, 633
- voltage/duty-cycle converter, 145
- voltage-sensing, 810
- wide-range voltage-controlled, 532
- x-ray tube, 381
- (See also Chopper; Clock; Computer; Flash; Function generator; Inverter; Latching circuit; Magnetic amplifier; Multivibrator; Noise; Radar; Remote control; Sawtooth generator; Signal generator; Staircase generator)
- Pulse height analyzer, 1 v or 3 v, 534
- amplitude window, 536
- high-stability, 534
- nanosecond display, 208
- positive or negative polarity, 115
- probability amplitude density function, 536
- pulse shock spectrum, 536
- pulse-width-modulation telemetry, 534
- random signal, 535
- secondary-emission pentode, 811
- solar x-rays, 533
- tunnel-diode, 535, 536
- voltage or current, 533
- voltage-difference, 534
- voltage limit detector, 535
- (See also Discriminator)
- Pulse height discriminator, paper flow detector, 479
- Pulse processing, 100-microamp pulse coincidence detector, 539
- amplitude-to-width conversion, 538
- anticoincidence detector, 539
- coincidence detector, 539
- d-c level shifter, 542
- differentiating, 538
- dual delay, 178
- linear pulse width control, 539
- magnetron beam switch, 537
- maintaining preadjusted duty cycle, 537
- measuring pulse length, 539
- missing-pulse detector, 540
- noise-rejecting scr trigger, 818
- phase-splitting, 538, 539
- prf discriminator, 203
- pulse train modulator, 402
- sample-and-hold circuit, 626
- sequence detector, 540
- sine-wave zero-crossing detector, 538
- target drone control, 615, 616
- timing signal generator, 673
- varying-width pulse trains, 540
- width measurement, 630
- (See also Pulse shaping)
- Pulse shaper, 1-Mc d-c restorer, 544
- 30-hour fat delay, 541
- bipolar limiter, 334
- computer-testing pulse generator, 542
- d-c level shifter, 542
- delay-line, 543
- delay mvbr, 189
- diversity combiner, 732
- encoder, 791
- facsimile sync, 586
- flash tube, 230
- frequency divider, 244
- frequency multiplier, 256
- G-M output, 541
- magnetostrictive delay line, 836
- missing-pulse detector, 502
- modem, 396
- Navy preferred, 541
- radar timer, 530
- reducing width from 400 millisecc to 100 millisecc, 544
- retina welder control, 381
- sawtooth-square, 544
- Schmitt scr trigger, 810
- self-resetting stretcher, 543
- sine-square wave, 544
- stretcher-expander, 542
- telemetry converter, 542
- time amplifier, 268
- triangular-to-sine, 542
- tv outline generator, 743
- uJT relaxation oscillator, 544
- ultrasonic receiver, 826
- variable pulse width, 529
- velocimeter, 367
- zener-diode stretching delay, 543
- Pulse shaping, golf cart motor drive, 408
- (See also Pulse processing)
- Pulse sorter, 1,000 pps, 540
- Pulse spike detector, 4
- Pulse stretcher, 96-millisecc mvbr, 417
- missing-pulse detector, 502
- Pulse stretcher, scs, 544
- self-resetting, 543
- Pulse switching, magnetron beam tube, 537
- Pulse width-analog converter, 36
- Pulse width control, mvbr, 297
- Pulse-width modulator, Schmitt trigger, 303
- Pulsor, flash tube, 223
- high-voltage thyatron, 493
- stepping switch, 530
- Punched card, strobe-pulse reader, 17
- Punched tape, photoelectric reader, 476
- Pupillograph, flying-spot scanner, 642
- Pyrometer, rocket surface, 756, 757
- Pyrometer control, photofeedback phototransistor, 760
- Q, 8-kc stable multiplier, 413
- Q multiplier, audio, 141
- audio amplifier, 41
- modulation percentage monitor, 416
- oscillator and filter, 24
- Quantizer, speech for pulse-code modulation, 144
- Quench, superregenerative, 286
- superregenerative detector, 621
- Quenching, telemetering transmitter, 758
- Quinary scaler, 500-Mc sync circuit, 164
- Radar, 230-kc c-w oscillator control, 193
- 2.2-Mc power amplifier, 548
- 30-Mc coherent oscillator, 547
- 30-Mc i-f, 549
- 30-Mc i-f preamp, 499
- 200-Mc pulsed distributed amplifier, 554
- 250-kw ten-diode modulator, 548
- 300-kw tube tester, 776
- 1-megawatt pulse generator, 552
- 0-190 mile nonprecise phantastron delay, 184
- 5-175 mile nonprecise phantastron delay, 184
- 0-2.440 microsec bootstrap delay, 188
- 0-2.440 microsec phantastron delay, 184
- 50-350 microsec bootstrap delay, 186
- 50-350 microsec phantastron delay, 188
- 0.5-ohm source-impedance amplifier, 23
- 200-300,800 pps blocking-oscillator prf generator, 527
- 200-400-800 pps mvbr prf generator, 428
- 200-2,000 pps blocking-oscillator prf generator, 555, 557
- A-scope trace intensifier, 98
- age for monopulse amplifier, 60, 64
- audio amplifier, 551
- auto speed, 545
- beacon decoder, 78
- beam-power video driver, 839
- beam-radar target-simulating noise generator, 443
- blocking-oscillator modulator, 555
- blocking-oscillator pulse-frequency divider, 244, 245
- boxcar detector, 557
- capacitor-storage feedback boxcar, 116
- cascaded distance-mark divider, 243
- clutter simulator, 546
- constant-amplitude sawtooth sweep, 696
- cosine-squared pulse generator, 550
- datum stabilizer for altimeter, 87
- diode-phantastron afc, 54, 56
- distance-mark generator, 549, 551-553, 555, 557
- encoder sweep generator, 556
- far-end-of-line modulator clipper, 551
- fixed bootstrap delay, 185
- fixed mvbr delay, 186
- four-diode magnetron modulator, 401
- four-input video mixer, 845
- four-trigger video mixer, 845
- gated clamp, 557
- hard-tube modulator, 554
- intermediate-level video amplifier, 842
- klystron oscillator mode-centering servo, 556
- logarithmic amplifier, 551
- low-level video amplifier, 839
- low-noise i-f backward-diode detector, 285
- magnetic pulser, 554
- main-gate mvbr, 427, 428, 432, 435-437
- measuring static stability under vibration, 460
- missile transponder, 548
- moving-target simulator, 680
- mti jitter indicator, 546
- mti simulator, 682
- multichannel monitor, 545
- mvbr prf generator, 420, 421
- narrow-band a-c amplifier, 547
- narrow-band balanced modulator, 555
- noise-analyzing clipper-limiter, 332
- noise-immune scr trigger, 809
- ppi blanking amplifier, 556
- ppi marker switch, 553
- precision time delay, 188
- preferred bistable mvbr gate, 419
- preferred common-cathode video mixer, 849
- preferred distance-mark generator, 525
- preferred fast-recovery phantastron delay, 181
- preferred phantastron delay, 182
- preferred video detector, 841
- preferred video limiter, 332
- pulse amplifier, 513, 514
- pulse-width discriminator, 552
- pulsed X-band transmitter, 556
- range accuracy tester, 683
- range tracking counter, 152
- rectangular gate generator, 420
- sine-cosine approximator, 268
- spiral sweep simulator, 679
- Radar, sweep generator, 549
- sweep oscillator, 700
- symmetrical i-f limiter, 286
- synchronous demodulator, 191
- target acceleration simulator, 683
- target pulse simulator, 680
- telescope-tracking servo, 662
- three-dimensional target simulator, 681
- three-input video mixer, 845
- three-range nonprecise phantastron delay, 184
- thyatron-controlled afc for klystron, 55, 56
- time-interval standard, 554
- timing pulse shaper, 550
- two-dimensional target simulator, 547
- two-input video mixer, 848
- unblanking signal generator for circular display, 106
- variable-bandwidth preamp, 498
- variable-length sweep generator, 551
- variable-pulse-width one-shot, 431
- video amplifier, 840
- video amplifier chain, 839
- video integrator, 309
- video switch, 553
- voltage-controlled delay, 178
- waveform-sampling boxcar, 627
- yoke driver and clamp, 550
- (See also Comparator; Converter; D-c amplifier; Display; Frequency multiplier; Gate; Oscillator; Regulated power supply; Sweep; Trigger; Video)
- Radar simulator, function generator, 266
- Radiation, 0.5-Mc scaler, 565
- alpha particle monitor, 563, 567
- control rod magnet current regulator, 129
- control rod magnet undercurrent-overcurrent protection, 133
- cosmic-ray G-M counter and ionization gage, 562
- dual-function bistable reactor scram, 130
- electrometer, 564
- fallout arrival-time clock, 560
- G-M counter, 558
- G-M counter for tracers, 564
- G-M counting-rate meter, 562
- G-M drive for scaler, 541
- industrial Geiger-counter monitor, 562
- jet fuel flow gage, 567
- liquid scintillation counter coincidence circuit, 566
- measurement with arithmetic binary, 564
- measurement with tachometer, 71
- measuring with basic binary block, 560
- measuring high-altitude alpha particles, 558, 563
- measuring low-energy space particles, 566
- measuring neutron diffraction, 559
- mechanical counter drive for G-M, 561
- monitoring dust particles, 560
- nuclear-explosion gamma-ray detector, 564
- nuclear explosion simulator, 682
- nuclear thermal detector, 559
- nuclear track scanner, 639
- pulse coincidence circuit, 561
- pulse height discriminator, 533, 534
- radiological vacuum gage, 564
- resistance-triggered alarm, 7
- robot remote-control switch, 618
- sampling cold-cathode counter, 625
- scintillation counter anticoincidence circuit, 565
- stopped-count indicator for alarm, 567
- strontium-90 phototransistor source, 560
- survey meter, 563
- Radiation measurement, random noise, 535
- Radio astronomy, lock-in amplifier, 568
- Radio direction finder, 15-500 kc preamp, 498
- Radiometer, 46-cps feedback amplifier, 294
- microwave iceberg detector, 190
- Radiosonde, neutron-detecting, 726
- Rail flow detector, amplifier, 584, 585
- Ramp, phantastron, 701
- voltage-controlled 10-cps to 20-kc, 633
- (See also Sawtooth)
- Ramp generator, linear, 636, 637
- linear sawtooth, 630
- long-time-constant fat, 638
- pdm keyer, 728
- positive or negative slope, 635
- single-stroke, 710
- trigger-driving, 451
- uJT triangular-wave, 637
- variable-slope, 638
- Random function, generator, 677
- Random noise, radioactive source, 535
- Random stepped function, generator, 672
- Raster display, cro, 91
- Raster generator, flying-spot scanner, 640
- Raster oscilloscope, timing generator, 98
- vertical sweep generator, 93
- vertical triggering generator, 97
- Rate effect, suppression, 4
- Rate meter, strobe flash, 229
- Ratio meter, voltage, 854
- Reactor, startup control, 128
- Reactor control, dual-function bistable trip, 130
- regulator amplifier, 129
- rod magnet undercurrent-overcurrent sensor, 133
- Reader, alphanumeric character, 640
- punched tape, 476
- scr photoelectric paper tape, 480
- Readout, telephone data matrix, 122
- Receiver, 100-350 cps crevasse detector, 51
- 2-kc magnetic induction field, 374
- 9-kc induction auto radio, 573
- 13-kc ultrasonic, 832
- 21-kc ultrasonic, 825
- 22-kc sonar, 828

Receiver, 60-kc ultrasonic hydrophone, 831
 455-kc i-f mobile, 288
 460-kc f-m wireless microphone, 571
 3-3.5 Mc Lampkin oscillator, 573, 577
 15-Mc microelectronic, 300
 27-Mc CB limiter, 334
 27-Mc eight-command remote control, 616
 27-Mc tone-modulated data, 148
 27.255-Mc remote control, 621
 75-Mc marker beacon, 77
 75-Mc one-light airborne marker beacon, 79
 75-Mc three-light marker beacon, 76
 75-Mc three-light marker beacon adapter, 80
 105-Mc i-f superhet, 139
 200-Mc r-f radiation alarm, 6
 406.549 Mc f-m, 619
 420-Mc slot antenna, 573
 460-Mc f-m, 615
 a-m/f-m i-f strip, 570
 a-m/f-m tuner, 574
 amplifying signals in noise, 568
 audio for a-m/f-m portable, 570
 automatic aircraft antenna phaser, 467
 carcinotron control, 572
 CB decoder, 795
 CB squelch, 794, 795
 CB three-transistor, 793
 CB transceiver, 794
 clock-pulse synchronizer, 112
 compactron f-m stereo adapter, 693
 current-tuned uhf tuner, 822
 d-c coupled transmission line, 133
 double-conversion superhet, 57, 574
 double superhet with squelch, 445
 electroluminescent display, 205
 fail-safe squelch, 439
 five-transistor auto radio, 569
 fixed-gain antenna, 569
 f-m stereo, 692
 f-m stereo demodulator, 694
 f-m stereo matrixing, 691
 f-m weak signal capture, 250
 four-transistor a-c line, 576
 four-transistor reflex, 571
 frequency-selective audio Q multiplier, 41
 ground pulse blanker, 95
 i-f/a-f reflex, 577
 improved codan, 812
 infrared space communication, 292
 klystron local-oscillator afc, 55
 l-f signals in high noise, 414
 limiter in mobile color tv relay receiver, 332
 Mercury spacecraft command, 50
 millimeter-wave detector, 574
 mobile 175-Mc r-f stage, 578
 mobile high-frequency oscillator, 578
 mobile limiter and discriminator, 330
 mobile low-frequency oscillator, 578
 mobile squelch, 446
 multipurpose integrated-circuit chip, 301
 noise correlator, 440, 520
 noise suppression with synchronous scr switch, 709
 out-of-baseband logarithmic noise amplifier, 440
 paging, 568
 parametric-amplifier-controlled, 576
 pcm demodulator, 192
 power-line carrier-current tv remote control, 617
 preferred agc, 62
 preferred audio squelch, 62
 protected output stage, 575
 radiosonde, 723
 radioteletype i-f filter, 217
 reflex portable a-m, 573
 signal generator, 670
 signal-powered, 576
 signal-powered alarm, 7
 signal-powered transponder, 80
 signal-seeking auto radio tuner, 824
 single-channel eyeglass-frame, 569
 single-frequency 100-Mc integrated-circuit f-m, 299
 single-transistor autodyne converter, 147
 six-transistor 6-v broadcast, 579
 six-transistor 9-v broadcast, 579
 six-transistor 12-v broadcast, 575
 six-transistor a-c line, 578
 six-transistor broadcast with tuned r-f stage, 577
 ssb frequency synthesizer, 580
 ssb product detector, 575
 superregenerative amplifier, 286
 synchronous sampler, 623
 target drone control, 616
 television remote control, 622
 tone-operated calling system, 572
 tone signal rejector, 570
 toy train control, 621
 tuned r-f transistor stage, 572
 tuning motor noise actuates muting, 412
 tv horizontal deflection, 741
 tv ultrasonic remote-control, 827
 two-transistor reflex portable, 574
 uhf/shf tuner, 821
 ultrasonic trf, 837
 varactor-tuned antenna, 571
 vlf preamp, 499
 voltage-tunable panoramic, 822
 (See also Audio; Intermediate frequency; Limiter; Preamplifier; Remote control; Transceiver)
 Reciprocal circuit, computer, 120
 Recorder, 100-cps-8 Mc preamplifier, 499
 16-mm sound-on-film, 581
 airplane vertical acceleration, 584
 analog voltage comparator, 586
 auto engine control, 66

Recorder, beeper for telephone conversation, 586
 color tv, 748
 constant-speed drive for 400-cps synchronous motor, 404
 data sampler, 624
 d-c positioning voltage inserter, 587
 drive amplifier, 582
 electrographic, 587
 end of sampling period, 625
 facsimile power amplifier, 585
 facsimile sweep generator, 584
 facsimile sweep pulse shaper, 586
 ferrite-core gated amplifier, 583
 f-m data demodulator, 191, 192
 f-m signals on tape, 626
 magnetic characteristics, 310
 magnetic drum clock pulse amplifier, 110
 magnetic-drum-controlled flip-flop, 587
 magnetic-drum JK flip-flop, 239
 magnetic drum read amplifier, 587
 magnetic drum write amplifier, 582
 magnetic tape flow detector, 779
 measuring wow and flutter, 201
 peak-reading circuit, 583
 power-line transients, 583
 rail fault, 584, 585
 ruggedized d-c amplifier, 583
 satellite slow-speed video, 582
 shockproof ferrite-core, 581
 soliton linear time base, 309
 sonar trainer, 835
 teleprinter control, 386
 timing-mark flash, 228
 timing motor power supply, 585
 timing signal modification, 586
 traffic data matrix readout, 122
 transducer exciter, 583
 video time division multiplex, 394
 volume level, 587
 (See also Tape recorder)
 Rectifier, sound level meter, 446
 Redundancy, d-c amplifier, 175
 inverter, 324
 passive in a-c amplifier, 28
 Reference amplifier, 8-25 v current-limiting power supply, 612
 12-v Darlington-connected supply, 602
 12-v regulated supply, 604
 precision constant-current regulated supply, 170
 Reference voltage source, 6-v zener, 591
 Reflex circuit, economy transistor radio, 577
 sensitive relay control amplifier, 137
 Refrigerator, temperature-controlled, 761
 Register, magnetic, 126
 saturated-transistor flip-flop, 427
 Regulated power supply, 50-2,000 cps a-c, 589, 593
 0-10 v adjustable two-zener, 605
 1-17 v short-circuit-proof, 508
 1-17 v temperature-stabilized, 600
 1.4-v two-zener, 589
 3-v, 595
 3 v using backward-diode reference, 611
 5-v output steps, 596
 5-25 v equal and opposite outputs, 600
 6 v at 20 amp, 613
 6-v d-c at 4 amp, 592
 6-v remote-sensing, 607
 6-v zener reference, 591
 8-v, 606
 8-25 v current-limiting with reference amplifier, 612
 9-v with zero output resistance, 611
 12-v with 2D21 thyatron, 611
 12-v Darlington-connected with reference amplifier, 602
 12-v d-c at 3 amp, 602
 12 v d-c bi-level, 588
 12-v reference amplifier, 593, 604, 614
 12-v voltage-splitting, 610
 15-v Middelbrook series-stabilized, 591
 16-v battery converter-regulator, 610
 20-v 100-w switching-mode, 603
 20-v inverted-output blocking-oscillator switching, 609
 24-v blocking-oscillator switching voltage regulator, 605
 25-v d-c, 598
 25-v ujt-scr a-c, 601
 28-v with overload protection, 594
 31-v series with preregulator, 590
 50 v at 400 ma, 610
 50-c d-c, 599
 62 v at 2 amp Darlington-connected series, 607
 100-v d-c, 597
 110-v a-c, 596
 150-v with external reference, 611
 150-v negative-output, 603
 150-v positive-reference pentode, 608
 150-v preferred, 599, 600, 608
 150- and 300-v negative-output, 603
 150- and 300-v triode-connected pentode series tube, 606
 152-v negative-output, 613
 152-v pentode series tube, 607
 200-v d-c to 24-v d-c, 599
 240 v for vacuum gage, 606
 250-v preferred, 601, 607
 250-v tube-zener, 594
 300 v at 200 ma, 596
 300-v negative-output, 613
 300-v pentode with 6L6 series tubes, 612
 300-v preferred, 597, 605
 320 v at 60 ma, 612
 10-kv r-f oscillator-type for crt, 601
 14-kv 400-cps crt, 610

Regulated power supply, 15-kv uvicon, 736
 20-kv inductive-storage, 604
 2.8-microamp constant-current, 170
 1-ma constant-current, 167
 10-ma constant-current, 167
 5 amp at 0-20 v, 595
 adjustable 30-v meter-testing, 775
 backward-wave oscillator filament, 593
 balanced-input pentode twin-triode, 614
 balanced-output pentode twin-triode, 612
 basic series-pass transistor, 609
 battery charger, 74
 capacitor-forming, 591
 CCTV repeater, 745
 constant-current, 168
 constant-current-diode, 588, 594
 current limiter, 331
 current-stabilized dry cell, 359
 d-c voltage level converter to pulse width, 145
 dual-polarity 15-v, 595
 fail-safe twt filament, 506
 feedback choke cuts ripple, 602
 five voltages for linear IC tester, 604
 focus coil, 92
 heater, 598
 high-voltage magamp, 355
 klystron oscillator, 592
 low-cost silicon-diode reference, 609
 low-ripple, 595
 magamp for reflex klystron, 355
 magnetic-amplifier, 351
 magnetron filament, 614
 magnetron injection electrode, 592
 minimum-dissipation, 506
 modified twin-triode cascade, 609
 overload and overvoltage protection, 504
 overload-protected, 502, 503, 509, 510
 overload protection with ripple clipping, 503
 overload protection with tunnel diode, 505
 pentode with ripple suppression, 614
 portable cro, 74
 precision constant-current, 170
 reference amplifier, 614
 reference for automatic charger control, 75
 satellite, 103
 saturable-reactor, 590
 scr, 608
 scr overload protection, 501, 507
 series-pass with delayed overload protection, 510
 series-pass with overload protection, 503
 short-circuit-protected, 510, 590
 short-proof, 510
 shunt with zener, 606
 simple series regulator, 614
 single-pentode, 611
 switched d-c, 597
 temperature-compensated, 613
 triggered scr, 598
 true twin-triode cascade, 609
 twin-triode cascade, 611, 612
 variable pulse width, 105
 vibration meter, 589
 zener-controlled high-current, 593
 (See also Power supply)
 Regulator, 0-100 ma current, 171
 6-v shunt transistor, 613
 audio generator, 169
 blood pressure, 385
 charging-current, 75
 closed-loop for high-power induction heater, 212
 current, 171
 d-c amplifier, 174
 d-c to d-c converter, 141, 142
 d-c signal, 194
 electromagnet servo, 648, 649
 focus coil current, 92
 gas analyzer filament emission, 127
 heart rate, 390
 integrated-circuit series, 305
 oven temperature, 757
 reactor control rad magnet current, 129
 series, 510
 temperature, 762
 three-phase static inverter, 316
 (See also Current control; Regulated power supply)
 Regulator-charger, portable cro, 74
 Relay, 12-v a-c drive by transistors, 713
 d-c amplifier responds to rapid input changes, 176
 delayed-dropout timer, 188
 fast-recycling time-delay, 187
 fast-rise current driver, 705
 fet light-activated latching, 328
 hysteresis-free, 127
 latch circuit, 327
 latching a-c static using toroids and scr's, 327
 latching magnetic-firing scr, 328
 multivibrator, 419, 421, 426
 mvbr-controlled, 438
 on-off servo, 659
 parking meter reset, 293
 photoelectric, 482
 reflex control amplifier, 137
 resonant-read control, 616
 saturable-reactor latch, 709
 six-transistor for 10-amp load, 705
 slow-break circuit, 186
 slow-make circuit, 182
 solid-state dpdt, 712
 stepper flip-flop drive, 230
 thyatron stops chatter, 488
 transmitter-damping, 801
 ujt time delay, 188

- Relay, variable-delay make, 180
- Relay chatter, tester, 774
- Relaxation oscillator, 1-Mc high-efficiency, 451
- uit for electronic organ, 215
- Relay circuit, hot-chassis lifesaver, 509
- Reliability, active redundant d-c amplifier, 175
- passive redundant a-c amplifier, 28
- Remote control, 27.255-Mc receiver, 621
- 27.255-Mc id-crystal transmitter, 620
- 27.255-Mc transmitter, 617
- 73.5-Mc transmitter, 617
- amplifier gain, 620
- balloon-borne gear, 618
- cameras on rocket sled, 615
- carrier-powered, 7
- commutator-motor synchronizer, 619
- Franklin superregenerative receiver, 620
- magnetostriiction tv, 830
- Miller superregenerative receiver, 618
- model tank, 616, 620
- photoelectric oscillator, 484
- power-line carrier-current receiver for tv set, 617
- power-line carrier-current transmitter for tv receiver, 620
- radioactive-area robot, 618
- target aircraft, 619
- target drone, 615
- target drone receiver, 616
- time-constant detector for tv, 619
- toy train, 621
- transmitter, 824
- tv transmitter programs, 622
- tv ultrasonic, 827
- uit-id garage-door transmitter, 618
- variable rectangular-wave modulator, 618
- (See also Control; Motor control; Receiver; Telemetry; Transmitter)
- Remote gain control, wideband amplifier, 14
- Repeater, 10-Mc CCTV, 740
- d-c power supply (CCTV), 745
- squellch-actuated mobile radio, 442
- Resistance, a-c milliohmmeter, 375
- go-no-go tester, 733
- Resistor, electronically variable, 312
- variable r-f, 83
- voltage-controlled fat, 220
- Resolver, sweep generator drive, 549
- unity-gain isolating amplifier, 11
- Resolver driver, a-c operational amplifier, 659
- Responder, 13-kc ultrasonic, 832
- Retina stimulator, electrical, 385
- RIAA equalization, phono preamplifier, 40
- Ring counter, scr motor control, 405
- Ringling choko, d-c to d-c converter, 141, 142
- Robot, commutator motor synchronizer, 619
- remote-control commutating switch, 618
- Rocket, pitch or yaw servo, 659
- Rotation analyzer, integrator, 67
- Sample-and-hold, analog, 36
- fat d-c amplifier, 172
- operational amplifier, 704
- pam pulse train, 626
- Sampler, digital receiver, 623
- random noise, 268
- Sampling, 10-Mc amplifier, 628
- 10-Mc flip-flop, 625
- 0.5 v telemetered information, 626
- 20 voltage sources, 624
- analog-digital, 628
- axis-crossing interval, 90
- boxcar circuit, 627
- cold-cathode radioactive, 625
- component waveforms, 773
- diode bridge, 82
- end-of-count time recorder, 625
- Hall generator, 623
- linear bootstrap time base, 634
- lacked-pair logic, 342
- magnetic-tape data, 624
- magnetic-tape reference channel, 626
- millivolt pulses, 84
- operational amplifier with tunnel-diode pair, 626
- thin-film waveforms, 628
- two-way electronic switch, 627
- video signal, 89
- zero-order data hold, 627
- Sampling circuit, analog-digital converter, 32
- Sampling gate, 360
- Satellite, 3-Mc telemetry oscillator, 450
- 10-channel multiplexor, 152
- digital-analog random-pulse converter, 35
- magnetometer, 368
- position-measuring phase meter, 468
- solar x-ray spectrometer, 533
- Saturable core, voltage control, 134
- Saturable reactor, frequency-dividing, 784
- inverter, 320
- Saturable-transformer chopper, 100-kc, 108
- Sawtooth, 25.75 kc frequency modulator, 720
- 0.2-300 sec Schmitt trigger, 483
- afc for mobile color tv pickup, 750
- analog voltage comparator, 586
- converting from sine wave, 147
- derivative pulse output, 523
- drum recorder control flip-flop, 587
- fast-reset, 699
- free-running, 699
- grounded grid, 698
- linear R-C radar encoder, 556
- low-frequency with 1 percent linearity, 687
- multi-range constant-amplitude, 696
- phantastron time base, 787
- radar delay, 178
- Sawtooth, recovering analog voltage, 583
- Shockley diode relaxation oscillator, 556
- three-transistor phantastron, 697, 701, 702
- transistor, 517
- variable-sweep subaudio signal generator, 676
- voltage bar graph display, 206
- Sawtooth generator, 20-100,000 cps triangular, 629
- 50-kc, 637
- 100-v ramp, 633
- 400-v gto, 638
- 100 sec to 5 hour ramp, 634
- adjustable slopes and peaks, 631
- boosting frequency, 631
- fast-retrace synchronous sweep, 631
- four-waveform, 634
- high-accuracy, 631
- high-linearity, 629
- linear, 632, 636
- linear 2-sec, 637
- linear bootstrap, 630
- linear bootstrap time base, 634
- linear ramp, 636, 637
- positive or negative slope, 635
- pulse width measurement, 630
- rising and falling, 632
- sampling or strobe, 632
- simple subaudio, 635
- slow-rise linear-ramp, 638
- subaudio, 633
- triggered Shockley diode with integrator, 635
- uit slope-reversing, 637
- uit variable-frequency sweep, 637
- ultralinear ramp as voltage to pulse width converter, 636
- unijunction, 635
- variable-slope ramp generator, 638
- variable triangular, 636
- voltage-controlled ramp, 633
- voltage-independent fat, 630
- waveform-synthesizing, 635
- (See also Sweep)
- Scaler, 1-kc glow-tube, 161
- 4-kc glow-tube, 154
- 0.5-Mc cold-cathode, 565
- 1-Mc low-dissipation, 664
- 500-Mc biquinary, 151, 158
- 500-Mc quinary control circuit, 164
- arithmetic binary, 564
- Scaler, basic bistable with trigger amplifier, 730
- battery-powered binary, 149
- decade, 157
- four-channel discriminator, 725
- G-M drive, 541
- Scanner, bone-marrow ratio, 382
- character, 640
- character-reading, 640
- densitometer, 364
- flat-display tv, 641
- flying-spot pupillograph, 642
- flying-spot sweep failure alarm, 696
- microfilm flying spot, 639, 642
- nuclear tracks, 639
- optical mark-sensing, 478
- photomultiplier gain stabilizer, 641
- photomultiplier signal subtractor, 641
- signal-level-controlled gain, 59
- transducer, 643, 649
- uit raster generator, 640
- wideband video amplifier, 641
- Scatter link, pulsed demodulator, 192
- Schmitt trigger, cro strobe, 89
- temperature alarm, 1
- voltage regulator, 105
- (See also Trigger)
- Scintillation counter, anticoincidence circuit, 565
- coincidence circuit, 566
- coincidence detector, 561
- Scope (see Cathode ray; Oscilloscope)
- Script, reading, 336
- SECAM, French color tv chrominance, 748
- Selector, quad-block, 304
- Sense amplifier, integrated-circuit, 301
- Sensing, core memory, 812
- Sensistor, 5-Mc temperature-compensated flip-flop, 236
- mono, 422
- Sensitivity control, vidicon, 737
- Sensor, a-c load, 321
- engine heat, 293
- frequency, 629
- germanium-diode temperature, 757, 758, 763,
- germanium-transistor temperature, 758, 759
- pressure, 301
- time-delayed Schmitt, 136
- toroid command pulse, 726
- Separator, clamped video sync, 744
- Septupler (see Frequency multiplier)
- Serializing, measuring nearly simultaneous events, 180
- Servo, 50, 60, and 400-cps amplifier, 656
- 60-cps amplifier for air-leak detector, 654
- 400-cps amplifier, 653
- 400-cps driver, 648
- 400-cps two-transistor preamp, 662
- 800-cps active parallel-T filter, 221
- 1.5-w class B amplifier, 651
- 1.5-w fet amplifier, 657
- 2-w amplifier, 653
- 3-w amplifier, 660
- 3-w power amplifier, 661
- 4-w class B amplifier, 645
- 4-w output stage, 660
- 5-w amplifier, 651
- 6-w high-efficiency amplifier, 647
- 7.5-w class B amplifier, 644
- Servo, 10-w class B amplifier, 650
- 35-w class B amplifier, 652
- alternator frequency control discriminator, 203
- amplification-15 preamplifier, 654
- amplification-70 preamplifier, 657
- amplification-300 preamplifier, 654
- amplification-1,200 preamplifier, 663
- anesthesia control, 387
- artificial hand, 388
- automatic search and control, 662
- balanced-bridge reversing drive, 411
- blood-volume level control, 380
- bootstrapped emitter-follower positive-feedback amplifier, 654
- bridge-balancing, 663
- chopper-input amplifier, 663
- clock control, 111
- comparator chopper, 115
- complementary-transistor amplifier, 655
- controlling extrusion of plastic on wire, 87
- current-subtracting modulator, 646
- d-c motor control, 412
- dewpoint measurement, 480
- d-f antenna, 649
- direct-coupled preamp, 657
- electric spark machine control, 128, 129
- electromagnet fine regulator, 648
- electromagnet rough regulator, 649
- frequency compensator, 647
- frequency control for 5-Mc primary standard, 109
- frequency control for dielectric heater, 209
- frequency standard control, 468
- full-wave push-pull a-c drive, 407
- goniometer amplifier, 405
- gyro pitch or yaw, 659
- instrument motor controller, 661
- integrator amplifier, 306
- klystron oscillator mode-centering, 556
- life-test cycling, 662
- magamp gyro indicator, 350
- measuring angular lag with photodiode pickoff, 481
- micromicroammeter, 371
- modulator for shaft position encoder, 661
- motor stall protection switch, 657
- negative-positive-feedback amplifier, 653
- nulling amplifier, 644
- on-off relay, 659
- operational preamplifier, 655
- parallel-mode transducer scanner, 643, 649
- Perkin-Elmer infrared analyzer, 289
- phase-sensitive null detector, 660
- printed-circuit assembler, 652
- quadrature-suppression thermistor, 648
- quadrature voltage rejection, 646
- receiver attenuator, 61
- resolver driver, 659
- response time speedup, 645
- self-balancing 400-cps servo motor drive, 352
- self-setting gate, 276
- sonar bearing indicator, 829
- star tracker gain-controlling, 651
- stepper tester, 777, 778
- switching-mode amplifier, 658
- synchronous filter, 650
- target drone control, 615, 616
- telescope positioning, 662
- ten-transistor amplifier, 645
- ten-transistor integrated-circuit amplifier, 304
- thermistor, 656
- three-transistor transformer-coupled amplifier, 650
- transmitter tuning, 643
- Triac-Diac reversing speed control, 411
- two-speed, 658
- two-speed magamp drive, 354
- Servo amplifier, 2-w, 646
- 6-w, 656
- Sextupler, voltage, 193
- Shape recognition, dilating circular scan, 640
- Shaper, complementary, 807
- facsimile sync, 586
- Shift register, 6-kc counting, 154
- 500-kc flip-flop, 666
- 1-Mc low-dissipation, 664
- 1.5-Mc flip-flop, 345
- 1.6-Mc clock rate, 665
- 10-nsec pulses, 667
- 2,000-sec counter pulse generator, 665
- 1-pps clock generator, 665
- analog-digital converter, 34
- basic bistable, 667
- basic scs, 666
- contents display, 664
- counting, 150
- driver, 667
- five npn one-bit units, 819
- flip-flop blocks, 668
- ring-counter with visual readout, 161
- self-indicating flip-flop, 666
- symmetrical saturated flip-flops, 666
- ten-stage scs, 668
- transformer-controlled, 668
- tunnel diode, 667
- tunnel diode-transistor, 341, 349
- (See also Computer; Logic)
- Shifter, d-c signal level, 768
- logic level, 335
- Shmoa curve plotter, computer memory, 306, 345,
- 432, 535, 637
- Shock spectrum, analyzer, 536
- Short-circuit, power supply protection, 590
- Short-circuit protection, series-regulator, 594
- Shutter, Kerr-cell, 487
- Sideband rejection, dual-detector demodulator, 192

Signal dropout alarm, 7
Signal generation, beep or tone, 6
Signal generator, 0.1-1,000 cps two-phase, 670
15-cps modulated 1-Mc, 771
20-40 cps variable sweep, 676
20-40 kc Wien-bridge, 675
850-cps constant-amplitude sine wave, 675
848-kc crystal filter, 221
5-15 Mc Clapp sweep-frequency, 674
attack control amplifier, 676
battle-station alarm, 3
carrier synthesizer, 675
coherent oscillator, 672
color-burst gating signal, 752
electronic music demonstrator, 671
electronic-organ neon, 215
frequency-analyzing filter, 669
incremental tuning, 674
incremental-tuning R-C output, 669
infrared power monitor, 291
integrated-circuit chopper drive, 303
level-stabilized, 672
linear-frequency sweep, 671
programmed digital, 673, 682
radio direction-finder calibrator, 676
random-function, 677
random periodically stepped wave, 672
r-f/a-f, 670
simple subaudio sawtooth, 635
siren warble, 3
square-wave, 672
standing-wave detector, 674
stereo f-m, 690, 691
subaudio sawtooth, 633
sweep-frequency shf, 697
timed pulse, 673
two-phase oscillator, 248
ultrasonic control tester, 833
varying-frequency alarm tone, 4
vht voltage-tunable ferroelectric, 676
Wien-bridge four-range, 670
(See also Function generator; Pulse generator; Sawtooth generator; Staircase generator)

Silicon-controlled rectifier, toy train control, 621
Silicon controlled switch, rfi protection, 446
Simulator, 16-bit serial binary words, 681
airborne mt clutter, 682
antenna-pattern function generator, 266
auto engine road test, 66
beam-rider missile target, 443
exponential-decay load, 684
ionospheric pulse transmitter, 677
ionospheric sounder pulses, 684
neurons, 683
nuclear blast thermal radiation, 682
programmed digital, 673, 682
radar clutter, 546
radar and sonar, 679
radar spiral sweep, 679
radar target, 680, 683
radar target acceleration, 683
radar target or noise pulses, 680
random-function control system, 672, 677
speech, 683
squib for propellant-actuated fastener, 681
telemetry commutator, 678
three-dimensional radar target, 681
transport delay, 179
two-dimensional radar target, 547
variable power-supply load, 684
Sing-around velocimeter, sonic deep-ocean, 371
Siren, 200-w power amplifier, 4
preamplifier, 2
three-transistor wailing, 5
warble generator, 3
zone coding generator, 2
Slave unit, photographic, 486
Slider, fast agc, 60
pulse height analyzer, 536
ultrasonic pulse, 828
Solar cell, strobo-generating amplifier, 17
voltage regulator, 105
Solar cell amplifier, character reader, 114
Solenoid, measuring closure time, 784
overdrive amplifier, 515
Solenoid actuator, drive circuit, 407
Soliton, electrical readout integrator, 310
noise dosimeter, 309
tetraode integrator, 308
Sonar, 22-kc receiver, 828
22-kc transmitter, 835
300-kc tone burst generator, 271, 273
1-megawatt pulse generator, 552
audio selection gate, 827
cathode-ray bearing indicator, 829
f-m demodulator for trainer, 831
f-m modulator for trainer, 835
peak-amplitude detector, 832
thumper, 833
timer and c-r display, 204
Sorter, component waveforms, 773
photoelectric gating, 479
pulses in train, 540
pulse width, 539
Sound (see Audio)
Sound level meter, 444
rectifier, 446
stable amplifier, 44
Sound movies, tape recorder, 718
Sound track drive, 16-mm camera, 581
Spacecraft, computer power conservation switch, 125
recovery beacon, 77
Spark coil, sonar thumper, 833
Spark gap, 1-millimicrosec switch, 710

Spark gap, fast high-voltage switch, 704
Spark machining, 4.5-kv power supply, 128
amplidyne servo control, 129
Specific gravity, measurement, 128
Spectrometer, missile plume, 291
Mossbauer-effect, 556
neutron, 559
nuclear-resonance temperature-sensing, 760
solar x-ray, 533
underwater gamma radiation, 475
wide-band amplifier, 20
Spectrum analyzer, low-pass i-f amplifier, 284
shock, 376
shock pulse, 536
Speed control, 1-kw fan, 471
series d-c motor, 406, 412
shunt-wound motor, 406
(See also Motor control)

Spike suppression, d-c/d-c converter, 138
Spin test, inductive telemetry, 768, 771
Splitter, phase, 568
power supply voltage, 610
Sporadic-E, measurement, 376
Square-wave generator, 0.2-300 sec, 483
conversion from audio oscillator, 531
(See also Pulse generator)

Squarer, electrostatic, 309
frequency analyzer, 669
high-accuracy fet, 267
Squaring, fet audio, 265
Squalch, audio, 444, 445
CB receiver, 794, 795
double superhet, 445
fail-safe, 439
magnetic-tape print-through, 720
mobile receiver, 446
preferred agc, 62
preferred audio, 62
tuning motor noise-actuated, 412
Stabilizer, load current, 171
photoelectric light source, 482
signal voltage, 22
Staircase generator, 10-minute stepped sweep, 685
64-step, 686, 687
blanking indicator, 686
counter, 685
cro raster display, 91
curve tracer, 688
frequency to voltage converter, 146
low-frequency, 687
multijunction semiconductor, 152
negative and positive-going, 688
segmented-sweep display, 687
single-step, 688
transistor curve tracer, 770
wide frequency range, 685
Standard cell, calibrating a-c meters, 781
Star tracker, gain-controlling, 651
photomultiplier, 477
Starter, free-running flip-flop, 233
Static alternator, three-phase motor control, 405
Steering, base-triggered flip-flop, 813
base-triggered flip-flop with hybrid gate, 818
collector-triggered flip-flop, 819
reversible counter, 153
Stellarator, crowbar switch, 708
pulse generator, 520
Step generator, tunnel-diode, 688
Stepper motor, test analyzer, 777, 778
Stepper relay, flip-flop control, 230
latch, 328
reset circuit, 328
Stepping circuit, pulse generator, 516
Stepping motor, drive oscillator, 412
Stereo, 60-cps parallel-T notch filter for reproducing tape, 218
10-w amplifier power supply, 694
audio amplifier for f-m transmitter, 690
audio preamplifier, 689
compactron f-m adapter, 693
driver, 693
dual-output i-f amplifier, 690
fet f-m tuner, 691
f-m automatic switch, 689
f-m multiplex adapter, 694
f-m multiplex tuner, 253
f-m receiver matrixing, 691
f-m tuner with pin diode, 695
headphone amplifier, 694
multiplex f-m signal generator, 690
multiplex phase modulator, 693
receiver multiplexer, 692
suppressed-carrier f-m, 691
transmitter subcarrier generator, 695
(See also Audio; Tape recorder)

Stethoscope, sliding gate, 389
Stimulator, neurophysiology, 384
paralyzed arm muscle, 385
retina electrical, 385
Storage, ferrite-core analog, 179
(See also Memory)

Storage readout, solid-state counter, 159
Storage register, flip-flop, 427
Strain gage, 0-25 kc amplifier, 366
self-balancing shunt bridge, 83
Stretching, pulse, 30, 542, 813
fast-rise-time pulse, 92
pulse width, 268, 541
Strobe, computer memory, 122, 124
oscillator-trigger, 224
ramp-staircase mixing, 632
(See also Flash)

Strobe pulse generator, crt, 96
Stroboscope, 1,000-flash-per-sec, 230
adjustable, 224

Stroboscope, flash rotameter, 229
high-voltage pulser, 223
modification of auto engine-timing strobe, 225
thyatron flashtube trigger, 225
(See also Flash; Strobe)

Strobocon, Geiger counter visual indicator, 562
sonar thumper, 833
Subaudio, 0.5-100 cps tunable amplifier, 49
Subtractor, photomultiplier signals, 641
sampled waveforms, 628
Summing, d-c operational amplifier, 103
Superconducting contact, test set, 778
Superheterodyne, double-conversion f-m, 574
Suppressor, scr noise, 445
Survey meter, radiation, 563
Surveying, datum stabilizer for radar altimeter, 87
Sweep, 20-40 cps variable, 676
10-minute stepped, 685
2 v increase per second, 700
100-v ramp, 633
800-v hybrid bootstrap, 698
backward-wave-tube shf, 697
bootstrap with flip-flop, 702
bootstrap ppm demodulator, 729
cro vertical, 697
Darlington bootstrap flip-flop, 699
double-bootstrap trianaval-wave, 632
fast-reset sawtooth, 699
fast-rotate sawtooth, 631
flying-spot scanner failure alarm, 696
gyro balancer cro, 89
high-precision facsimile recorder, 584
high-speed hybrid bootstrap, 697
horizontal scr, 702
horizontal tv, 417
hybrid bootstrap, 702
improved horizontal tv, 746
klystron, 697
klystron oscillator, 700
linear sawtooth, 632, 636
linearly varying 400-600 kc, 671
logarithmic bootstrap, 701
modified phantastron, 701
multi-range airborne radar, 696
power amplifier, 585
radar ppi, 549
radar ppi yoke driver, 550
radar timing pulse shaper, 550
sampling staircase, 686
sawtooth phantastron, 697, 701, 702
subaudio, 633, 635
tangent-function, 696
triangular-wave crt, 96
tunnel diode curve tracer, 700
tv horizontal gate-controlled switch, 702
two-transistor phantastron, 698
two-triode phantastron, 699
uit variable-frequency sawtooth, 637
ultrasonic remote control tester, 833
variable-length radar, 551
(See also Sawtooth)

Sweep generator, camera control, 489
cro horizontal, 97
cro raster, 98
cro vertical, 93
cro vertical triggering, 97
Switch, 20-kc control amplifier, 118
100-kc gate-turnoff chopper, 108
100-kc repetition rate for light-emitting diode, 226
10-Mc, 704
1-millimicrosec spark gap, 710
126-channel-sampling, 181
20-w one-shot mvbr, 437
360-kw electric fish fence, 707
Switch, 6.0 v, 808
a-c power outlet interlock, 713
a-c relay drive, 713
acceleration-sensing, 408, 410
acceleration-torsion-actuated, 406, 407
alarm lamp, 3
analog, 103
analog voltage comparator, 33
automatic f-m stereo, 689
avalanche, 527
balanced shunting, 278
balanced variable-threshold control, 441
bilateral logic, 345
capacitance-transducer, 705
capacitor-discharging neon, 709
capacitor-discharging power one-shot, 432
closed-ring sampling counter, 624
computer memory, 708
core readout, 33
counter disconnect for motor transients, 135
current-controlled threshold, 422
current-mode, 790
current-mode for light-emitting diode, 223, 228
current-mode transistor, 158
electronic ignition, 68
encoder diode matrix, 789
falling-sphere accelerometer, 424
fast a-c power, 712
fast d-c power, 712
fast-rise current for inductive load, 705
fine-deflection character generator, 101
flip-flop controlled audio, 710
forced speedup with shunt inductor, 707
four-layer diode gate, 277
frequency-sensitive photoreed, 483
gate-turnoff d-c circuit breaker, 708
goniometer r-f, 704
Hall generator, 623
high-power scr, 710
high-speed six-transistor relay, 705

- Switch, high-voltage, 703
- hold-sample-hold phase detector, 472
- hybrid 1,500-v, 711
- hybrid boxcar, 627
- hydrogen thyatron, 395
- ignitron discharge-shorting, 708
- Kita diode, 706
- light-activated, 484
- light-coupled binary, 709
- line-voltage-sensing, 501
- low-level differential, 33
- magnetic ignition, 69
- memory current, 124
- memory matrix, 713
- microwave fault alarm, 3
- missile count-down, 706
- modulator for synchronous full-wave scr, 864
- multiple spark gap, 704
- multiplexer transistor series, 270
- mvbr-driven, 520
- mvbr input with delay, 417
- nanosecond thyatron driver, 513
- negative-resistance-diode bistable, 709
- operational amplifier control, 704
- photodiode, 481
- polarity-sensitive, 618
- power-line overvoltage, 505
- power-transistor coarse-deflection, 101
- press safety, 706
- pulse integrator, 312
- pulsed-light scr, 484
- radar ppi marker, 553
- rate-effect suppression, 328, 329
- ring counter, 111, 113
- ring-of-five neon oscillator, 450
- rocket roll channel, 132
- saturable-reactor latch, 709
- scr, 707
- scr mvbr, 428
- scr for photoelectric tape reader, 480
- scr-read a-c, 711
- scr three-phase inverter, 326
- scr contact isolator, 704, 713
- sensitive a-c power, 709
- sensitive d-c power, 710
- serial decoder reference, 37
- sequential, 710
- solid-state dpdt, 712
- stalled servo motor protection, 657
- standby battery, 507
- stallarator crowbar, 708
- stepper drive, 530
- storage-capacitor for display, 708
- synchronous full-wave scr for a-c loads, 709
- three-lamp indicator, 207
- thyatron X-band magnetron control, 556
- touch control, 85
- transistor for auto ignition, 711
- Triac latching a-c, 329
- Triac on-off a-c, 713
- triggered-gap 50,000-amp pulse, 170
- two-stage d-c voltage comparator, 118
- two-way sampling, 627
- tunnel-diode bistable, 711
- u-jt latch for inductive-load scr, 329
- ultrasonic thyatron, 835
- variable-time one-shot, 429
- video display, 89
- video radar, 553
- video time-sharing, 703
- voice-controlled, 133
- voltage-controlled high-voltage, 506
- voltage-sensing scr, 5
- waveform-generating, 516
- white-noise-controlled, 679
- Switched regulator, d-c, 597
- Switching, adjustable-hysteresis flip-flop, 236
- automatic turnoff of unused computer sections, 125
- Switching, diode tester, 777
- glow-tube in nutrient diffractometer, 559
- high-speed nor gate, 341
- Kerr cell, 487
- magnetron beam tube, 537
- phasometer, 471
- recorder cores, 581
- transient-pulse detector, 329
- transistor-bridge, 83
- tunnel-diode, 117
- tunnel-diode pulser, 522
- unijunction ring counter, 164
- Switching circuit, analog-digital converter, 32
- character reader, 114
- static alternator, 314
- stepped-output power supply, 596
- temperature control, 759
- tunnel diode test, 766
- Switching-mode amplifier, servo motor, 658
- Switching-mode control, series d-c motor speed, 406
- Switching speed, magnetic film, 628
- Switching tube, pulse memory, 376
- Sync generator, synchrotron, 527
- Sync lock, television field pickup, 740
- Sync separator, transistorized color tv, 750
- Synchronization, 68.4-kc oscillator by 400-cps sync, 459
- 10:1 blocking oscillator, 447
- Synchronizer, motor-driven commutators, 619
- uhf for oscilloscope, 97
- Synchronizer, ultrasonic wavetrain zero-crossing, 828
- wire-line transmitter and receiver, 112
- Synchronous detector, pcm, 192
- Synchrotron, triggered square-wave generator, 527
- Synthesizer, 3,500-channel uhf transceiver, 242
- Synthesizer, frequency, 580
- Tachometer, 1-pps gate-opening turbine, 69
- 30,000-rpm capacitance transducer, 87
- auto engine, 65, 68
- conveyor belt slip detector, 353
- counter display, 70
- quick-disconnect automotive, 69
- scale-of-ten divider, 240
- scr single-pulse generator, 66
- single-transistor auto, 71
- test set, 771
- two-channel amplitude-discriminating, 71
- ultralinear high-precision auto, 67
- zero-power, 68
- Tank, 50-Mc phase-locked, 345
- Tape (see Magnetic tape; Tape recorder)
- Tape control, milling machine, 410
- Tape reader, photoelectric comparator, 119
- scr photoelectric, 480
- Tape recorder, 250 cps-250 kc instrumentation, 715
- 25-75 kc frequency modulator, 720
- 50-kc reference base oscillator for magnetic tape, 722
- 8-mm sound stripe, 716
- digital data read amplifier, 721
- digital data write amplifier, 722
- direct-reproduce instrumentation tape, 714
- erase and bias oscillator, 721
- fat preamplifier, 716
- f-m demodulator, 717
- magnetic tape, 721
- magnetic tape amplifier, 719
- movie camera magnetic, 718
- playback compensation, 717
- portable dictating machine, 719
- print-through squelch, 720
- read preamp, 715
- synchronous detector for zero-speed tape play-back, 714
- tape wow and flutter measurement, 716
- timing signal modification, 716
- transport test mvbr, 718, 720, 722
- video tape preamp, 715
- video time division multiplex, 715
- volume compressor, 721
- zero-speed tape playback, 718
- Target simulator, radar, 680, 683
- Teaching machine, reward-dispensing mvbr gate, 272
- Telemetry, 8-kc Q multiplier for tone filter, 413
- 60-kc mine-testing ultrasonic transmitter, 832
- 60-kc underwater hydrophone receiver, 831
- 2.2-Mc transmitter power amplifier, 548
- 3-Mc Colpitts oscillator, 450
- 3 Mc to 54 Mc frequency multipliers, 259
- 15-Mc command receiver, 300
- 23-Mc discriminator, 56
- 23-Mc voltage-controlled oscillator, 460
- 24-Mc varactor-modulated f-m, 252
- 27-Mc tone-modulated transmitter, 803
- 37.7-Mc oeg signal transmitter, 388
- 54-Mc low-power transmitter, 733
- 54-162 Mc frequency multiplier, 256
- 54-324 Mc frequency doubler-tripler, 257
- 92-Mc f-m transmitter, 732
- 95-Mc two-channel f-m/f-m transmitter, 724
- 100-Mc wireless link transmitter, 729
- 223-Mc transmitter, 800
- 230-Mc pam/f-m beacon, 252
- 460-Mc f-m command receiver, 615
- 10-channel satellite multiplexer, 152
- 20-channel audio tone decoder, 132
- 60-mw transmitter, 730
- airborne pdm key, 728
- amplifier overload protection, 331
- analog switch, 103
- anemovane audio oscillator, 727
- avalanche warning transmitter, 2
- baroreceptor nerve potential, 389
- basic scaler, 730
- camera control, 490
- command pulse monitor, 726
- commutator simulator, 678
- cosmic-ray gage, 562
- current-controlled subcarrier, 731
- daylight intensity, 484
- d-c coupled transmission line, 133
- d-c/d-c regulated power supply, 495
- d-c summing amplifier, 177
- differential voltage comparator, 117
- diversity combiner, 723, 730
- double-conversion f-m, 574
- electron density, 376
- energy-loss telescope, 726
- five-bit encoder, 731
- f-m/f-m helmet transmitter, 725
- f-m modulator, 730
- f-m strain-gage oscillator, 255
- four-channel discriminator, 725
- four-stage differential amplifier, 195
- heartbeat transmitter, 377
- high-altitude balloon, 76
- inductive, 768, 771
- logarithmic amplifier, 724
- low-drift d-c amplifier, 174
- low-pass filter, 219
- lunar probe, 727
- Mercury spacecraft audio driver, 50
- micro-multiplexer, 730
- missile performance monitor, 727
- multiplex driver, 725
- muscle potential, 391
- neutron-counting, 726
- Telometry, pcm frequency divider, 243
- pcm multiplexer, 729
- phone-line shaft position modulator, 661
- ppm demodulator, 729
- projectile acceleration, 731
- projectile nose pressure, 731
- pulse height-time converter, 143
- pulse reshaper for diversity combiner, 732
- pulse stretcher, 542
- radiation satellite transmitter, 728
- receiving decoder, 733
- resistance-controlled subcarrier oscillator, 732
- ringing-type pulse generator, 528
- sample-and-hold with bilateral charging, 626
- satellite cloud pictures, 582
- sideband rejection, 192
- slow-scan monitor, 742
- slow-scan tv camera, 735
- stratosphere balloon temperature, 756
- strobe for cro display, 89
- subcarrier oscillator stabilization, 728
- temperature, 760
- temperature monitor, 763
- tone-modulated transmitter, 618
- transducer d-c amplifier, 70
- water depth receiver, 825, 831
- wind-velocity transmitter, 724
- Telephone, baseband amplifier, 82
- data matrix readout, 122
- Telephone channel, thick-film amplifier, 43
- Telephone dial tester, flip-flop, 781
- Telephone system, low-voltage alarm, 5
- Teletypewriter, character counter, 787
- control, 386
- Telescope, energy-loss, 726
- Television, 3.58-Mc color burst oscillator, 753
- 4.5-Mc low-cost f-m sound discriminator, 251
- automatic chroma control, 751
- automatic color control and killer, 750
- bone section scanner, 382
- b-w 12-Mc distribution amplifier, 744
- b-w 100-Mc bandwidth, 745
- b-w automatic fringe tuning, 746
- b-w automatic local-fringe tuning, 739
- b-w CCTV 10-Mc carrier-modulator terminal, 745
- b-w CCTV 10-Mc repeater, 740
- b-w CCTV receiving terminal, 740
- b-w CCTV repeater power supply, 745
- b-w clamped sync separator, 744
- b-w delta sound system, 743
- b-w diode mixer, 746
- b-w germanium-transistor horizontal deflection, 741
- b-w horizontal oscillator stabilization coil, 746
- b-w horizontal sync transient display, 741
- b-w laser, 739
- b-w linear transistorized horizontal sweep, 746
- b-w mesa-transistor oscillator, 740
- b-w mesa-transistor tuner, 745
- b-w one-transistor video amplifier, 742
- b-w outline generator, 743
- b-w peak picture control, 741
- b-w pulse-cross generator, 746
- b-w reactance-transistor afc, 740
- b-w scr horizontal output, 744
- b-w slope detector for sound, 743
- b-w slow-scan monitor, 742
- b-w two-stage solid-state video amplifier, 741
- b-w two-transistor horizontal deflection, 745
- b-w video output amp, 746
- B-Y demodulator, 754
- cctv voltage bar graph display, 206
- color afc for mobile pickup, 750
- color age, 753
- color automatic fine tuning, 751
- color-burst amplifier, 753
- color-burst gate, 753
- color-burst gating-signal generator, 752
- color demodulator, 747, 752
- color killer and hold, 753
- color pickup reference oscillator, 752
- color sound i-f, 749
- color video i-f, 754
- community-tv up converter, 146
- eye-motion tracker, 483
- fet uhf tuner, 823
- flat-display scanner, 641
- French SECAM chrominance stages, 748
- gate-controlled switch horizontal sweep, 702
- gated color-burst regenerator, 748
- horizontal sweep mvbr, 417
- infrared modulator, 293
- keyed age, 59
- low-noise uhf tuner, 823
- magnetostriiction remote control, 830
- mobile color pickup limiter, 332
- modulation percentage monitor, 416
- power-line carrier-current remote-control transmitter, 620
- program rating scanner, 639, 642
- remote-control power-line carrier receiver, 617
- remote control of programs, 622
- remote ultrasonic volume control, 829
- sync-generator frequency divider, 241, 243
- three-transistor color audio, 752
- time-constant detector for remote control, 619
- transistorized chroma amplifier, 747
- transistorized color sync separator, 750
- transistorized luminance amplifier, 751
- transistorized portable tuner, 821
- two-color system, 749
- uhf tuner, 821
- u-jt raster generator, 640
- ultrasonic control receiver, 827
- ultrasonic control tester, 833

- Television, ultrasonic remote control, 409
- video i-f amplifier, 286
- weather satellite, 582
- wideband video amplifier, 641
- Television camera, 31.5-kc sweep oscillator, 736
- 15,000-v uvicon supply, 736
- aperture correcting circuit, 735
- generator-modulator, 734
- highlight equalizer, 738
- r-f modulator, 736
- slow-scan airborne, 735
- sync distributor, 737
- uvicon preamp, 734
- video gain control, 737
- vidicon sensitivity control, 737
- Temperature, automatic compensation, 380
- resistance-triggered alarm, 7
- Temperature coefficient measurement, power supply, 614
- Temperature compensation, 5-Mc flip-flop, 236
- junction diode, 595
- Temperature control, 1,000-cps phase-shift oscillator, 310
- 1.1-Mc tunnel-diode sensing oscillator, 761
- 0.01°C differential, 757
- body-temperature sensor, 758
- compensated resistance-thermometer, 761
- compensating bridge, 757
- crystal oven, 757, 759
- dual-output, 760
- fast-response, 762
- germanium-diode sensor, 757, 758, 763
- heat sink, 760
- high-accuracy, 761
- hybrid tube-transistor, 756
- liquid bath to 0.01°C, 763
- low-hysteresis d-c level detector, 762
- mercury thermostat and scr, 756
- mylar capacitor td oscillator, 762
- negative-feedback Schmitt trigger, 755
- nuclear-resonance sensor, 760
- oven, 755
- photographic developer, 762
- photographic dryer, 487
- proportioning, 762
- road icing alarm, 758
- rocket gyro, 758
- rocket surface, 756, 757, 760
- shunt-regulated power supply, 600
- soldering iron, 473
- space probe, 763
- stratosphere balloon telemeter, 756
- thermoelectric cooler, 759
- transistor thermometer, 759
- transmitter for frozen food, 758
- zero-crossing sync for scr's, 136
- Temperature measurement, 90°-105°F, 758
- transistor sensor, 759
- Temperature sensor, rate-of-rise, 1
- transistor, 757
- Test set, capacitance-tracking, 357
- Tester, 10-Mc computer circuits, 113
- 1-gc noise-figure converter, 441
- 0.5-v power supply, 596
- 300-kw uhf tube, 776
- airborne mtl, 682
- antenna radiation pattern, 802
- armature, 774
- auto engine, 66
- cable fault finder, 764, 769, 772
- cable propagation delay, 249
- cable wiring locator, 769, 770
- calibrating a-c meters to standard cell, 781
- capacitor and insulation leakage, 782
- capacitors in circuits, 88
- component waveforms, 773
- computer memory shmoov curve plotter, 432
- conductivity, 781
- contacts under vibration, 779
- counter, 527
- c-w receiver calibration oscillator, 676
- d-c amplifier for a-c meter calibration, 107
- d-c level-shifting, 768
- decade counter, 517
- decimal counter, 427
- detecting momentary opens during vibration, 781
- dial telephone, 781
- dielectric strength, 775
- diode, 767
- eddy-current crack and void detection, 367, 370
- electro-explosive devices, 777, 780
- fet adaptor for curve tracer, 764
- germanium diode leakage, 782
- go-no-go vtm, 858
- harness, 765
- hi-pot, 771
- high-power modulator tubes, 765
- high-speed digital computer, 110, 681
- in-flight missile, 727
- instrument servo cycling, 662
- insulation resistance, 772
- invisible flaws in wire, 772
- ionospheric sounding pulse receiver, 677
- keyed d-c or tone signal generator, 434
- lamp circuit, 769, 779
- linear integrated-circuit, 295, 303, 604
- magnetic core, 525, 767
- magnetic tape, 779
- meter and transistor regulated 30-v supply, 775
- movie camera frame rate, 485
- movie projector frame rate, 485
- nine-bit generator for digital systems, 526
- nor logic, 348
- nuclear explosion alarm system, 682
- Tester, overtone crystal bridge, 83
- overtone quartz crystal, 776
- parallel-path continuity, 780
- parametric amplifier, 766
- phase detector, 265
- production transistor, 779
- pulse-type power transistor, 768, 770
- radar with ignitron crowbar, 765
- radar target simulator, 680, 683
- regulated power supply, 614
- relay, 780
- relay contact chatter, 774
- resistance go-no-go, 733
- resistor, 808
- saturated transistor, 813
- scr turnoff, 778
- servo swept signal generator, 676
- short-circuit alarm, 4
- spin-system inductive-telemetry, 768, 771
- quib simulator, 681
- stepper motor, 777, 778
- stopping switch pulser, 530
- stereo f-m multiplex receiver, 690
- superconducting contacts, 778
- switching diode, 777
- switching time of and gate, 300
- tape recorder flutter, 360
- thermocouple, 768
- time-shared computer, 122
- transistor, 770
- transistor 200-Mc noise figure, 441
- transistor with audio indicator, 774
- transistor beta, 358, 774
- transistor beta display, 93
- transistor circuit, 493, 507
- transistor conversion gain, 358
- transistor curve tracer, 688
- transistor and diode curve tracer, 770
- transistor frequency response, 232
- transistor h-fa at 100 Mc, 782
- transistor with lamp indicator, 774
- transistor leakage, 195, 770
- transistor noise figure, 439, 779
- transistor power gain, 362, 782
- transistor tv mixer, 771
- tube life-test pulser, 773
- tunnel diode, 373, 374, 700, 766, 771, 781
- tunnel-diode switching-time, 766
- tv vhf transistor, 769
- two-channel tachometer, 771
- ultrasonic reactor-slug cladding nonbond, 830
- ultrasonic remote controls, 833
- underwater mines, 831, 832
- vlf amplifier and servo, 636
- voltage go-no-go, 812
- voltage limit monitor, 858
- Thermistor, crystal oven control, 759
- sawtooth circuit, 636
- servo control, 648, 656
- temperature control, 755
- Thermistor bridge, temperature control, 762
- Thermocouple, amplifier, 366
- calibrating a-c meters to standard cell, 781
- test set, 768
- Thermocouple gage, regulated power supply, 606
- Thermoelectric cooler, temperature control, 759
- Thermometer, resistance-bridge, 761
- transistor, 759
- Thermostat, scr, 757
- (See also Temperature control)
- Thickness measurement, coating, 362, 365
- Thin film, logic, 349
- memory current driver, 124
- memory sense amplifier, 124
- toggling logic, 340
- Threshold circuit, analog-digital converter, 32
- balanced variable, 441
- balanced variable a-f, 446
- Threshold detector, ultrasonic remote control, 829
- Thyatron, 360-kw switching circuit, 707
- pulsor, 493
- solid-state, 726
- Tick-tack-toe, logic gates, 337
- Time amplifier, pulse width stretcher, 268
- Time base, wide-range linear bootstrap, 634
- Time-base generator, missile fuze, 111
- Time delay, photoelectric, 484
- Time delay (See also Delay)
- Timer, 200-Mc gate, 269
- 500-Mc scalar control, 164
- 1-pps gate-opening, 69
- 400-pps cold-cathode, 157
- 0.1 to 50-sec low-cost fot, 787
- 0.1 sec to 2 hr for data-recording camera, 486
- 0.3-6 sec variable-duty-cycle recycling, 789
- 1-sec scs controls 1 amp, 788
- 1-400 sec thyatron, 785
- 1-5 sec one-shot mybr, 784
- 2 sec-10 min, 298
- 5-10 sec low-cost intervalometer, 791
- 10-sec scs, 787
- 13-sec, 791
- 15 sec-2 min thermal, 422
- 30 sec-350 hr delay, 188
- 4-hour, 813
- 8-to-1 frequency-dividing for 80-Mc oscillator, 244
- a-c zero locator, 783
- adjustable-interval, 790
- adjustable saturable-reactor, 784
- basic hybrid ujt-npn, 784
- basic thyatron, 785
- bootstrap, 488
- camera flash, 228
- cement-setting, 359
- Timer, clock oscillator, 111
- clock output driver, 111
- constant-current generator, 787
- current-mode switch, 790
- dual-polarity start-stop control, 790
- electronic camera shutter, 485
- fencing foil touch, 785
- fot monostable mybr, 785
- five-function word programming, 864
- five-thyatron resistance welder sequence control, 860
- four-function welding control, 861
- free-running dual-output, 788
- hall acoustic measurement, 787
- linear-scale fot, 786
- magnification-compensating darkroom, 487
- nuclear particle flight, 783
- operational trigger, 813
- photographic exposure with relay chatter suppression, 488
- photomultiplier for enlarger, 486
- precision weld interval, 863
- print contrast and density, 490
- pulse-stretching, 541
- radar, 550
- random interval, 789, 791
- recorder-controlling, 786
- relaxation, 329
- relay contact chatter, 774
- relay dropout, 188
- repeat-cycle, 784
- resistance welding gun control, 859
- retina welder, 391
- ring counter, 113
- scs pulse-stretching, 544
- sonar, 204
- stalled servo motor protection, 657
- switching sequence, 710
- teletypewriter character-counting, 787
- tool running-time, 785
- transient-immune with wide dynamic range, 784
- transistor-controlled motor, 790
- transistor start-stop control, 786
- tv image photography, 489
- ujt, 787
- ujt-npn basic hybrid, 789
- ujt-relay interval, 788
- ultrasonic sing-around transmitter, 834
- valve closure, 784
- variable-delay relay, 180
- variable-program camera, 488
- vhf counter, 788
- watch, 790
- weld-heat program, 860
- (See also Counter; Delay)
- Timing, automotive automatic advance, 65
- Timing signal, modification for low-cost tape recorder, 716
- Timing signal playback, magnetic tape recorder, 586
- Tagging circuit, thin-film, 340
- thin-film with transistor, 349
- Tone control, fet, 50
- Tone-operated calling, citizens band, 572
- Tonometer, crystal oscillator, 392
- electronic, 378
- Touch control switch, body-capacitance, 85
- Tracer, tunnel diode curves, 771
- Tracking, telescope servo, 662
- Tracking system, missile, 430
- Train generator, eight-pulse, 142
- Trainer, sonar, 832
- Transceiver, 809.11-kc clock oscillator, 112
- 920-channel crystal-controlled mobile radio, 792
- 3,500-channel frequency synthesizer, 242
- CB 5-w overlay-transistor, 795
- CB class-B, 794
- CB class-C, 794
- CB with electromechanical filter, 792
- CB with noise squelch, 795
- CB squelch, 794
- CB three-transistor, 793
- CB tone-pulse decoder, 795
- CB transmitter without relay, 794
- divide-by-5 counter for frequency synthesizer, 240
- infrared, 290
- noise-actuated avc, 793
- single tunnel-diode, 793
- (See also Receiver; Transmitter)
- Transducer, beam switching, 705
- electro-optic, 415
- frequency-to-voltage, 146
- parallel-mode scanner, 649
- scanner, 643
- stress-sensitive integrated-circuit, 301
- temperature using transistor base-emitter voltage, 756
- voltage-resistance converter, 84
- Transducer drive, constant-current, 168
- Transfluxor, 1-Mc oscillator, 526
- analog-digital converter, 30, 34
- Transformer, 6-v regulating, 613
- broadband impedance, 14
- saturable-core frequency septupler, 258
- Transient suppression, scr latch, 328
- Transistor, fet pinchoff measurement, 369
- measuring alpha cutoff, 375
- measuring forward-current transfer ratio, 360
- measuring noise figure at 200 Mc, 444
- measuring open-circuit output admittance, 361
- measuring power gain, 362
- measuring reverse-voltage transfer ratio, 371
- measuring short-circuit input resistance, 360
- measuring thermal constants, 359

Transistor, saturation tester, 813
 Translator, infrared width gage, 294
 Transmission line, d-c coupled driver-receiver, 133
 Transmission-line feedback, pentode trigger, 809
 Transmitter, 2-kc magnetic induction field, 376
 9-kc oscillator stabilizer, 799
 21-kc ultrasonic, 831
 22-kc sonar, 835
 60-kc ultrasonic mine-testing, 832
 460-kc f-m wireless microphone, 800
 500 kc and 8.326-Mc distress, 79
 670-kc crystal-controlled low-power a-m, 799
 1.686-Mc high-altitude balloon, 76
 2.50 Mc crystal, 802
 2.185-Mc marine band, 805
 2.2-Mc radar power output stage, 548
 27-Mc CB transformer-coupled collector-modulated, 397
 Transmitter, 27-Mc eight-command remote control, 620
 27-Mc remote-event, 803
 27.12-Mc retina welding, 379
 27.255-Mc ujt-id remote control, 618
 37.7-Mc eeg signal, 388
 50-Mc 1-w a-m, 802
 50-Mc 10-w, 797
 50-Mc at 40 w, 805
 50-250 Mc tunnel-diode, 824
 54-Mc low-power telemetry, 733
 73.5-Mc self-modulated crystal, 806
 80-160 Mc standard output doubler, 796
 92-Mc f-m, 732
 96-110 Mc f-m wireless microphone, 800
 100-Mc telemetry link, 729
 150-Mc telemetry, 731
 160-Mc 15-w power amplifier, 255
 160-Mc 15-w three-transistor power amplifier, 803
 162-180 Mc narrowband, 806
 170-Mc 1-w, 797
 175-Mc f-m mobile amplifier, 796
 220-260 Mc beacon, 77
 223-Mc telemetry, 800
 243-Mc aircraft crash beacon, 80
 60-mw telemetry, 730
 2-w parallel-transistor output, 804
 2-w parallel-transistor output with emitter tuning, 798
 2-w push-pull output, 804
 4-w emergency, 78
 30-w pulsed ultrasonic, 837
 10-meter ssb exciter, 798
 80-meter amateur vfo c-w, 799
 afc for mobile color tv pickup, 750
 avalanche warning, 2
 beacon pulse modulator, 80
 CB, 795
 CB class-C, 794
 CB without relay, 794
 CB three-transistor, 793
 damped voice-operated, 801
 duty-cycle limiter for pulse modulation, 332
 electroluminescent display, 205
 extending transistor frequency, 797
 f-m/f-m telemetry, 725
 f-m one-transistor, 803
 f-m wireless mike, 802, 803
 frozen-food temperature, 758
 heartbeat telemetry, 377
 infrared tv, 293
 lunar probe, 727
 minimum-jitter pulsed ultrasonic, 835
 modulation percentage monitor, 416
 muscle-potential induction, 391
 phase-shift keyer, 805
 power-line carrier-current for tv remote control, 620
 pulsed X-band magnetron radar, 556
 push-pull lattice-filter output, 803
 push-push doubler, 804
 radiation satellite, 728
 radiosonde, 726
 remote control toy, 617
 r-f monitor, 362
 satellite video recorder, 582
 selective paging, 801
 servo-tuned, 643
 stepped frequency exciter, 804
 stereo multiplex a-f amplifier, 690
 stereo multiplex oscillator, 692
 stereo multiplex subcarrier generator, 695
 stereo phase modulator, 693
 suppressed-carrier, 801
 target drone control, 615
 temperature telemeter, 756
 tone-modulated telemetry, 618
 tripler-doubler for 700-1,200 Mc, 262
 tropospheric scatter fading control, 192
 ultrasonic velocity measurement, 825
 (See also Frequency modulation; Radar; Remote control; Telemetry; Television; Transceiver; Ultrasonics)
 Transponder, missile, 548
 signal-powered, 80
 Trap, f-m weak-signal capture, 250
 Traveling-wave tube, fail-safe filament regulator, 506
 modulator, 398, 403
 Triac, motor control, 412
 Triac-Diac, reversing a-c motor control, 411
 Triangular-sine converter, fat, 542
 10-cps to 20-kc voltage-controlled positive or negative, 633
 Trigger, 0-500 kc Schmitt, 819
 3-kc scr, 426
 7-kc Schmitt, 371
 Trigger, 25-kc, 808
 100-kc counter, 155
 100-kc Schmitt, 818
 0-1 Mc Schmitt, 818
 10-Mc Schmitt square-wave generator, 818
 40-na current-sensing, 815
 1-amp npn pulse generator, 813
 0.2-300 sec Schmitt, 483
 acoustic timer, 787
 adjustable-prr blocking-oscillator, 814
 adjustable Schmitt, 814
 analog-computer overvoltage, 5
 any input pulse shape, 819
 astable mvbr, 434
 automatic ignition control, 65
 basic npn memory, 815
 battery-operated cro, 94
 binary ramp-generator control, 306
 cathode-coupled mvbr, 419
 closed-ring counter, 624
 cold-cathode ring counter, 157
 conventional Schmitt, 817
 cro, 697
 cro sweep, 89
 crystal lapping control, 249
 decade decimal counter, 814
 delayed, 812
 delta-sigma modulator, 393
 diode bridge drive, 820
 diode-coupled Schmitt, 811
 diode-modified Schmitt, 815
 diode-protected modified Schmitt, 819
 dual-pulse, 807
 eeg waveform zero detector, 383
 encoder limit, 791
 fast-recovery flip-flop, 817
 flip-flop, 815
 flip-flop steering circuit, 813, 819
 f-m/f-m telemetry frequency divider, 243
 four-stage npn binary counter, 814
 generator, 538
 half-sinusoid pulse generator, 812
 high-voltage pulse generator, 587
 with hybrid gate, 818
 integrated-circuit Schmitt, 300
 low-cost Schmitt, 810, 820
 low-hysteresis differential amplifier, 819
 low-level threshold detector, 816
 low-output-impedance Schmitt, 809
 magnetoresistor bridge, 84
 memory-core, 126
 missing-pulse detector, 6
 modified Schmitt frequency quintupler, 260
 motor control, 406, 412
 multiple-input Schmitt, 817
 noise-immune scr without pulse-forming network, 809
 noncutoff Schmitt, 811
 operational, 770, 807, 808, 813
 operational hi-pot, 771
 operational liquid-level control, 136
 operational temperature-control, 762
 operational voltage-monitoring, 812
 photoflash, 136
 pnp plus npn, 809
 pnp flip-flop, 811
 pnp one-shot, 809
 pnp power flip-flop, 820
 pnp ring counter, 820
 pnp shift register, 819
 polarity-sensitive, 632
 pulse, 336
 pulse-resolving Schmitt, 394
 pulse sampler, 92
 pulse width, 539
 push-pull six-transistor relay, 705
 random-pulse, 813
 regenerative switching, 812
 retina welder pulse-shaping, 381
 Schmitt, 425, 506, 534, 700, 810, 820
 Schmitt adf phasemeter, 465
 Schmitt combined with mono mvbr, 814
 Schmitt counting for buttons, 148
 Schmitt with delay, 417
 Schmitt double-triode, 717
 Schmitt f-m stereo signal generator, 690
 Schmitt high-impedance, 812
 Schmitt ionospheric sounder simulator, 684
 Schmitt leakage-testing, 782
 Schmitt odd-even logic, 346
 Schmitt with operational amplifier, 807
 Schmitt phase-difference, 808
 Schmitt pulse-width modulation, 303
 Schmitt quad-block, 304
 Schmitt in sawtooth generator, 636
 Schmitt scr, 810
 Schmitt signal analyzer, 536
 Schmitt sine to square wave, 629
 Schmitt substitute, 807
 scr-ignitron, 809
 secondary-emission pentode, 809-811
 secondary-emission pentode variable-sensitivity, 811
 sense amplifier, 812
 sensitive a-c power switch, 709
 sensitive d-c power switch, 710
 series Schmitt, 810
 simplified full-wave ujt-scr, 817
 simplified ujt-scr, 820
 sing-around ultrasonic transmitter, 826
 single 50,000-amp current pulse, 170
 slider, 202
 sound pulse, 833
 with speed-boosting trigger amplifier, 814
 stepped-sweep Schmitt, 685
 Trigger, strobe pulse, 96
 Stroboscope scr, 225
 Synchronizing to 3.3-Mc clock, 113
 Teaching-machine reward dispenser, 272
 Time-delayed Schmitt, 136
 Time-recording, 625
 Transient-rejecting scr, 818
 Transient-suppressing, 816
 Transistorized for 50-kc counter, 155
 Tunnel-diode, 117
 Tunnel-diode pulser, 249
 ujt, 425, 608
 ujt/triac phase control, 816
 Valve closure measurement, 784
 Van de Graaff accelerator, 512
 Variable-hysteresis Schmitt, 813
 Variable negative-feedback Schmitt, 755
 Variable-program camera timer, 488
 Variable Schmitt, 119
 Voltage-level, 808
 Voltage-sensing, 810, 816
 Zero-hysteresis Schmitt, 815
 Trigger control, scr, 83
 Trigger generator, 20-kc, 817
 Tripler, SOS beacon frequency, 78
 (See also Frequency multiplier)
 Troposcatter, out-of-baseband logarithmic noise amplifier, 440
 Troposcatter receiver, level control, 576
 Truth table, script recognition, 336
 Tube aging, vtvm compensator, 857
 Tuner, 35-Mc reactance-tube, 466
 60-90 Mc voltage-tuned, 18
 1,000-Mc npn oscillator, 824
 3-transistor uhf, 821
 a-m/f-m three-transistor portable, 574
 automatic stereo f-m switch, 689
 capacitively tuned tunnel-diode, 823
 current-tuned uhf, 822
 electronically variable capacitance, 88
 fet f-m stereo, 691
 fet uhf tv, 823
 f-m afc, 822
 f-m stereo multiplex, 253
 improved codan, 812
 low-cost single-chip f-m, 254
 low-noise uhf tv, 823
 mesa-transistor tv, 745
 mesa-transistor tv oscillator, 740
 pin diode in stereo f-m, 695
 Q multiplier, 141
 remote-control transmitter, 824
 rotary-axial ferrite-cup, 822
 servo-driven, 643
 signal-seeking auto radio, 824
 three-transistor vhf tv, 821
 transit-time diode uhf/shf, 821
 tunnel-diode uhf, 823
 tv diode mixer, 746
 two-chip f-m, 250
 uhf tv, 823
 Tuner (See also Receiver; Television; Transceiver)
 Tuning, 500-kc i-f transformer delay-line, 282
 automatic color tv, 751
 automatic fringe tv, 746
 automatic local-fringe tv, 739
 voltage-controlled ferroelectric capacitor, 462, 676, 822
 Tuning fork, 400-cps inverter, 324
 Tuning-fork filter, adjustable, 218
 Tunnel diode, curve-tracer, 94
 measuring d-c parameters, 373
 measuring negative resistance, 374
 Typewriter control, neutron diffractometer, 559
 Ultrasonic control, motor, 409
 Ultrasonics, 13-kc responder for blind, 832
 delay line drive, 462
 19.5-kc sonar f-m demodulator, 831
 19.5-kc sonar f-m modulator, 835
 20-40 kc generator, 830
 22-kc sonar receiver, 828
 22-kc sonar transmitter, 835
 25-kc 100-w generator, 835
 60-kc hydrophone receiver, 831
 60-kc mine-testing transmitter, 832
 85-kc transmitter, 837
 250-kc broadband preamp, 833
 fet hydrophone preamp, 828
 fish finder, 836
 magnetostriction tv remote control, 830
 magnetostrictive delay line amplifier, 836
 neurosurgery oscillator, 386
 prf multiplier, 836
 reactor-slug flow detector, 830
 remote control tester, 833
 remote tv volume control, 829
 sing-around cycle counter and timer, 834
 sing-around transmitter, 825
 sing-around transmitter trigger, 826
 sonar audio selection gate, 827
 sonar bearing indicator, 829
 sonar peak-amplitude detector, 832
 sonar pulse generator, 833
 thyatron-switch transmitter, 835
 trf receiver, 837
 tv control receiver, 827
 tv remote-control time-constant detector, 619
 undersea propagation gate generators, 834
 undersea propagation receiver, 826
 undersea propagation transmitter, 829
 water depth receiver, 825, 831
 zero-crossing synchronizer, 828
 Ultraviolet, portable inverter supply, 321